

XRT Timeline to be uploaded on 2012/08/11

Period: 2012/08/11 10:28:00 - 2012/08/16 09:13:00

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Normal mode

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XOB #1913: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band Test													
Term	Pointing (x, y)		Comment										
08/11 10:41:00 - 08/11 12:19:54	Track (-41.7, 75.9)	# OP start + 10min HOP160 for 1.5hr										
08/12 06:13:00 - 08/12 10:10:30	Track (-89.6, -332.4)	Cont,										
08/12 10:38:37 - 08/12 11:53:30	Track (172.1, 76.7)	HOP160 for 1.5hr										
08/12 12:17:06 - 08/12 16:49:00	Track (-38.0, -333.0)	Track AR11542										
08/13 06:36:30 - 08/13 10:29:54	Track (126.0, -332.8)	Track AR11542										
08/13 10:33:00 - 08/13 11:59:54	Track (377.8, 83.2)	HOP160 for 1.5hr										
08/13 12:03:00 - 08/13 17:26:30	Track (173.6, -332.1)	Track AR11542										
08/13 18:23:00 - 08/13 22:22:30	Track (228.3, -330.9)	Cont,										
08/14 01:03:00 - 08/14 05:56:54	Track (285.1, -329.2)	Track AR11542										
08/14 06:10:00 - 08/14 08:55:45	Track (327.8, -327.6)	Cont,										
PROG= 11 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 18 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 9 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 10 25-time(s) 80.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1912: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad With G-band Test													
Term	Pointing (x, y)		Comment										
08/11 12:23:00 - 08/11 16:59:54	Track (-244.7, -328.5)	Track AR11542										
08/11 18:33:36 - 08/12 05:59:54	Track (-196.0, -330.0)	Track AR 11542										
PROG= 08 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 18 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 9 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 10 30-time(s) 60.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18FF: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(12)													
Term	Pointing (x, y)		Comment										
08/11 17:03:00 - 08/11 17:50:00	Fixed (0.0, 0.0)	synoptic, shifted manually and SOT flat field obs for 1hr										
08/12 06:03:00 - 08/12 06:09:54	Fixed (0.0, 0.0)	synoptic										
08/12 17:38:00 - 08/12 18:27:30	Fixed (0.0, 0.0)	synoptic, shifted manually and SOT flat field obs for 1hr										
08/13 06:27:54 - 08/13 06:33:24	Fixed (0.0, 0.0)	synoptic, shifted 23.5 min										
08/13 18:13:00 - 08/13 18:19:54	Fixed (0.0, 0.0)	synoptic, shifted 10.0 min										
08/14 06:00:00 - 08/14 06:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min										
PROG= 01 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 11 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 12 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1919: AR Standard-B(Morphology) with PFB, FW1=Open, 384x384 at 1064 1048, 30s cad With G-band Test

Term	Pointing (x, y)	Comment
08/12 19:10:36 - 08/13 05:47:00	Track (20.3, -333.3) @ 08/12 18:35:00	AR11542 and HOP128(23:00 - 01:00)
08/13 23:03:00 - 08/14 00:59:54	Track (104.0, 223.3) @ 08/13 23:00:00	HOP128 for 2hr on AR11543

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 19 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 18 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 9 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 21 60-time(s) 30.0sec												
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1914: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops (45ms Gband)

Term	Pointing (x, y)	Comment
08/11 10:41:00 - 08/11 12:19:54	Track (-41.7, 75.9) @ 08/11 10:38:00	# OP start + 10min HOP160 for 1.5hr
08/11 12:23:00 - 08/11 16:59:54	Track (-244.7, -328.5) @ 08/11 12:20:00	Track AR11542
08/11 18:33:36 - 08/12 05:59:54	Track (-196.0, -330.0) @ 08/11 18:00:00	Track AR 11542
08/12 06:13:00 - 08/12 10:10:30	Track (-89.6, -332.4) @ 08/12 06:10:00	Cont,
08/12 10:38:37 - 08/12 11:53:30	Track (172.1, 76.7) @ 08/12 10:30:00	HOP160 for 1.5hr
08/12 12:17:06 - 08/12 16:49:00	Track (-38.0, -333.0) @ 08/12 12:00:00	Track AR11542
08/12 19:10:36 - 08/13 05:47:00	Track (20.3, -333.3) @ 08/12 18:35:00	AR11542 and HOP128(23:00 - 01:00)
08/13 06:36:30 - 08/13 10:29:54	Track (126.0, -332.8) @ 08/13 06:33:30	Track AR11542
08/13 10:33:00 - 08/13 11:59:54	Track (377.8, 83.2) @ 08/13 10:30:00	HOP160 for 1.5hr
08/13 12:03:00 - 08/13 17:26:30	Track (173.6, -332.1) @ 08/13 12:00:00	Track AR11542
08/13 18:23:00 - 08/13 22:22:30	Track (228.3, -330.9) @ 08/13 18:20:00	Cont,
08/13 23:03:00 - 08/14 00:59:54	Track (104.0, 223.3) @ 08/13 23:00:00	HOP128 for 2hr on AR11543
08/14 01:03:00 - 08/14 05:56:54	Track (285.1, -329.2) @ 08/14 01:00:00	Track AR11542
08/14 06:10:00 - 08/14 08:55:45	Track (327.8, -327.6) @ 08/14 06:07:00	Cont,

PROG= 09 15-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 13 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 14 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Seqn= 20 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

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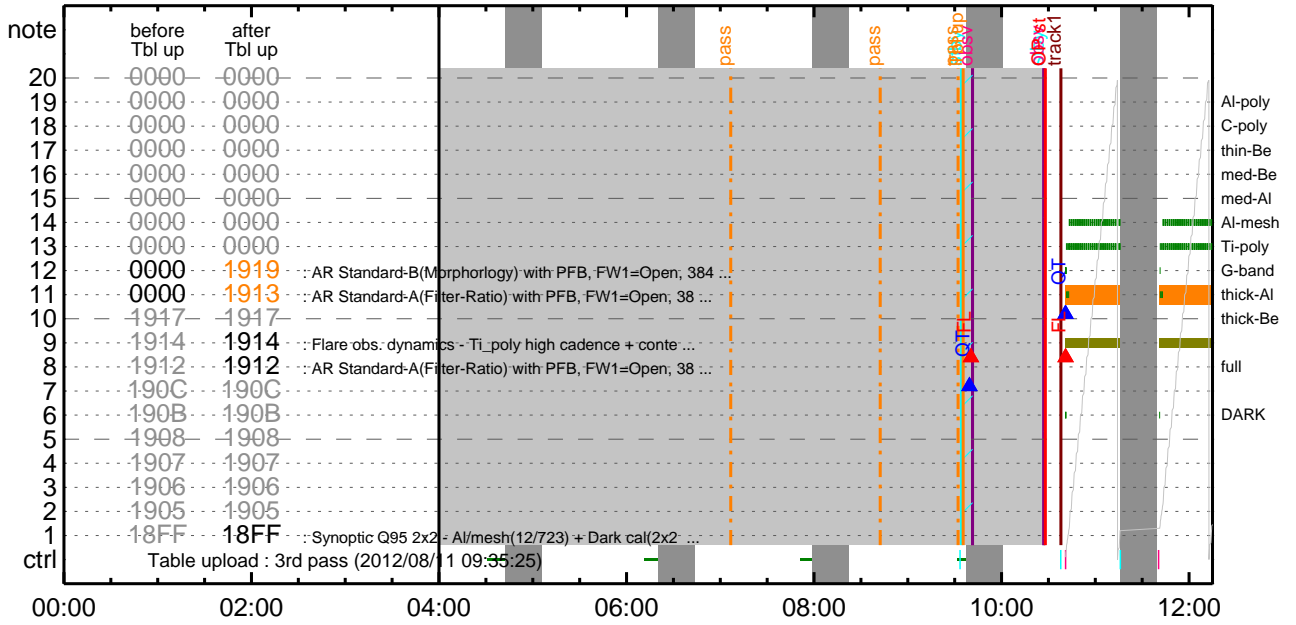
FLD Patrol

Term	Pointing (x, y)	Comment
08/11 10:38:16 - 08/11 17:00:16	Track (-41.7, 75.9) @ 08/11 10:38:00	# OP start + 10min HOP160 for 1.5hr

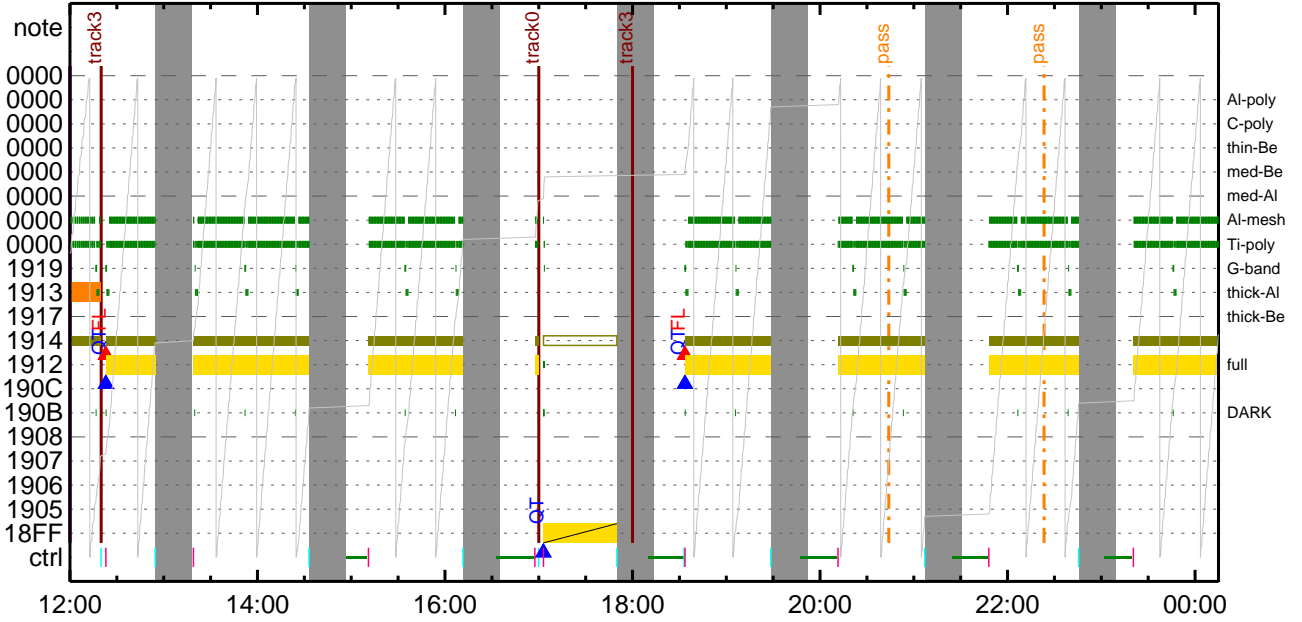
08/11 18:33:22 - 08/12 06:00:16 Track (-196.0, -330.0) @ 08/11 18:00:00 Track AR 11542
 08/12 06:10:16 - 08/12 17:35:16 Track (-89.6, -332.4) @ 08/12 06:10:00 Cont,
 08/12 19:10:22 - 08/13 06:27:46 Track (20.3, -333.3) @ 08/12 18:35:00 AR11542 and HOP128(23:00 - 01:00)
 08/13 06:33:46 - 08/13 18:10:16 Track (126.0, -332.8) @ 08/13 06:33:30 Track AR11542
 08/13 18:20:16 - 08/14 05:57:16 Track (228.3, -330.9) @ 08/13 18:20:00 Cont,
 08/14 06:07:16 - 08/16 09:13:00 Track (327.8, -327.6) @ 08/14 06:07:00 Cont,

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

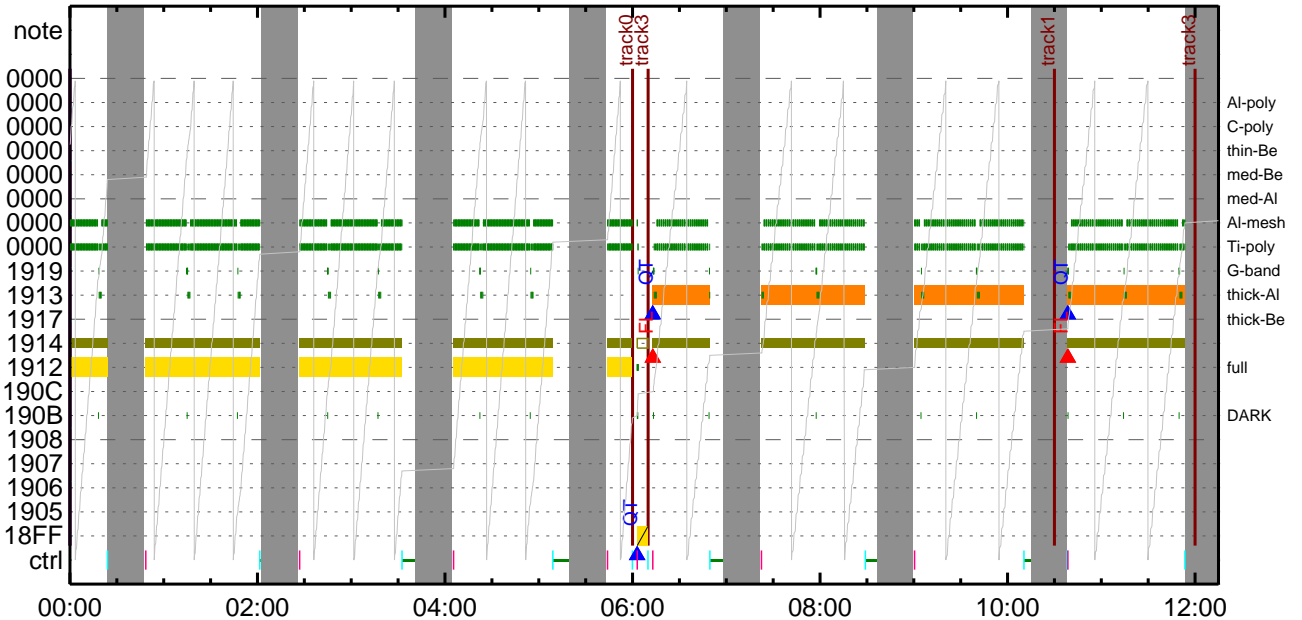
CMDI #0840 2012/08/11



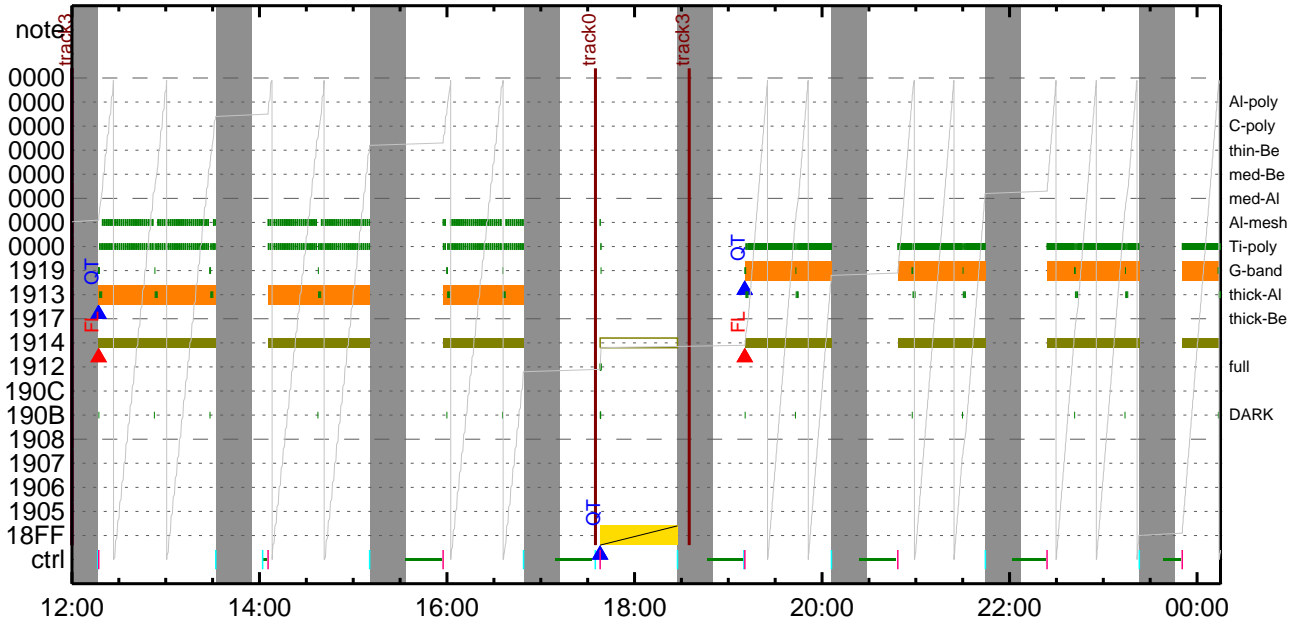
CMDI #0840 2012/08/11



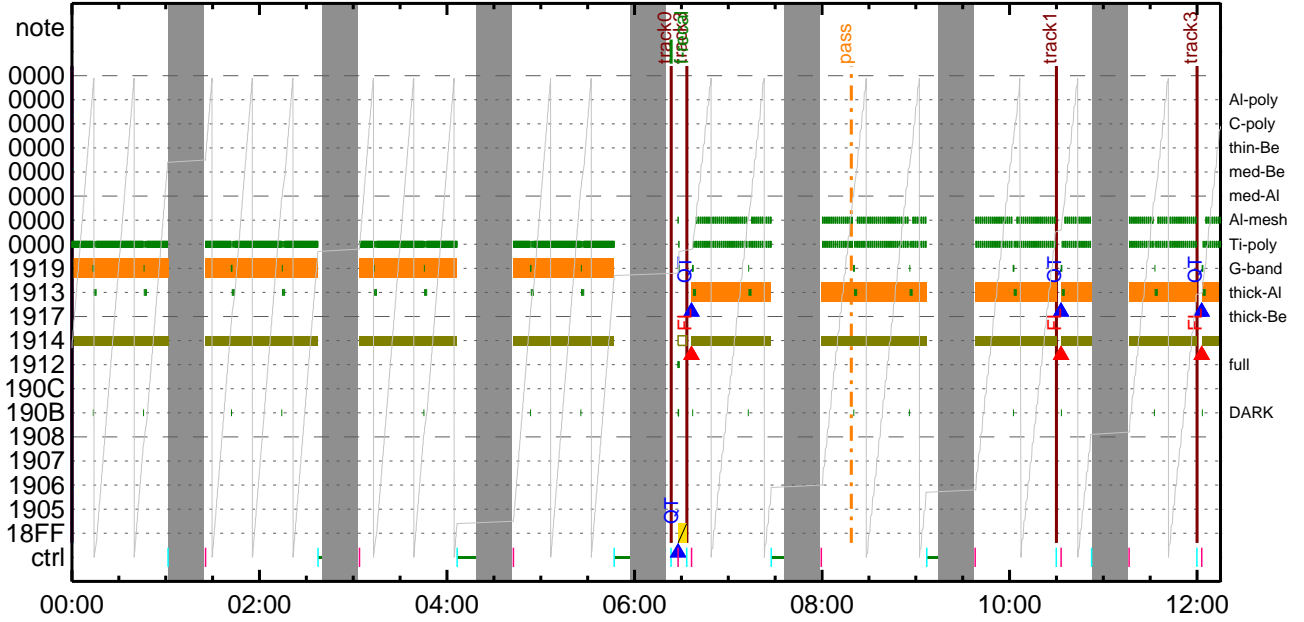
CMDI #0840 2012/08/12



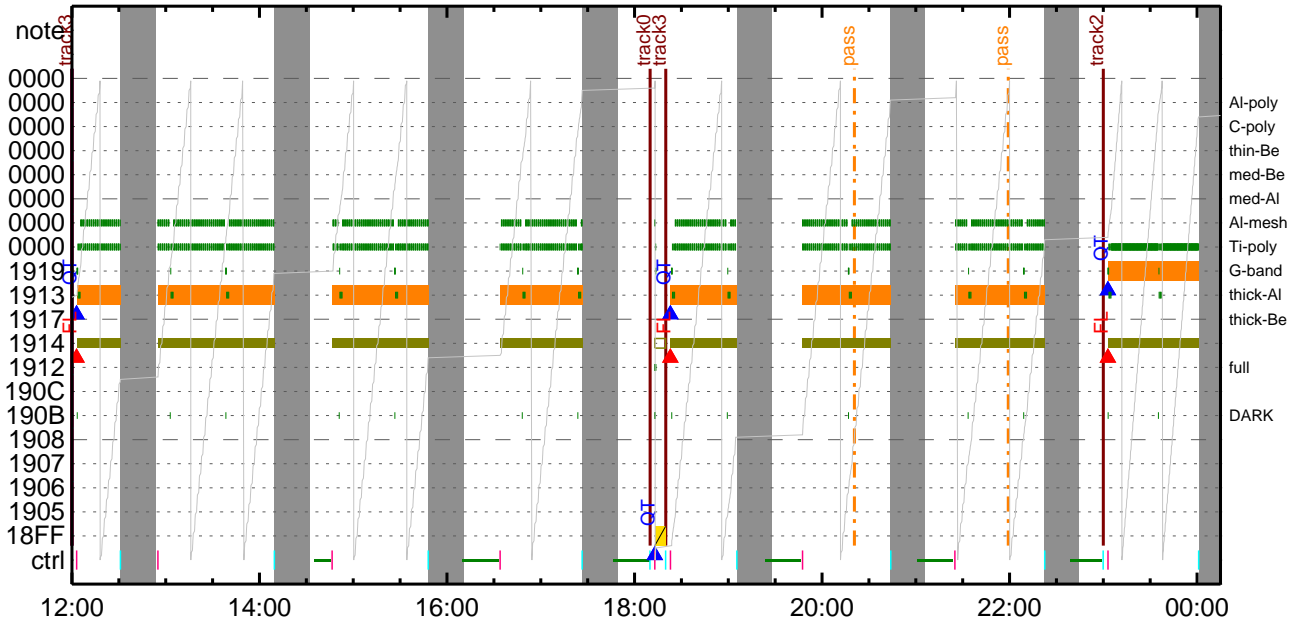
CMDI #0840 2012/08/12



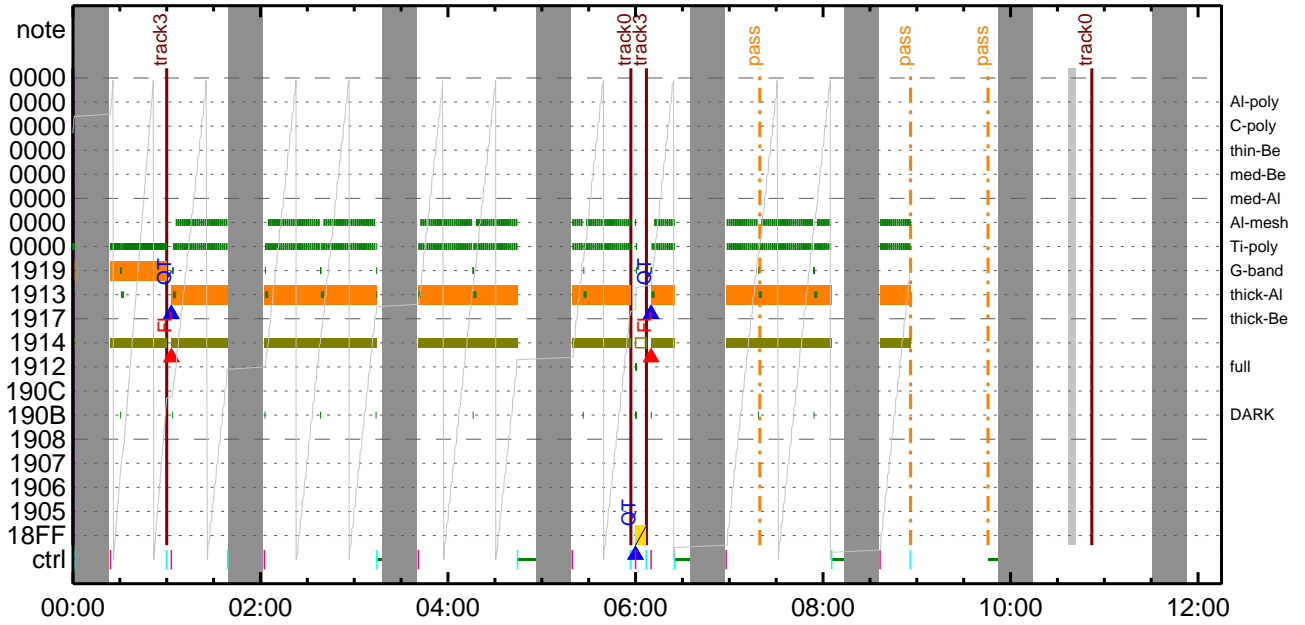
CMDI #0840 2012/08/13



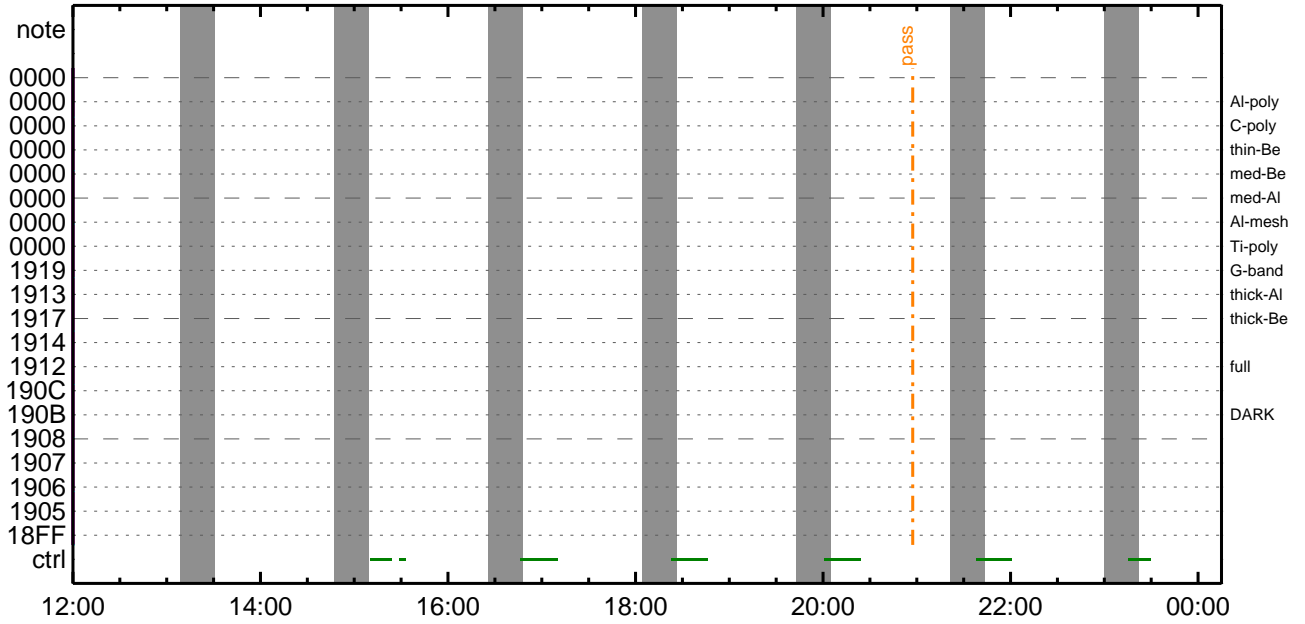
CMDI #0840 2012/08/13



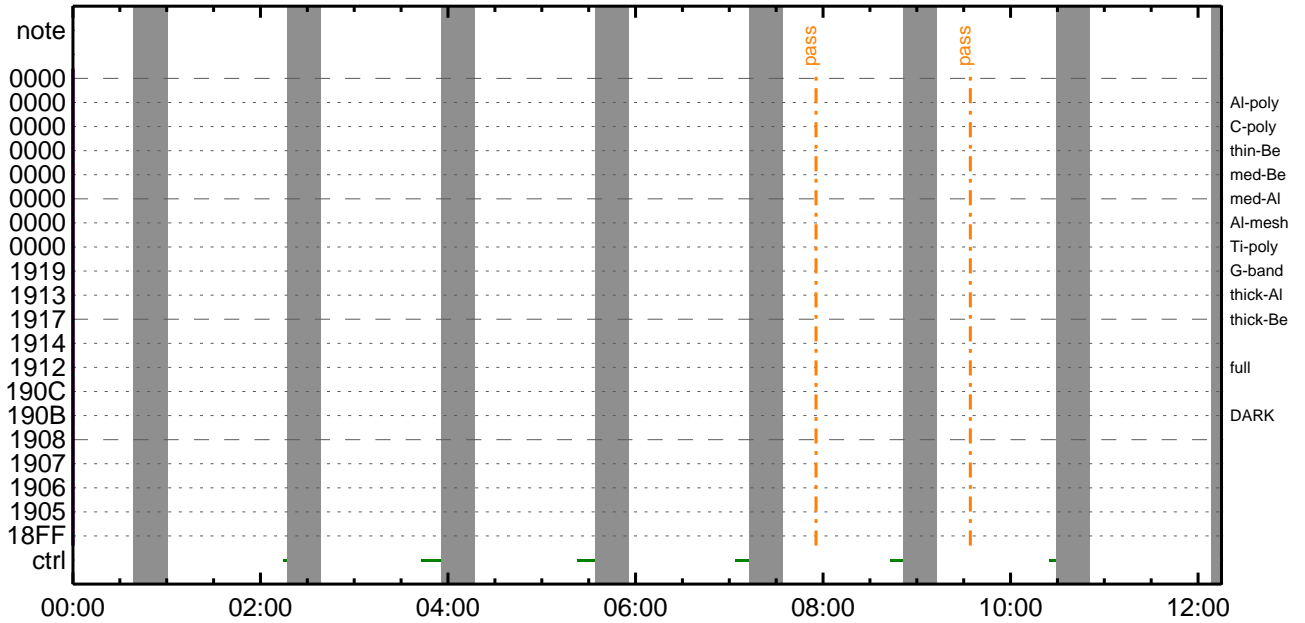
CMDI #0840 2012/08/14



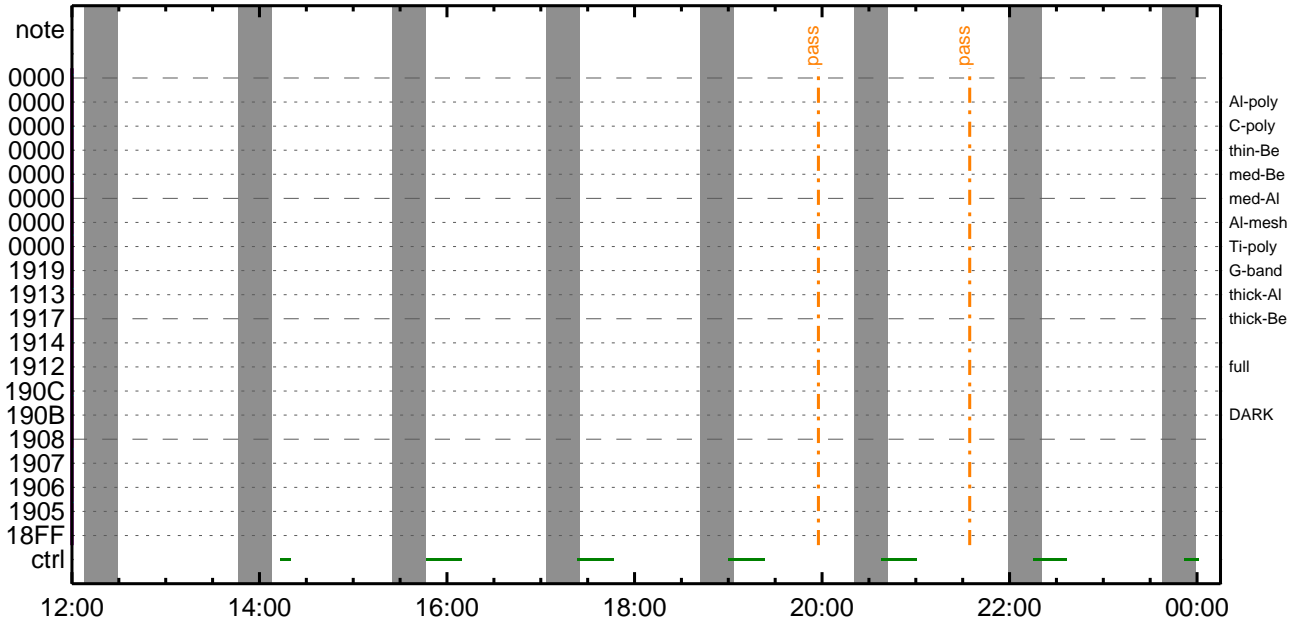
CMDI #0840 2012/08/14



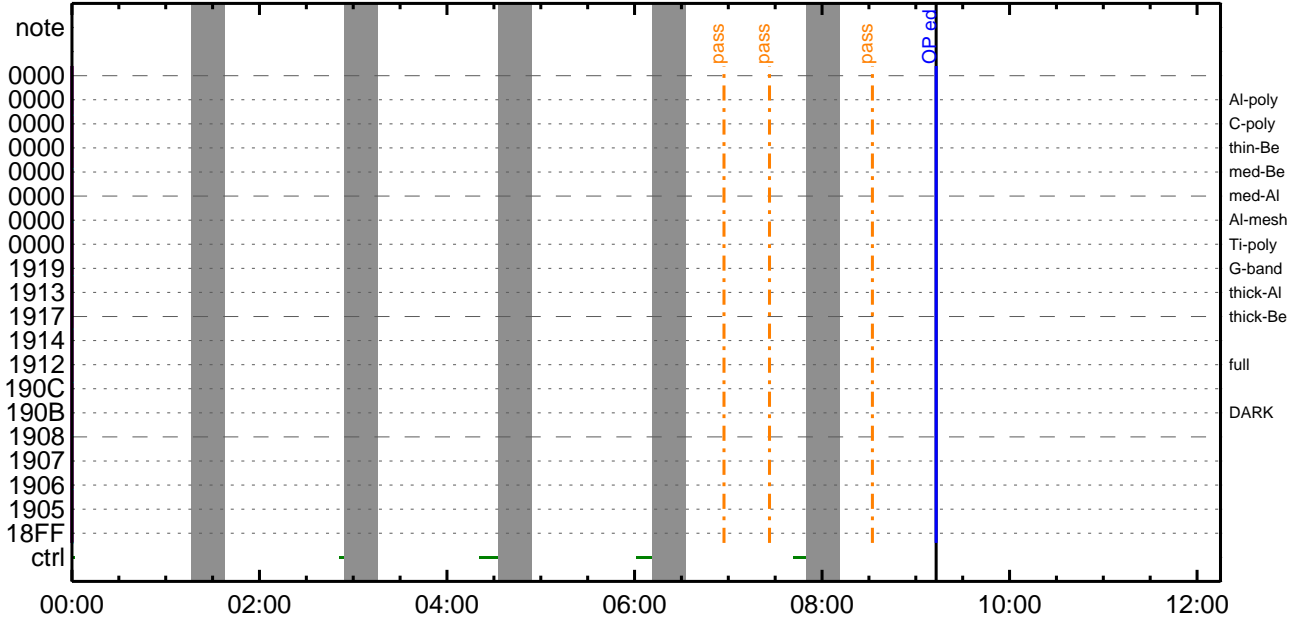
CMDI #0840 2012/08/15



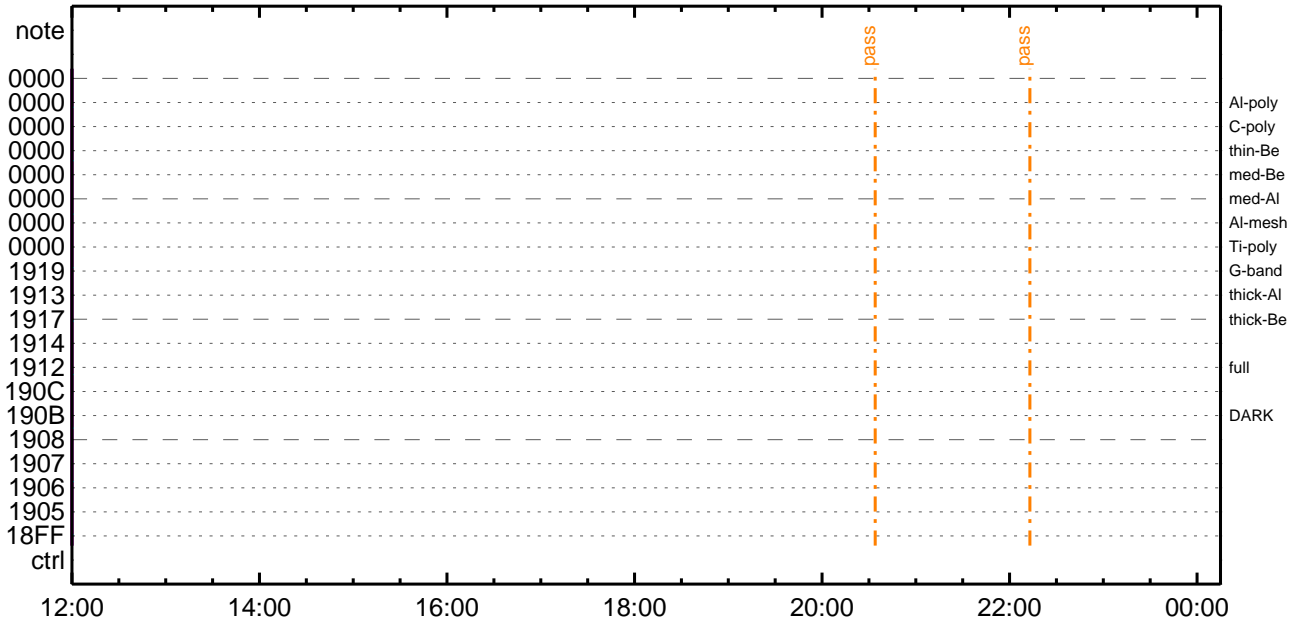
CMDI #0840 2012/08/15



CMDI #0840 2012/08/16



CMDI #0840 2012/08/16




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-074:OP
0104 ( )
0105 S. OG og-074:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½E²òðÁDî¿(UT)
0183 +. TI 2012-08-11 10:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-08-11 10:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-08-11 10:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194 C.
0195 +. TI 2012-08-11 10:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼õİÄë%İİñõİŸÄŸ§ŸÄŸ~¹âİÜ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]             EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]             EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]             EQ          0xB3
0204 C.
0205 C. *****
0206 C. Tİİİ°êŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC       (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]           EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]           EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]            EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]          EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]             EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC       (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]              EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]             EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]          EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]          EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]             EQ          EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½¹İ»õ³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]             EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLõİ¾Ê¹ç.ê²İOKõ³İÇ§
0229 C.
0230 C. DHUŸâ;¼ŸÊ;Ê¼Ÿ¼.Ÿİ;¼ŸÊ;Ëõõİãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC       (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]              EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]             EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]          EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]          EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-08-11 10:27:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC       (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-08-11 10:27:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC       (21 02)
0258 +. TI 2012-08-11 10:27:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC       (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]             EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-08-11 10:27:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC       (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]             EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃİõİ¾Ê¹ç.ê²İOKõ³İÇ§.x²è *****
0276 C. (%á°İŸÖŸÄŸËŸŸŸŸÄŸŸçŸèõÊ¼õõ¼Ä»Ûõ¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŸĐŸ¹.İ Daily±çİñõË'Øõ¹õèDCBC.x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŸÄŸ§ŸÄŸ~¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-076 2012-08-11 13:33:14 144 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY`¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YFÝóYÉÁ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿òÁò•µ°È»Í×ÁÇóíYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÁ@, ùò¹òèòòòçÁ+¿@ò•òÈòòòòòÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-266:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-289:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2012-08-11 10:27:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 + DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR ____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 80 80 20 20)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 08 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0f 80 80 06 06)
0102 + DC 07-F0 MDP_XRT_FLD_DIS
0103 BC (d9)
0104 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0105 BC (c9)
0106 + DC 07-F0 MDP_XRT_AEC_RESET
0107 BC (d0)
0108 + DC 07-F0 MDP_XRT_ARS_DIS
0109 BC (d5)
0110 + DC 07-F0 MDP_XRT_FLD_RESET
0111 BC (da)
0112 + DC 07-F0 MDP_XRT_QT_PROG_SET
0113 BC (c4 08)
0114 + DC 07-F0 MDP_XRT_FL_PROG_SET
0115 BC (c5 09)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C.
0119 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0120 C.
0121 +. DC 07-F0 MDP_XRT_MODE_OBSV
0122 BC (c2)
0123 +. TI 2012-08-11 10:27:02.0
0124 DC 07-F0 MDP_XRT_MODE_OBSV
0125 BC (c2)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C. ***** XRT END *****
0129 C.
0130 . C. ***** MDP 'uAÎaî»ô%ŸαÊÂĐα¹αēDCBC•x²è *****
0131 C. (¼â°îŸÓŸĀŸÈŸPŸŸŸĀŸĉŸēαÊ¼αα¼Ā»Ūα¹αè)
0132 . S. DC-BC dcbc-402:DCBC
0133 (MDP_known_event)
0134 C.
0135 C.
0136 . C. ***** ŸDŸ¹.İ Daily±;îÑαÊ'Øα¹αēDCBC•x²è *****
0137 . S. DC-BC dcbc-153:DCBC
0138 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0139 C.
0140 C.
0141 . C. ;ãLOSŸĀŸSŸĀŸ¼Ā»Ū;ã
0142 C.
0143 . C. ***** LOS *****
0144 C.
```

*** OP Sequence for XRT ***

2012/08/11	10:37:54.0	XRT_CTRL_MANU_427_OG [0x1ab]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	10:37:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/08/11	10:38:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2012/08/11	10:38:16.0	XRT_FLD_ENA_428_OG [0x1ac]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/11	10:38:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/11	10:38:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/11	10:38:22.0	XRT_ARS_DIS_436_OG [0x1b4]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/11	10:40:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	10:40:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2012/08/11	10:40:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09			
2012/08/11	10:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	11:16:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	11:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	11:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	11:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	11:39:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/11	11:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	12:19:54.0	XRT_CTRL_MANU_427_OG [0x1ab]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	12:19:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/08/11	12:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2012/08/11	12:20:16.0	XRT_FLD_ENA_428_OG [0x1ac]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/11	12:20:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/11	12:20:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/11	12:20:22.0	XRT_ARS_DIS_436_OG [0x1b4]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/11	12:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	12:22:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2012/08/11	12:22:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09			
2012/08/11	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	12:54:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	12:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	12:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	12:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	13:18:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/11	13:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	14:33:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	14:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	14:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	14:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	15:10:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/11	15:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	16:11:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	16:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	16:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	16:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	16:56:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/11	16:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	16:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						

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2012/08/11	17:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2012/08/11	17:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/08/11	17:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/08/11	17:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/11	17:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2012/08/11	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	17:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	17:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	17:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	17:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00	00	00
2012/08/11	18:33:00.0	XRT_CTRL_MANU_427_OG [0x1ab]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	18:33:02.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/08/11	18:33:22.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/11	18:33:24.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/11	18:33:26.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/11	18:33:28.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/11	18:33:30.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	18:33:32.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2012/08/11	18:33:34.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	09		
2012/08/11	18:33:36.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	19:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	19:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	19:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	19:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	20:10:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/11	20:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	21:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	21:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	21:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	21:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	21:47:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/11	21:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/11	22:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/11	22:45:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/11	22:45:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/11	22:48:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/11	23:19:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/11	23:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	00:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	00:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	00:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/12	00:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/12	00:47:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/12	00:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	02:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	02:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	02:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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2012/08/12	02:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/12	02:26:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/12	02:27:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	03:32:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	03:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	03:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/12	03:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/12	04:04:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/12	04:05:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	05:09:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	05:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	05:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/12	05:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/12	05:43:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/12	05:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/08/12	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2012/08/12	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/08/12	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/08/12	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/12	06:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2012/08/12	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	06:09:54.0	XRT_CTRL_MANU_427_OG [0x1ab]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/08/12	06:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2012/08/12	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/12	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/12	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/12	06:10:22.0	XRT_ARS_DIS_436_OG [0x1b4]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/12	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	06:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2012/08/12	06:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09			
2012/08/12	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	06:49:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	06:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	06:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/12	06:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/12	07:21:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/12	07:22:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	08:29:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	08:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	08:29:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/12	08:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/12	08:59:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/12	09:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/12	10:10:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/12	10:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/12	10:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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2012/08/12	10:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/12	10:30:00.0	AOCs_Orе-point_Start_1_OG [0x097]									
		AOCU_NM	5	02-76	01 00 00 00 00						
2012/08/12	10:38:01.0	XRT_CTRL_MANU_427_OG [0x1ab]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	10:38:03.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00						
2012/08/12	10:38:23.0	XRT_FLD_ENA_428_OG [0x1ac]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2012/08/12	10:38:25.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2012/08/12	10:38:27.0	XRT_AEC_RESET_423_OG [0x1a7]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2012/08/12	10:38:29.0	XRT_ARS_DIS_438_OG [0x1b6]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2012/08/12	10:38:31.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	10:38:33.0	XRT_QT_PROG_SET_437_OG [0x1b5]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b						
2012/08/12	10:38:35.0	XRT_FL_PROG_SET_431_OG [0x1af]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09						
2012/08/12	10:38:37.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/12	11:53:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	11:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	11:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/12	11:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/12	12:00:00.0	AOCs_Orе-point_Start_2_OG [0x098]									
		AOCU_NM	5	02-76	03 00 00 00 00						
2012/08/12	12:16:30.0	XRT_CTRL_MANU_427_OG [0x1ab]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	12:16:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00						
2012/08/12	12:16:52.0	XRT_FLD_ENA_428_OG [0x1ac]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2012/08/12	12:16:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2012/08/12	12:16:56.0	XRT_AEC_RESET_423_OG [0x1a7]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2012/08/12	12:16:58.0	XRT_ARS_DIS_438_OG [0x1b6]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2012/08/12	12:17:00.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	12:17:02.0	XRT_QT_PROG_SET_437_OG [0x1b5]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b						
2012/08/12	12:17:04.0	XRT_FL_PROG_SET_431_OG [0x1af]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09						
2012/08/12	12:17:06.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/12	13:32:00.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	13:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	13:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/12	13:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/12	14:02:00.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	14:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	14:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/12	14:04:30.0	XRT_Custom_434_OG [0x1b2]									
2012/08/12	14:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/12	14:05:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/12	15:10:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	15:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	15:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/12	15:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/12	15:56:30.0	XRT_Custom_434_OG [0x1b2]									
2012/08/12	15:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/12	16:49:00.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/12	16:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/12	16:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/12	16:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/12	17:34:54.0	XRT_CTRL_MANU_402_OG [0x192]									

2012/08/12	17:34:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/12	17:35:00.0	AOCS_Orе-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/12	17:35:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00	
2012/08/12	17:35:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/12	17:35:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/12	17:37:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/12	17:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2012/08/12	18:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/12	18:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/12	18:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/12	18:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/12	18:35:00.0	AOCS_Orе-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/12	19:10:00.0	XRT_CTRL_MANU_427_OG [0x1ab]	AOCU_NM	5	02-76	03 00 00 00	
2012/08/12	19:10:02.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/12	19:10:22.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/08/12	19:10:24.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/08/12	19:10:26.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/08/12	19:10:28.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/08/12	19:10:30.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/12	19:10:32.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/12	19:10:34.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2012/08/12	19:10:36.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 09	
2012/08/12	20:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/12	20:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/12	20:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/12	20:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/12	20:47:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/12	20:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]				
2012/08/12	21:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/12	21:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/12	21:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/12	21:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/12	22:23:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/12	22:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]				
2012/08/12	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/12	23:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/12	23:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/12	23:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/12	23:49:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/12	23:50:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]				
2012/08/13	01:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/13	01:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/13	01:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/13	01:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/13	01:24:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/13	01:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]				
2012/08/13	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/13	02:37:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2012/08/13	02:37:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/13	02:40:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/13	03:03:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/13	03:04:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/13	04:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	04:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/13	04:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/13	04:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/13	04:41:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/13	04:42:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/13	05:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	05:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/13	05:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/13	05:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/13	06:23:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	06:23:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/08/13	06:23:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/08/13	06:27:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/13	06:27:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/13	06:27:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/13	06:27:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/13	06:27:52.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/08/13	06:27:54.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/13	06:33:24.0	XRT_CTRL_MANU_427_OG [0x1ab]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	06:33:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/13	06:33:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/08/13	06:33:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/13	06:33:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/13	06:33:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/13	06:33:52.0	XRT_ARS_DIS_436_OG [0x1b4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/13	06:36:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/13	06:36:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/13	06:36:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09				
2012/08/13	06:36:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/13	07:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	07:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/13	07:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/13	07:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/13	07:58:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/13	07:59:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/13	09:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	09:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/13	09:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/13	09:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/13	09:37:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/13	09:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/13	10:29:54.0	XRT_CTRL_MANU_427_OG [0x1ab]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/13	10:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/13	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							

2012/08/13	10:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	01	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0		d8			
2012/08/13	10:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		1	07-F0		c8			
		MDP_XRT_FLRCTRL_ENA		1	07-F0		c8			
2012/08/13	10:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]		1	07-F0		d0			
		MDP_XRT_AEC_RESET		1	07-F0		d0			
2012/08/13	10:30:22.0	XRT_ARS_DIS_436_OG [0x1b4]		1	07-F0		d5			
		MDP_XRT_ARS_DIS		1	07-F0		d5			
2012/08/13	10:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	10:32:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]		2	07-F0		c4			
		MDP_XRT_QT_PROG_SET		2	07-F0		c4			0b
2012/08/13	10:32:58.0	XRT_FL_PROG_SET_431_OG [0x1af]		2	07-F0		c5			
		MDP_XRT_FL_PROG_SET		2	07-F0		c5			09
2012/08/13	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0		c0			
		MDP_XRT_CTRL_AUTO		1	07-F0		c0			
2012/08/13	10:52:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			
2012/08/13	10:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	10:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0		e8			
		MDP_XRT_PREFLR_STRT		1	07-F0		e8			
2012/08/13	10:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0		e9			
		MDP_XRT_PREFLR_STOP		1	07-F0		e9			
2012/08/13	11:15:30.0	XRT_Custom_434_OG [0x1b2]								
2012/08/13	11:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0		c0			
		MDP_XRT_CTRL_AUTO		1	07-F0		c0			
2012/08/13	11:59:54.0	XRT_CTRL_MANU_427_OG [0x1ab]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			
2012/08/13	11:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		4	07-F8	22	fe			97 00
		XRT_FOCUS_POSITION		4	07-F8	22	fe			97 00
2012/08/13	12:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]		5	02-76	03	00			00 00
		AOCU_NM		5	02-76	03	00			00 00
2012/08/13	12:00:16.0	XRT_FLD_ENA_428_OG [0x1ac]		1	07-F0		d8			
		MDP_XRT_FLD_ENA		1	07-F0		d8			
2012/08/13	12:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		1	07-F0		c8			
		MDP_XRT_FLRCTRL_ENA		1	07-F0		c8			
2012/08/13	12:00:20.0	XRT_AEC_RESET_423_OG [0x1a7]		1	07-F0		d0			
		MDP_XRT_AEC_RESET		1	07-F0		d0			
2012/08/13	12:00:22.0	XRT_ARS_DIS_436_OG [0x1b4]		1	07-F0		d5			
		MDP_XRT_ARS_DIS		1	07-F0		d5			
2012/08/13	12:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	12:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]		2	07-F0		c4			
		MDP_XRT_QT_PROG_SET		2	07-F0		c4			0b
2012/08/13	12:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]		2	07-F0		c5			
		MDP_XRT_FL_PROG_SET		2	07-F0		c5			09
2012/08/13	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0		c0			
		MDP_XRT_CTRL_AUTO		1	07-F0		c0			
2012/08/13	12:31:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			
2012/08/13	12:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	12:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0		e8			
		MDP_XRT_PREFLR_STRT		1	07-F0		e8			
2012/08/13	12:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0		e9			
		MDP_XRT_PREFLR_STOP		1	07-F0		e9			
2012/08/13	12:54:00.0	XRT_Custom_434_OG [0x1b2]								
2012/08/13	12:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0		c0			
		MDP_XRT_CTRL_AUTO		1	07-F0		c0			
2012/08/13	14:09:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			
2012/08/13	14:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	14:09:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0		e8			
		MDP_XRT_PREFLR_STRT		1	07-F0		e8			
2012/08/13	14:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0		e9			
		MDP_XRT_PREFLR_STOP		1	07-F0		e9			
2012/08/13	14:45:30.0	XRT_Custom_434_OG [0x1b2]								
2012/08/13	14:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0		c0			
		MDP_XRT_CTRL_AUTO		1	07-F0		c0			
2012/08/13	15:48:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			
2012/08/13	15:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	15:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0		e8			
		MDP_XRT_PREFLR_STRT		1	07-F0		e8			
2012/08/13	15:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0		e9			
		MDP_XRT_PREFLR_STOP		1	07-F0		e9			
2012/08/13	16:33:00.0	XRT_Custom_434_OG [0x1b2]								
2012/08/13	16:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0		c0			
		MDP_XRT_CTRL_AUTO		1	07-F0		c0			
2012/08/13	17:26:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			
2012/08/13	17:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0		da			
		MDP_XRT_FLD_RESET		1	07-F0		da			
2012/08/13	17:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0		e8			
		MDP_XRT_PREFLR_STRT		1	07-F0		e8			
2012/08/13	17:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0		e9			
		MDP_XRT_PREFLR_STOP		1	07-F0		e9			
2012/08/13	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
		MDP_XRT_CTRL_MANU		1	07-F0		c1			

2012/08/13	18:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/08/13	18:10:00.0	AOCs_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00	00	00	00
2012/08/13	18:10:16.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0				d9
2012/08/13	18:10:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0				c9
2012/08/13	18:10:20.0	XRT_ARS_DIS_406_OG [0x196]						
		MDP_XRT_ARS_DIS	1	07-F0				d5
2012/08/13	18:12:58.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0				c4 01
2012/08/13	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0				c0
2012/08/13	18:19:54.0	XRT_CTRL_MANU_427_OG [0x1ab]						
		MDP_XRT_CTRL_MANU	1	07-F0				c1
2012/08/13	18:19:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/08/13	18:20:00.0	AOCs_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	03	00	00	00
2012/08/13	18:20:16.0	XRT_FLD_ENA_428_OG [0x1ac]						
		MDP_XRT_FLD_ENA	1	07-F0				d8
2012/08/13	18:20:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8
2012/08/13	18:20:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0				d0
2012/08/13	18:20:22.0	XRT_ARS_DIS_436_OG [0x1b4]						
		MDP_XRT_ARS_DIS	1	07-F0				d5
2012/08/13	18:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0				da
2012/08/13	18:22:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
		MDP_XRT_QT_PROG_SET	2	07-F0				c4 0b
2012/08/13	18:22:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
		MDP_XRT_FL_PROG_SET	2	07-F0				c5 09
2012/08/13	18:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0				c0
2012/08/13	19:05:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0				c1
2012/08/13	19:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0				da
2012/08/13	19:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0				e8
2012/08/13	19:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0				e9
2012/08/13	19:46:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/13	19:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0				c0
2012/08/13	20:44:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0				c1
2012/08/13	20:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0				da
2012/08/13	20:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0				e8
2012/08/13	20:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0				e9
2012/08/13	21:24:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/13	21:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0				c0
2012/08/13	22:22:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0				c1
2012/08/13	22:22:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0				da
2012/08/13	22:22:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0				e8
2012/08/13	22:25:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0				e9
2012/08/13	22:59:54.0	XRT_CTRL_MANU_427_OG [0x1ab]						
		MDP_XRT_CTRL_MANU	1	07-F0				c1
2012/08/13	22:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/08/13	23:00:00.0	AOCs_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	02	00	00	00
2012/08/13	23:00:16.0	XRT_FLD_ENA_428_OG [0x1ac]						
		MDP_XRT_FLD_ENA	1	07-F0				d8
2012/08/13	23:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8
2012/08/13	23:00:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0				d0
2012/08/13	23:00:22.0	XRT_ARS_DIS_436_OG [0x1b4]						
		MDP_XRT_ARS_DIS	1	07-F0				d5
2012/08/13	23:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0				da
2012/08/13	23:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]						
		MDP_XRT_QT_PROG_SET	2	07-F0				c4 0c
2012/08/13	23:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
		MDP_XRT_FL_PROG_SET	2	07-F0				c5 09
2012/08/13	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0				c0
2012/08/14	00:01:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0				c1
2012/08/14	00:01:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0				da
2012/08/14	00:01:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						

2012/08/14	00:04:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/14	00:23:00.0	XRT_Custom_434_OG [0x1b2]					
2012/08/14	00:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/14	00:59:54.0	XRT_CTRL_MANU_427_OG [0x1ab]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/14	00:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/08/14	01:00:00.5	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2012/08/14	01:00:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/08/14	01:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/08/14	01:00:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/08/14	01:00:22.0	XRT_ARS_DIS_436_OG [0x1b4]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/14	01:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/14	01:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2012/08/14	01:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 09	
2012/08/14	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/14	01:39:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/14	01:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/14	01:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/14	01:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/14	02:01:30.0	XRT_Custom_434_OG [0x1b2]					
2012/08/14	02:02:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/14	03:14:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/14	03:14:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/14	03:14:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/14	03:17:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/14	03:40:00.0	XRT_Custom_434_OG [0x1b2]					
2012/08/14	03:41:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/14	04:44:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/14	04:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/14	04:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/14	04:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/14	05:18:30.0	XRT_Custom_434_OG [0x1b2]					
2012/08/14	05:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/14	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/14	05:56:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/14	05:57:00.5	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/08/14	05:57:16.0	XRT_FLD_DIS_404_OG [0x194]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/14	05:57:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/14	05:57:20.0	XRT_ARS_DIS_406_OG [0x196]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/14	05:59:58.0	XRT_QT_PROG_SET_407_OG [0x197]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2012/08/14	06:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/14	06:06:54.0	XRT_CTRL_MANU_427_OG [0x1ab]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/14	06:06:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/08/14	06:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2012/08/14	06:07:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/08/14	06:07:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/08/14	06:07:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/08/14	06:07:22.0	XRT_ARS_DIS_436_OG [0x1b4]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/14	06:09:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2012/08/14	06:09:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2012/08/14	06:09:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	09			
2012/08/14	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/14	06:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/14	06:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/14	06:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/14	06:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/14	06:57:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/14	06:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/14	08:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/14	08:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/14	08:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/14	08:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/14	08:35:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/14	08:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/14	08:55:45.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/14	10:52:00.5	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00