XRT Timeline to be uploaded on 2012/08/16

Period: 2012/08/16 09:13:00 - 2012/08/21 10:08:00

* * * * * **Normal mode** * * * * *

XOB #1913: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band Test

Term	Pointing (x, y)	Comment
08/16 09:50:06 - 08/16 11:07:00	Track (830.4, 131.1) @ 08/16 09:23:00	# OP start + 10min, HOP160 for 1.5hr
08/17 10:33:00 - 08/17 11:44:30	Fixed (906.0, 155.0)	HOP160 for 1.5hr

PROG= 11 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 19 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 2-time(s) 2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 9 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 10 25-time(s) 80.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

XOB #191E: Synoptic 9 Filter- 2x2 Q98 even Shorter exp -Gband(12ms)

Term	Pointing (x, y)	Comment
08/16 11:33:00 - 08/16 13:29:54	Fixed (0.0, 0.0)	SOT flat fiels obs for 2hr

PROG= 14 2-time(s)

Subr= 1 1-time(s) 180.0sec

Seqn= 29 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 30 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 24 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 26 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 25 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 31 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 180.0sec

Seqn= 27 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 22 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1923: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 60s cad

Term	Pointing (x, y)	Comment
08/16 13:33:00 - 08/16 16:02:30	Track (561.9, 261.4) @ 08/16 13:30:00	Track AR11543
08/16 20:03:00 - 08/16 23:15:00	Track (602.9, 265.4) @ 08/16 20:00:00	Track AR11543 (and HOP128 23:15 - 01:15)
08/17 01:15:36 - 08/17 05:54:54	Track (602.9, 265.4) @ 08/16 20:00:00	Track AR11543 (and HOP128 23:15 - 01:15)
08/17 12:06:06 - 08/17 17:59:54	Track (693.8, 276.6) @ 08/17 12:00:00	Track AR11543
08/17 18:13:00 - 08/17 23:14:00	Track (724.6, 281.3) @ 08/17 18:10:00	Track AR11543 (and HOP128 23:45 - 01:45)
08/18 01:45:37 - 08/18 05:36:30	Track (724.6, 281.3) @ 08/17 18:10:00	Track AR11543 (and HOP128 23:45 - 01:45)
08/18 06:26:00 - 08/18 08:57:00	Track (778.3, 291.3) @ 08/18 06:23:00	Track AR11543

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 19 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 2-time(s) 2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 32 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 33 30-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

XOB #191D: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2

Term	Pointing (x, y)	Comment
08/16 16:53:00 - 08/16 16:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually
08/17 05:58:00 - 08/17 06:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min
08/17 18:03:00 - 08/17 18:09:54	Fixed (0.0, 0.0)	synoptic
08/18 06:16:00 - 08/18 06:22:54	Fixed (0.0, 0.0)	synoptic, shifted 13.0 min

PROG= 13 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 11 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 12 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 28 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
08/16 17:03:00 - 08/16 19:19:30	Fixed (-940.0, 0.0)	Cross-cal with COMP/Hawaii for EIS

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 120.0sec

Seqn= 34 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

XOB #1919: AR Standard-B(Morphorlogy) with PFB, FW1=Open, 384x384 at 1064 1048, 30s cad With G-band Test

Term	Pointing (x, y)	Comment
08/16 23:15:36 - 08/17 01:15:00	Track (602.9, 265.4) @ 08/16 20:00:00	Track AR11543 (and HOP128 23:15 - 01:15)
08/17 23:45:36 - 08/18 01:45:00	Track (724.6, 281.3) @ 08/17 18:10:00	Track AR11543 (and HOP128 23:45 - 01:45)

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 19 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 2-time(s) 2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 9 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 21 60-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
08/17 06:20:00 - 08/17 08:04:54	Fixed (0.0, 945.0)	Co-alignment at N pole

PROG= 06 1-time(s)
Subr= 1 1-time(s) 360.0sec
Seqn= 16 24-time(s) 300.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment
08/17 08:20:00 - 08/17 10:29:54	Fixed (-945.0, 0.0)	Co-alignment at E limb

PROG= 07 1-time(s)
Subr= 1 1-time(s) 360.0sec
Seqn= 17 15-time(s) 480.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec

Flare mode

XOB #1914: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops (45ms Gband)

Term	Pointing (x, y)	Comment
08/16 09:50:06 - 08/16 11:07:00	Track (830.4, 131.1) @ 08/16 09:23:00	# OP start + 10min, HOP160 for 1.5hr
08/16 17:03:00 - 08/16 19:19:30	Fixed (-940.0, 0.0)	Cross-cal with COMP/Hawaii for EIS
08/16 23:15:36 - 08/17 01:15:00	Track (602.9, 265.4) @ 08/16 20:00:00	Track AR11543 (and HOP128 23:15 - 01:15)
08/17 10:33:00 - 08/17 11:44:30	Fixed (906.0, 155.0)	HOP160 for 1.5hr
08/17 23:45:36 - 08/18 01:45:00	Track (724.6, 281.3) @ 08/17 18:10:00	Track AR11543 (and HOP128 23:45 - 01:45)

PROG= 09 15-time(s)
Subr= 1 45-time(s) 10.0sec
Seqn= 13 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec

Subr= 2 1-time(s) 10.0sec
Seqn= 14 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec

Seqn= 20 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
08/16 13:33:00 - 08/16 16:02:30	Track (561.9, 261.4) @ 08/16 13:30:00	Track AR11543
08/16 20:03:00 - 08/16 23:15:00	Track (602.9, 265.4) @ 08/16 20:00:00	Track AR11543 (and HOP128 23:15 - 01:15)
08/17 01:15:36 - 08/17 05:54:54	Track (602.9, 265.4) @ 08/16 20:00:00	Track AR11543 (and HOP128 23:15 - 01:15)
08/17 12:06:06 - 08/17 17:59:54	Track (693.8, 276.6) @ 08/17 12:00:00	Track AR11543
08/17 18:13:00 - 08/17 23:14:00	Track (724.6, 281.3) @ 08/17 18:10:00	Track AR11543 (and HOP128 23:45 - 01:45)
08/18 01:45:37 - 08/18 05:36:30	Track (724.6, 281.3) @ 08/17 18:10:00	Track AR11543 (and HOP128 23:45 - 01:45)
08/18 06:26:00 - 08/18 08:57:00	Track (778.3, 291.3) @ 08/18 06:23:00	Track AR11543

PROG= 16 15-time(s)
Subr= 1 45-time(s) 10.0sec
Seqn= 35 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 10.0sec
Seqn= 36 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 37 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 38 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Active Region Search

NOT USED

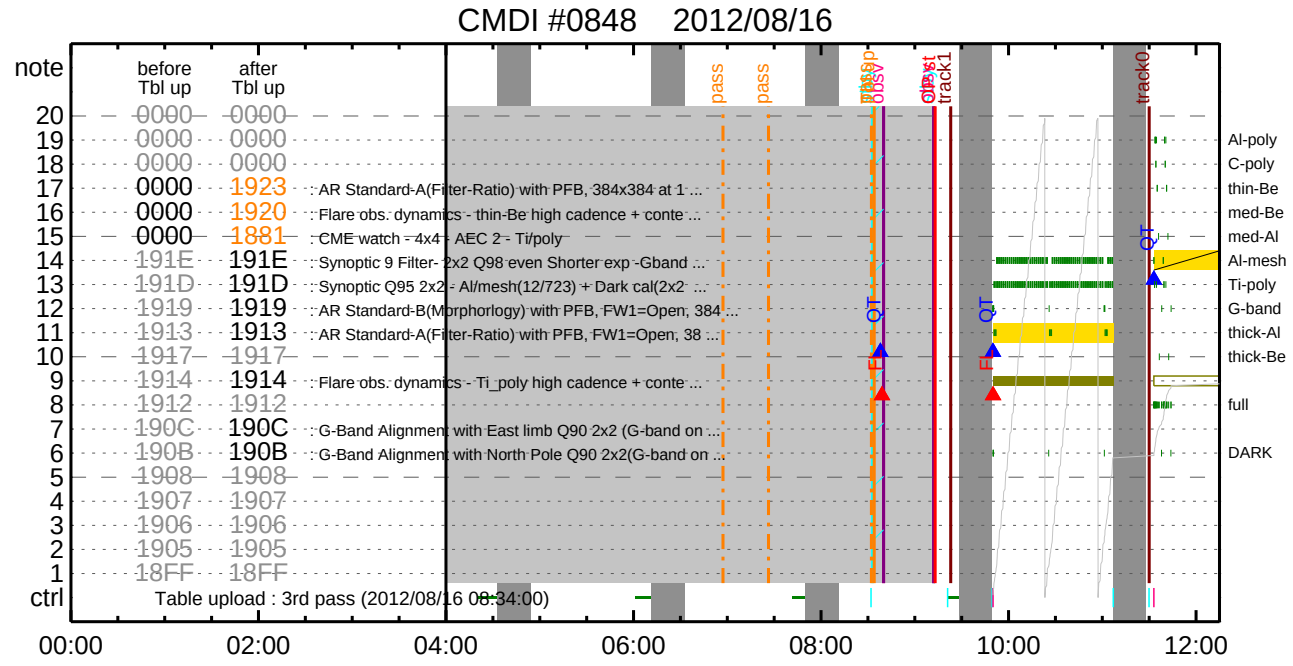
Flare Detection

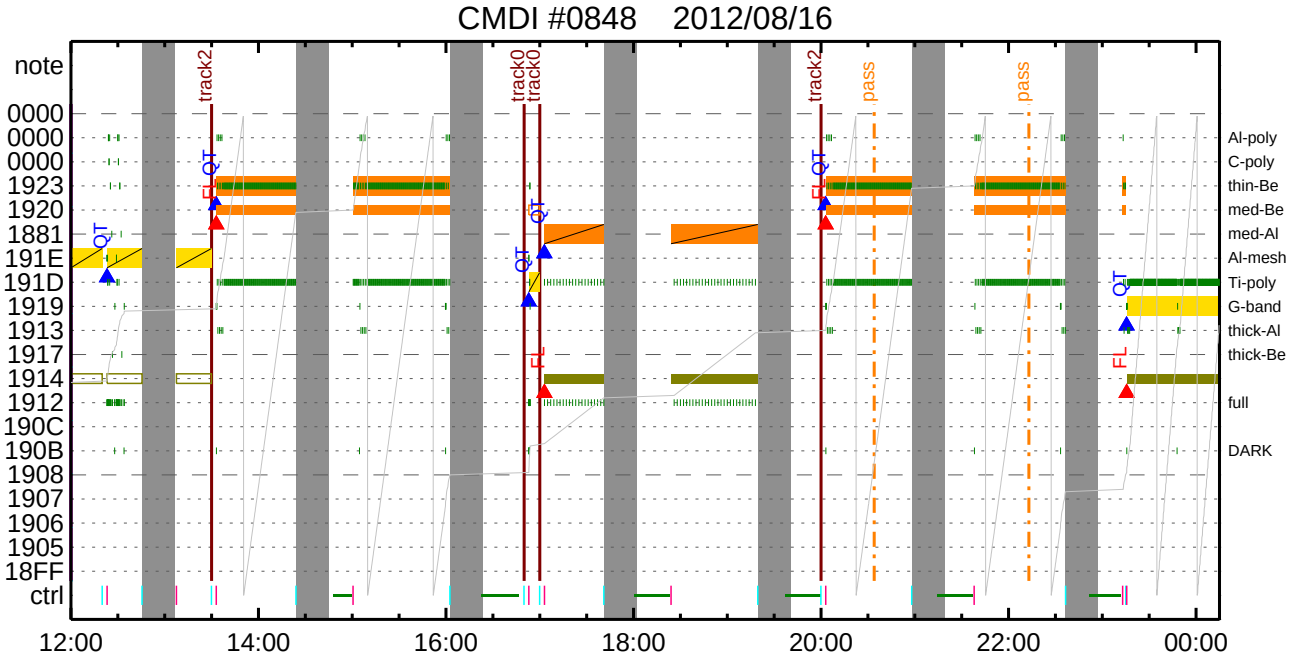
FLD Patrol

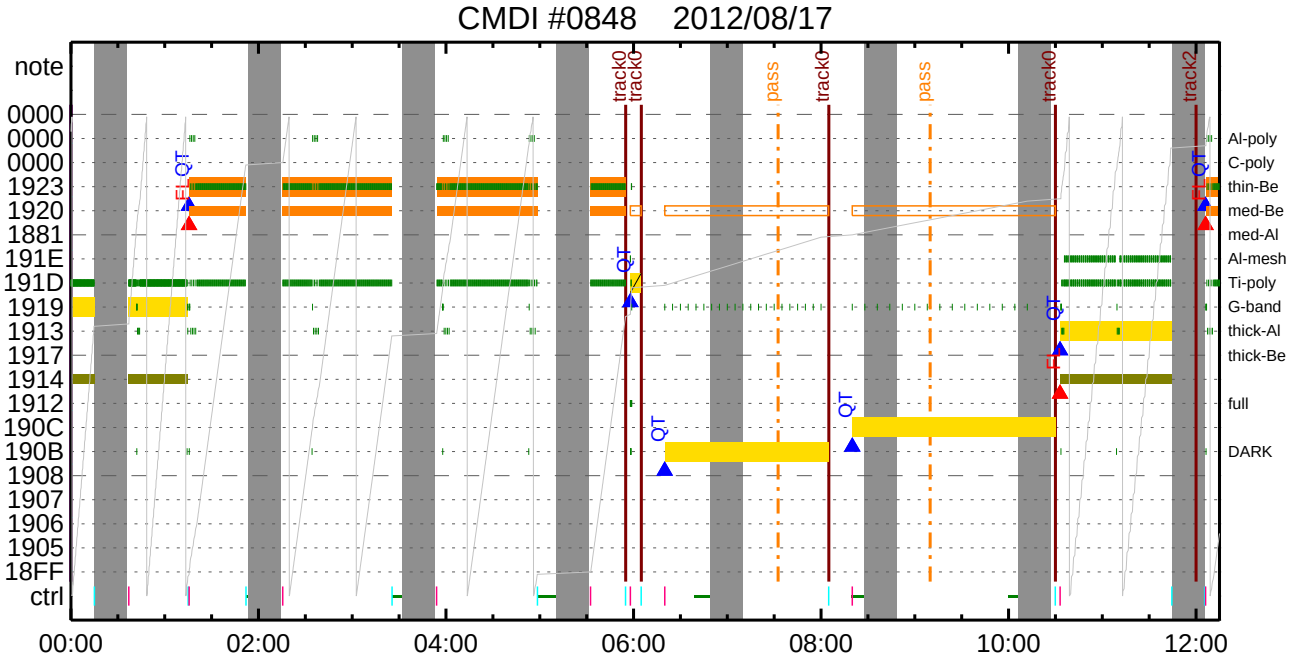
Term	Pointing (x, y)	Comment

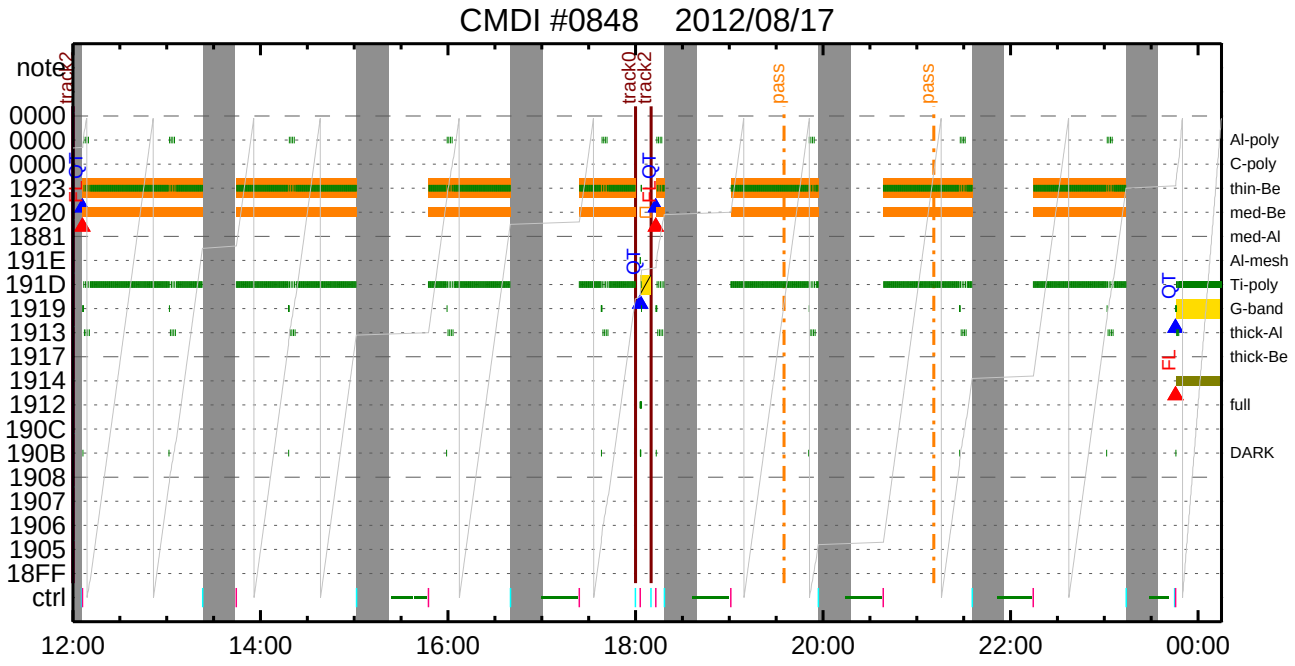
08/16 09:49:52 - 08/16 11:30:16 Track (830.4, 131.1) @ 08/16 09:23:00 # OP start + 10min, HOP160 for 1.5hr

	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval


CMDI #0848 2012/08/16
note
before Tbl up
after Tbl up
0000 1923 : AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1 ...
0000 1920 : Flare obs. dynamics - thin-Be high cadence + conte ...
0000 1881 : CME watch - 4x4 - AEC 2 - Ti/poly
191E 191E : Synoptic 9 Filter- 2x2 Q98 even Shorter exp -Gband ...
191D 191D : Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 ...
1919 1919 : AR Standard-B(Morphorlogy) with PFB, FW1=Open, 384 ...
1913 1913 : AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 38 ...
1914 1914 : Flare obs. dynamics - Ti_poly high cadence + conte ...
190C 190C : G-Band Alignment with East limb Q90 2x2 (G-band on ...
190B 190B : G-Band Alignment with North Pole Q90 2x2(G-band on ...
Table upload : 3rd pass (2012/08/16 08:34:00)
pass
pass
track1
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0848 2012/08/16
note
track2
track0
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0848 2012/08/17
note
track0
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0848 2012/08/17
note
0000
0000
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1923
1920
1881
191E
191D
1919
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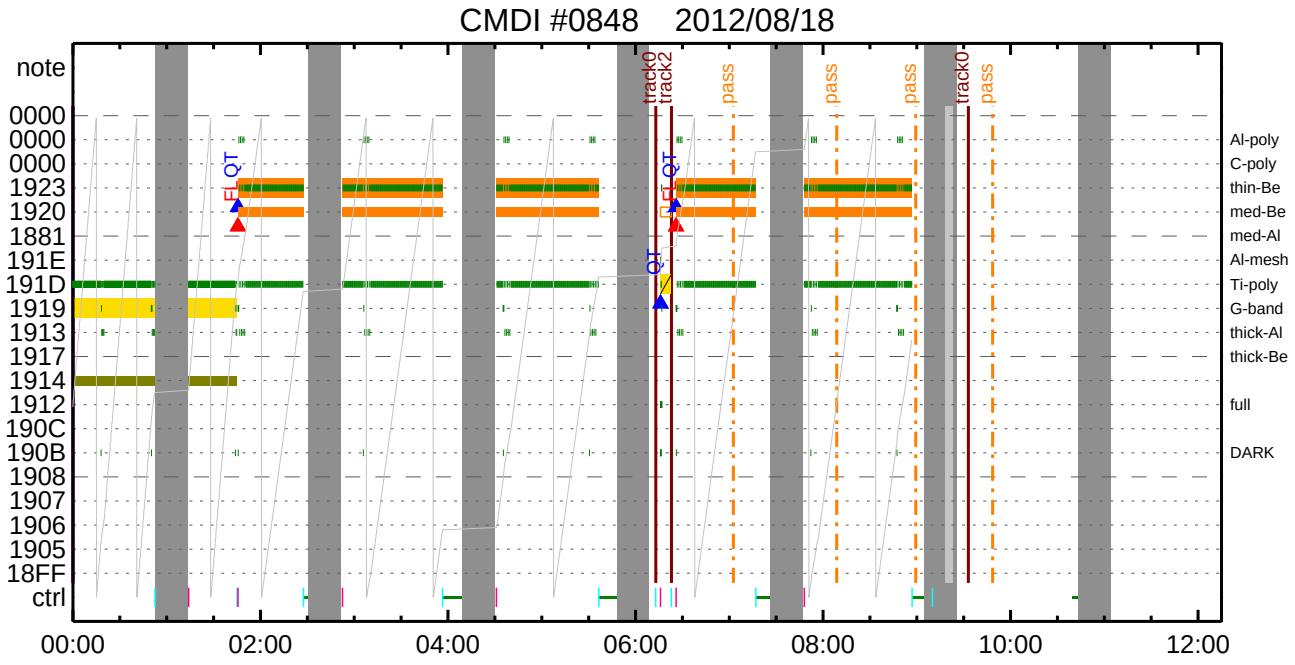
1914
1912
190C
190B
1908
1907
1906
1905
18FF
ctrl
track2
track0
track2
pass
pass
FL
QT
FL
QT
QT
QT
FL
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0848 2012/08/18
note
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1881
191E
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190C
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18FF
ctrl
track0
track2
pass
pass
pass
track0
pass
FL
QT
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QT
00:00
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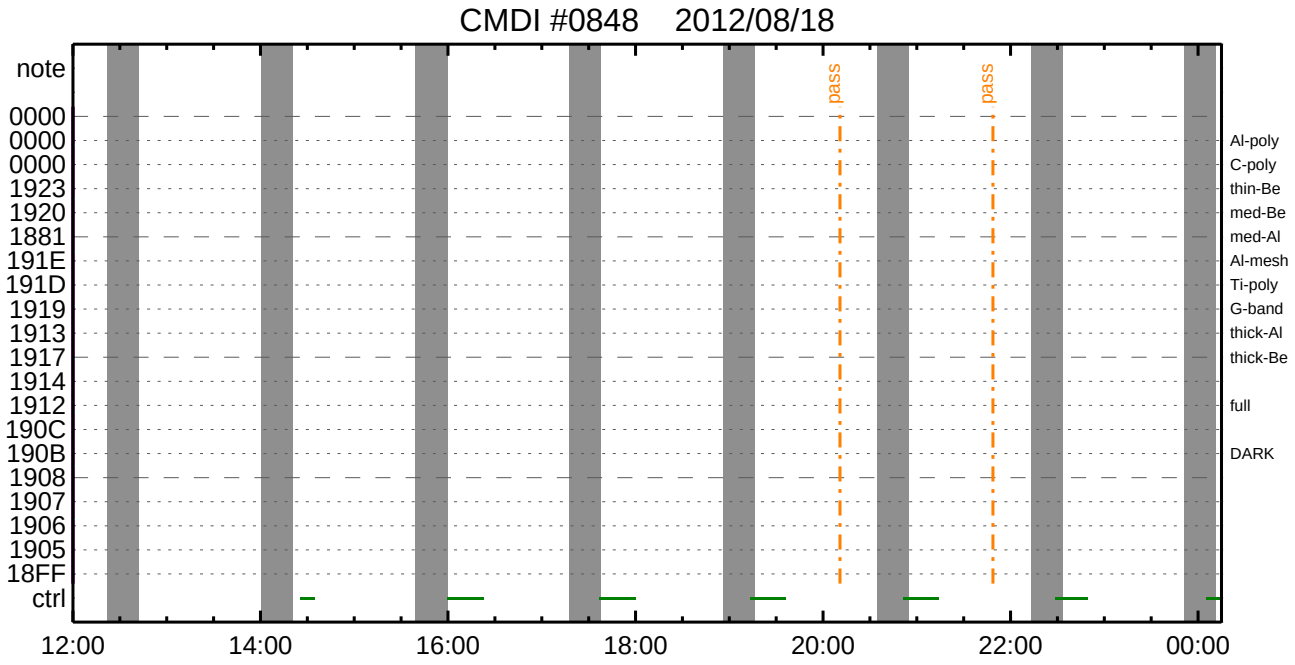
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0848 2012/08/18
note
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pass
pass
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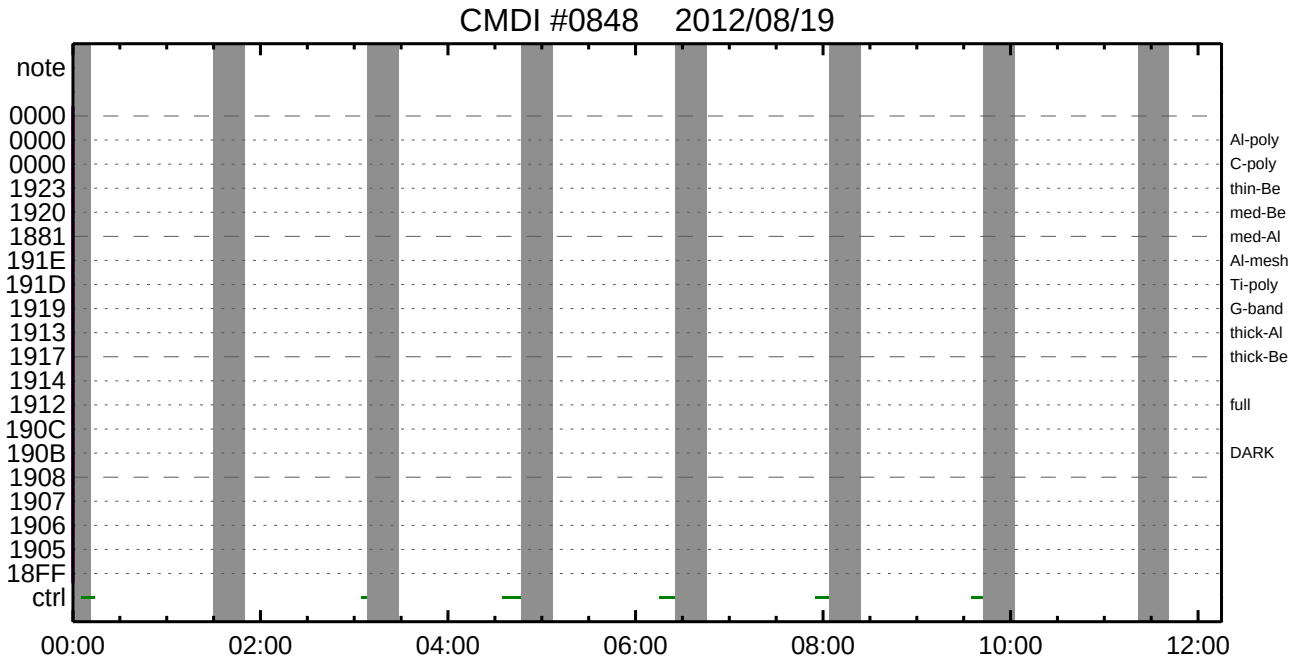
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0848 2012/08/19
note
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1923
1920
1881
191E
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1912
190C
190B
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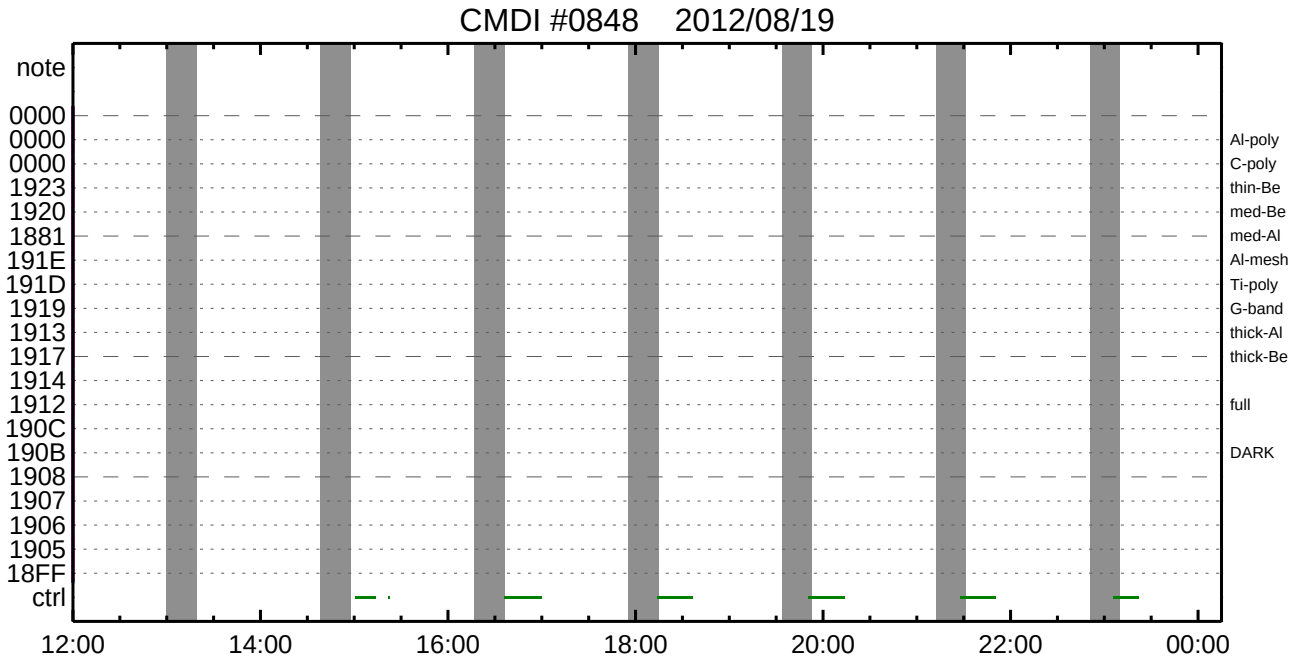
1907
1906
1905
18FF
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0848 2012/08/19
note
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1923
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1881
191E
191D
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1917
1914
1912
190C
190B
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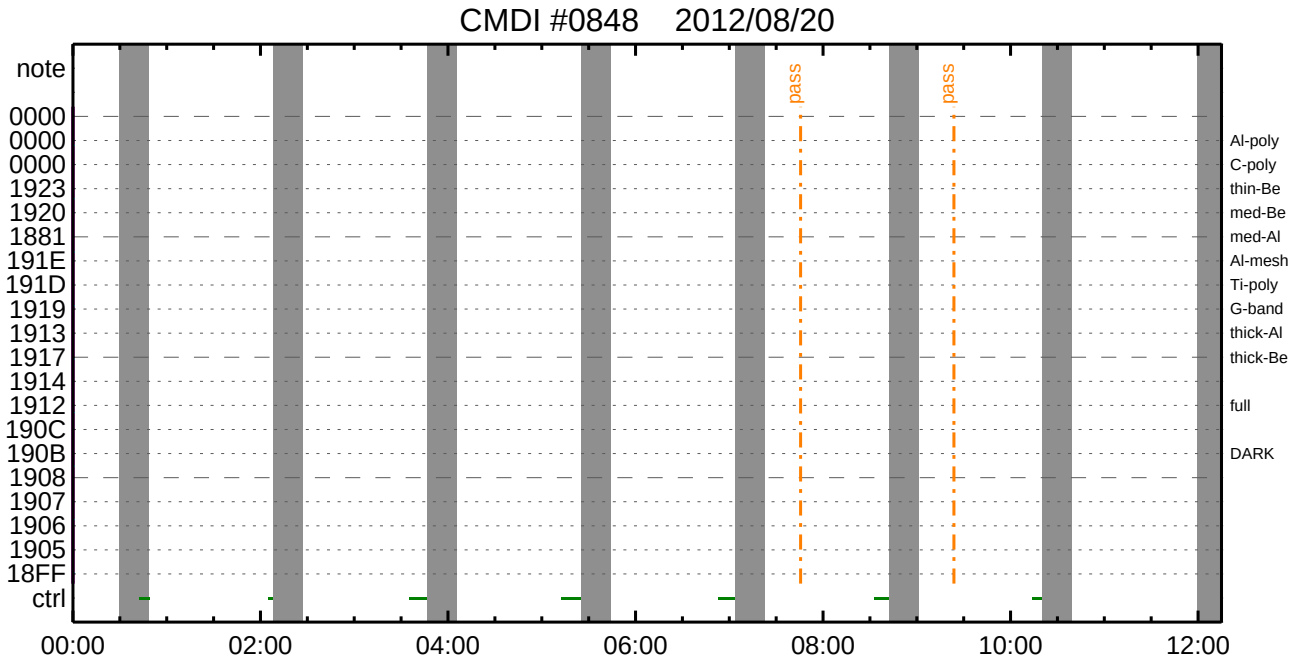
1907
1906
1905
18FF
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0848 2012/08/20
note
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1923
1920
1881
191E
191D
1919
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190C
190B
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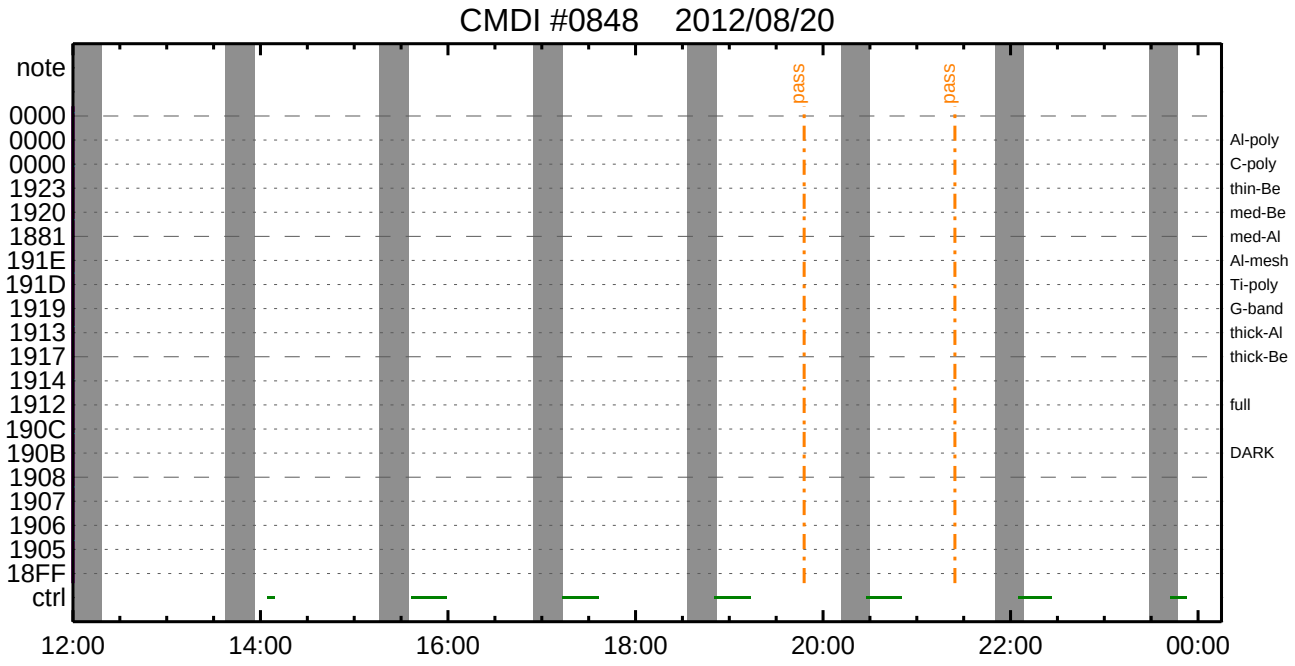
1905
18FF
ctrl
pass
pass
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0848 2012/08/20
note
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1923
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191E
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190C
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1905
18FF
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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22:00
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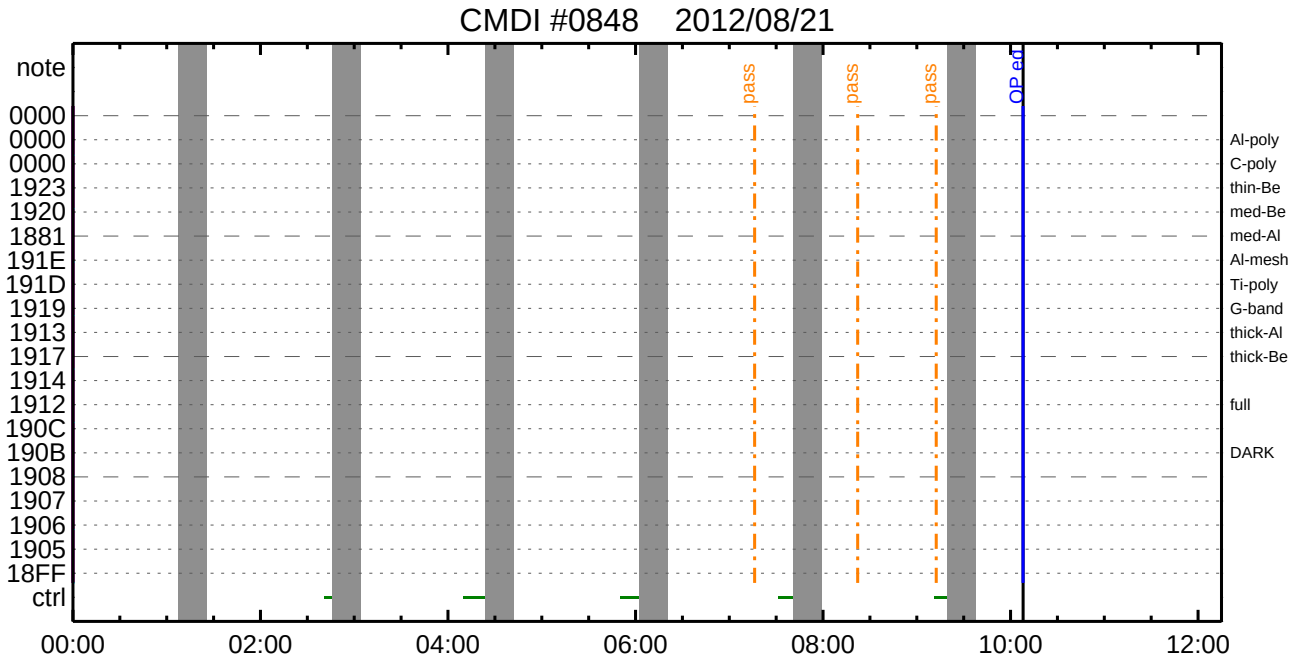
CMDI #0848 2012/08/21
note
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1923
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1881
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190C
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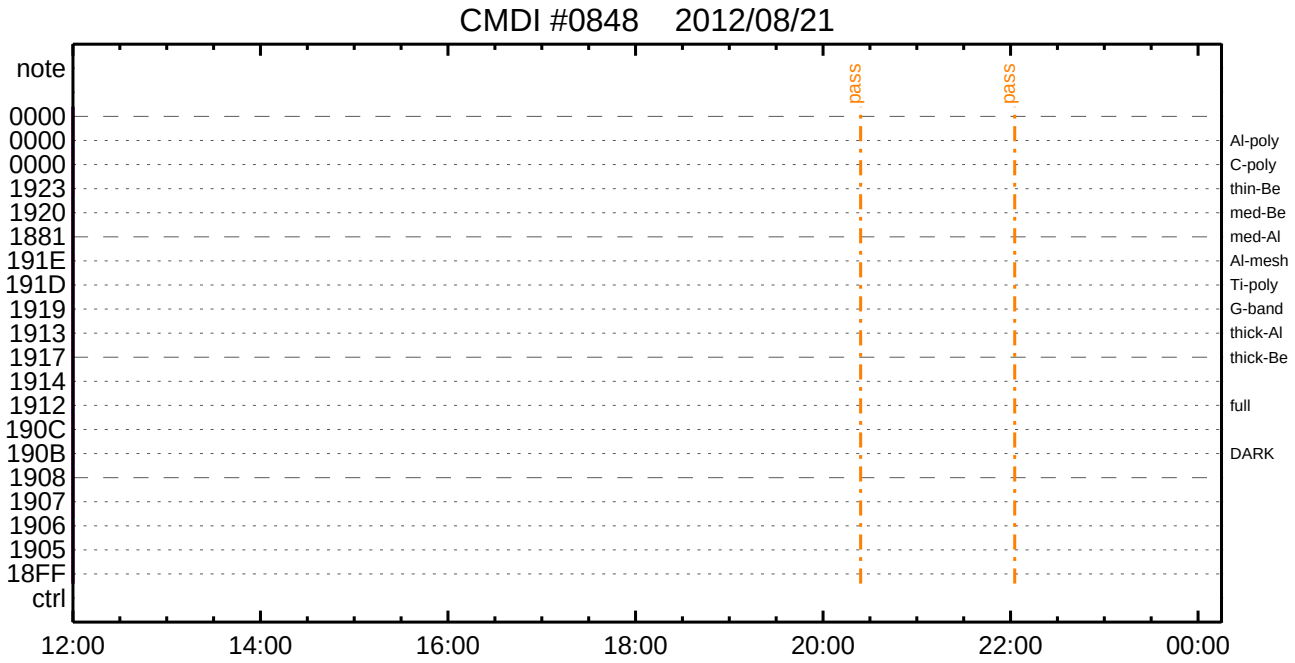
1906
1905
18FF
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0848 2012/08/21
note
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191E
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1906
1905
18FF
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-083 2012-08-16 13:19:46 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-083:OP
0104     ()
0105   . S. OG    og-083:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2012-08-16 09:08:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2012-08-16 09:08:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2012-08-16 09:08:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2012-08-16 09:12:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2012-08-16 09:12:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2012-08-16 09:12:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2012-08-16 09:12:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2012-08-16 09:12:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-084 2012-08-16 13:19:46 82 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_MANU
0047     BC       (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 17s
0050     C. ********************************
0051     C. EIS START OBSTBL LOAD
0052     C. ********************************
0053   . S. RAM   ram-820:EIS_OBSTBL
0054     ()
0055   +. DC 07-FC EIS_DUMP_OBSTBL
0056     BC       (07 07 07 00 00 70 00)
0057     C.
0058     C. Execute, after the success of OBSTBL upload.
0059     C. Set EIS TI-commands
0060   +. TI 2012-08-16 09:12:50.0
0061     DC 07-FC EIS_MODE_CHG_ENA
0062     BC       (20)
0063   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0064     C. ********************************
0065     C. EIS END OBSTBL LOAD
0066     C. ********************************
0067     C.
0068   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0069     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0070   . S. DC-BC dcbc-402:DCBC
0071     (MDP_known_event)
0072     C.
0073     C.
0074   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0075   . S. DC-BC dcbc-153:DCBC
0076     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077     C.
0078     C.
0079   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0080     C.
0081   . C. *********** LOS ***********
0082     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-085 2012-08-16 13:19:46 218 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ********************************
0099     C.  SOT table upload
0100     C. ********************************
0101   . C. < Stop FG table >
0102   +. DC 07-F0 MDP_FG_CTRL_MANU
0103     BC       (51)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108   . C. <Upload FG Observation Table>
0109   . S. RAM   ram-268:MDP_OBS_F
0110     ()
0111     C.
0112   . C. < Dump RAMID=MDP_OBS_F >
0113   +. DC 07-F0 MDP_DUMP_FGTBL
0114     BC       (82 07 00 00 00 38 b8)
0115     C. ------------------------------------
0116     C.  MDP_OBS_F verify = OK/NG        [ ]
0117     C. ------------------------------------
0118     C.
0119     C. ********************************
0120     C.  SOT TI command set
0121     C. ********************************
0122     C. Execute, after the success of TBL upload.
0123   +. TI 2012-08-16 09:12:18.0
0124     DC 07-F0 MDP_SOT_MODE_OBSV
0125     BC       (40)
0126    . C. ------------------------------------
0127     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0128     C. ------------------------------------
0129     C.
0130     C.
0131     C. ************ XRT START ************
0132     C.
0133   +. DC 07-F0 MDP_XRT_CTRL_MANU
0134     BC       (c1)
0135   +  DC 07-F0 MDP_XRT_MODE_STBY
0136     BC       (c3)
0137    . C. ----------- Success Verify ?              OK / NG____
0138     C.
0139     C. XRT Obs. Table Upload
0140   . S. RAM   ram-291:MDP_OBS_X
0141     ()
0142     C.
0143   +. DC 07-F0 MDP_DUMP_XRTTBL
0144     BC       (84 07 00 00 00 3a d4)
0145    . C. ----------- Comparison Check ?                        OK / ERR ____
0146     C.
0147     C.
0148   +. DC 07-F0 MDP_XRT_ROI_SET
0149     BC       (cd 01 b1 b1 04 04)
0150   +  DC 07-F0 MDP_XRT_ROI_SET
0151     BC       (cd 02 b1 b1 08 08)
0152   +  DC 07-F0 MDP_XRT_ROI_SET
0153     BC       (cd 03 b1 b1 08 08)
0154   +  DC 07-F0 MDP_XRT_ROI_SET
0155     BC       (cd 04 b1 b1 06 06)
0156   +  DC 07-F0 MDP_XRT_ROI_SET
0157     BC       (cd 05 85 83 06 06)
0158   +  DC 07-F0 MDP_XRT_ROI_SET
0159     BC       (cd 06 85 83 06 06)
0160   +  DC 07-F0 MDP_XRT_ROI_SET
0161     BC       (cd 07 80 80 20 20)
0162   +  DC 07-F0 MDP_XRT_ROI_SET
0163     BC       (cd 08 85 83 08 08)
0164   +  DC 07-F0 MDP_XRT_ROI_SET
0165     BC       (cd 09 80 80 20 08)
0166   +  DC 07-F0 MDP_XRT_ROI_SET
0167     BC       (cd 0a 80 80 08 20)
0168   +  DC 07-F0 MDP_XRT_ROI_SET
0169     BC       (cd 0b 80 60 20 18)
0170   +  DC 07-F0 MDP_XRT_ROI_SET
0171     BC       (cd 0c a0 80 18 20)
0172   +  DC 07-F0 MDP_XRT_ROI_SET
0173     BC       (cd 0f 80 80 06 06)
0174   +  DC 07-F0 MDP_XRT_ROI_SET
0175     BC       (cd 10 80 80 08 08)
0176   +  DC 07-F0 MDP_XRT_FLD_DIS
0177     BC       (d9)
0178   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0179     BC       (c9)
0180   +  DC 07-F0 MDP_XRT_AEC_RESET
0181     BC       (d0)
0182   +  DC 07-F0 MDP_XRT_ARS_DIS
0183     BC       (d5)
0184   +  DC 07-F0 MDP_XRT_FLD_RESET
0185     BC       (da)
0186   +  DC 07-F0 MDP_XRT_QT_PROG_SET
0187     BC       (c4 0b)
0188   +  DC 07-F0 MDP_XRT_FL_PROG_SET
0189     BC       (c5 09)
0190    . C. ---------- Success Verify ?           OK / NG ____
0191     C.
0192     C.
0193   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
```

```
0194     C.
0195  +. DC 07-F0 MDP_XRT_MODE_OBSV
0196     BC       (c2)
0197  +. TI 2012-08-16 09:12:02.0
0198     DC 07-F0 MDP_XRT_MODE_OBSV
0199     BC       (c2)
0200   . C. ---------- Success Verify ?            OK / NG ____
0201     C.
0202     C. ************ XRT END ***********
0203     C.
0204   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0205     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0206   . S. DC-BC dcbc-402:DCBC
0207     (MDP_known_event)
0208     C.
0209     C.
0210   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0211   . S. DC-BC dcbc-153:DCBC
0212     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0213     C.
0214     C.
0215   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0216     C.
0217   . C. ********** LOS **********
0218     C.
```

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*** OP Sequence for XRT ***

2012/08/16  09:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  09:21:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  09:21:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  09:23:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 01 00 00 00 00
2012/08/16  09:24:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  09:49:30.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  09:49:32.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/16  09:49:52.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/16  09:49:54.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/16  09:49:56.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/16  09:49:58.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  09:50:00.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  09:50:02.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2012/08/16  09:50:04.0  XRT_FL_PROG_SET_431_OG [0x1af]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 09
2012/08/16  09:50:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  11:07:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  11:07:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  11:07:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  11:10:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  11:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  11:29:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/16  11:30:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/08/16  11:30:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/16  11:30:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/16  11:30:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  11:32:58.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2012/08/16  11:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  12:20:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  12:20:02.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/16  12:20:22.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/16  12:20:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/16  12:20:26.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  12:23:04.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2012/08/16  12:23:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  12:45:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  12:45:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  12:45:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  12:48:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  13:06:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/16  13:07:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  13:29:54.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  13:29:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/16  13:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/08/16  13:30:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/16  13:30:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/16  13:30:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
```

```
2012/08/16  13:30:22.0  XRT_ARS_DIS_436_OG [0x1b4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  13:32:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  13:32:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/16  13:32:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/16  13:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  14:24:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  14:24:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  14:24:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  14:27:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  14:59:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/16  15:00:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  16:02:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  16:02:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  16:02:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  16:05:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  16:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  16:49:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/16  16:50:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/08/16  16:50:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/16  16:50:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/16  16:50:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  16:52:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/08/16  16:53:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  16:59:54.0  XRT_CTRL_MANU_445_OG [0x1bd]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  17:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 53 8d
2012/08/16  17:02:26.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/16  17:02:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/16  17:02:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/16  17:02:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/16  17:02:52.0  XRT_ARS_DIS_412_OG [0x19c]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  17:02:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  17:02:56.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2012/08/16  17:02:58.0  XRT_FL_PROG_SET_431_OG [0x1af]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 09
2012/08/16  17:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  17:41:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  17:41:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  17:41:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  17:44:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  18:23:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/16  18:24:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  19:19:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  19:19:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  19:19:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  19:22:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  19:59:54.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  19:59:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/16  20:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/08/16  20:00:16.0  XRT_FLD_ENA_428_OG [0x1ac]
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                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/16  20:00:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/16  20:00:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/16  20:00:22.0  XRT_ARS_DIS_436_OG [0x1b4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  20:02:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  20:02:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/16  20:02:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/16  20:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  20:58:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  20:58:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  20:58:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  21:01:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  21:37:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/16  21:38:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  22:36:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  22:36:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  22:36:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/16  22:39:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/16  23:12:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/16  23:13:00.5  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/16  23:15:00.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/16  23:15:02.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/16  23:15:22.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/16  23:15:24.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/16  23:15:26.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/16  23:15:28.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/16  23:15:30.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/16  23:15:32.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2012/08/16  23:15:34.0  XRT_FL_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 09
2012/08/16  23:15:36.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  00:15:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  00:15:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  00:15:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  00:18:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  00:36:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  00:37:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  01:15:00.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  01:15:02.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  01:15:22.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/17  01:15:24.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/17  01:15:26.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  01:15:28.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  01:15:30.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  01:15:32.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/17  01:15:34.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/17  01:15:36.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  01:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  01:52:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  01:52:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
```

```
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  01:55:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  02:14:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  02:15:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  03:25:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  03:25:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  03:25:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  03:28:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  03:53:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  03:54:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  04:58:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  04:58:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  04:58:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  05:01:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  05:31:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  05:32:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  05:54:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  05:54:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/17  05:55:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2012/08/17  05:55:16.0  XRT_FLD_DIS_404_OG [0x194]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/17  05:55:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/17  05:55:20.0  XRT_ARS_DIS_406_OG [0x196]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  05:57:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/08/17  05:58:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  06:04:54.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  06:05:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 00 ac 00 00 00
2012/08/17  06:19:28.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  06:19:48.0  XRT_FLD_DIS_421_OG [0x1a5]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/17  06:19:50.0  XRT_FLRCTRL_DIS_422_OG [0x1a6]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/17  06:19:52.0  XRT_AEC_RESET_423_OG [0x1a7]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  06:19:54.0  XRT_ARS_DIS_412_OG [0x19c]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  06:19:56.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  06:19:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2012/08/17  06:20:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  08:04:54.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  08:05:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                    AOCU_NM              5 02-76 00 00 00 54 00
2012/08/17  08:19:28.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  08:19:48.0  XRT_FLD_DIS_421_OG [0x1a5]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/17  08:19:50.0  XRT_FLRCTRL_DIS_422_OG [0x1a6]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/17  08:19:52.0  XRT_AEC_RESET_423_OG [0x1a7]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  08:19:54.0  XRT_ARS_DIS_412_OG [0x19c]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  08:19:56.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  08:19:58.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2012/08/17  08:20:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  10:29:54.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  10:29:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  10:30:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                    AOCU_NM              5 02-76 00 f2 36 af 75
2012/08/17  10:30:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/17  10:30:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
```

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                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/17  10:30:20.5  XRT_AEC_RESET_423_OG [0x1a7]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  10:30:22.5  XRT_ARS_DIS_436_OG [0x1b4]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  10:32:54.5  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  10:32:56.5  XRT_QT_PROG_SET_437_OG [0x1b5]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2012/08/17  10:32:58.5  XRT_FL_PROG_SET_431_OG [0x1af]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 09
2012/08/17  10:33:00.5  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  11:44:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  11:44:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  11:44:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  11:47:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  12:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2012/08/17  12:05:30.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  12:05:32.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  12:05:52.0  XRT_FLD_ENA_428_OG [0x1ac]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/17  12:05:54.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/17  12:05:56.0  XRT_AEC_RESET_423_OG [0x1a7]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  12:05:58.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  12:06:00.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  12:06:02.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/17  12:06:04.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/17  12:06:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  13:23:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  13:23:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  13:23:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  13:26:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  13:43:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  13:44:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  15:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  15:01:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  15:01:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  15:04:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  15:46:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  15:47:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  16:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  16:40:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  16:40:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  16:43:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  17:23:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  17:24:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  17:59:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/17  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2012/08/17  18:00:16.0  XRT_FLD_DIS_404_OG [0x194]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/17  18:00:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/17  18:00:20.0  XRT_ARS_DIS_406_OG [0x196]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  18:02:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/08/17  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  18:09:54.0  XRT_CTRL_MANU_427_OG [0x1ab]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  18:09:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  18:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2012/08/17  18:10:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/17  18:10:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/17  18:10:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  18:10:22.0  XRT_ARS_DIS_436_OG [0x1b4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  18:12:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  18:12:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/17  18:12:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/17  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  18:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  18:18:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  18:18:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  18:21:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  19:00:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  19:01:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  19:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  19:57:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  19:57:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  20:00:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  20:37:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  20:38:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  21:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  21:35:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  21:35:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  21:38:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  22:13:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/17  22:14:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/17  23:14:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  23:14:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  23:14:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/17  23:17:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/17  23:45:00.5  XRT_CTRL_MANU_427_OG [0x1ab]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/17  23:45:02.5  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/17  23:45:22.5  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/17  23:45:24.5  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/17  23:45:26.5  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/17  23:45:28.5  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/17  23:45:30.5  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/17  23:45:32.5  XRT_QT_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2012/08/17  23:45:34.5  XRT_FL_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 09
2012/08/17  23:45:36.5  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  00:52:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  00:52:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  00:52:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/18  00:55:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/18  01:13:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/18  01:14:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

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2012/08/18  01:45:00.5  XRT_CTRL_MANU_427_OG [0x1ab]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  01:45:03.5  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/18  01:45:23.5  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/18  01:45:25.5  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/18  01:45:27.5  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/18  01:45:29.5  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/18  01:45:31.5  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  01:45:33.5  XRT_QT_PROG_SET_443_OG [0x1bb]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/18  01:45:35.5  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/18  01:45:37.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  02:27:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  02:27:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  02:27:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/18  02:30:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/18  02:51:30.0  XRT_Custom_434_OG [0x1b2]
2012/08/18  02:52:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  03:56:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  03:56:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  03:56:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/18  03:59:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/18  04:30:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/18  04:31:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  05:36:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  05:36:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  05:36:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/18  05:39:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/18  06:12:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  06:12:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/08/18  06:13:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/08/18  06:13:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/08/18  06:13:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/08/18  06:13:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/18  06:15:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/08/18  06:16:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  06:22:54.0  XRT_CTRL_MANU_427_OG [0x1ab]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  06:22:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/08/18  06:23:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/08/18  06:23:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/08/18  06:23:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/08/18  06:23:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/08/18  06:23:22.0  XRT_ARS_DIS_436_OG [0x1b4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/08/18  06:25:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  06:25:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/08/18  06:25:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/08/18  06:26:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  07:17:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  07:17:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  07:17:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
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                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/18  07:20:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/18  07:47:00.0  XRT_Custom_434_OG [0x1b2]
2012/08/18  07:48:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/08/18  08:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  08:57:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/08/18  08:57:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/08/18  09:00:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/08/18  09:10:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/08/18  09:33:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
```