

XRT Timeline to be uploaded on 2012/08/18

Period: 2012/08/18 09:23:00 - 2012/08/23 10:50:00

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Normal mode

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XOB #1913: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band Test													
Term	Pointing (x, y)		Comment										
08/18 09:38:00 - 08/18 17:17:30	Fixed (860.0, 310.0)		# OP start + 10min Fixed AR11543										
08/18 18:13:00 - 08/18 22:59:54	Fixed (860.0, 310.0)		Fixed AR11543										
08/19 03:30:00 - 08/19 05:46:54	Track (-834.6, 280.2)		Track Emerging AR on E-limb										
08/19 06:00:00 - 08/19 17:44:54	Track (-825.5, 277.4)		Track Emerging AR on E-limb										
08/19 18:38:00 - 08/19 22:50:30	Track (-781.7, 266.5)		Track Emerging AR on E-limb										
08/20 03:03:00 - 08/20 05:59:54	Track (-741.7, 258.6)		Track Emerging AR on E-limb										
08/20 06:13:00 - 08/20 17:53:24	Track (-726.4, 256.0)		Track Emerging AR										
08/20 18:06:30 - 08/20 22:59:54	Track (-663.1, 246.4)		Track Emerging AR										
PROG= 11 Inf.-time(s)													
Subr= 1	1-time(s) 2.0sec												
Seqn= 19	2-time(s) 2.0sec												
	Open/G-band	Open/G-band close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Subr= 2	2-time(s) 2.0sec												
Seqn= 18	1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Seqn= 9	4-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Be close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 10	25-time(s) 80.0sec												
	Open/Al-mesh	Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Ti-poly	Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Al-mesh	Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	Open/Ti-poly	Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	Open/Al-mesh	Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
	Open/Ti-poly	Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
	Open/Al-mesh	Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
	Open/Ti-poly	Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1924: Synoptic Q95 2x2 - Al/mesh(64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(128/1443) + Thin-Be(30)													
Term	Pointing (x, y)		Comment										
08/18 18:04:00 - 08/18 18:09:54	Fixed (0.0, 0.0)		synoptic										
08/19 05:50:00 - 08/19 05:56:54	Fixed (0.0, 0.0)		synoptic, shifted -13.0 min										
08/19 17:48:00 - 08/19 17:54:54	Fixed (0.0, 0.0)		synoptic, shifted -15.0 min										
08/20 06:03:00 - 08/20 06:09:54	Fixed (0.0, 0.0)		synoptic										
08/20 17:56:30 - 08/20 18:03:24	Fixed (0.0, 0.0)		synoptic, shifted -6.5 min										
08/21 06:30:00 - 08/21 06:36:54	Fixed (0.0, 0.0)		synoptic, shifted 27.0 min										
PROG= 18 1-time(s)													
Subr= 1	1-time(s) 12.0sec												
Seqn= 41	1-time(s) 4.0sec												
	Open/Al-mesh	Open/Al-mesh close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 6	1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 43	1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 42	1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 8	1-time(s) 2.0sec												
	Open/G-band	Open/G-band open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1919: AR Standard-B(Morphology) with PFB, FW1=Open, 384x384 at 1064 1048, 30s cad With G-band Test													
Term	Pointing (x, y)		Comment										
08/18 23:03:00 - 08/19 02:59:54	Fixed (895.0, -210.0)		HOP 128 Target 1 AR11542										
08/19 23:28:00 - 08/20 02:59:54	Fixed (845.0, 345.0)		HOP 128 Target 2 AR11543 - filament 1										
08/20 23:03:00 - 08/21 02:41:00	Fixed (880.0, 247.0)		HOP 128 Target 3 AR11543 - filament 2										
PROG= 12 Inf.-time(s)													
Subr= 1	1-time(s) 2.0sec												
Seqn= 19	2-time(s) 2.0sec												
	Open/G-band	Open/G-band close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Subr= 2	2-time(s) 2.0sec												
Seqn= 18	1-time(s) 2.0sec												

├	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 9		4-time(s)	2.0sec										
├	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 21		60-time(s)	30.0sec										
├	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1923: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 60s cad

Term	Pointing (x, y)	Comment
08/21 03:05:00 - 08/21 06:26:54	Track (-609.9, 239.9) @ 08/21 03:00:00	Track Emerging AR
08/21 06:40:30 - 08/21 09:11:00	Track (-587.1, 237.3) @ 08/21 06:37:00	Track Emerging AR
PROG= 17 15-time(s)		
Subr= 1 1-time(s) 2.0sec		
├ Seqn= 19 2-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2 2-time(s) 2.0sec		
├ Seqn= 18 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 32 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 33 30-time(s) 60.0sec		
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
08/18 09:38:00 - 08/18 17:17:30	Fixed (860.0, 310.0)	# OP start + 10min Fixed AR11543
08/18 18:13:00 - 08/18 22:59:54	Fixed (860.0, 310.0)	Fixed AR11543
08/19 03:30:00 - 08/19 05:46:54	Track (-834.6, 280.2) @ 08/19 03:00:00	Track Emerging AR on E-limb
08/19 06:00:00 - 08/19 17:44:54	Track (-825.5, 277.4) @ 08/19 05:57:00	Track Emerging AR on E-limb
08/19 18:38:00 - 08/19 22:50:30	Track (-781.7, 266.5) @ 08/19 17:55:00	Track Emerging AR on E-limb
08/20 03:03:00 - 08/20 05:59:54	Track (-741.7, 258.6) @ 08/20 03:00:00	Track Emerging AR on E-limb
08/20 06:13:00 - 08/20 17:53:24	Track (-726.4, 256.0) @ 08/20 06:10:00	Track Emerging AR
08/20 18:06:30 - 08/20 22:59:54	Track (-663.1, 246.4) @ 08/20 18:03:30	Track Emerging AR
08/21 03:05:00 - 08/21 06:26:54	Track (-609.9, 239.9) @ 08/21 03:00:00	Track Emerging AR
08/21 06:40:30 - 08/21 09:11:00	Track (-587.1, 237.3) @ 08/21 06:37:00	Track Emerging AR
PROG= 16 15-time(s)		
Subr= 1 45-time(s) 10.0sec		
├ Seqn= 35 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
├ Seqn= 36 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 37 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 38 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1914: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops (45ms Gband)

Term	Pointing (x, y)	Comment
08/18 23:03:00 - 08/19 02:59:54	Fixed (895.0, -210.0)	HOP 128 Target 1 AR11542
08/19 23:28:00 - 08/20 02:59:54	Fixed (845.0, 345.0)	HOP 128 Target 2 AR11543 - filament 1
08/20 23:03:00 - 08/21 02:41:00	Fixed (880.0, 247.0)	HOP 128 Target 3 AR11543 - filament 2
PROG= 09 15-time(s)		
Subr= 1 45-time(s) 10.0sec		

└─ Seqn= 13		1-time(s)		2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)			DPCM	2	0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)			DPCM	3	0	2.0sec		
└─ Subr= 2		1-time(s)		10.0sec													
└─ Seqn= 14		1-time(s)		2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)			DPCM	2	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)			DPCM	3	0	2.0sec		
└─ Seqn= 20		1-time(s)		2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)			Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)			Q=98	0	0	2.0sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

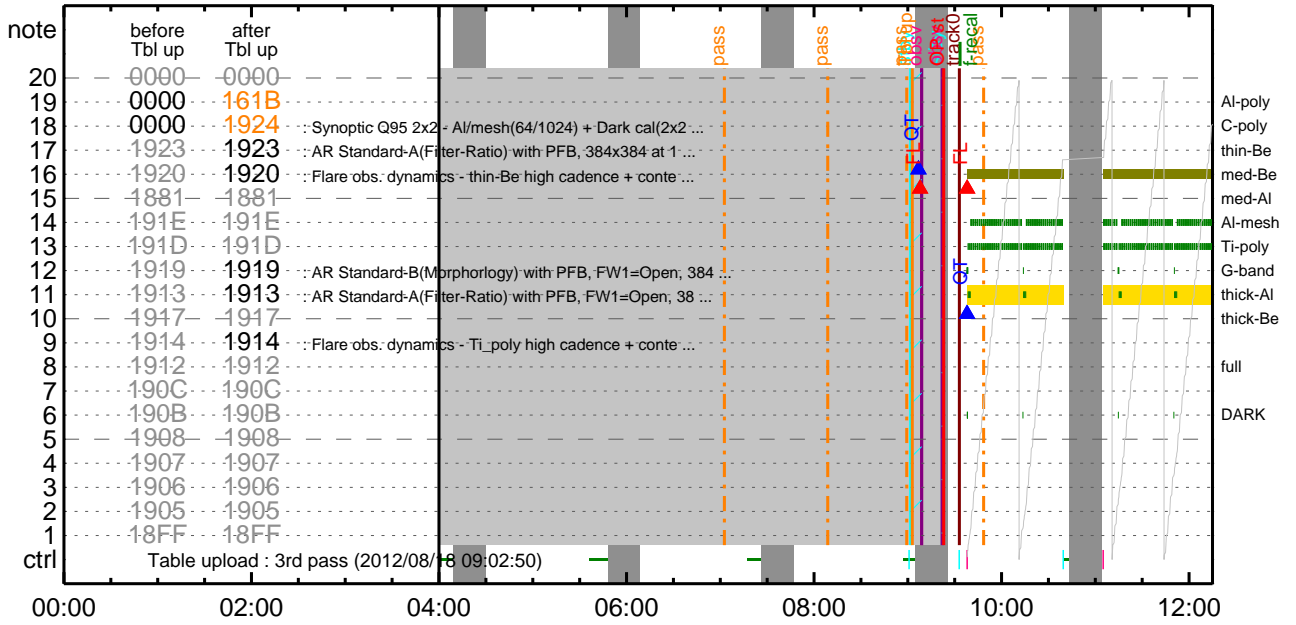
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Flare Detection

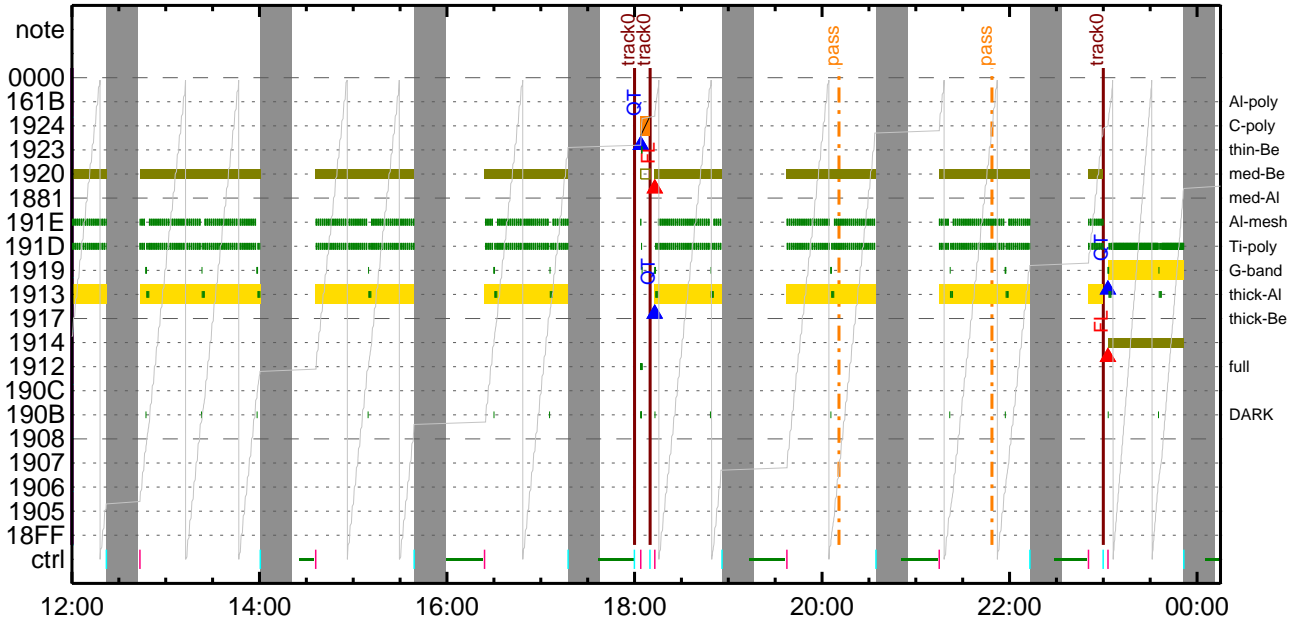
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FLD Patrol														
Term		Pointing (x, y)				Comment								
08/18 09:37:46 - 08/18 18:00:16		Fixed (860.0, 310.0)				# OP start + 10min Fixed AR11543								
08/18 18:12:46 - 08/19 05:47:16		Fixed (860.0, 310.0)				Fixed AR11543								
08/19 05:59:46 - 08/19 17:45:16		Track (-825.5, 277.4) ^{Ⓢ 08/19 05:57:00}				Track Emerging AR on E-limb								
08/19 18:37:46 - 08/20 06:00:16		Track (-781.7, 266.5) ^{Ⓢ 08/19 17:55:00}				Track Emerging AR on E-limb								
08/20 06:12:46 - 08/20 17:53:46		Track (-726.4, 256.0) ^{Ⓢ 08/20 06:10:00}				Track Emerging AR								
08/20 18:06:16 - 08/21 06:27:16		Track (-663.1, 246.4) ^{Ⓢ 08/20 18:03:30}				Track Emerging AR								
08/21 06:39:46 - 08/23 10:50:00		Track (-587.1, 237.3) ^{Ⓢ 08/21 06:37:00}				Track Emerging AR								
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

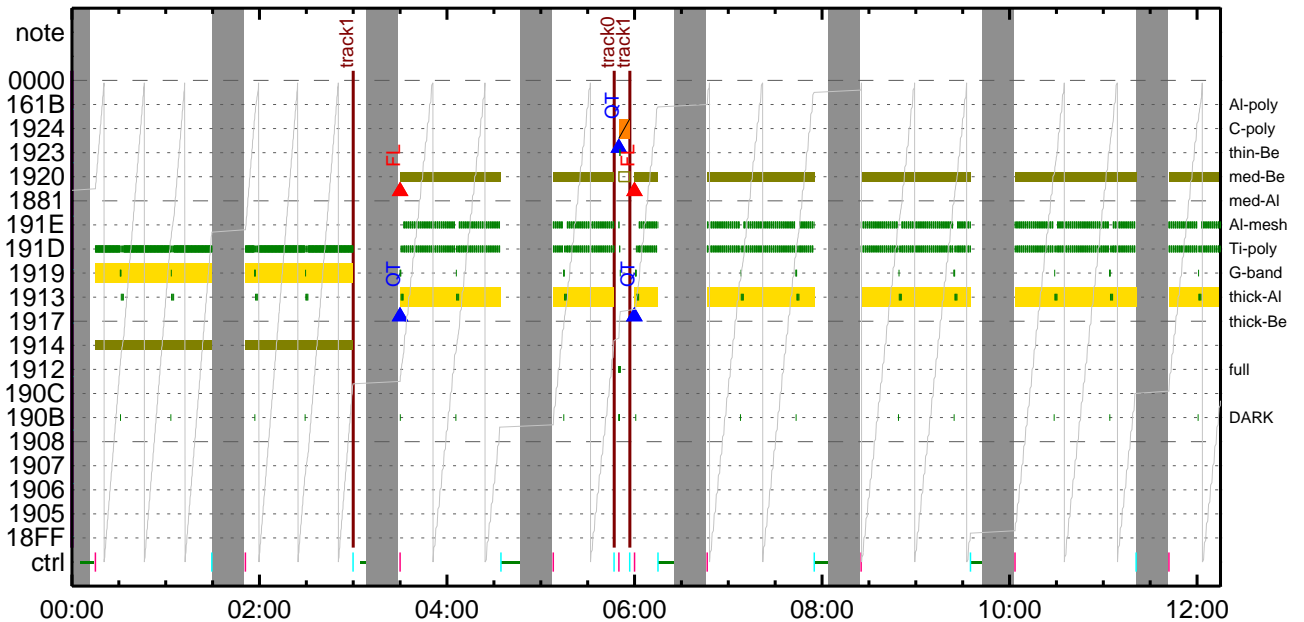
CMDI #0854 2012/08/18



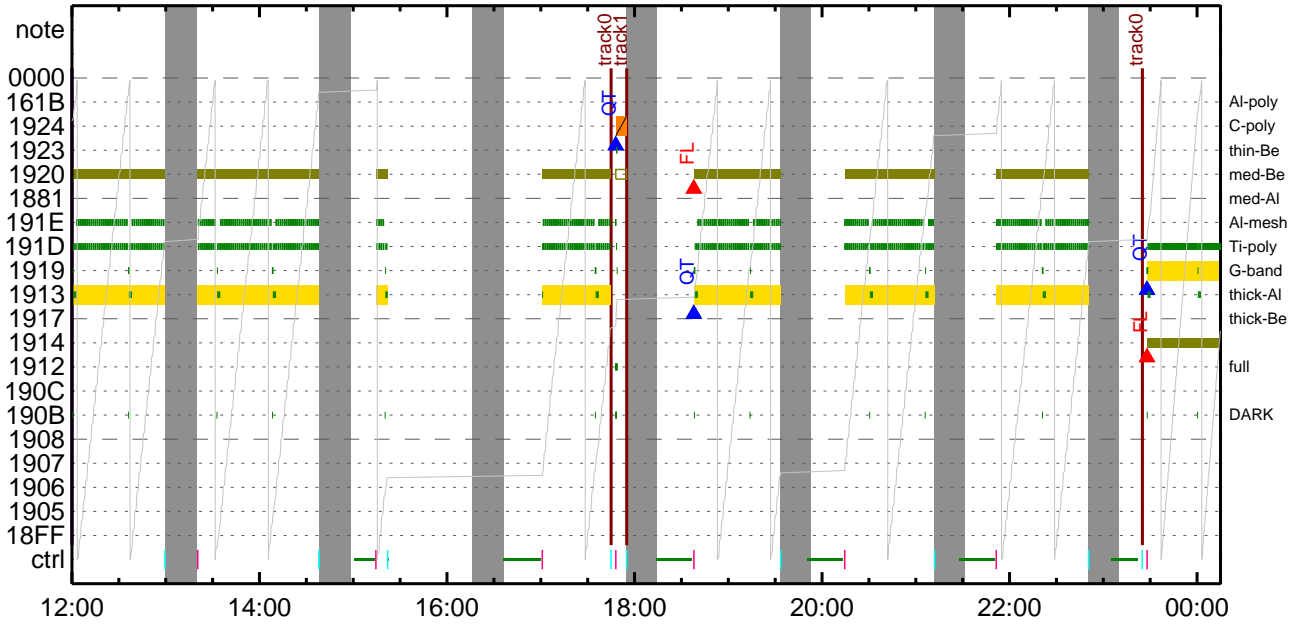
CMDI #0854 2012/08/18



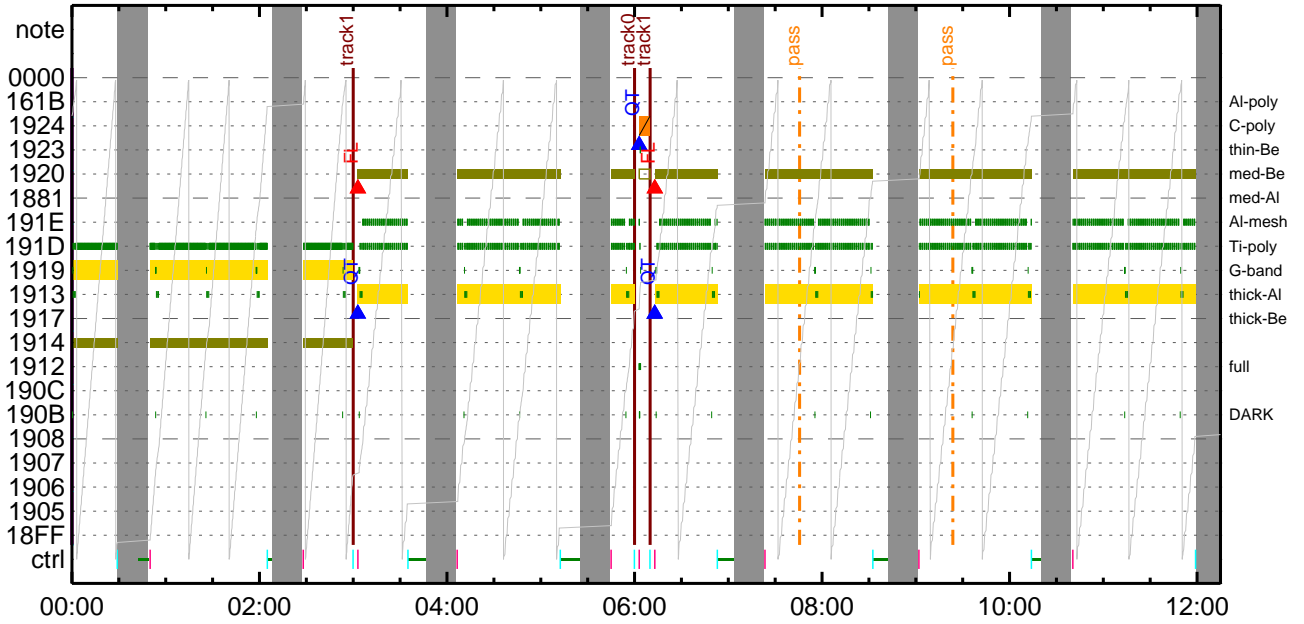
CMDI #0854 2012/08/19



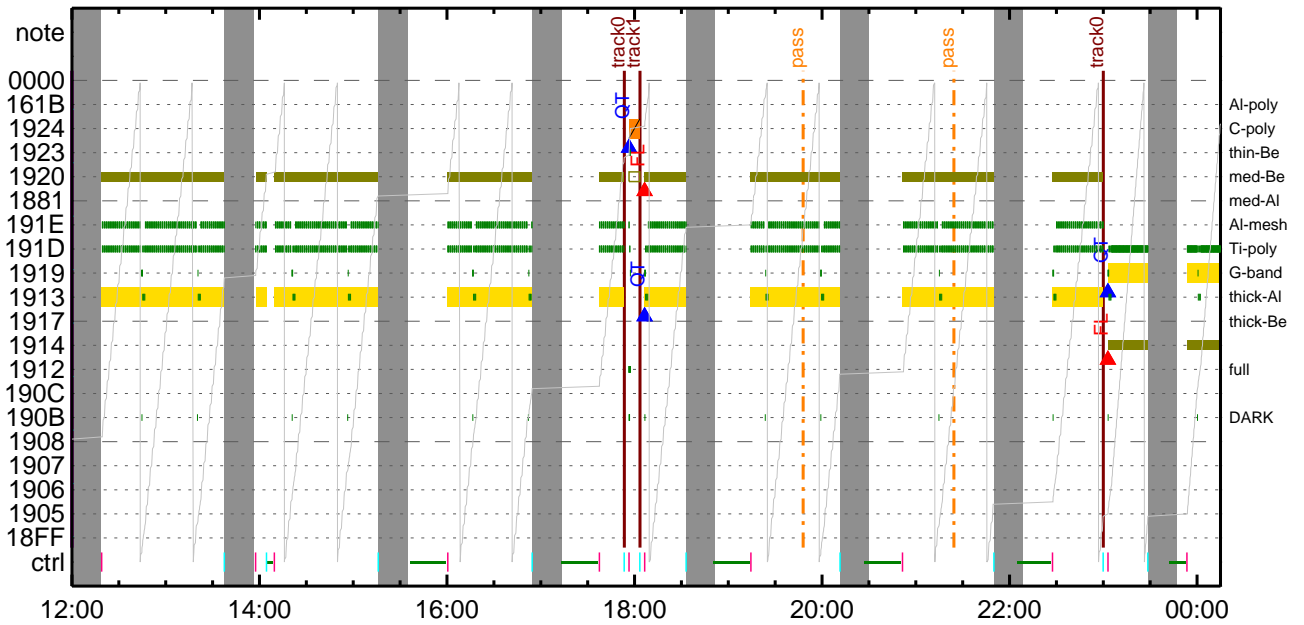
CMDI #0854 2012/08/19



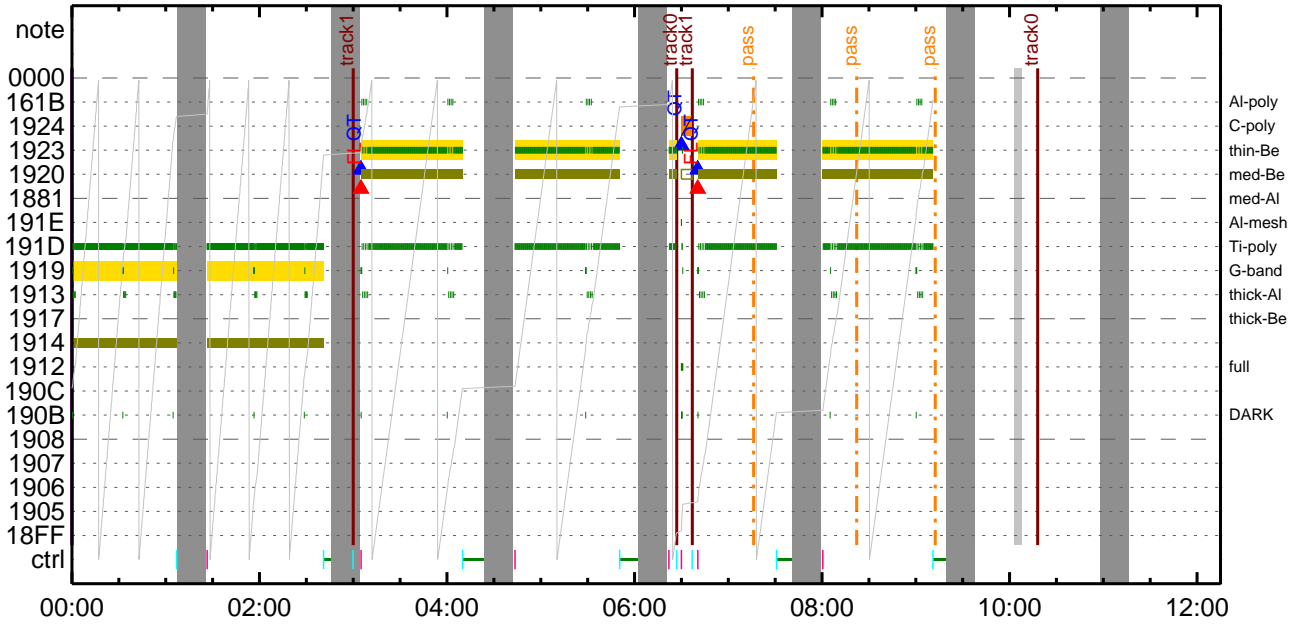
CMDI #0854 2012/08/20



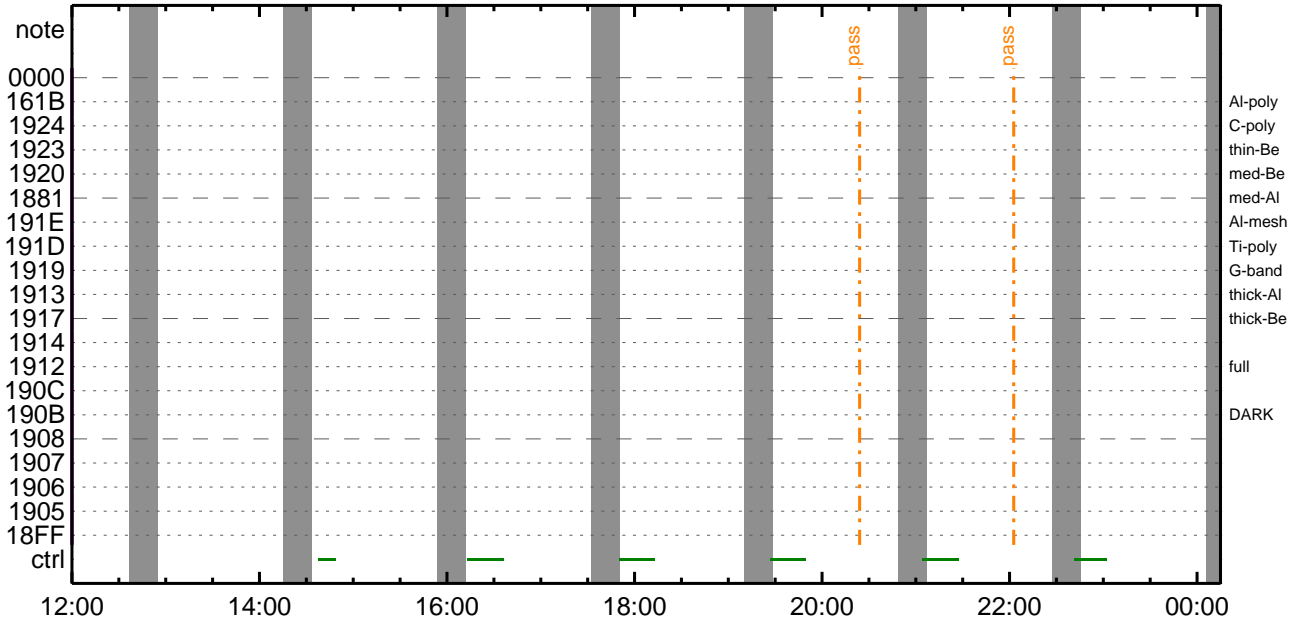
CMDI #0854 2012/08/20



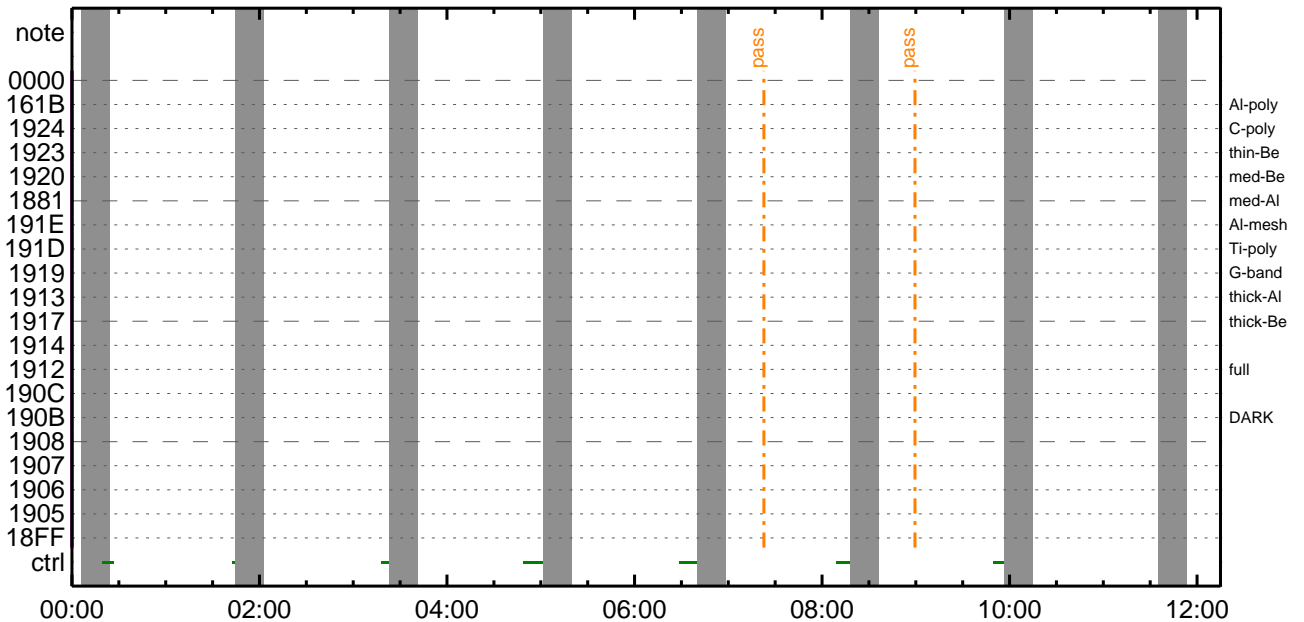
CMDI #0854 2012/08/21



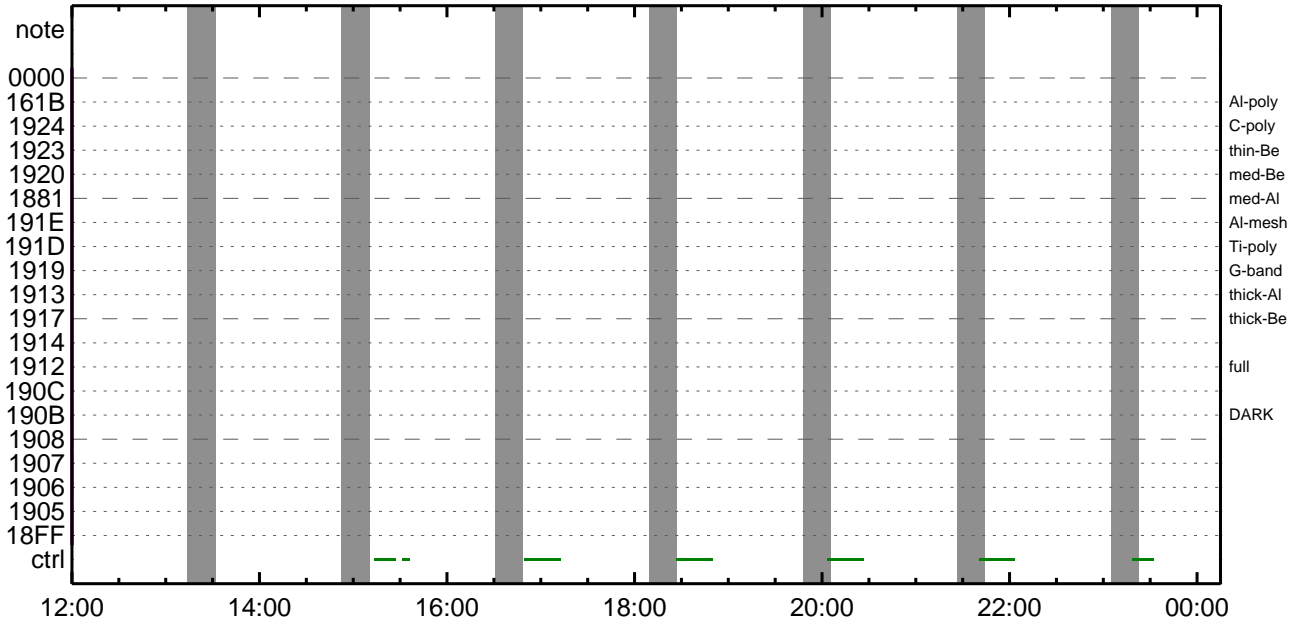
CMDI #0854 2012/08/21



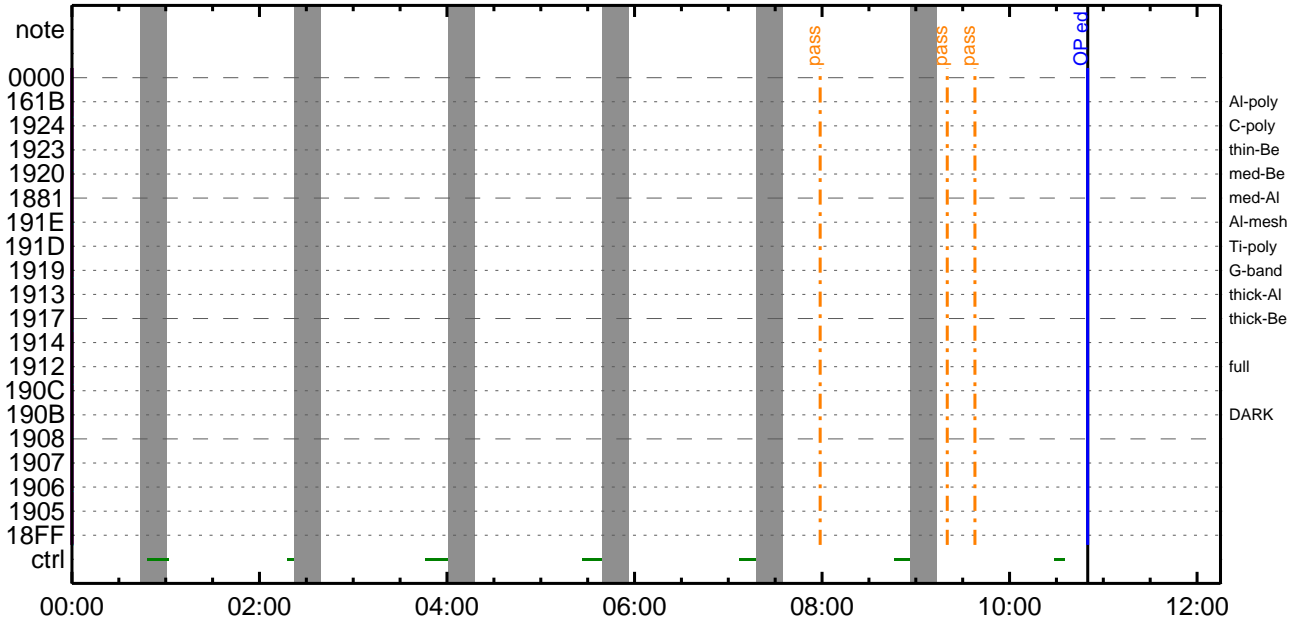
CMDI #0854 2012/08/22



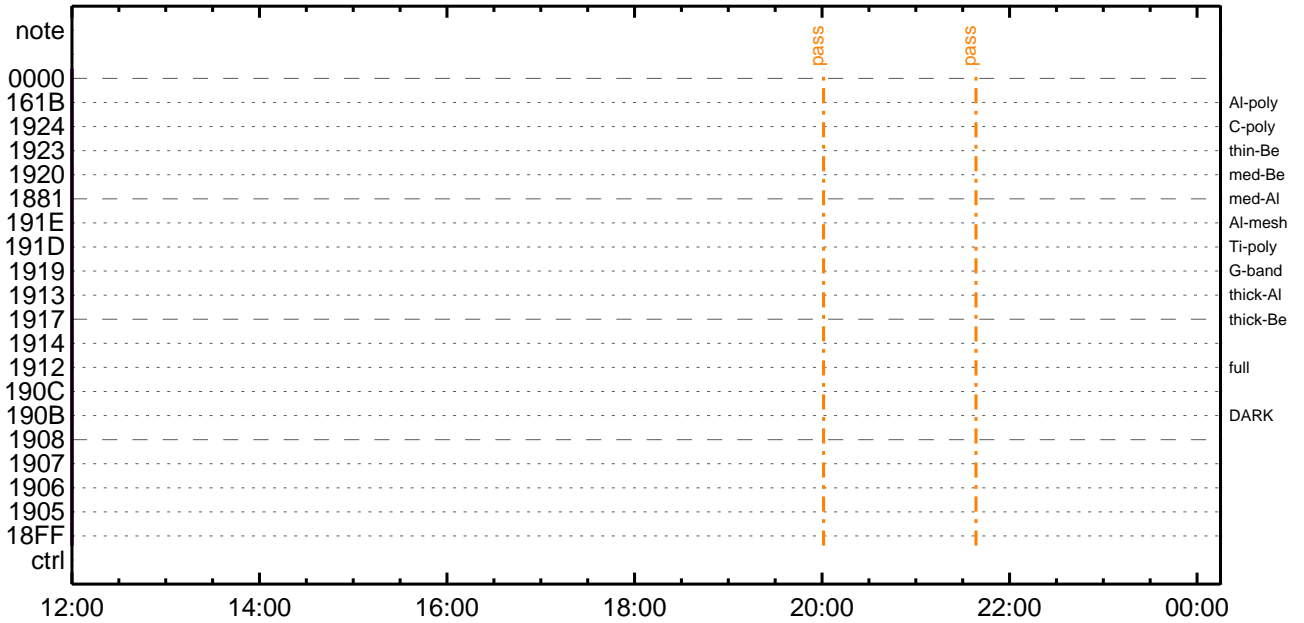
CMDI #0854 2012/08/22



CMDI #0854 2012/08/23



CMDI #0854 2012/08/23




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0096 C.                0x0000; SET 0x0000 DUMP 0x0000 0x0000 0x0000 0x0000;
0097 C.
0098 C. TI 2012-08-18 09:18:00.0
0099 +. TI 2012-08-18 09:18:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2012-08-18 09:18:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2012-08-18 09:18:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2012-08-18 09:22:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.                0x0000 [HK1_TI_CMD_ENA/DIS] EQ ENA
0116 C.                0x0000 [HK1_TI_CMD_NUM] EQ 4
0117 C.                0x0000 [HK1_NEXT_EXEC_PIM] EQ DHU
0118 C.                0x0000 [HK1_NEXT_EXEC_DC] EQ 0xB3
0119 C.
0120 C.
0121 C. *****
0122 C. TI 2012-08-18 09:22:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                0x0000 [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.                0x0000 [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.                0x0000 [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.                0x0000 [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.                0x0000 [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                0x0000 [HK1_PKT_FORM_NO] EQ 7
0136 C.                0x0000 [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.                0x0000 [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.                0x0000 [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.                0x0000 [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.                0x0000 [HK1_DMP_CHK_FLG] EQ NON
0142 C.
0143 C.
0144 C. RAM ID=TI_TBL 0x03AB00-0x03AEFF
0145 C.
0146 C. DHU 2012-08-18 09:22:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                0x0000 [HK1_PKT_FORM_NO] EQ 2
0150 C.                0x0000 [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.                0x0000 [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.                0x0000 [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2012-08-18 09:22:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C.                HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2012-08-18 09:22:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2012-08-18 09:22:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2012-08-18 09:22:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 2012-08-18 09:22:00.0 *****
0192 C. (0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP)
0193 S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-090 2012-08-18 14:05:53 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αB•μ°E»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈè%μ•ííÉ;ÈBÈ¼°ÇÓα•α¿¼í¹çαÍ;çÁ®, ùα¹αèαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_DIS
0057 BC (d9)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0059 BC (c9)
0060 + DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 + DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 11)
0068 + DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 10)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2012-08-18 09:22:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop FG table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload FG Observation Table>
0094 . S. RAM ram-269:MDP_OBS_F
0095 ( )
```

```

0096      C.
0097      . C. < Dump RAMID=MDP_OBS_F >
0098      +. DC 07-F0 MDP_DUMP_FGTBL
0099      BC      (82 07 00 00 00 38 b8)
0100      C. -----
0101      C. MDP_OBS_F verify = OK/NG      [ ]
0102      C. -----
0103      C.
0104      C. *****
0105      C. SOT TI command set
0106      C. *****
0107      C. Execute, after the success of TBL upload.
0108      +. TI 2012-08-18 09:22:18.0
0109      DC 07-F0 MDP_SOT_MODE_OBSV
0110      BC      (40)
0111      . C. -----
0112      C. HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0113      C. -----
0114      C.
0115      C.
0116      . C. ***** MDP 'úÃîñî»ö¼ÝñÈÄðñ¹ñèDCBC•x²è *****
0117      C. (¼ãºîÝÓÝÄÝÈÝÞÝËÝáÝçÝèñÈ¼ñ¼Ä»Ûñ¹ñè)
0118      . S. DC-BC dcbc-402:DCBC
0119      (MDP_known_event)
0120      C.
0121      C.
0122      . C. ***** ÝÐÝ¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0123      . S. DC-BC dcbc-153:DCBC
0124      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125      C.
0126      C.
0127      . C. ;ãLOSÝÄÝ§ÝÄÝ¬¼Ä»Û;ã
0128      C.
0129      . C. ***** LOS *****
0130      C.

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*** OP Sequence for XRT ***

2012/08/18	09:32:54.0	XRT_CTRL_MANU_443_OG [0x1bb]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	09:33:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 e4 73 b3 8e		
2012/08/18	09:33:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		
2012/08/18	09:37:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/08/18	09:37:46.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/08/18	09:37:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/08/18	09:37:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/08/18	09:37:52.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/08/18	09:37:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/18	09:37:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2012/08/18	09:37:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/08/18	09:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/18	10:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	10:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/18	10:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/18	10:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/18	11:04:00.0	XRT_Custom_434_OG [0x1b2]					
2012/08/18	11:05:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/18	12:22:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	12:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/18	12:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/18	12:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/18	12:42:30.0	XRT_Custom_434_OG [0x1b2]					
2012/08/18	12:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/18	14:00:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	14:00:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/18	14:00:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/18	14:03:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/18	14:35:00.0	XRT_Custom_434_OG [0x1b2]					
2012/08/18	14:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/18	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	15:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/18	15:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/18	15:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/18	16:23:00.0	XRT_Custom_434_OG [0x1b2]					
2012/08/18	16:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/18	17:17:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	17:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/18	17:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/18	17:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/18	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/18	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/08/18	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2012/08/18	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/08/18	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/08/18	18:00:20.0	XRT_ARS_DIS_445_OG [0x1bd]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/08/18	18:03:58.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2012/08/18	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

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2012/08/18	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/18	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/18	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 e4 73 b3 8e
2012/08/18	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/08/18	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/08/18	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/08/18	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/08/18	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/08/18	18:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/08/18	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/08/18	18:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/18	18:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/18	18:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	18:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/18	19:36:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/18	19:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/08/18	20:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_413_OG [0x19d]			
2012/08/18	20:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/18	20:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/18	20:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	21:14:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/18	21:15:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/18	22:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_434_OG [0x1b2]			
2012/08/18	22:13:02.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_CTRL_AUTO_413_OG [0x19d]			
2012/08/18	22:13:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/18	22:16:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/18	22:49:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	22:50:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/18	22:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/18	23:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_Custom_434_OG [0x1b2]			
2012/08/18	23:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_CTRL_AUTO_413_OG [0x19d]			
2012/08/18	23:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/18	23:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/18	23:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	23:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/08/18	23:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/08/18	23:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	23:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/08/18	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 09
2012/08/18	23:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/18	23:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/18	23:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/18	23:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/19	00:14:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/19	00:15:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/08/19	01:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_413_OG [0x19d]			
2012/08/19	01:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da

2012/08/19	01:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/19	01:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/19	01:50:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/19	01:51:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/19	02:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/19	03:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/19	03:29:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/19	03:29:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/19	03:29:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/19	03:29:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/19	03:29:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/19	03:29:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/19	03:29:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/19	03:29:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/19	03:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/19	04:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/19	04:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/19	04:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/19	04:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/19	05:07:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/19	05:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/19	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/19	05:46:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/19	05:47:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/08/19	05:47:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/19	05:47:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/19	05:47:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/19	05:49:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/08/19	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/19	05:56:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/19	05:57:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/19	05:59:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/19	05:59:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/19	05:59:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/19	05:59:50.5	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/19	05:59:52.5	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/19	05:59:54.5	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/19	05:59:56.5	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/19	05:59:58.5	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/19	06:00:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/19	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/19	06:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/19	06:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/19	06:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/19	06:45:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/19	06:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/19	07:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/19	07:55:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/08/19	07:55:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/19	07:58:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/19	08:24:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/19	08:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	09:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/19	09:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/19	09:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/19	10:02:30.0	XRT_Custom_434_OG [0x1b2]				
2012/08/19	10:03:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	11:21:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	11:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/19	11:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/19	11:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/19	11:41:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/19	11:42:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	12:59:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	12:59:32.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/19	12:59:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/19	13:02:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/19	13:19:30.0	XRT_Custom_434_OG [0x1b2]				
2012/08/19	13:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	14:38:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	14:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/19	14:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/19	14:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/19	15:13:30.0	XRT_Custom_434_OG [0x1b2]				
2012/08/19	15:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	15:22:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	15:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2012/08/19	15:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/08/19	15:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/08/19	17:00:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/19	17:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	17:44:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	17:44:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/19	17:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2012/08/19	17:45:16.0	XRT_FLD_DIS_404_OG [0x194]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/19	17:45:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/19	17:45:20.0	XRT_ARS_DIS_406_OG [0x196]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/19	17:47:58.0	XRT_QT_PROG_SET_446_OG [0x1be]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2012/08/19	17:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/19	17:54:54.0	XRT_CTRL_MANU_449_OG [0x1c1]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/19	17:55:00.0	AOCS_Ore-point_Start_4_OG [0x09a]				
		AOCU_NM	5	02-76	01 00 00 00 00	
2012/08/19	18:37:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/08/19	18:37:46.0	XRT_FLD_ENA_428_OG [0x1ac]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/08/19	18:37:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/08/19	18:37:50.0	XRT_AEC_RESET_423_OG [0x1a7]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/08/19	18:37:52.0	XRT_ARS_DIS_438_OG [0x1b6]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/19	18:37:54.0	XRT_FLD_RESET_424_OG [0x1a8]				

2012/08/19	18:37:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2012/08/19	18:37:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/08/19	18:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/19	19:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/19	19:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/19	19:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/19	19:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/19	20:13:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/19	20:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/19	21:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/19	21:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/19	21:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/19	21:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/19	21:50:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/19	21:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/19	22:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/19	22:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/19	22:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/19	22:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/19	23:24:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/19	23:25:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	e1 59 b4 e6		
2012/08/19	23:27:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2012/08/19	23:27:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/19	23:27:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/19	23:27:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/19	23:27:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/19	23:27:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/19	23:27:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/08/19	23:27:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	09		
2012/08/19	23:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/20	00:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/20	00:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/20	00:29:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/20	00:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/20	00:49:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	00:50:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/20	02:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/20	02:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/20	02:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/20	02:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/20	02:27:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	02:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/20	02:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/20	03:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/08/20	03:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2012/08/20	03:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/20	03:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/20	03:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			

2012/08/20	03:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/20	03:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	03:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/20	03:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/20	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	03:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	03:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	03:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	04:05:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	04:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	05:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	05:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	05:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	05:44:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	05:45:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/20	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/08/20	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/20	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/20	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/20	06:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/08/20	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	06:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	06:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/20	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/20	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/20	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/20	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/20	06:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/20	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	06:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/20	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/20	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	06:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	06:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	06:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	06:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	07:22:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	07:23:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	08:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	08:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	08:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	09:01:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	09:02:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	10:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2012/08/20	10:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	10:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/20	10:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	10:39:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/20	10:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	11:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	11:59:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/20	11:59:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	12:02:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/20	12:18:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	12:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	13:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	13:37:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/20	13:37:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	13:40:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/20	13:56:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	13:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	14:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/20	14:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	14:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	14:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/20	14:08:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	14:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	15:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/20	15:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	15:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	15:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/20	15:59:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	16:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	16:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/20	16:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	16:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/20	16:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/20	17:36:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	17:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/08/20	17:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00 00 00 00 00
2012/08/20	17:53:30.0	AOCs_Or-e-point_Start_2_OG [0x098]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/08/20	17:53:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/08/20	17:53:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/08/20	17:53:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/08/20	17:56:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/20	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/20	18:03:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	AOCs_Or-e-point_Start_4_OG [0x09a]	5	02-76	01 00 00 00 00
2012/08/20	18:03:30.0	AOCs_Or-e-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/08/20	18:05:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/08/20	18:06:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/08/20	18:06:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/08/20	18:06:20.0	XRT_AEC_RESET_423_OG [0x1a7]				

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2012/08/20	18:06:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/20	18:06:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	18:06:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/20	18:06:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/20	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	18:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	18:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	18:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	18:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	19:13:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	19:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	20:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	20:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	20:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	20:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	20:50:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	20:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	21:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	21:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	21:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	21:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	22:26:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	22:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	22:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	23:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 ea 0d b1 cb				
2012/08/20	23:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/20	23:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/20	23:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/20	23:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/20	23:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/20	23:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	23:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/08/20	23:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09				
2012/08/20	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/20	23:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/20	23:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/20	23:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/20	23:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/20	23:52:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/20	23:53:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	01:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	01:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	01:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	01:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	01:25:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/21	01:26:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	02:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	02:41:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	02:41:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2012/08/21	02:44:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/21	02:59:54.0	XRT_CTRL_MANU_409_OG [0x199]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/21	03:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	01 00 00 00 00					
2012/08/21	03:03:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/08/21	03:04:16.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/08/21	03:04:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/08/21	03:04:20.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/08/21	03:04:22.0	XRT_ARS_DIS_410_OG [0x19a]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/08/21	03:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/21	03:04:56.0	XRT_QT_PROG_SET_411_OG [0x19b]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11					
2012/08/21	03:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2012/08/21	03:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/21	04:10:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/21	04:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/21	04:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/21	04:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/21	04:42:30.0	XRT_Custom_434_OG [0x1b2]								
2012/08/21	04:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/21	05:50:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/21	05:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/21	05:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/21	05:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/21	06:21:00.0	XRT_Custom_434_OG [0x1b2]								
2012/08/21	06:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/21	06:26:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/21	06:26:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2012/08/21	06:27:00.0	AOCS_ORe-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2012/08/21	06:27:16.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2012/08/21	06:27:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/08/21	06:27:20.0	XRT_ARS_DIS_406_OG [0x196]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/08/21	06:29:58.0	XRT_QT_PROG_SET_446_OG [0x1be]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12					
2012/08/21	06:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/21	06:36:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/21	06:37:00.0	AOCS_ORe-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	01 00 00 00 00					
2012/08/21	06:39:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/08/21	06:39:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/08/21	06:39:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/08/21	06:39:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/08/21	06:39:52.0	XRT_ARS_DIS_410_OG [0x19a]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/08/21	06:40:24.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/21	06:40:26.0	XRT_QT_PROG_SET_411_OG [0x19b]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11					
2012/08/21	06:40:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2012/08/21	06:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/21	07:31:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/21	07:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/21	07:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/21	07:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/21	07:59:30.0	XRT_Custom_434_OG [0x1b2]								

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2012/08/21	08:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	09:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	09:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	09:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	09:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	10:18:00.0	AOCS_Or-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				