

XRT Timeline to be uploaded on 2012/08/21

Period: 2012/08/21 10:08:00 - 2012/08/25 10:27:00

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Normal mode

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XOB #1913: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band Test												
Term	Pointing (x, y)		Comment									
08/21 10:21:00 - 08/21 17:32:00	Track (-623.9, 245.3)	<small>@ 08/21 10:18:00</small>	# OP start + 10min - Tracking AR11548									
08/21 18:26:00 - 08/21 23:59:54	Track (-573.1, 239.7)	<small>@ 08/21 18:23:00</small>	AR 11548 cont.									
08/22 02:03:30 - 08/22 05:59:54	Track (-522.1, 234.8)	<small>@ 08/22 02:00:00</small>	AR 11548 resume tracking									
08/22 06:13:00 - 08/22 17:59:24	Track (-493.0, 232.3)	<small>@ 08/22 06:10:00</small>	AR 11548 tracking									
08/22 18:51:00 - 08/22 23:05:00	Track (-405.1, 225.9)	<small>@ 08/22 18:09:30</small>	AR 11548 tracking									
08/23 02:40:30 - 08/23 06:02:54	Track (-340.5, 222.2)	<small>@ 08/23 02:32:00</small>	AR 11548 tracking									
08/23 06:16:00 - 08/23 10:28:30	Track (-311.3, 220.7)	<small>@ 08/23 06:13:00</small>	AR 11548 cont.									
PROG= 11 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 19 2-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─ Subr= 2 2-time(s) 2.0sec												
└─┬─┬─ Seqn= 18 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─┬─ Seqn= 9 4-time(s) 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─┬─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─ Seqn= 10 25-time(s) 80.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1924: Synoptic Q95 2x2 - Al/mesh(64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(128/1443) + Thin-Be(36)												
Term	Pointing (x, y)		Comment									
08/21 18:16:00 - 08/21 18:22:54	Fixed (0.0, 0.0)		synoptic, shifted 13.0 min									
08/22 06:03:00 - 08/22 06:09:54	Fixed (0.0, 0.0)		synoptic									
08/22 18:02:30 - 08/22 18:09:24	Fixed (0.0, 0.0)		synoptic, shifted -0.5 min									
08/23 06:06:00 - 08/23 06:12:54	Fixed (0.0, 0.0)		synoptic, shifted 3.0 min									
PROG= 18 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 41 1-time(s) 4.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 6 1-time(s) 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Seqn= 43 1-time(s) 4.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 42 1-time(s) 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 8 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1919: AR Standard-B(Morphology) with PFB, FW1=Open, 384x384 at 1064 1048, 30s cad With G-band Test												
Term	Pointing (x, y)		Comment									
08/22 00:34:00 - 08/22 01:42:30	Track (-103.2, 135.0)	<small>@ 08/22 00:31:00</small>	HOP 128 - Target 1 - AR 11546 tracking									
08/22 23:36:30 - 08/23 02:18:00	Track (99.6, 134.4)	<small>@ 08/22 23:33:30</small>	HOP 128 - Target 2 - AR 11546									
PROG= 12 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 19 2-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─ Subr= 2 2-time(s) 2.0sec												
└─┬─┬─ Seqn= 18 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─┬─ Seqn= 9 4-time(s) 2.0sec												
└─┬─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												

Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 21 60-time(s) 30.0sec												
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
08/21 10:21:00 - 08/21 17:32:00	Track (-623.9, 245.3) @ 08/21 10:18:00	# OP start + 10min - Tracking AR11548
08/21 18:26:00 - 08/21 23:59:54	Track (-573.1, 239.7) @ 08/21 18:23:00	AR 11548 cont.
08/22 02:03:30 - 08/22 05:59:54	Track (-522.1, 234.8) @ 08/22 02:00:00	AR 11548 resume tracking
08/22 06:13:00 - 08/22 17:59:24	Track (-493.0, 232.3) @ 08/22 06:10:00	AR 11548 tracking
08/22 18:51:00 - 08/22 23:05:00	Track (-405.1, 225.9) @ 08/22 18:09:30	AR 11548 tracking
08/23 02:40:30 - 08/23 06:02:54	Track (-340.5, 222.2) @ 08/23 02:32:00	AR 11548 tracking
08/23 06:16:00 - 08/23 10:28:30	Track (-311.3, 220.7) @ 08/23 06:13:00	AR 11548 cont.

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 35 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 36 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1914: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops (45ms Gband)

Term	Pointing (x, y)	Comment
08/22 00:34:00 - 08/22 01:42:30	Track (-103.2, 135.0) @ 08/22 00:31:00	HOP 128 - Target 1 - AR 11546 tracking
08/22 23:36:30 - 08/23 02:18:00	Track (99.6, 134.4) @ 08/22 23:33:30	HOP 128 - Target 2 - AR 11546

PROG= 09 15-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 13 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 14 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Seqn= 20 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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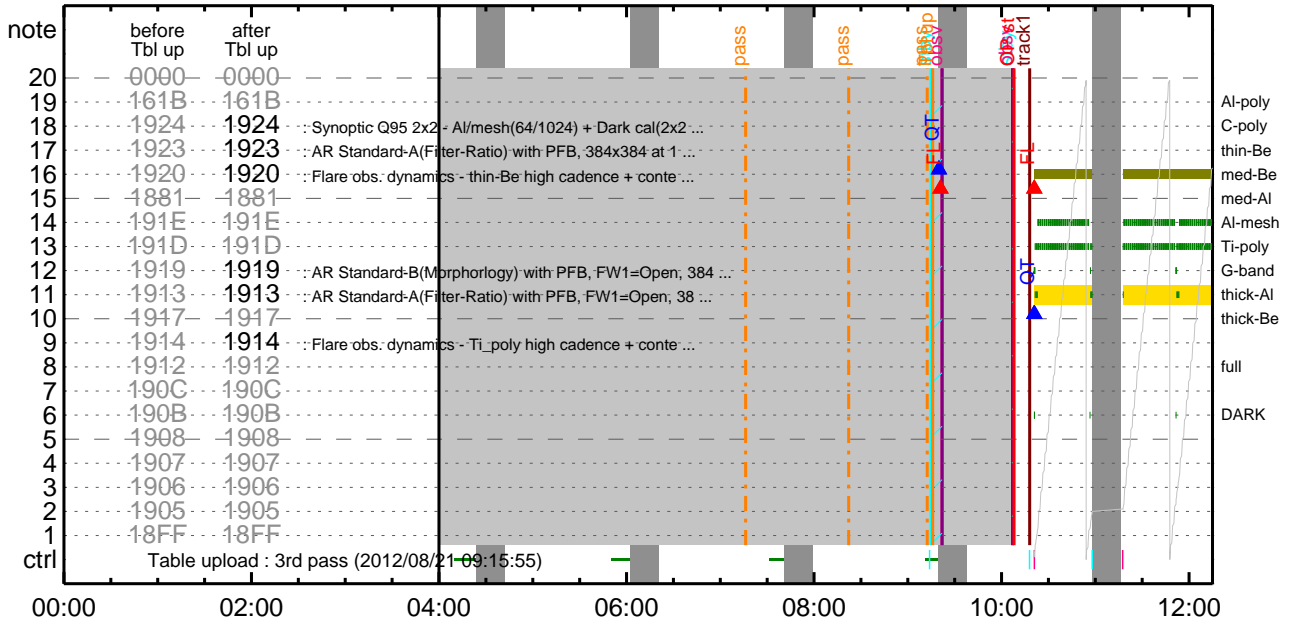
Flare Detection

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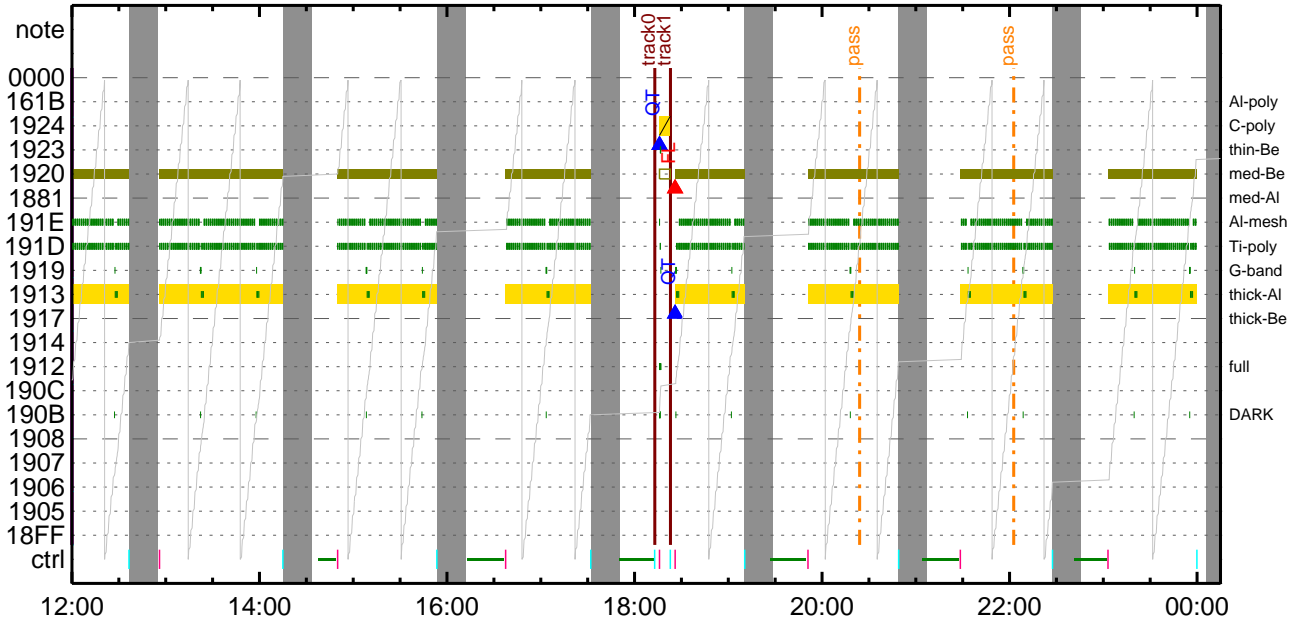
FLD Patrol

Term	Pointing (x, y)	Comment									
08/21 10:20:46 - 08/21 18:13:16	Track (-623.9, 245.3) @ 08/21 10:18:00	# OP start + 10min - Tracking AR11548									
08/21 18:25:46 - 08/22 06:00:16	Track (-573.1, 239.7) @ 08/21 18:23:00	AR 11548 cont.									
08/22 06:12:46 - 08/22 17:59:46	Track (-493.0, 232.3) @ 08/22 06:10:00	AR 11548 tracking									
08/22 18:50:46 - 08/23 06:03:16	Track (-405.1, 225.9) @ 08/22 18:09:30	AR 11548 tracking									
08/23 06:15:46 - 08/25 10:27:00	Track (-311.3, 220.7) @ 08/23 06:13:00	AR 11548 cont.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

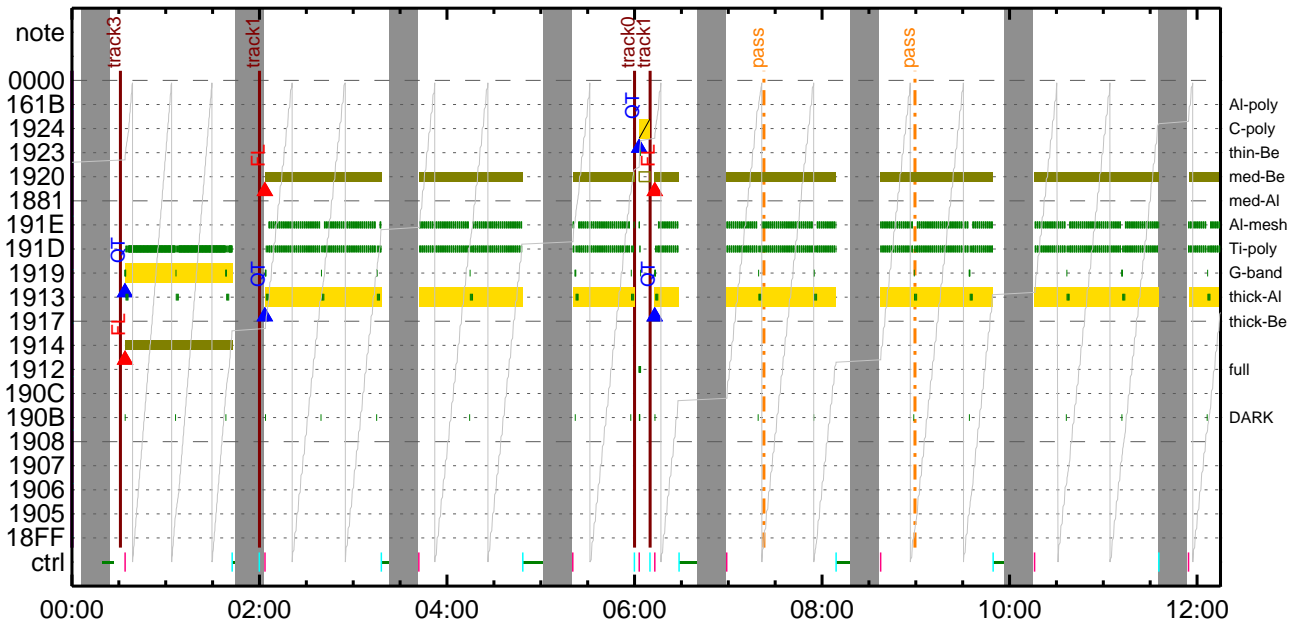
CMDI #0859 2012/08/21



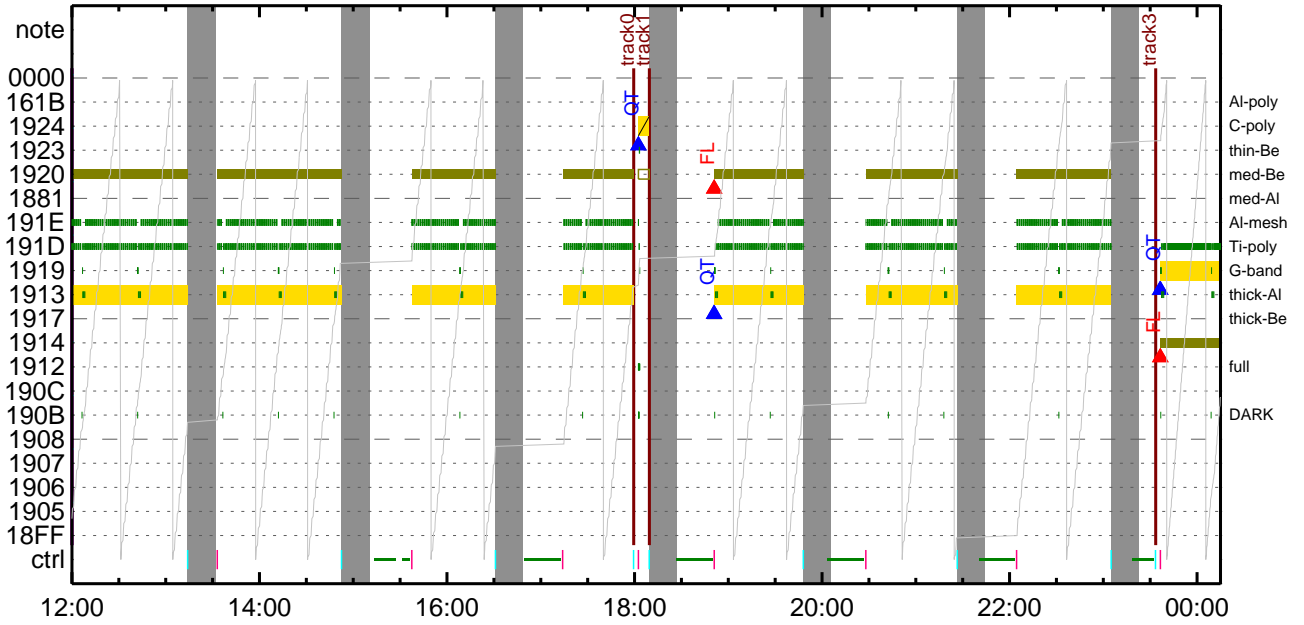
CMDI #0859 2012/08/21



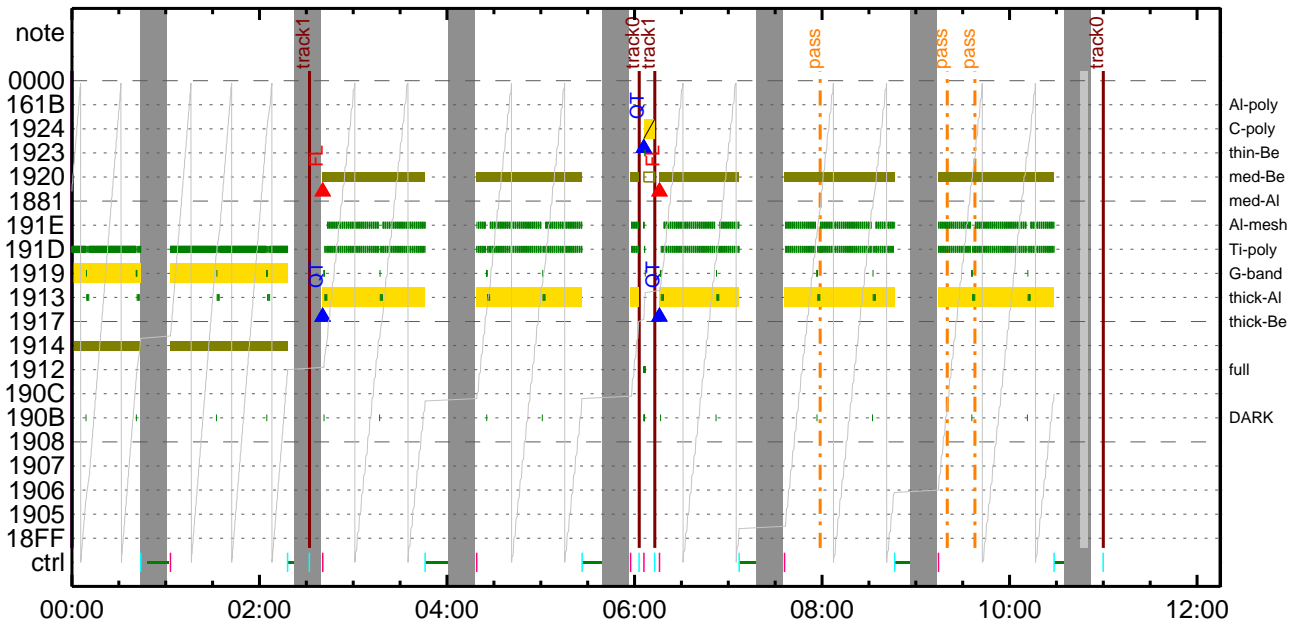
CMDI #0859 2012/08/22



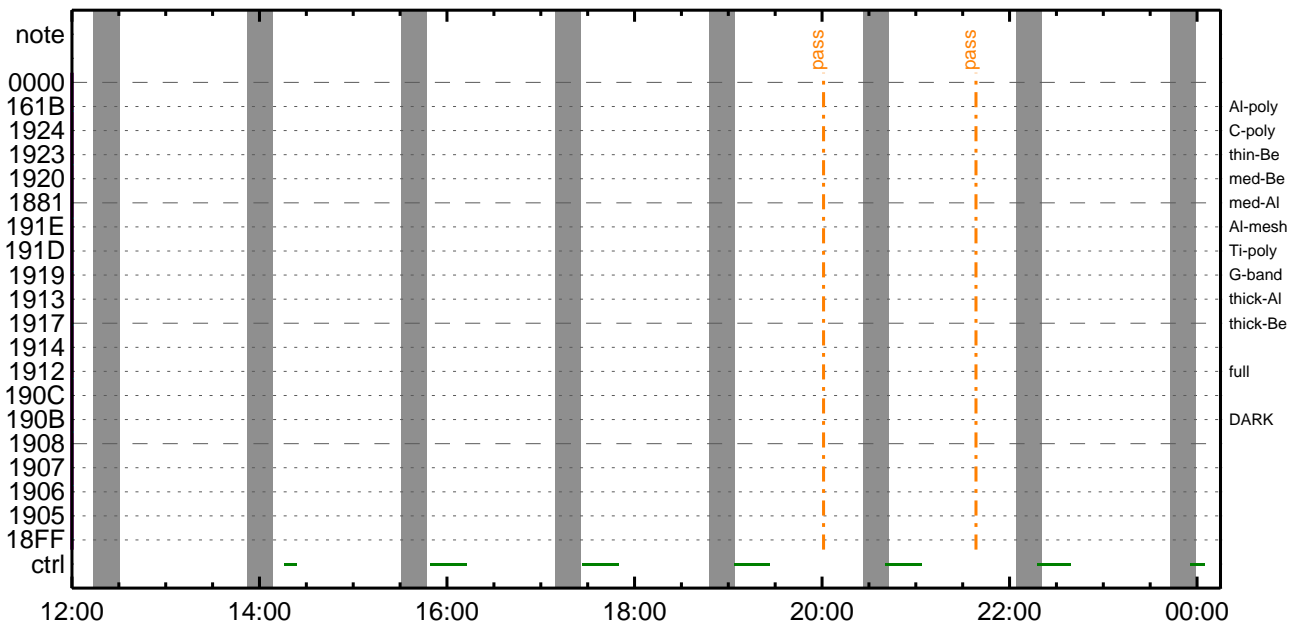
CMDI #0859 2012/08/22



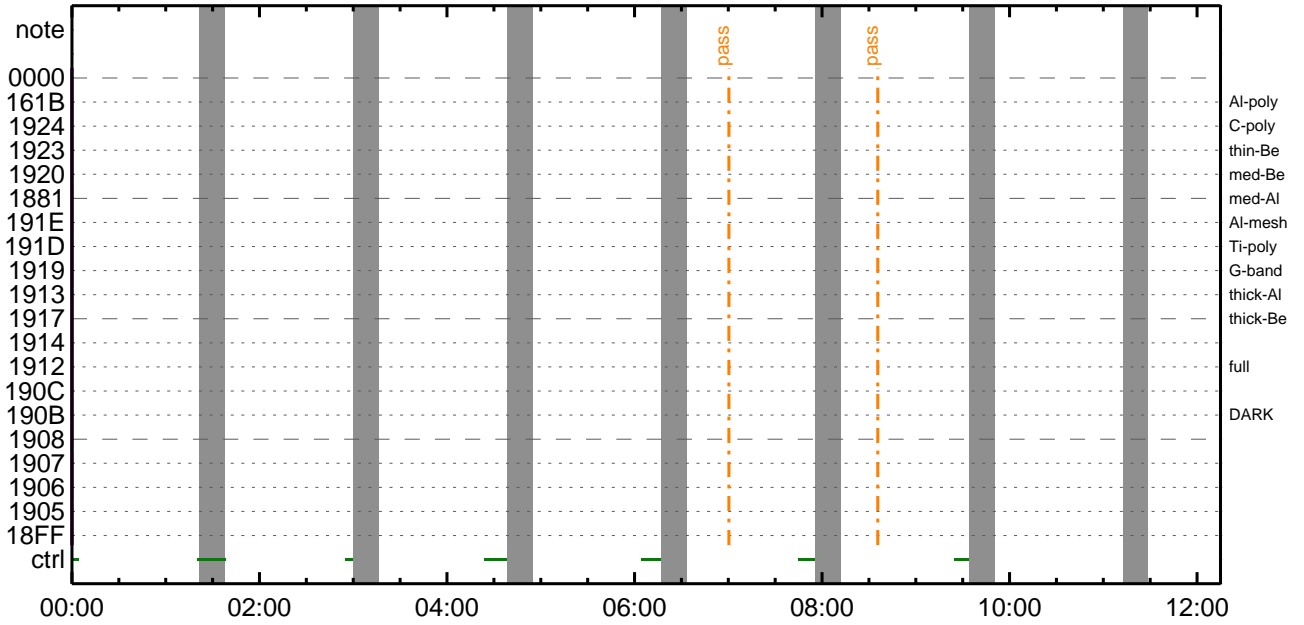
CMDI #0859 2012/08/23



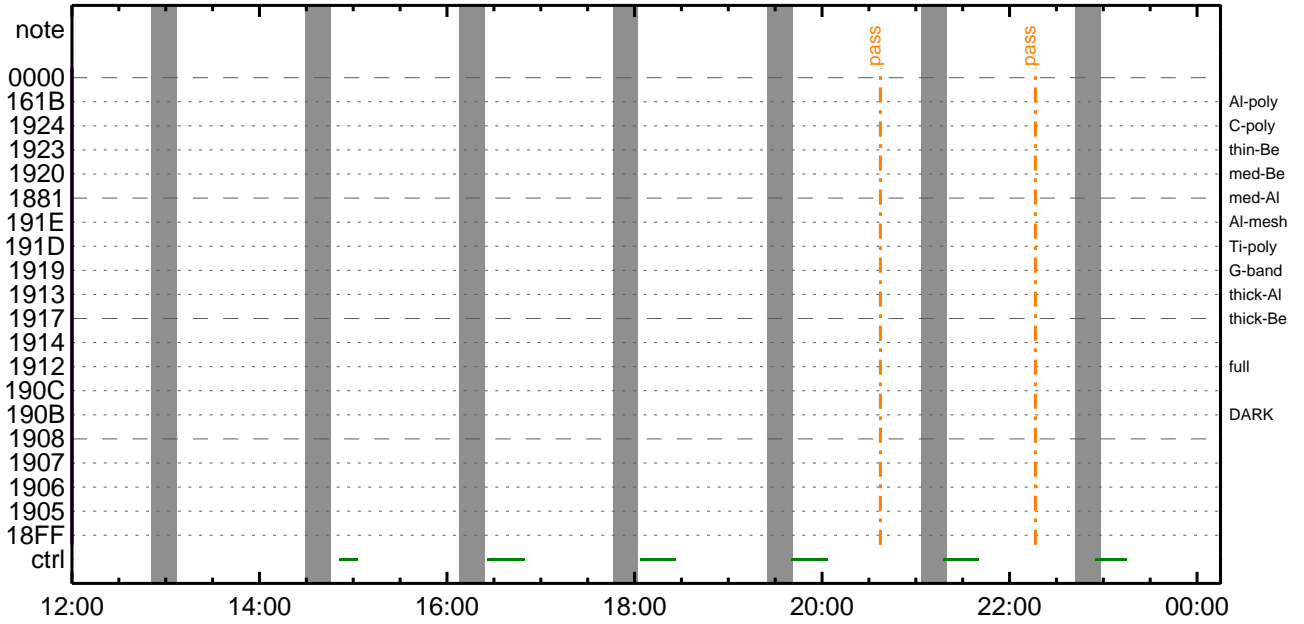
CMDI #0859 2012/08/23



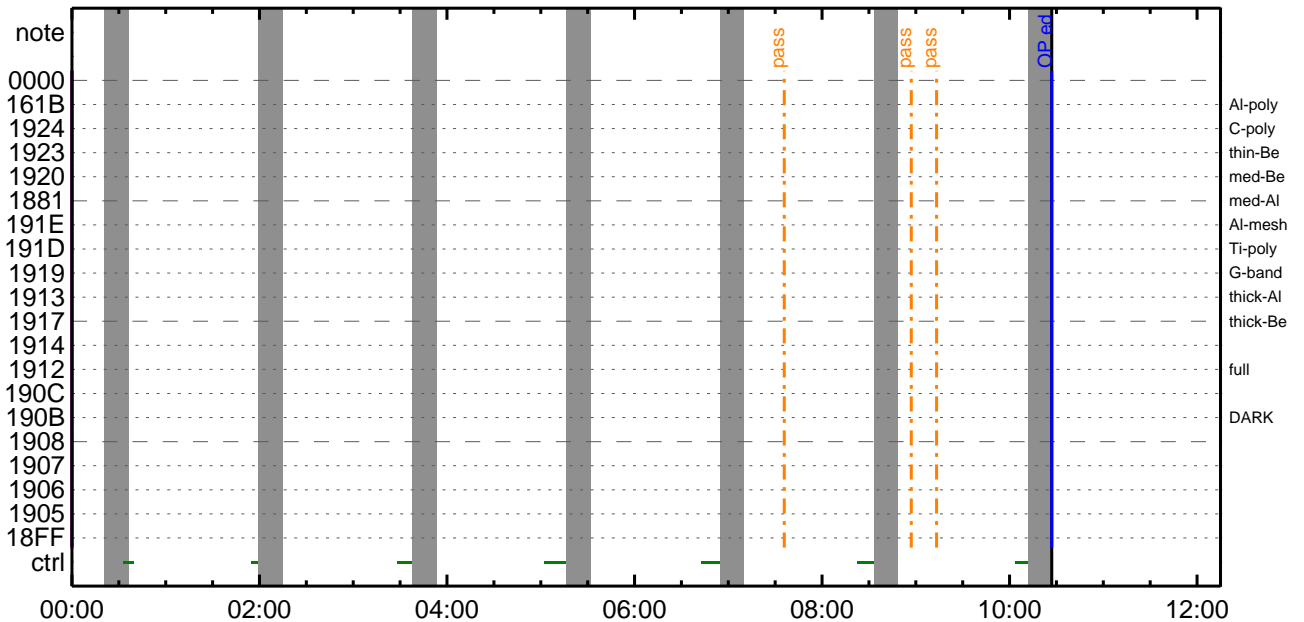
CMDI #0859 2012/08/24



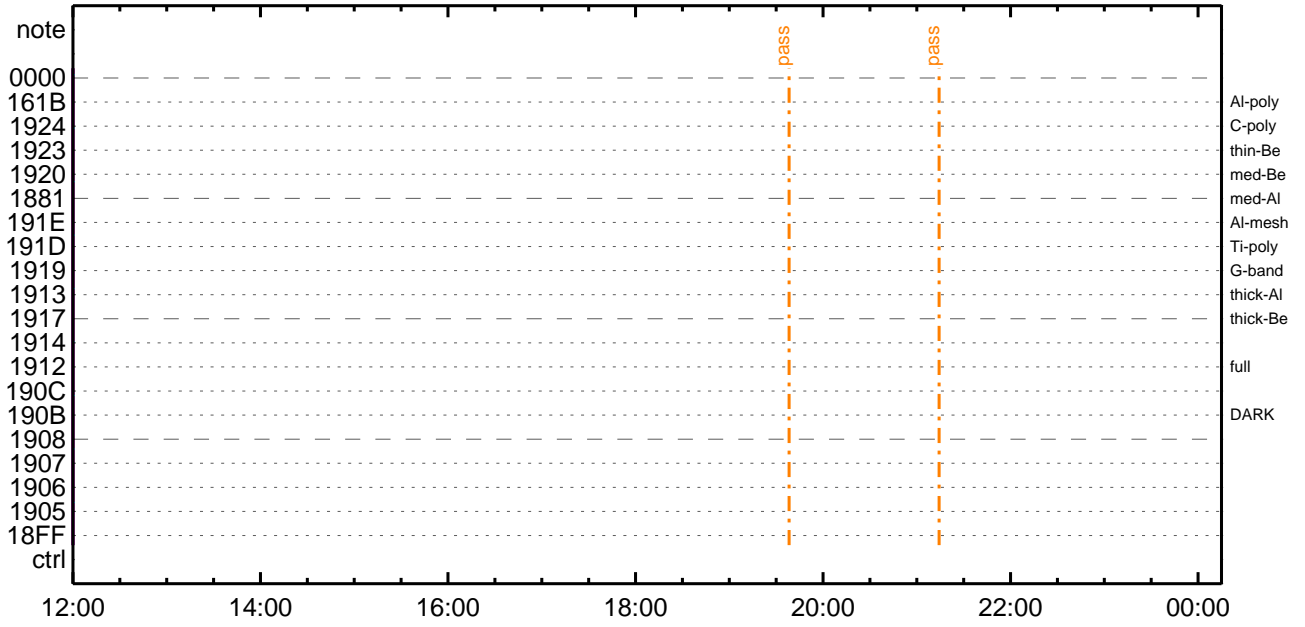
CMDI #0859 2012/08/24



CMDI #0859 2012/08/25



CMDI #0859 2012/08/25




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0096 C.                0300; SET0EDUMP0I00iYNY10C100|030E;E
0097 C.
0098 C. TIY3YBY0YH000ADi (UT)
0099 +. TI 2012-08-21 10:03:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2012-08-21 10:03:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2012-08-21 10:03:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2012-08-21 10:07:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. 0E2%0IAE%iIN0IYAYSYAY-1aiU
0116 C.                00[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                00[HK1_TI_CMD_NUM]                EQ        4
0118 C.                00[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                00[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 C. *****
0122 C. TI0I0eYAY0Yx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;S 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                00[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                00[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                00[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                00[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]                EQ        7
0136 C.                00[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                00[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                00[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 C. YAY0Yx%aI»003IC$
0142 C.                00[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 C. RAM ID=TI_TBL0I%E1C.e2IOK0003IC$
0145 C.
0146 C. DHUYa;Y%E;E%Y%,Yi;Y%E;E00Ia01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]                EQ        2
0150 C.                00[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                00[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2012-08-21 10:07:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2012-08-21 10:07:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2012-08-21 10:07:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2012-08-21 10:07:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP `uAI0I»0Y0EAD010EDCBC.x2e *****
0192 C. (%a0IY0YAYEYBPEYAYC%e0E%0004A»U010e)
0193 C. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_DIS
0113 BC (d9)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0115 BC (c9)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_QT_PROG_SET
0119 BC (c4 11)
0120 + DC 07-F0 MDP_XRT_FL_PROG_SET
0121 BC (c5 10)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C.
0125 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0126 C.
0127 +. DC 07-F0 MDP_XRT_MODE_OBSV
0128 BC (c2)
0129 +. TI 2012-08-21 10:07:02.0
0130 DC 07-F0 MDP_XRT_MODE_OBSV
0131 BC (c2)
0132 . C. ----- Success Verify ? OK / NG ____
0133 C.
0134 C. ***** XRT END *****
0135 C.
0136 . C. ***** MDP `uAÎaî»ô%YnÊAÐa¹nêDCBC•x²è *****
0137 C. (%a°iYÓYÁYÈYÏYËYáYçYèaE%¼aa¼A»Ûa¹aè)
0138 . S. DC-BC dcbc-402:DCBC
0139 (MDP_known_event)
0140 C.
0141 C.
0142 . C. ***** YDÿ¹.Ï Daily±;îÑnÊ´Øa¹nêDCBC•x²è *****
0143 . S. DC-BC dcbc-153:DCBC
0144 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0145 C.
0146 C.
0147 . C. ;ãLOS¥Á¥S¥Ã¥´¼A»Û;ã
0148 C.
0149 . C. ***** LOS *****
0150 C.

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*** OP Sequence for XRT ***

2012/08/21	10:17:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	10:18:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/21	10:20:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/21	10:20:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/21	10:20:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/21	10:20:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/21	10:20:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/21	10:20:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	10:20:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/21	10:20:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/21	10:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	10:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	10:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	10:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	11:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	11:16:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/21	11:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	12:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	12:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	12:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	12:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	12:55:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/21	12:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	14:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	14:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	14:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	14:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	14:49:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/21	14:50:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	15:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	15:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	15:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	15:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	16:36:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/21	16:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	17:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	17:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/21	17:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/21	17:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/21	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/21	18:12:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/21	18:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/08/21	18:13:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/21	18:13:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/21	18:13:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/21	18:15:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/08/21	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/21	18:22:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							

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2012/08/21	18:23:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	01 00 00 00 00
2012/08/21	18:25:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/08/21	18:25:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/08/21	18:25:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/08/21	18:25:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/08/21	18:25:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/08/21	18:25:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/21	18:25:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/08/21	18:25:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/08/21	18:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/21	19:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/21	19:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/21	19:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/21	19:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/21	19:50:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/21	19:51:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/21	20:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/21	20:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/21	20:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/21	20:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/21	21:27:30.0	XRT_Custom_434_OG [0x1b2]				
2012/08/21	21:28:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/21	22:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/21	22:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/21	22:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/21	22:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/21	23:02:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/21	23:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/21	23:59:54.0	XRT_CTRL_MANU_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/22	00:31:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	03 00 00 00 00
2012/08/22	00:33:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/08/22	00:33:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/08/22	00:33:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/08/22	00:33:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/08/22	00:33:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/08/22	00:33:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/22	00:33:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/08/22	00:33:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 09
2012/08/22	00:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/22	01:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/22	01:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/22	01:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/22	01:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/22	01:59:54.0	XRT_CTRL_MANU_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/22	02:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	01 00 00 00 00
2012/08/22	02:02:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/08/22	02:03:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/08/22	02:03:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]				

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2012/08/22	02:03:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/08/22	02:03:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/08/22	02:03:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/22	02:03:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2012/08/22	02:03:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2012/08/22	02:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/22	03:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/22	03:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/22	03:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/22	03:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/22	03:41:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	03:42:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/22	04:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/22	04:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/22	04:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/22	04:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/22	05:19:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	05:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/22	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/22	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2012/08/22	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00 00 00
2012/08/22	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/08/22	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/08/22	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/08/22	06:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2012/08/22	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/22	06:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/22	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00	00 00 00
2012/08/22	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2012/08/22	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/08/22	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/08/22	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/08/22	06:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/08/22	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/22	06:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2012/08/22	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2012/08/22	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/22	06:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/22	06:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/22	06:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/22	06:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/22	06:58:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	06:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/08/22	08:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/08/22	08:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/08/22	08:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/08/22	08:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/08/22	08:36:30.0	XRT_Custom_434_OG [0x1b2]						

2012/08/22	08:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	09:49:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	09:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/22	09:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/22	09:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/22	10:15:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	10:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	11:35:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	11:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/22	11:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/22	11:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/22	11:53:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	11:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	13:14:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	13:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/22	13:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/22	13:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/22	13:32:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	13:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	14:52:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	14:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/22	14:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/22	14:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/22	15:36:30.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	15:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	16:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/22	16:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/22	16:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/22	17:13:00.0	XRT_Custom_434_OG [0x1b2]						
2012/08/22	17:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	17:59:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/08/22	17:59:30.0	AOCS_Orе-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2012/08/22	17:59:46.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/08/22	17:59:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/08/22	17:59:50.0	XRT_ARS_DIS_406_OG [0x196]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/22	18:02:28.0	XRT_QT_PROG_SET_446_OG [0x1be]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2012/08/22	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/22	18:09:24.0	XRT_CTRL_MANU_416_OG [0x1a0]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/22	18:09:30.0	AOCS_Orе-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2012/08/22	18:50:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/08/22	18:50:46.0	XRT_FLD_ENA_428_OG [0x1ac]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/22	18:50:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/22	18:50:50.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/22	18:50:52.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/22	18:50:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/22	18:50:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2012/08/22	18:50:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			

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2012/08/22	18:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/22	19:48:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/22	19:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/22	19:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/22	19:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/22	20:27:00.0	XRT_Custom_434_OG [0x1b2]								
2012/08/22	20:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/22	21:26:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/22	21:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/22	21:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/22	21:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/22	22:03:30.0	XRT_Custom_434_OG [0x1b2]								
2012/08/22	22:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/22	23:05:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/22	23:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/22	23:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/22	23:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/22	23:33:24.0	XRT_CTRL_MANU_447_OG [0x1bf]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/22	23:33:30.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	03 00 00 00 00					
2012/08/22	23:35:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/08/22	23:36:16.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/08/22	23:36:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/08/22	23:36:20.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/08/22	23:36:22.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/08/22	23:36:24.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/22	23:36:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c					
2012/08/22	23:36:28.0	XRT_FL_PROG_SET_431_OG [0x1af]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09					
2012/08/22	23:36:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/23	00:44:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/23	00:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/23	00:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/23	00:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/23	01:02:01.0	XRT_Custom_434_OG [0x1b2]								
2012/08/23	01:03:01.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/08/23	02:18:00.5	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/23	02:18:03.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/23	02:18:05.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/08/23	02:21:15.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/08/23	02:31:54.0	XRT_CTRL_MANU_417_OG [0x1a1]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/08/23	02:32:00.0	AOCS_OrE-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	01 00 00 00 00					
2012/08/23	02:39:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/08/23	02:40:16.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/08/23	02:40:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/08/23	02:40:20.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/08/23	02:40:22.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/08/23	02:40:24.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/08/23	02:40:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b					
2012/08/23	02:40:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					

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2012/08/23	02:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	03:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	03:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	03:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	03:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	04:18:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/23	04:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	05:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	05:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	05:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	05:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	05:56:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/23	05:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	06:02:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/23	06:03:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/08/23	06:03:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	06:03:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	06:03:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	06:05:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/08/23	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	06:12:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	06:13:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/23	06:15:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/23	06:15:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/23	06:15:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/23	06:15:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/23	06:15:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	06:15:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	06:15:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/23	06:15:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/23	06:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	07:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	07:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	07:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	07:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	07:35:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/23	07:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	08:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	08:46:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	08:46:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	08:49:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	09:13:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/23	09:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	10:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	10:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	10:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	10:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							

2012/08/23	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00