

XRT Timeline to be uploaded on 2012/08/23

Period: 2012/08/23 10:50:00 - 2012/08/28 10:40:00

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Normal mode

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XOB #1925: Plasma at Short Wavelength - Med-Be-12s - no AEC												
Term	Pointing (x, y)		Comment									
08/23 11:03:00 - 08/23 11:15:54	Fixed (936.0, 0.0)	# OP start + 10min, HOP 130, EIS full-disk slot scan									
08/23 11:19:00 - 08/23 11:31:54	Fixed (468.0, 0.0)										
08/23 11:41:50 - 08/23 11:47:54	Fixed (0.0, 0.0)										
08/23 11:51:00 - 08/23 12:03:54	Fixed (-	468.0, 0.0)										
08/23 12:33:00 - 08/23 12:45:54	Fixed (-	936.0, 0.0)										
08/23 12:49:00 - 08/23 12:55:54	Fixed (815.0, 470.0)										
08/23 12:59:00 - 08/23 13:11:54	Fixed (411.0, 847.0)										
08/23 13:15:00 - 08/23 13:27:54	Fixed (0.0, 941.0)										
08/23 13:31:00 - 08/23 13:43:54	Fixed (-	411.0, 847.0)										
08/23 14:33:00 - 08/23 14:39:54	Fixed (-	815.0, 470.0)										
08/23 14:43:00 - 08/23 14:49:54	Fixed (815.0, -470.0)										
08/23 14:53:00 - 08/23 15:05:54	Fixed (411.0, -847.0)										
08/23 15:09:00 - 08/23 15:21:54	Fixed (0.0, -941.0)										
08/23 16:22:00 - 08/23 16:34:54	Fixed (-	411.0, -847.0)										
08/23 16:38:00 - 08/23 16:43:54	Fixed (-	815.0, -471.0)	* HOP 130, final pointing.									
PROG= 02 Inf.-time(s)												
└ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 39 1-time(s) 2.0sec												
	med-Be/Open	med-Be/Open	close	Safe	Norm	11.3s	Obs	8x8	2048x2048	(1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 47 1-time(s) 2.0sec												
	med-Be/Open	med-Be/Open	close	Safe	Dark	11.3s	Obs	8x8	2048x2048	(1024, 1024)	DPCM	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1915: HOP130-Synoptic 3 Filter- 2x2 Q95 (Almesh 12/723ms; Tipoly 24/1443ms; Althick 65536ms)												
Term	Pointing (x, y)		Comment									
08/23 11:35:00 - 08/23 11:41:20	Fixed (0.0, 0.0)										
PROG= 20 2-time(s)												
└ Subr= 1 1-time(s) 180.0sec												
└─ Seqn= 11 1-time(s) 25.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 12 1-time(s) 25.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1913: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band Test												
Term	Pointing (x, y)		Comment									
08/23 16:47:00 - 08/23 17:09:00	Track (-202.2, 189.6)	* Track AR 11548									
08/23 18:19:00 - 08/23 22:04:30	Track (-189.3, 189.2)	# AR 11548									
08/24 03:37:30 - 08/24 04:24:00	Track (-115.1, 187.5)	* AR 11548									
08/24 06:34:30 - 08/24 16:08:00	Track (-91.2, 187.1)	# Cont.									
08/24 21:41:30 - 08/25 05:58:24	Track (38.0, 186.4)	* Track AR 11548.									
08/25 06:11:30 - 08/25 10:03:30	Track (118.5, 187.1)	# Cont.									
PROG= 11 Inf.-time(s)												
└ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 2-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0 0 2.0sec
└ Subr= 2 2-time(s) 2.0sec												
└─ Seqn= 18 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384	(1064, 1048)	Q=98	0 0 2.0sec
└─ Seqn= 9 4-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3 0 2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3 0 2.0sec
└─ Seqn= 10 25-time(s) 80.0sec												
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3 0 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3 0 2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3 1 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3 1 2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3 2 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3 2 2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3 3 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3 3 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1924: Synoptic Q95 2x2 - Al/mesh(64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(128/1443) + Thin-Be(30)

Term	Pointing (x, y)	Comment
08/23 18:09:00 - 08/23 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min
08/24 05:40:00 - 08/24 05:46:54	Fixed (0.0, 0.0)	synoptic, shifted -23.0 min
08/24 17:39:30 - 08/24 17:46:24	Fixed (0.0, 0.0)	synoptic, shifted -23.5 min
08/25 06:01:30 - 08/25 06:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min
PROG= 18 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 41 1-time(s) 4.0sec		
Open/Al-mesh Open/Al-mesh close	Safe Norm 63ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 43 1-time(s) 4.0sec		
Open/Ti-poly Open/Ti-poly close	Safe Norm 125ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close	Safe Norm 1.41s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 42 1-time(s) 2.0sec		
thin-Be/Open thin-Be/Open close	Safe Norm 354ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 12ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

Term	Pointing (x, y)	Comment
08/23 23:03:00 - 08/24 02:54:30	Track (301.0, 139.4) @ 08/23 23:00:00	* HOP 128, joint with Hida, sunspots.
PROG= 12 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 19 2-time(s) 2.0sec		
Open/G-band Open/G-band close	Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Subr= 2 2-time(s) 2.0sec		
└─ Seqn= 18 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band Open/G-band open	Safe Norm 44ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 9 4-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al Open/thick-Be close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 21 60-time(s) 30.0sec		
Open/Ti-poly Open/thick-Be close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly Open/thick-Be close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly Open/thick-Be close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly Open/thick-Be close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

Term	Pointing (x, y)	Comment
08/24 18:27:30 - 08/24 19:25:00	Fixed (-944.0, 0.0)	# EIS offlimb obs, joint with COMP, for calibration.
PROG= 03 Inf.-time(s)		
└─ Subr= 1 4-time(s) 480.0sec		
└─ Seqn= 48 1-time(s) 60.0sec		
C-poly/Open med-Be/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
└─ Seqn= 46 1-time(s) 60.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 4.00s	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
└─ Seqn= 45 1-time(s) 60.0sec		
med-Be/Open med-Be/Open close	Safe Norm 22.6s	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
└─ Subr= 2 1-time(s) 60.0sec		
└─ Seqn= 49 1-time(s) 30.0sec		
C-poly/Open C-poly/Open close	Safe Dark 1.00s	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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Flare mode

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Term	Pointing (x, y)	Comment
08/23 16:47:00 - 08/23 17:09:00	Track (-202.2, 189.6) @ 08/23 16:44:00	* Track AR 11548
08/23 18:19:00 - 08/23 22:04:30	Track (-189.3, 189.2) @ 08/23 18:16:00	# AR 11548
08/23 23:03:00 - 08/24 02:54:30	Track (301.0, 139.4) @ 08/23 23:00:00	* HOP 128, joint with Hida, sunspots.
08/24 03:37:30 - 08/24 04:24:00	Track (-115.1, 187.5) @ 08/24 03:00:00	* AR 11548
08/24 06:34:30 - 08/24 16:08:00	Track (-91.2, 187.1) @ 08/24 05:47:00	# Cont.
08/24 18:27:30 - 08/24 19:25:00	Fixed (-944.0, 0.0)	# EIS offlimb obs, joint with COMP, for calibration.
08/24 21:41:30 - 08/25 05:58:24	Track (38.0, 186.4) @ 08/24 20:46:30	* Track AR 11548.
08/25 06:11:30 - 08/25 10:03:30	Track (118.5, 187.1) @ 08/25 06:08:30	# Cont.
PROG= 16 15-time(s)		
└─ Subr= 1 45-time(s) 10.0sec		

└─ Seqn= 35		1-time(s)	2.0sec														
└─ thin-Be/Open		med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)				Q=95	3	0	2.0sec	
└─ Subr= 2		1-time(s)	10.0sec														
└─ Seqn= 36		1-time(s)	2.0sec														
└─ med-Al/Open		med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)				Q=95	3	0	2.0sec	
└─ Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)				Q=95	3	0	2.0sec	
└─ Seqn= 37		1-time(s)	2.0sec														
└─ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)				Q=95	2	0	2.0sec	
└─ Seqn= 38		1-time(s)	2.0sec														
└─ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)				Q=98	0	0	2.0sec	
└─ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)				Q=98	0	0	2.0sec	
└─ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)				Q=98	0	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)				Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

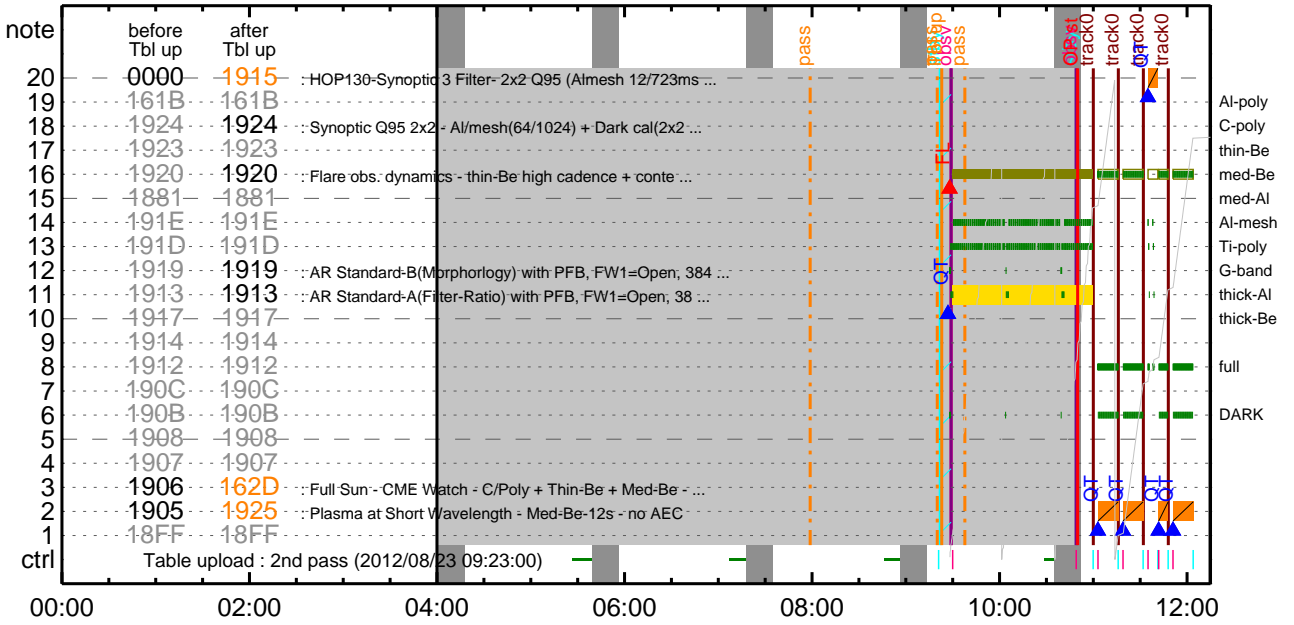
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Flare Detection

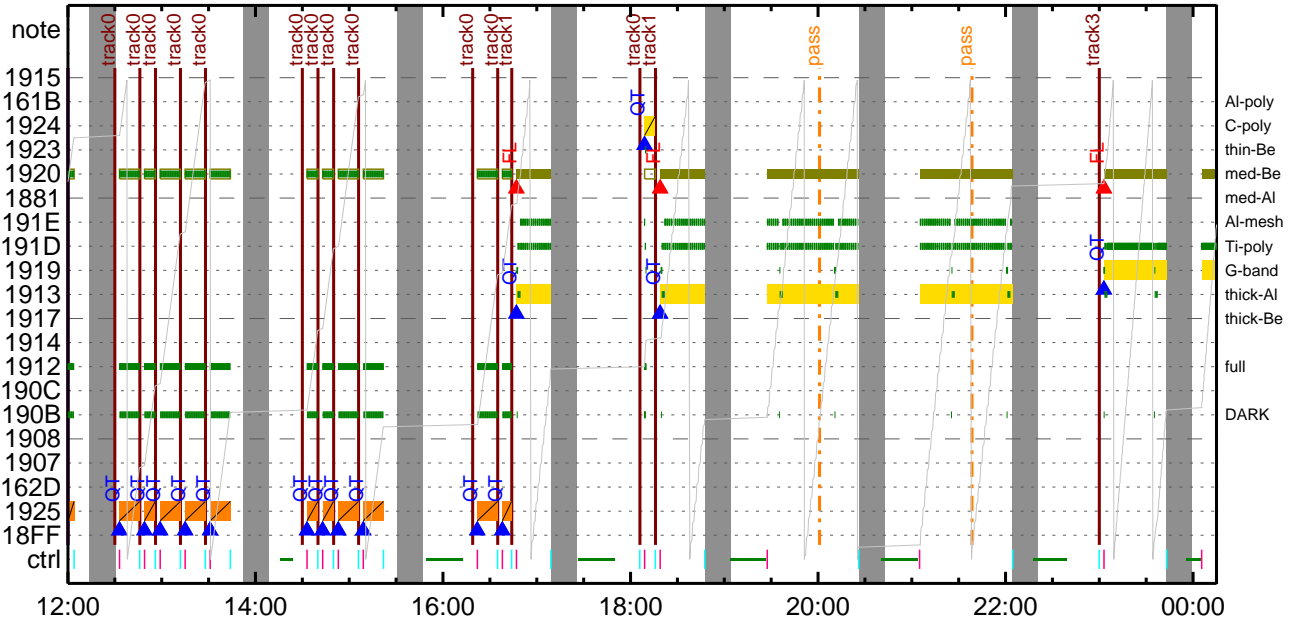
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FLD Patrol												
Term		Pointing (x, y)							Comment			
08/23 16:46:46 - 08/23 18:06:16		Track (-202.2, 189.6) ^{Ⓢ 08/23 16:44:00}							* Track AR 11548			
08/23 18:18:46 - 08/24 05:37:16		Track (-189.3, 189.2) ^{Ⓢ 08/23 18:16:00}							# AR 11548			
08/24 06:34:16 - 08/24 17:36:46		Track (-91.2, 187.1) ^{Ⓢ 08/24 05:47:00}							# Cont.			
08/24 18:27:16 - 08/25 05:58:46		Fixed (-944.0, 0.0)							# EIS offlimb obs, joint with COMP, for calibration.			
08/25 06:11:16 - 08/28 10:40:00		Track (118.5, 187.1) ^{Ⓢ 08/25 06:08:30}							# Cont.			
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

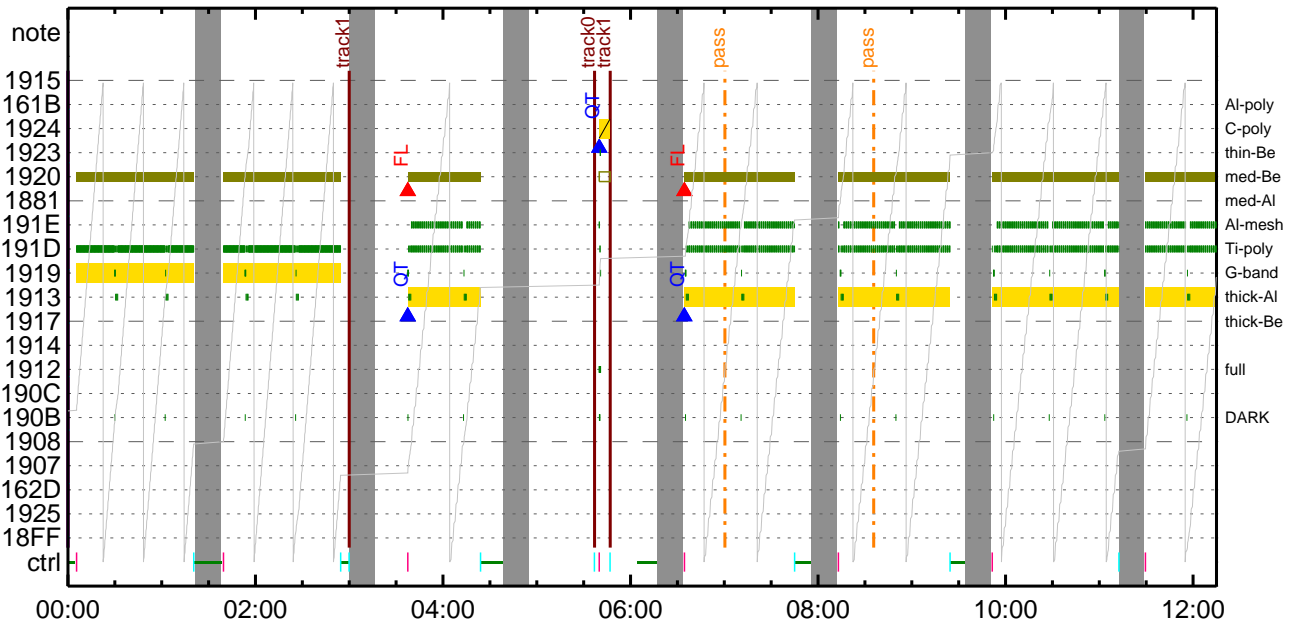
CMDI #0863 2012/08/23



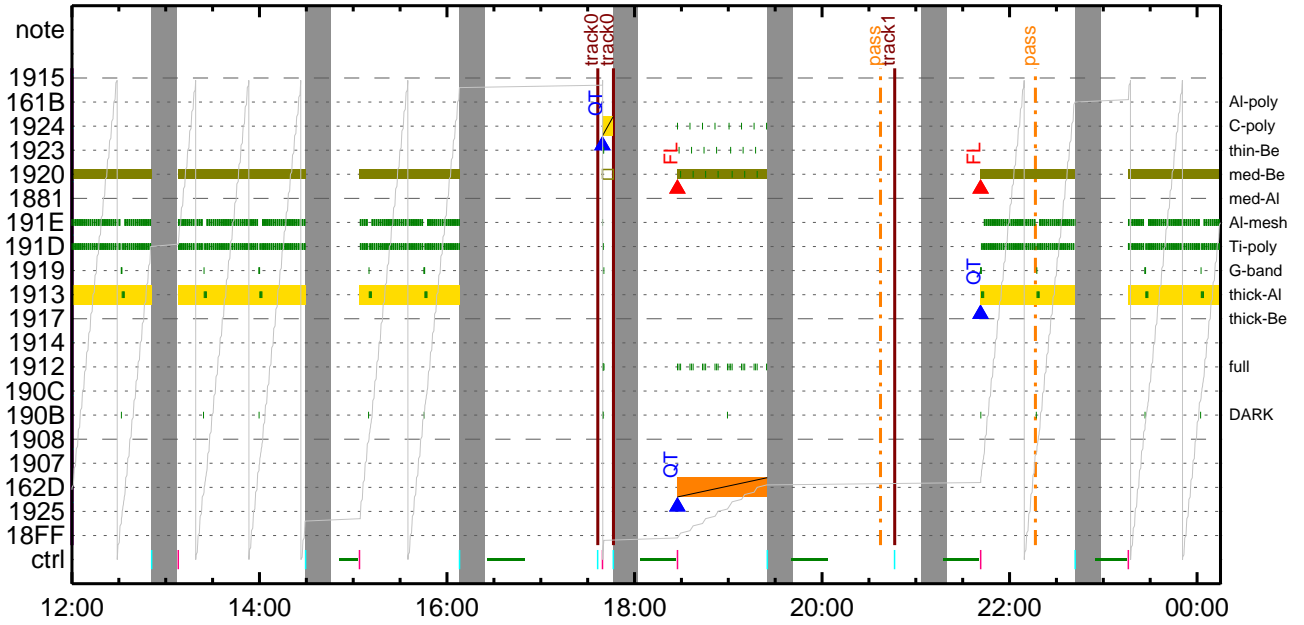
CMDI #0863 2012/08/23



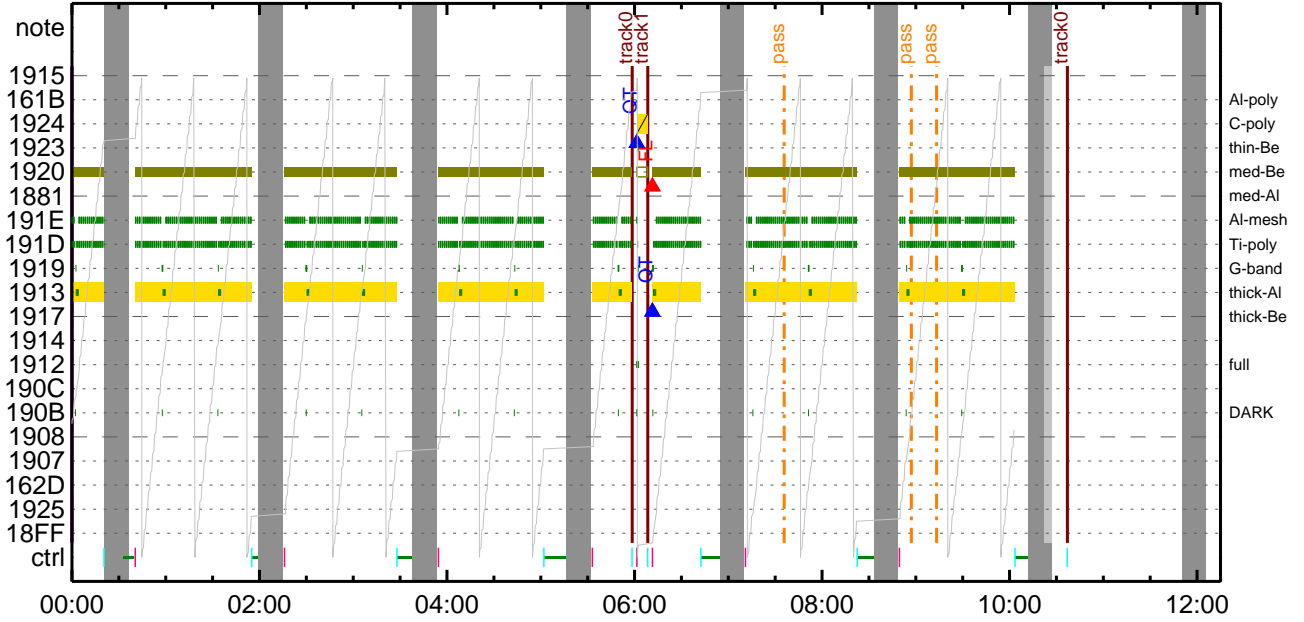
CMDI #0863 2012/08/24



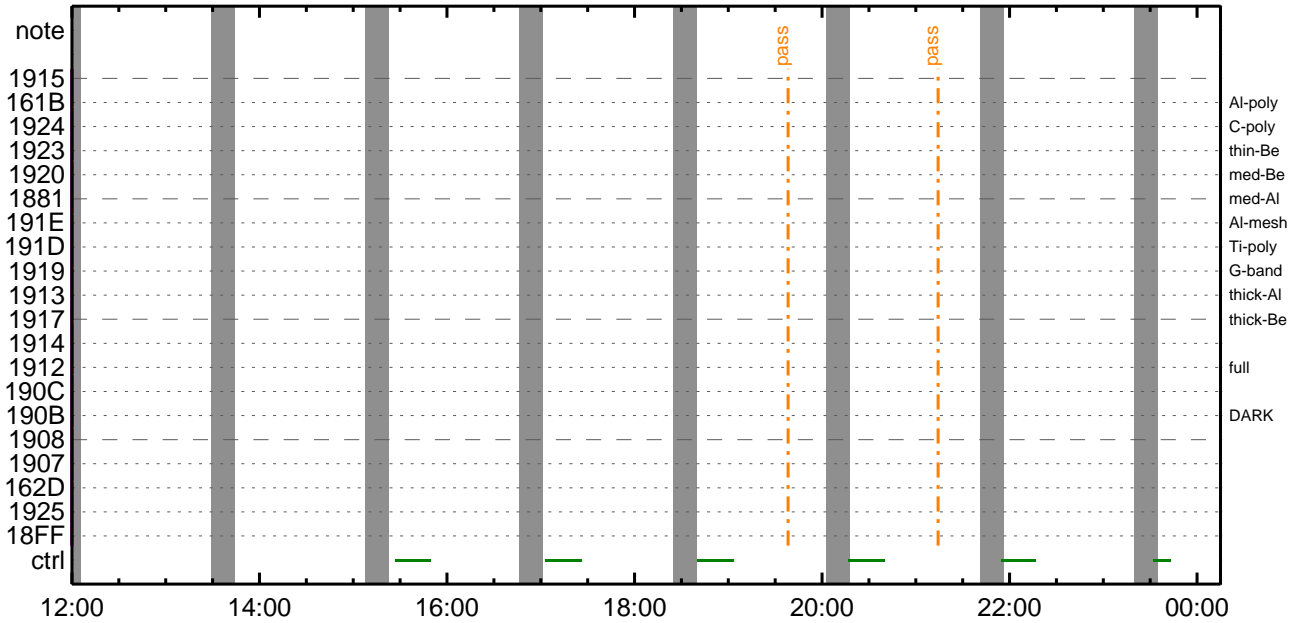
CMDI #0863 2012/08/24



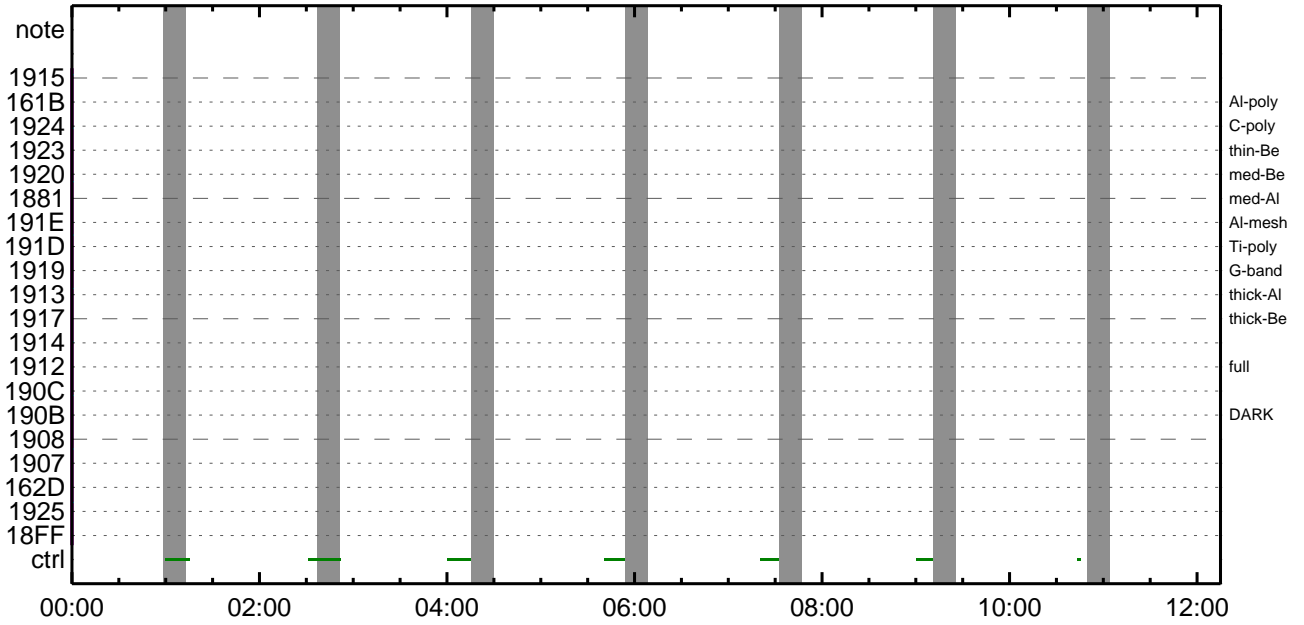
CMDI #0863 2012/08/25



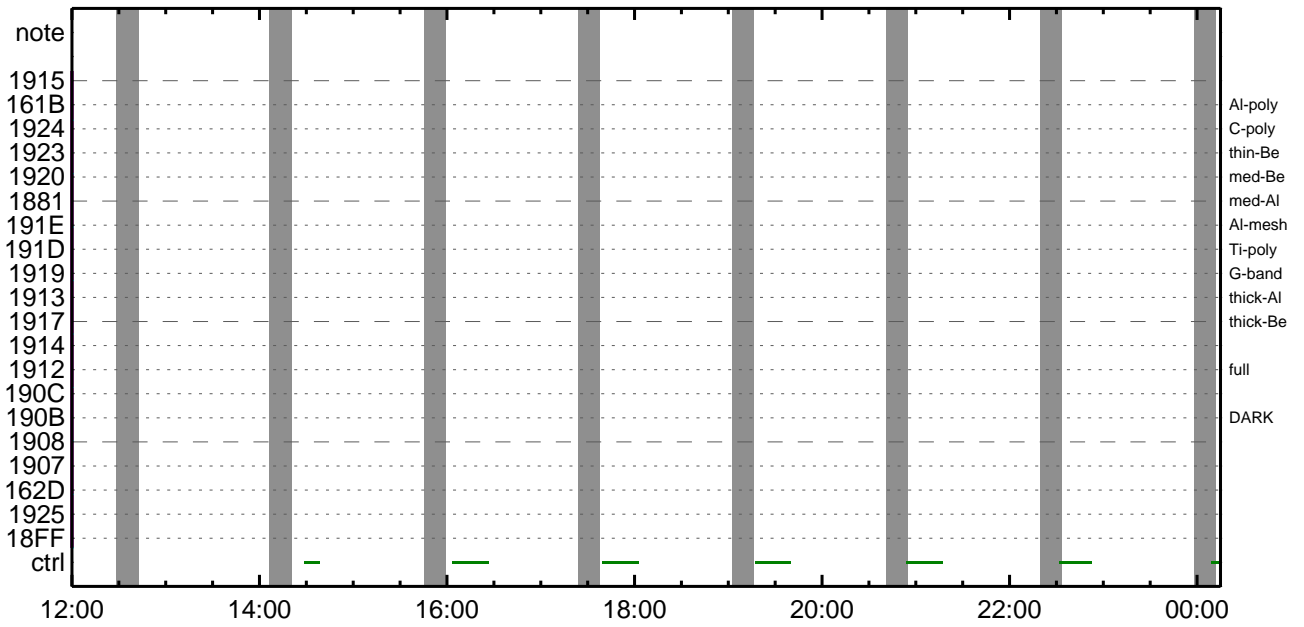
CMDI #0863 2012/08/25



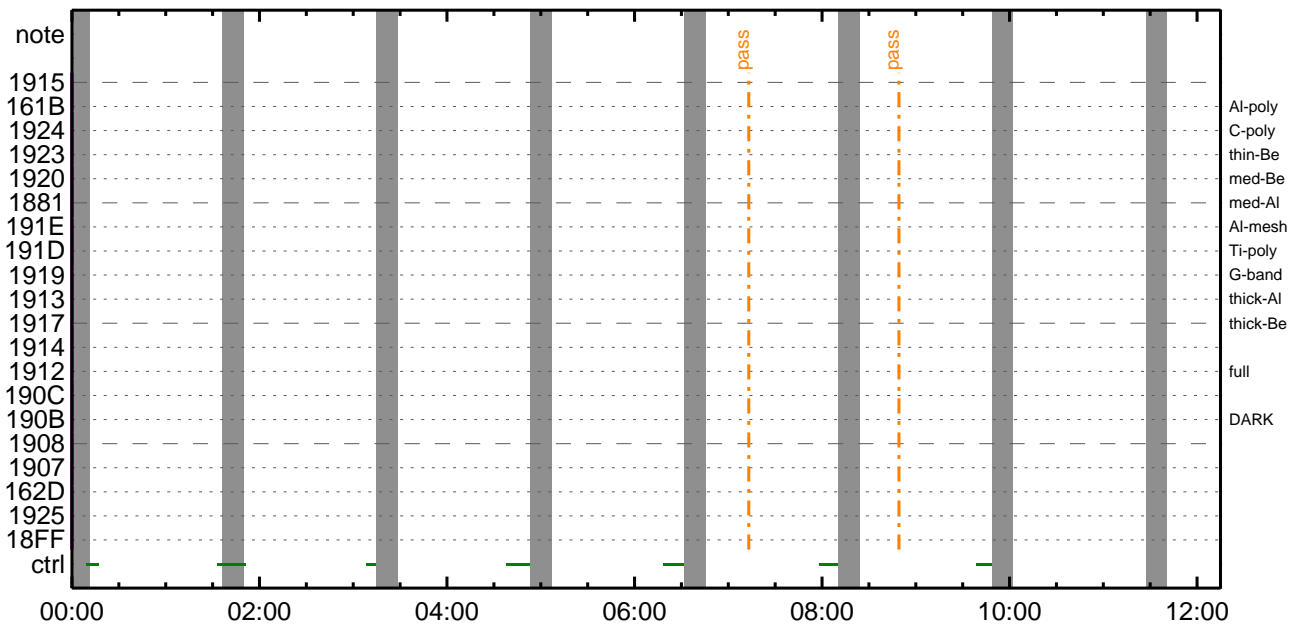
CMDI #0863 2012/08/26



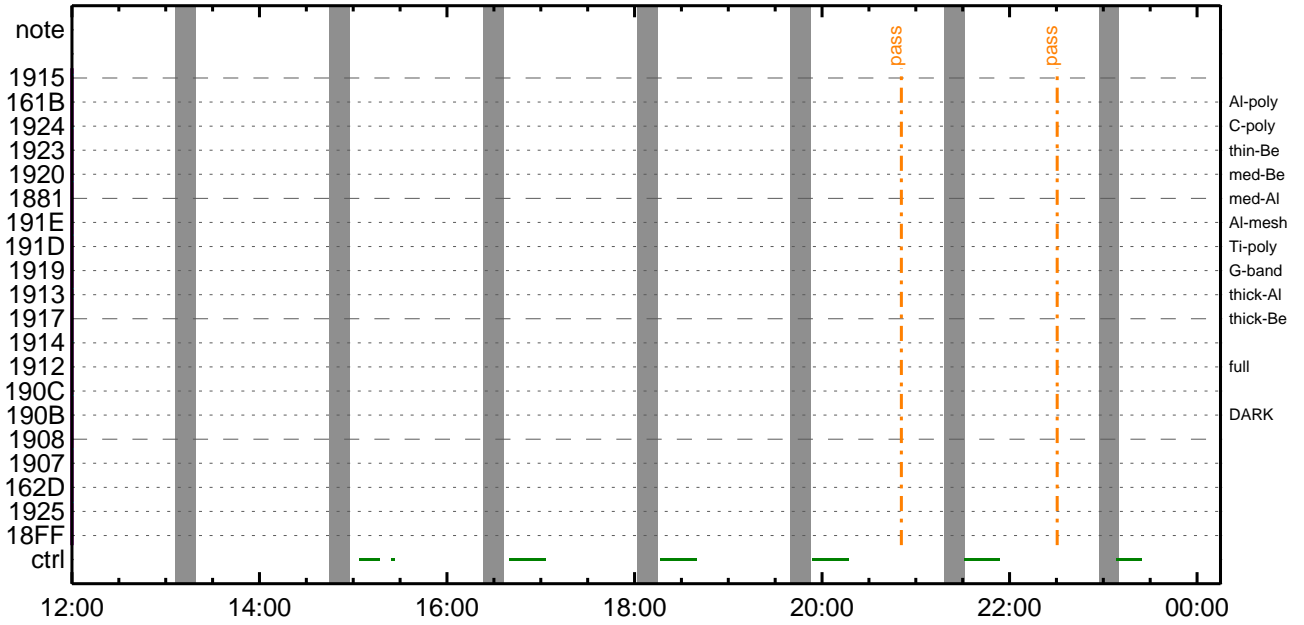
CMDI #0863 2012/08/26



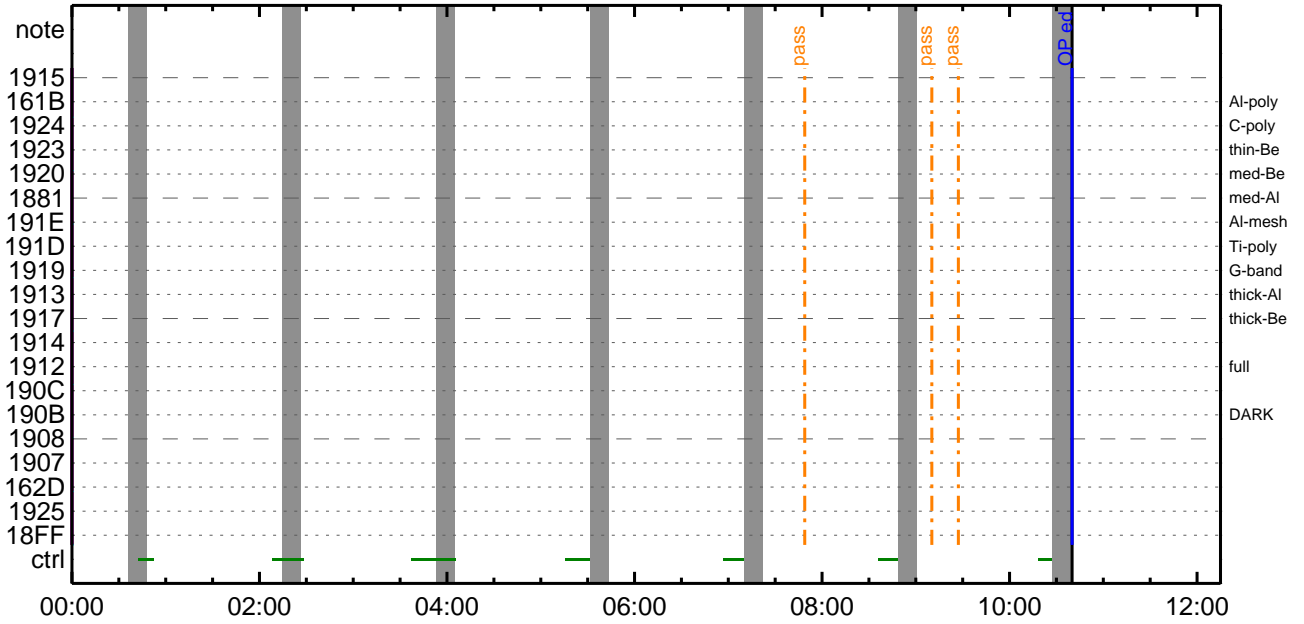
CMDI #0863 2012/08/27



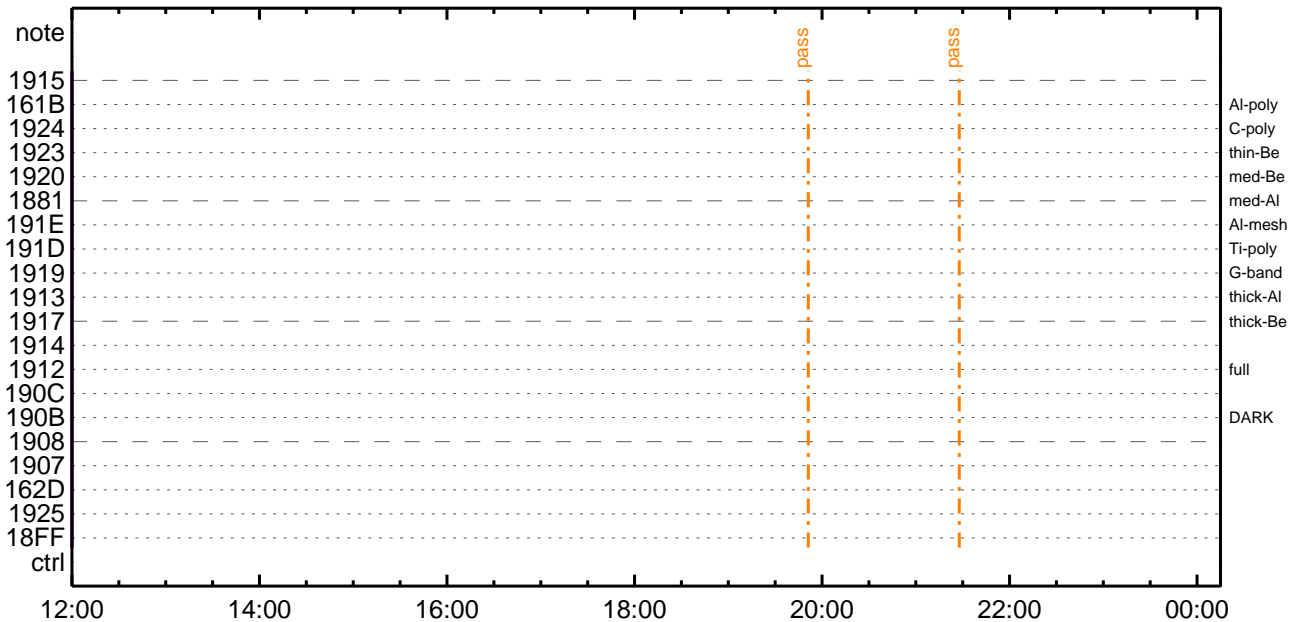
CMDI #0863 2012/08/27



CMDI #0863 2012/08/28



CMDI #0863 2012/08/28




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0194 C.
0195 +. TI 2012-08-23 10:49:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αİÄē%İİñαİŷÄŷ§ŷÄŷ-¹àİŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. Tİİİ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                      EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªİ»αò³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαİŷÈ¹ç•è²İOKαò³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷİ;¼ŷÈ;Èαòİāα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                      EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-08-23 10:49:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-08-23 10:49:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-08-23 10:49:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-08-23 10:49:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃİαİ»ö¼ŷ¼αÈÄα¹αèDCBC•x²è *****
0276 C. (¼ª°İŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼αα¼Ä»Űα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñαÈ'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-099 2012-08-23 12:32:48 154 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È±¿±Á±•µ°È»Í×ÁÇ±Í¥çYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;È±È¼°ÇÔ±•±¿¼í¹ç±Í;çÁ®, ù±¹±±±±±±ÇÁ+¿®±•±È±±±±±±È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C.
0046 C. ***** XRT START *****
0047 C.
0048 +. DC 07-F0 MDP_XRT_CTRL_MANU
0049 BC (c1)
0050 + DC 07-F0 MDP_XRT_MODE_STBY
0051 BC (c3)
0052 . C. ----- Success Verify ? OK / NG_____
0053 C.
0054 C. XRT Obs. Table Upload
0055 . S. RAM ram-291:MDP_OBS_X
0056 ( )
0057 C.
0058 +. DC 07-F0 MDP_DUMP_XRTTBL
0059 BC (84 07 00 00 00 3a d4)
0060 . C. ----- Comparison Check ? OK / ERR _____
0061 C.
0062 C.
0063 +. DC 07-F0 MDP_XRT_ROI_SET
0064 BC (cd 01 b1 b1 04 04)
0065 + DC 07-F0 MDP_XRT_ROI_SET
0066 BC (cd 02 b1 b1 08 08)
0067 + DC 07-F0 MDP_XRT_ROI_SET
0068 BC (cd 03 b1 b1 08 08)
0069 + DC 07-F0 MDP_XRT_ROI_SET
0070 BC (cd 04 b1 b1 06 06)
0071 + DC 07-F0 MDP_XRT_ROI_SET
0072 BC (cd 05 85 83 06 06)
0073 + DC 07-F0 MDP_XRT_ROI_SET
0074 BC (cd 06 85 83 06 06)
0075 + DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 07 80 80 20 20)
0077 + DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 08 80 80 20 08)
0079 + DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 09 80 80 08 20)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 0f 80 80 06 06)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 10 80 80 08 08)
0085 + DC 07-F0 MDP_XRT_FLD_ENA
0086 BC (d8)
0087 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0088 BC (c8)
0089 +. DC 07-F0 MDP_XRT_AEC_RESET
0090 BC (d0)
0091 + DC 07-F0 MDP_XRT_ARS_DIS
0092 BC (d5)
0093 +. DC 07-F0 MDP_XRT_FLD_RESET
0094 BC (da)
0095 + DC 07-F0 MDP_XRT_QT_PROG_SET
```


0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU []
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ()
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG []
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2012-08-23 10:49:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP []
0128 C. -----
0129 C.
0130 C.
0131 . C. ***** MDP 'úÄîáî»ö¼ýðËÄð¹ãèDCBC·x²è *****
0132 C. (¼áºìºÛÝÄÝÈÝÞÝÈÝÁÝ¼ÝÈøÈ%¼ã¼Ä»Û¹ãè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ÝÐÝ¹·Ï Daily±¿ÎÑøÈ´Ø¹øèDCBC·x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOSÝÄÝ\$ÝÄÝ¬¼Ä»Û;ä
0143 C.
0144 . C. ***** LOS *****
0145 C.

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*** OP Sequence for XRT ***

2012/08/23	10:59:54.0	XRT_CTRL_MANU_418_OG [0x1a2]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	11:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00	ac	cd		
2012/08/23	11:02:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2012/08/23	11:02:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	11:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	11:02:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	11:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2012/08/23	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	11:15:54.0	XRT_CTRL_MANU_418_OG [0x1a2]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	11:16:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00	d6	67		
2012/08/23	11:18:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2012/08/23	11:18:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	11:18:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	11:18:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	11:18:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2012/08/23	11:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	11:31:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	11:31:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2012/08/23	11:32:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00	00	00		
2012/08/23	11:32:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	11:32:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	11:32:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	11:34:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2012/08/23	11:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	11:41:20.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	11:41:22.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2012/08/23	11:41:42.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	11:41:44.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	11:41:46.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	11:41:48.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2012/08/23	11:41:50.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	11:47:54.0	XRT_CTRL_MANU_418_OG [0x1a2]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	11:48:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00	29	99		
2012/08/23	11:50:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2012/08/23	11:50:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	11:50:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	11:50:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	11:50:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2012/08/23	11:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	12:03:54.0	XRT_CTRL_MANU_422_OG [0x1a6]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	12:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00	53	33		
2012/08/23	12:32:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2012/08/23	12:32:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	12:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	12:32:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	12:32:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							

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2012/08/23	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/23	12:45:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/23	12:46:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d6 36 b7 8e	
2012/08/23	12:48:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/23	12:48:52.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/23	12:48:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/23	12:48:56.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/23	12:48:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2012/08/23	12:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/23	12:55:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/23	12:56:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 b4 b5 db 75	
2012/08/23	12:58:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/23	12:58:52.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/23	12:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/23	12:58:56.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/23	12:58:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2012/08/23	12:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/23	13:11:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/23	13:12:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 ac 5b 00 00	
2012/08/23	13:14:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/23	13:14:52.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/23	13:14:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/23	13:14:56.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/23	13:14:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2012/08/23	13:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/23	13:27:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/23	13:28:00.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 b4 b5 24 8b	
2012/08/23	13:30:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/23	13:30:52.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/23	13:30:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/23	13:30:56.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/23	13:30:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2012/08/23	13:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/23	13:43:54.0	XRT_CTRL_MANU_425_OG [0x1a9]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/23	14:30:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 d6 36 48 72	
2012/08/23	14:32:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/23	14:32:52.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/23	14:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/08/23	14:32:56.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/08/23	14:32:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2012/08/23	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/08/23	14:39:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/08/23	14:40:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 29 ca b7 8e	
2012/08/23	14:42:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/08/23	14:42:52.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/08/23	14:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	

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2012/08/23	14:42:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	14:42:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2012/08/23	14:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	14:49:54.0	XRT_CTRL_MANU_418_OG [0x1a2]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	14:50:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 4b 4b db 75				
2012/08/23	14:52:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/23	14:52:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	14:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	14:52:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	14:52:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2012/08/23	14:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	15:05:54.0	XRT_CTRL_MANU_418_OG [0x1a2]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	15:06:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2012/08/23	15:08:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/23	15:08:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	15:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	15:08:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	15:08:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2012/08/23	15:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	15:21:54.0	XRT_CTRL_MANU_426_OG [0x1aa]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	16:19:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2012/08/23	16:21:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/23	16:21:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	16:21:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	16:21:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	16:21:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2012/08/23	16:22:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	16:34:54.0	XRT_CTRL_MANU_418_OG [0x1a2]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	16:35:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2012/08/23	16:37:32.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/23	16:37:52.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	16:37:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	16:37:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	16:37:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2012/08/23	16:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	16:43:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	16:44:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/23	16:46:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/23	16:46:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/23	16:46:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/23	16:46:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/23	16:46:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	16:46:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	16:46:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/23	16:46:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/23	16:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	17:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2012/08/23	17:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	17:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	17:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/08/23	18:06:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2012/08/23	18:06:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/23	18:06:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/23	18:06:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	18:08:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2012/08/23	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	18:15:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	18:16:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	01	00	00	00	00
2012/08/23	18:18:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/08/23	18:18:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/23	18:18:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/23	18:18:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/23	18:18:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	18:18:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	18:18:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2012/08/23	18:18:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/08/23	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	18:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	18:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	18:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	18:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	19:26:30.0	XRT_Custom_434_OG [0x1b2]								
2012/08/23	19:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	20:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	20:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	20:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	20:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	21:04:00.0	XRT_Custom_434_OG [0x1b2]								
2012/08/23	21:05:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/23	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	22:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	22:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/23	22:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/23	22:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/23	23:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00	00	00	00
2012/08/23	23:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/08/23	23:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/23	23:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/23	23:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/23	23:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/23	23:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/23	23:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			

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2012/08/23	23:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10					
2012/08/23	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/23	23:43:00.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/23	23:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/23	23:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/23	23:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/24	00:04:31.0	XRT_Custom_434_OG [0x1b2]									
2012/08/24	00:05:31.0	XRT_CTRL_AUTO_413_OG [0x19d]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/24	01:20:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/24	01:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/24	01:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/24	01:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/24	01:38:30.0	XRT_Custom_434_OG [0x1b2]									
2012/08/24	01:39:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/24	02:54:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/24	02:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/24	02:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/24	02:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/24	02:59:54.0	XRT_CTRL_MANU_435_OG [0x1b3]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/24	03:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]									
		AOCU_NM	5	02-76	01	00 00 00 00					
2012/08/24	03:36:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00					
2012/08/24	03:37:16.0	XRT_FLD_ENA_428_OG [0x1ac]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2012/08/24	03:37:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2012/08/24	03:37:20.0	XRT_AEC_RESET_423_OG [0x1a7]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2012/08/24	03:37:22.0	XRT_ARS_DIS_438_OG [0x1b6]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2012/08/24	03:37:24.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/24	03:37:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b					
2012/08/24	03:37:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10					
2012/08/24	03:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/24	04:24:00.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/24	04:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/08/24	04:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/08/24	04:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/08/24	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/24	05:36:56.0	XRT_FOCUS_POSITION_403_OG [0x193]									
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00					
2012/08/24	05:37:00.0	AOCS_ORe-point_Start_3_OG [0x099]									
		AOCU_NM	5	02-76	00	00 00 00 00					
2012/08/24	05:37:16.0	XRT_FLD_DIS_404_OG [0x194]									
		MDP_XRT_FLD_DIS	1	07-F0	d9						
2012/08/24	05:37:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]									
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2012/08/24	05:37:20.0	XRT_ARS_DIS_406_OG [0x196]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2012/08/24	05:39:58.0	XRT_QT_PROG_SET_446_OG [0x1be]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12					
2012/08/24	05:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/08/24	05:46:54.0	XRT_CTRL_MANU_407_OG [0x197]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/08/24	05:47:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]									
		AOCU_NM	5	02-76	01	00 00 00 00					
2012/08/24	06:33:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00					
2012/08/24	06:34:16.0	XRT_FLD_ENA_428_OG [0x1ac]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2012/08/24	06:34:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2012/08/24	06:34:20.0	XRT_AEC_RESET_423_OG [0x1a7]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2012/08/24	06:34:22.0	XRT_ARS_DIS_438_OG [0x1b6]									

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2012/08/24	06:34:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	06:34:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/08/24	06:34:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/08/24	06:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	07:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	07:45:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	07:45:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/24	07:48:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/24	08:12:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/24	08:13:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	09:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	09:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	09:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/24	09:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/24	09:50:30.0	XRT_Custom_434_OG [0x1b2]				
2012/08/24	09:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	11:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	11:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	11:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/24	11:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/24	11:28:30.0	XRT_Custom_434_OG [0x1b2]				
2012/08/24	11:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	12:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	12:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	12:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/24	12:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/24	13:07:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/24	13:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	14:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	14:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	14:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/24	14:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/24	15:03:00.0	XRT_Custom_434_OG [0x1b2]				
2012/08/24	15:04:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	16:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	16:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/08/24	16:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/08/24	16:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/08/24	17:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	17:36:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/08/24	17:36:30.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2012/08/24	17:36:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/08/24	17:36:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/08/24	17:36:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/08/24	17:39:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/08/24	17:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/08/24	17:46:24.0	XRT_CTRL_MANU_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/08/24	17:46:30.0	AOCS_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 00 00 53 e7
2012/08/24	18:26:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				

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2012/08/24	18:27:16.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2012/08/24	18:27:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/24	18:27:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/24	18:27:22.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/24	18:27:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/24	18:27:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2012/08/24	18:27:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/08/24	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/24	19:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/24	19:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/24	19:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/24	19:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/24	20:46:24.0	XRT_CTRL_MANU_436_OG [0x1b4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/24	20:46:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	01	00	00	00
2012/08/24	21:40:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/08/24	21:41:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/24	21:41:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/24	21:41:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/24	21:41:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/24	21:41:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/24	21:41:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2012/08/24	21:41:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/08/24	21:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/24	22:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/24	22:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/24	22:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/24	22:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/24	23:15:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/24	23:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/25	00:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/25	00:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/25	00:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/25	00:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/25	00:39:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/25	00:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/25	01:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/25	01:55:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/25	01:55:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/25	01:58:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/25	02:15:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/25	02:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/25	03:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/25	03:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/25	03:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/25	03:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/25	03:53:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/25	03:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/25	05:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2012/08/25	05:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/25	05:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/25	05:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/25	05:32:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/25	05:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/25	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/25	05:58:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/25	05:58:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/08/25	05:58:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/25	05:58:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/25	05:58:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/25	06:01:28.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/08/25	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/25	06:08:24.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/25	06:08:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/08/25	06:10:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/25	06:11:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/25	06:11:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/25	06:11:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/25	06:11:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/25	06:11:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/25	06:11:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/08/25	06:11:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/25	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/25	06:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/25	06:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/25	06:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/25	06:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/25	07:10:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/25	07:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/25	08:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/25	08:22:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/25	08:22:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/25	08:25:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/25	08:48:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/25	08:49:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/25	10:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/25	10:03:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/25	10:03:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/25	10:06:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/25	10:36:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/25	10:37:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				