

XRT Timeline to be uploaded on 2012/08/30

Period: 2012/08/30 10:17:00 - 2012/09/04 10:07:00

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Normal mode

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XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2												
Term	Pointing (x, y)	Comment										
08/30 10:30:00 - 08/30 13:59:54	Track (244.9, 315.0) ^{Ⓜ 08/30 10:27:00}	# OP start + 10min, HOP-213 filament observations.										
08/31 09:12:00 - 08/31 13:59:54	Track (411.2, 323.4) ^{Ⓜ 08/31 09:00:00}	# HOP-213, filament observations.										
08/31 17:03:00 - 08/31 20:34:30	Fixed (-945.0, 0.0)	# EIS cross-calibration with COMP/Hawaii.										
09/01 09:03:00 - 09/01 09:29:54	Track (567.8, 337.0) ^{Ⓜ 09/01 09:00:00}	# HOP-213, filament observations.										
PROG= 03 1-time(s)												
└─ Subr= 1 4-time(s) 480.0sec												
└─ Seqn= 48 1-time(s) 60.0sec												
	C-poly/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0 2.0sec
└─ Seqn= 46 1-time(s) 60.0sec												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0 2.0sec
└─ Seqn= 45 1-time(s) 60.0sec												
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0 2.0sec
└─ Subr= 2 1-time(s) 60.0sec												
└─ Seqn= 49 1-time(s) 30.0sec												
	C-poly/Open	C-poly/Open	close	Safe	Dark	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2												
Term	Pointing (x, y)	Comment										
08/30 14:15:00 - 08/30 15:59:54	Fixed (0.0, 945.0)	# Coalignment at North Pole.										
PROG= 06 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 16 24-time(s) 300.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec - 8 min cadence-wNGT-2												
Term	Pointing (x, y)	Comment										
08/30 16:15:00 - 08/30 17:59:54	Fixed (-945.0, 0.0)	# Coalignment at East limb.										
PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 17 15-time(s) 480.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1926: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/1443) + Thin-Be(250ms)												
Term	Pointing (x, y)	Comment										
08/30 18:03:00 - 08/30 18:09:54	Fixed (0.0, 0.0)	synoptic										
08/31 04:33:00 - 08/31 04:39:54	Fixed (0.0, 0.0)	synoptic, shifted an hour or so.										
08/31 16:53:00 - 08/31 16:59:54	Fixed (0.0, 0.0)	synoptic, shifted by an hour or so.										
09/01 04:03:00 - 09/01 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted an hour or so.										
PROG= 05 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 50 1-time(s) 4.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0 2.0sec
└─ Seqn= 51 1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 54 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1923: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 60s cad												
Term	Pointing (x, y)	Comment										
08/30 18:13:00 - 08/30 20:59:54	Fixed (865.0, 270.0)	# Observe AR 11548 at the limb for EIS.										
08/30 21:03:00 - 08/31 00:07:54	Track (-111.3, -26.6) ^{Ⓜ 08/30 21:00:00}	# Track AR 11555.										
08/31 02:03:00 - 08/31 04:29:54	Fixed (-892.0, -360.0)	# HOP 173 - prominence at east limb.										
08/31 04:43:00 - 08/31 08:50:00	Track (-41.0, -27.4) ^{Ⓜ 08/31 04:40:00}	# HOP-173, observe AR 11555.										

08/31 14:07:30 - 08/31 16:49:54 Track (44.9, -27.5) ^{Ⓢ 08/31 14:00:00} # Track AR 11555.
 08/31 21:08:30 - 09/01 03:59:54 Track (109.1, -26.9) ^{Ⓢ 08/31 21:00:00} # Track AR 11555. (HOP-173 from 2UT).
 09/01 04:13:00 - 09/01 08:59:54 Track (174.3, -25.8) ^{Ⓢ 09/01 04:10:00} # HOP-173, track AR 11555.

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 18 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 32 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 33 30-time(s) 60.0sec													
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1929: Fast Cadence Study on AR - 128FOV - thick Al

Term	Pointing (x, y)	Comment
08/31 00:08:24 - 08/31 01:59:54	Track (-111.3, -26.6) ^{Ⓢ 08/30 21:00:00}	# Track AR 11555.

PROG= 15 Inf.-time(s)

Subr= 1 50-time(s) 2.0sec													
└─ Seqn= 58 1-time(s) 4.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	128x128 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 57 1-time(s) 4.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	128x128 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 59 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
08/30 10:30:00 - 08/30 13:59:54	Track (244.9, 315.0) ^{Ⓢ 08/30 10:27:00}	# OP start + 10min, HOP-213 filament observations.
08/30 18:13:00 - 08/30 20:59:54	Fixed (865.0, 270.0)	# Observe AR 11548 at the limb for EIS.
08/30 21:03:00 - 08/31 00:07:54	Track (-111.3, -26.6) ^{Ⓢ 08/30 21:00:00}	# Track AR 11555.
08/31 02:03:00 - 08/31 04:29:54	Fixed (-892.0, -360.0)	# HOP 173 - prominence at east limb.
08/31 04:43:00 - 08/31 08:50:00	Track (-41.0, -27.4) ^{Ⓢ 08/31 04:40:00}	# HOP-173, observe AR 11555.
08/31 09:12:00 - 08/31 13:59:54	Track (411.2, 323.4) ^{Ⓢ 08/31 09:00:00}	# HOP-213, filament observations.
08/31 14:07:30 - 08/31 16:49:54	Track (44.9, -27.5) ^{Ⓢ 08/31 14:00:00}	# Track AR 11555.
08/31 17:03:00 - 08/31 20:34:30	Fixed (-945.0, 0.0)	# EIS cross-calibration with COMP/Hawaii.
08/31 21:08:30 - 09/01 01:59:54	Track (109.1, -26.9) ^{Ⓢ 08/31 21:00:00}	# Track AR 11555. (HOP-173 from 2UT).
09/01 02:03:00 - 09/01 03:59:54	Track (109.1, -26.9) ^{Ⓢ 08/31 21:00:00}	# Track AR 11555. (HOP-173 from 2UT).
09/01 04:13:00 - 09/01 08:59:54	Track (174.3, -25.8) ^{Ⓢ 09/01 04:10:00}	# HOP-173, track AR 11555.
09/01 09:03:00 - 09/01 09:29:54	Track (567.8, 337.0) ^{Ⓢ 09/01 09:00:00}	# HOP-213, filament observations.

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec													
└─ Seqn= 35 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec													
└─ Seqn= 36 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 37 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 38 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

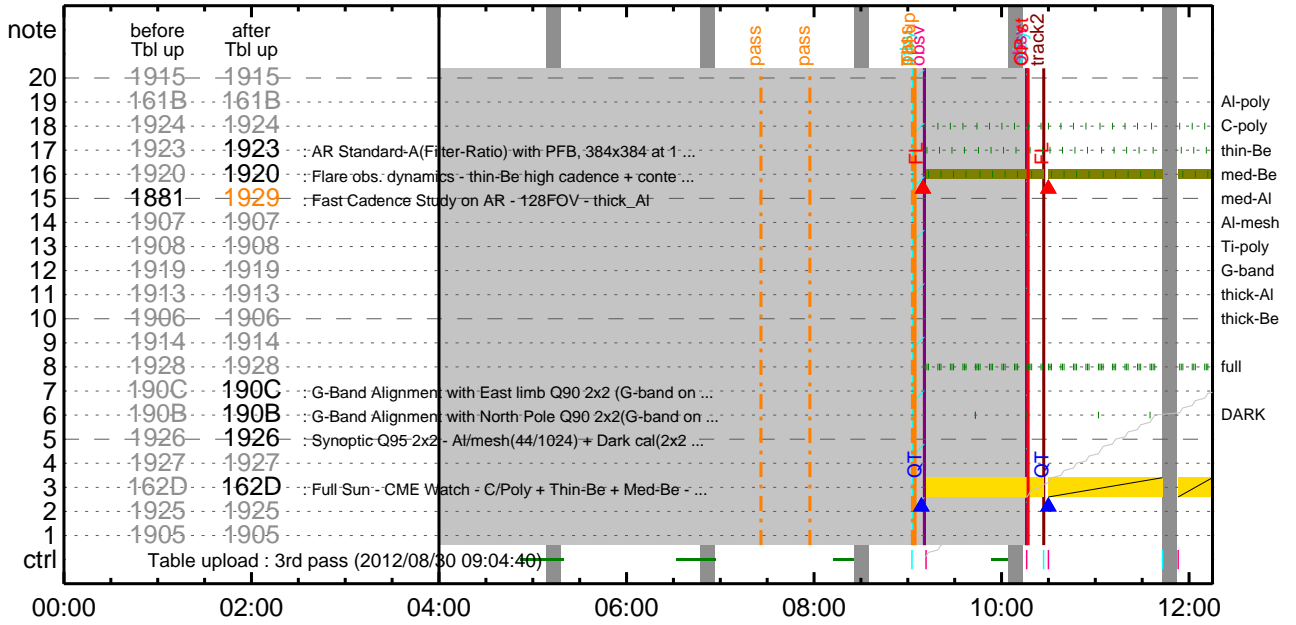
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Flare Detection

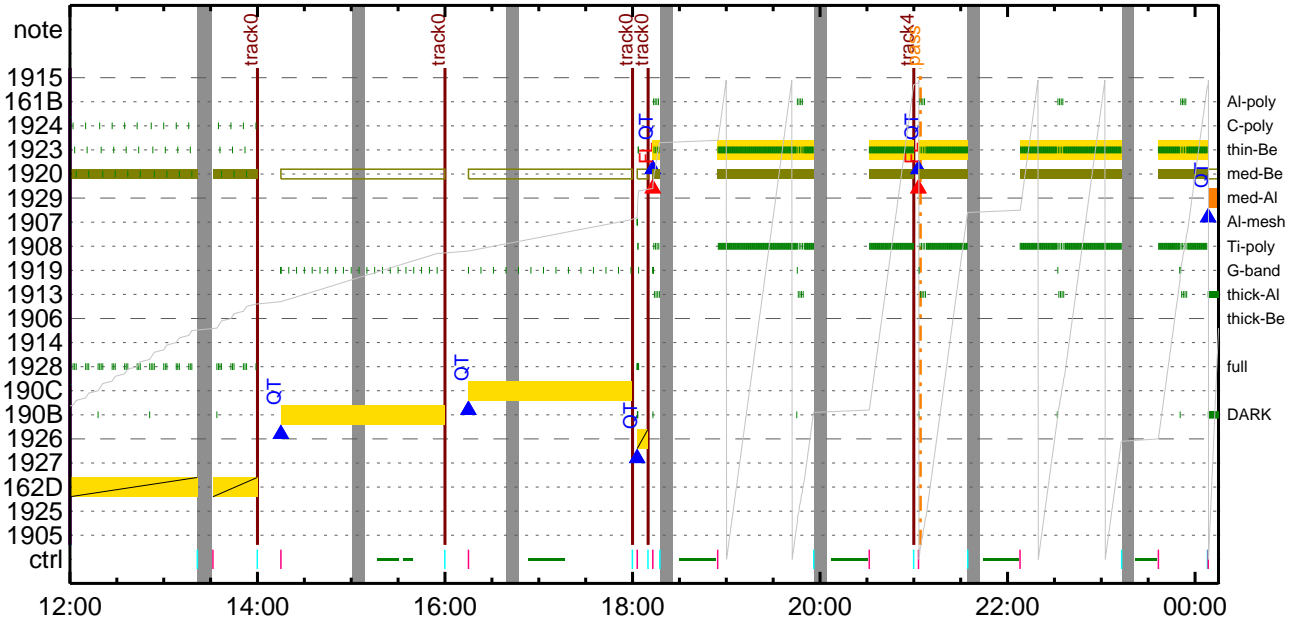
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FLD Patrol												
Term		Pointing (x, y)					Comment					
08/30 18:12:46 - 08/31 00:08:16		Fixed (865.0, 270.0)					# Observe AR 11548 at the limb for EIS.					
08/31 02:00:16 - 08/31 04:30:16		Fixed (-892.0, -360.0)					# HOP 173 - prominence at east limb.					
08/31 04:40:16 - 08/31 16:50:16		Track (-41.0, -27.4) @ 08/31 04:40:00					# HOP-173, observe AR 11555.					
08/31 17:02:46 - 09/01 04:00:16		Fixed (-945.0, 0.0)					# EIS cross-calibration with COMP/Hawaii.					
09/01 04:10:16 - 09/04 10:07:00		Track (174.3, -25.8) @ 09/01 04:10:00					# HOP-173, track AR 11555.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8				Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

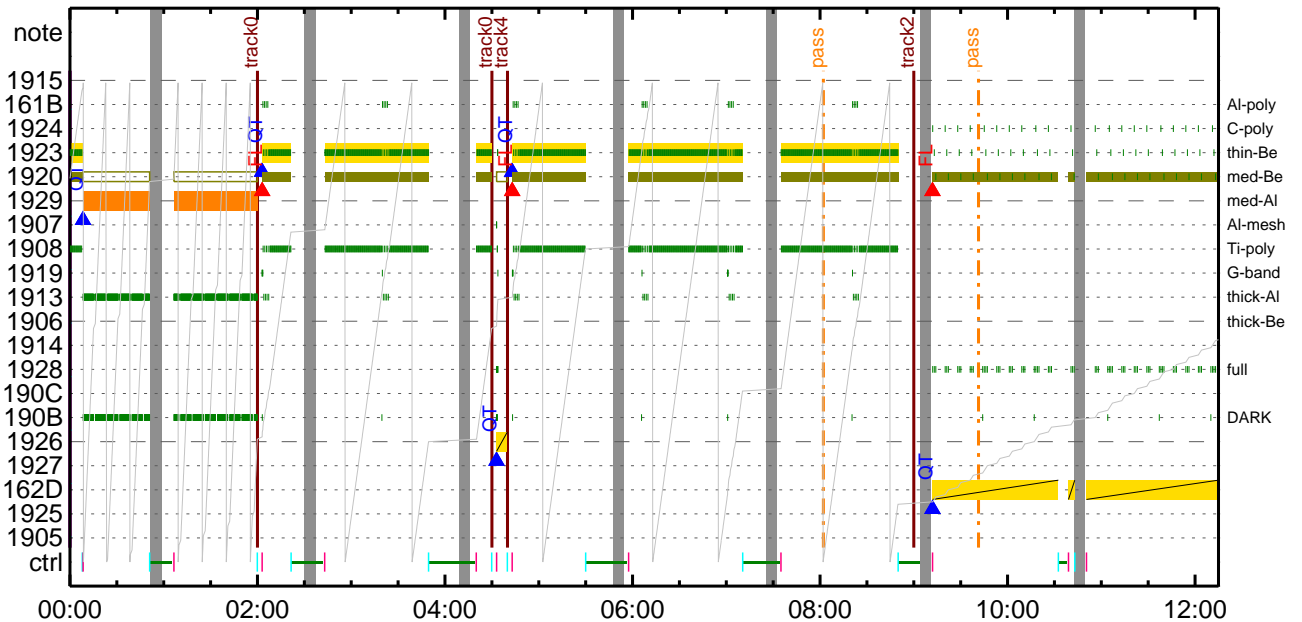
CMDI #0882 2012/08/30



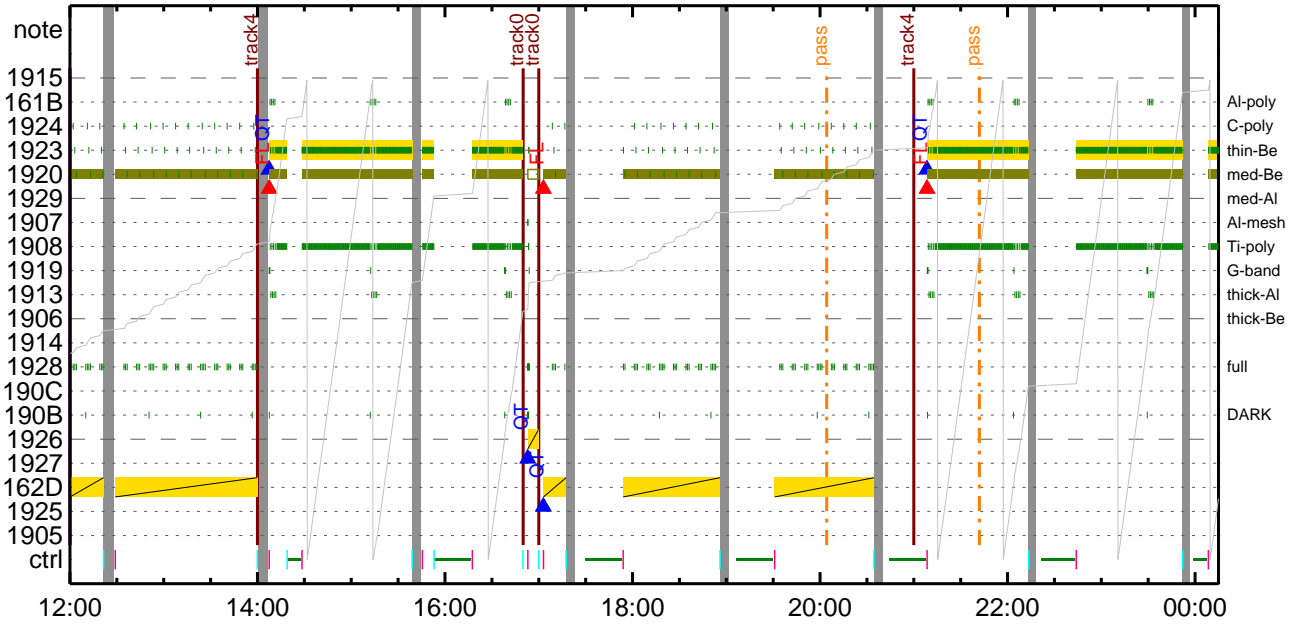
CMDI #0882 2012/08/30



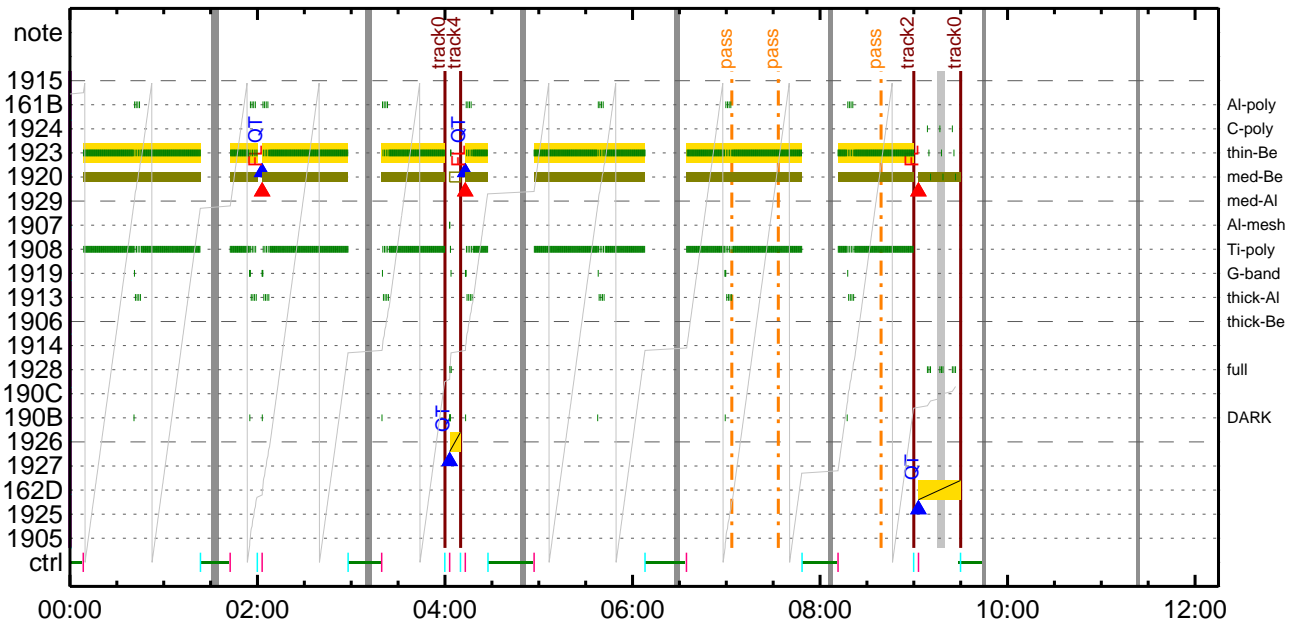
CMDI #0882 2012/08/31



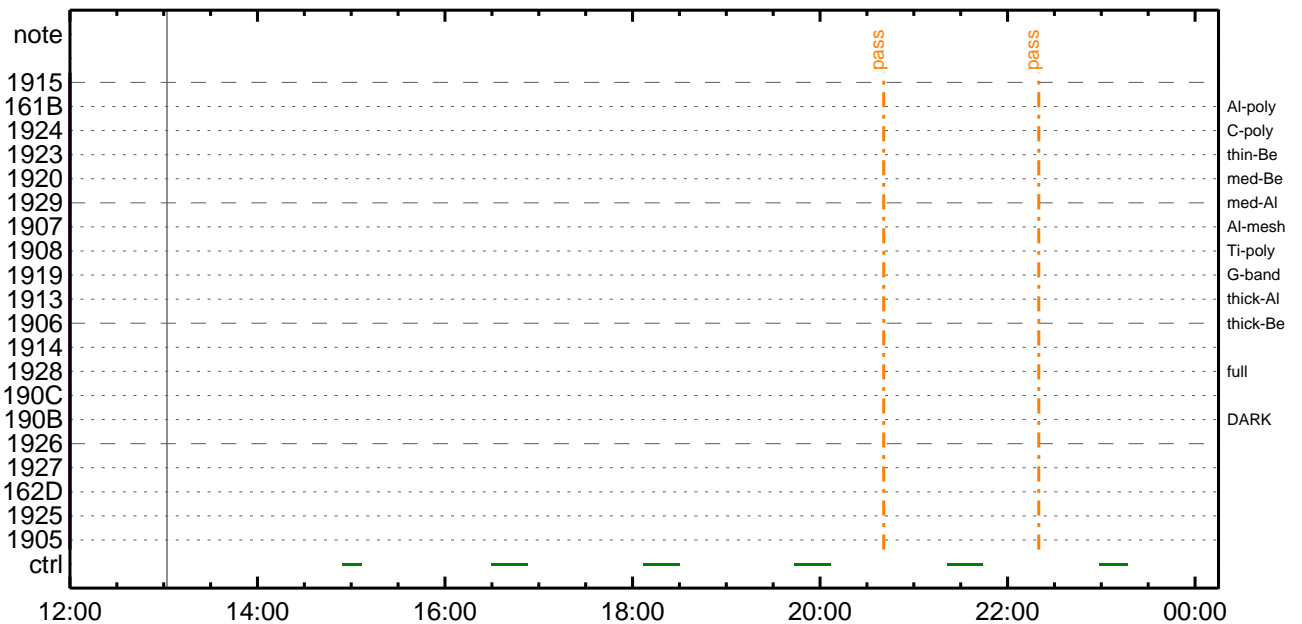
CMDI #0882 2012/08/31



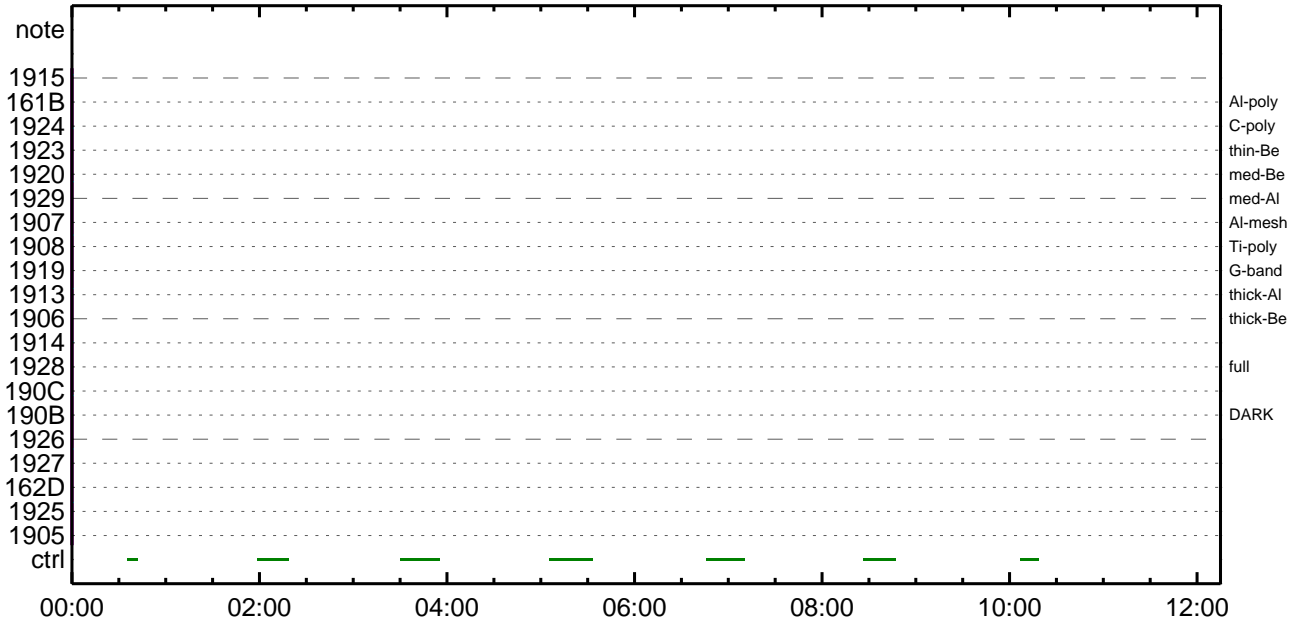
CMDI #0882 2012/09/01



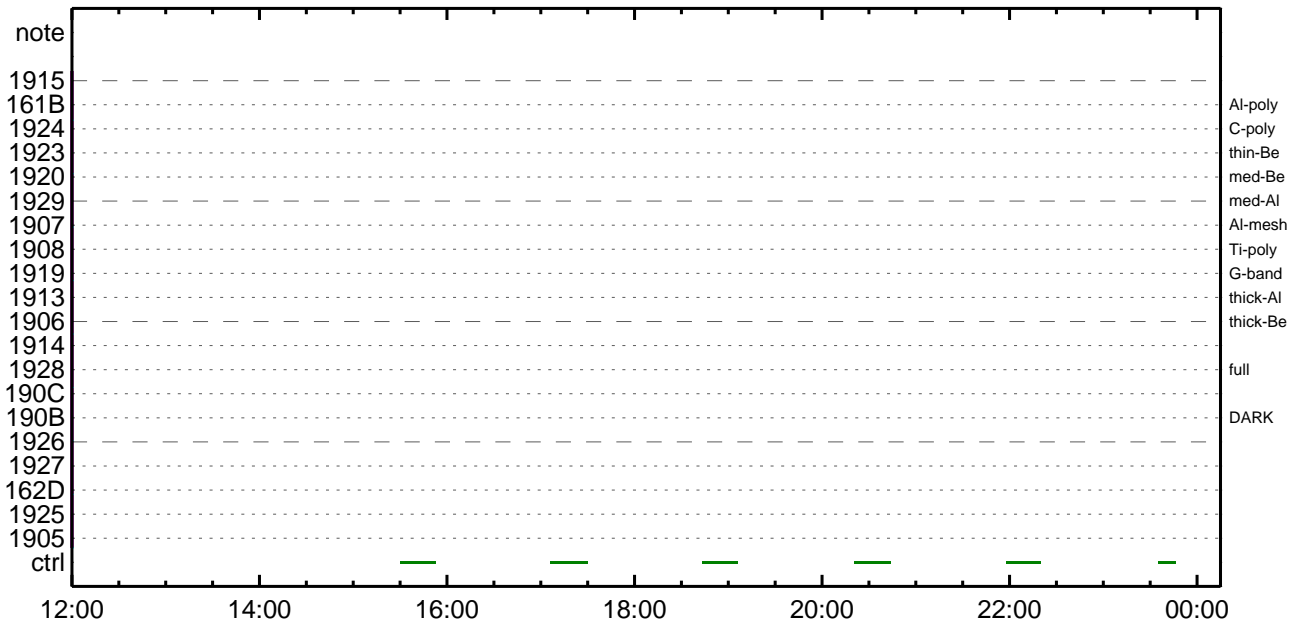
CMDI #0882 2012/09/01



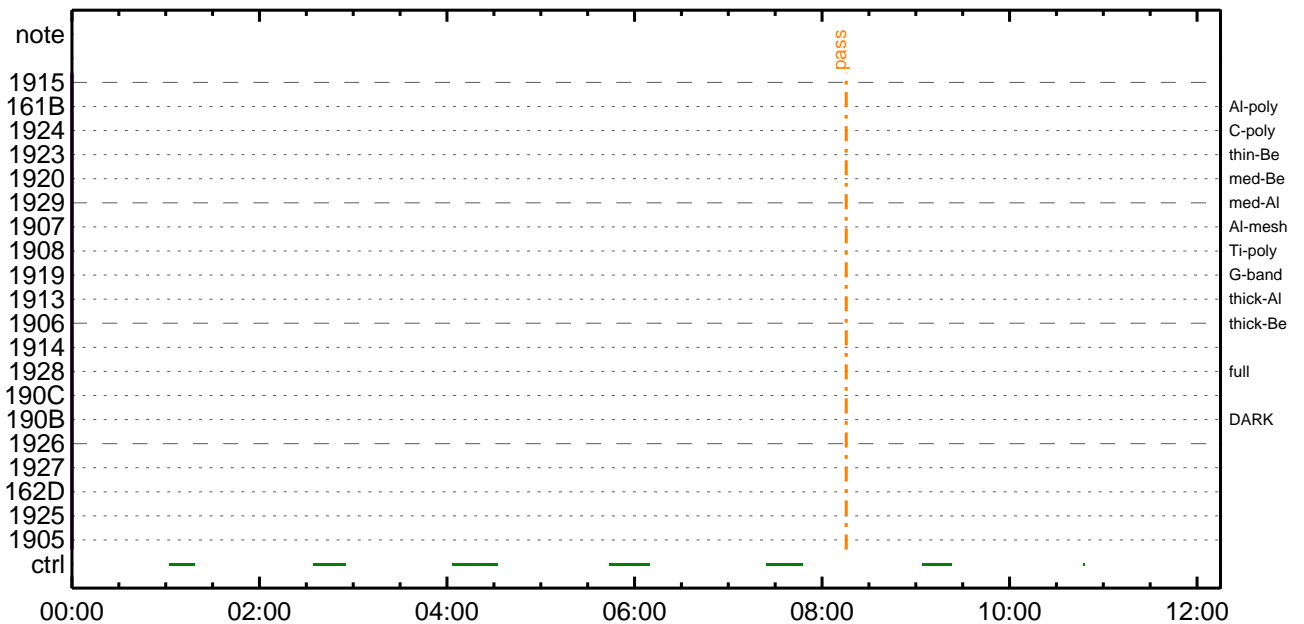
CMDI #0882 2012/09/02



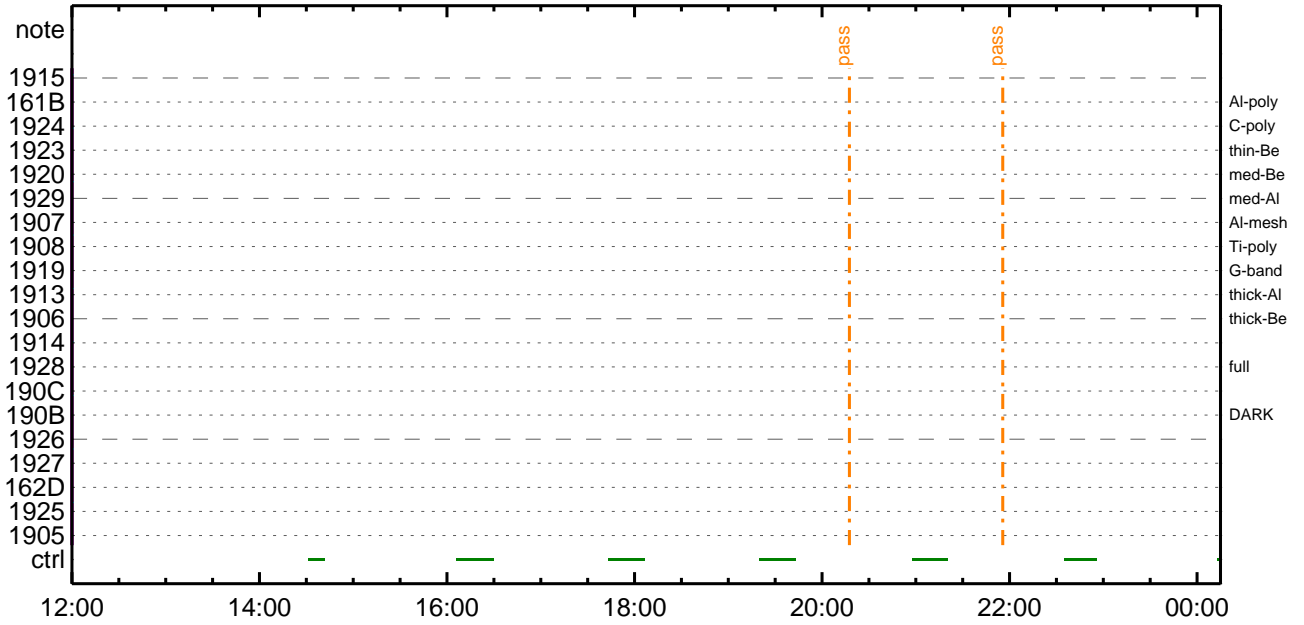
CMDI #0882 2012/09/02



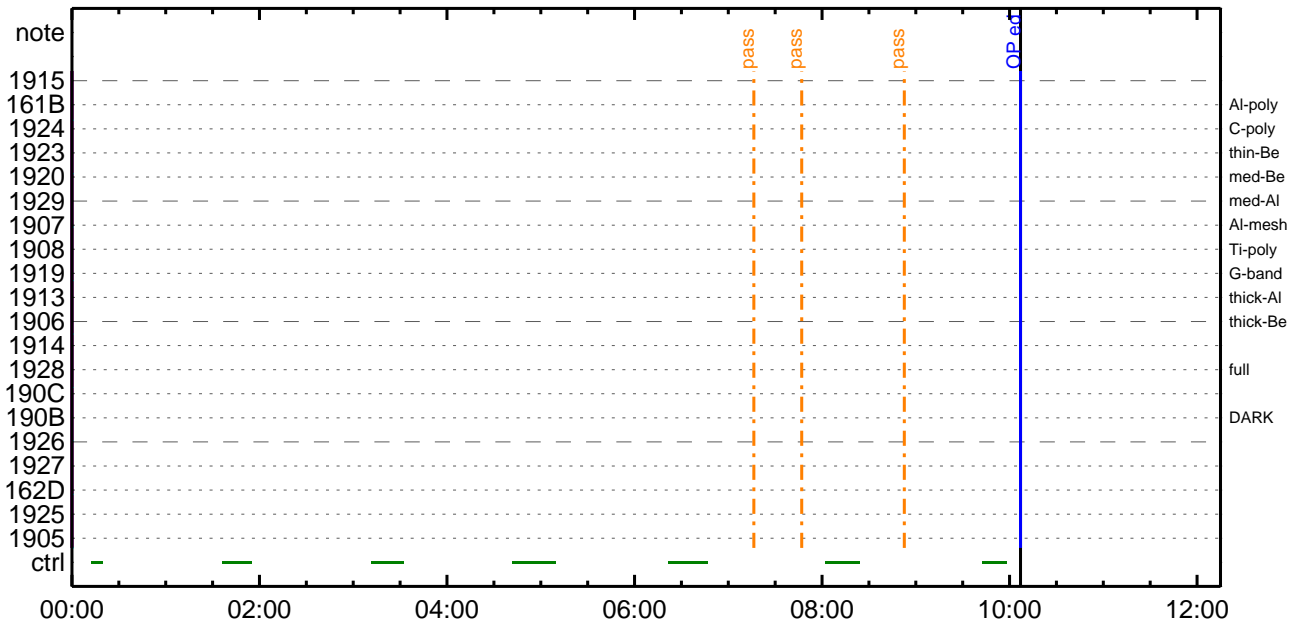
CMDI #0882 2012/09/03



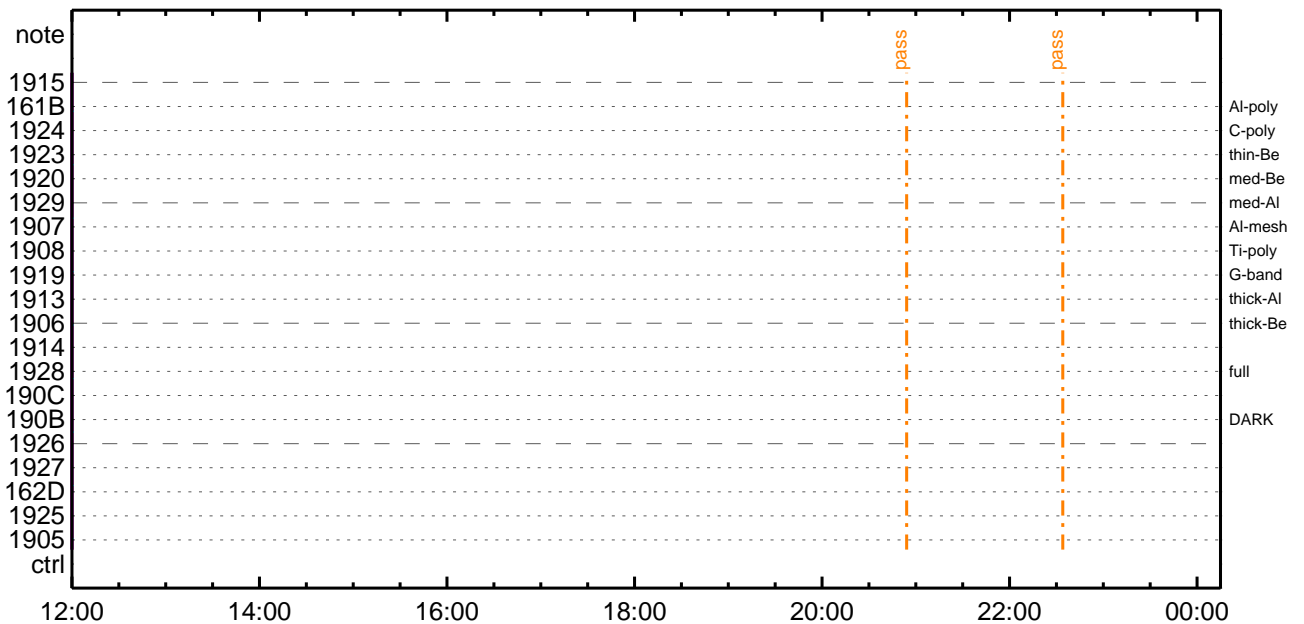
CMDI #0882 2012/09/03



CMDI #0882 2012/09/04



CMDI #0882 2012/09/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-113:OP
0104 ( )
0105 S. OG og-113:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°eYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî½E¹ç•ë²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî½E¹ç•ë²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPñî½E¹ç•ë²îOKò³îÇ§
0165 C.
0166 C. ***** òE²¼òî½Ä´¶Á°òEÉ¬òÁ÷¿@ (¼âµ-YAYOXx½E½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Á÷¿@NGñî½E¹ç;ç°E²¼òî½TI-CMDÁ÷¿@ñî½Ä¹Ôª•ñEòò³òE;f
0180 C. òEò¿;çSETòEDUMPAîE±°îYNY¹ç¹Ôª|ò³òE;f
0181 C.
0182 C. TIY³Y½Y½EòðÁDî¿(UT)
0183 +. TI 2012-08-30 10:12:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-08-30 10:12:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-08-30 10:12:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

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0194 C.
0195 +. TI 2012-08-30 10:16:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîî¥Á¥§¥Á¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]    EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHU¥â;¼¥É;È¼¥¼. ¥î;¼¥È;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-08-30 10:16:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-08-30 10:16:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2012-08-30 10:16:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-08-30 10:16:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÃîαî»ö¼ÝαÈÃα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Á¥È¥¥¥È¥á¥ç¥èè%¼αα¼Ã»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Ã»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-114 2012-08-30 13:36:04 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÃŸ-¼Á»Û;ã
0005 C.
0006 C. ŸÀŸB;¼Ÿ³ŸFŸóŸÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿Àß•µ°È»Í×ÁÇ¿ÍŸçŸÁŸ×Ÿí;¼ŸÉ;ÈÈ%µ•íÊ;ÈßÈ¼°ÇÒß•¿¿¼í¹ç¿Í;çÀ®, ù¿¹¿èß¿çÀ+¿®ß•¿Èß¿¿¿¿¿¿Ë;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 34s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-08-30 10:16:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP ´ûÁîŸÍ»ó¼Ÿ¿ÈÄß¿¹¿èDCBC•×²è *****
0069 C. (¼¿°íŸóŸÁŸBŸPŸEŸfáŸçŸè%¼¿¿¼¿»Û¿¹¿è)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** ŸBŸ¹.İ Daily±¿İŸ¿È´Ø¿¹¿èDCBC•×²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSŸÁŸSŸÃŸ-¼Á»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-265:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Resume FG table (auto mode) >
0120 +. DC 07-F0 MDP_FG_CTRL_AUTO
0121 BC (50)
0122 . C. -----
0123 C. MDP_FG_CTRL_MODE = AUTO [ ]
0124 C. -----
0125 C.
0126 C. *****
0127 C. SOT TI command set
0128 C. *****
0129 C. Execute, after the success of TBL upload.
0130 +. TI 2012-08-30 10:16:18.0
0131 DC 07-F0 MDP_SOT_MODE_OBSV
0132 BC (40)
0133 . C. -----
0134 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0135 C. -----
0136 C.
0137 C. Only when FG_CTRL_AUTO is used in RT.
0138 +. TI 2012-08-30 10:16:20.0
0139 DC 07-F0 MDP_FG_CTRL_AUTO
0140 BC (50)
0141 . C. -----
0142 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0143 C. -----
0144 C. ***** SOT END *****
0145 C.
0146 C. ***** XRT START *****
0147 C.
0148 +. DC 07-F0 MDP_XRT_CTRL_MANU
0149 BC (c1)
0150 + DC 07-F0 MDP_XRT_MODE_STBY
0151 BC (c3)
0152 . C. ----- Success Verify ? OK / NG____
0153 C.
0154 C. XRT Obs. Table Upload
0155 . S. RAM ram-291:MDP_OBS_X
0156 ( )
0157 C.
0158 +. DC 07-F0 MDP_DUMP_XRTTBL
0159 BC (84 07 00 00 00 3a d4)
0160 . C. ----- Comparison Check ? OK / ERR ____
0161 C.
0162 C.
0163 +. DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 01 b1 b1 04 04)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 02 b1 b1 08 08)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 03 b1 b1 08 08)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 04 b1 b1 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 05 85 83 06 06)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 06 80 80 20 20)
0175 + DC 07-F0 MDP_XRT_ROI_SET
0176 BC (cd 07 80 60 20 18)
0177 + DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 08 a0 80 18 20)
0179 + DC 07-F0 MDP_XRT_ROI_SET
0180 BC (cd 09 80 80 20 08)
0181 + DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 0a 80 80 08 20)
0183 + DC 07-F0 MDP_XRT_ROI_SET
0184 BC (cd 0b 85 83 06 06)
0185 + DC 07-F0 MDP_XRT_ROI_SET
0186 BC (cd 0c 85 83 08 08)
0187 + DC 07-F0 MDP_XRT_ROI_SET
0188 BC (cd 0d 80 80 02 02)
0189 + DC 07-F0 MDP_XRT_ROI_SET
0190 BC (cd 0e 80 80 06 06)
0191 + DC 07-F0 MDP_XRT_ROI_SET
0192 BC (cd 0f 80 80 06 06)
0193 + DC 07-F0 MDP_XRT_ROI_SET

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0194 BC (cd 10 80 80 08 08)
0195 + DC 07-F0 MDP_XRT_FLD_ENA
0196 BC (d8)
0197 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0198 BC (c8)
0199 +. DC 07-F0 MDP_XRT_AEC_RESET
0200 BC (d0)
0201 + DC 07-F0 MDP_XRT_ARS_DIS
0202 BC (d5)
0203 +. DC 07-F0 MDP_XRT_FLD_RESET
0204 BC (da)
0205 + DC 07-F0 MDP_XRT_QT_PROG_SET
0206 BC (c4 03)
0207 + DC 07-F0 MDP_XRT_FL_PROG_SET
0208 BC (c5 10)
0209 . C. ----- Success Verify ? OK / NG ____
0210 C.
0211 C.
0212 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0213 C.
0214 +. DC 07-F0 MDP_XRT_MODE_OBSV
0215 BC (c2)
0216 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0217 BC (c0)
0218 +. TI 2012-08-30 10:16:02.0
0219 DC 07-F0 MDP_XRT_MODE_OBSV
0220 BC (c2)
0221 +. TI 2012-08-30 10:16:04.0
0222 DC 07-F0 MDP_XRT_CTRL_AUTO
0223 BC (c0)
0224 . C. ----- Success Verify ? OK / NG ____
0225 C.
0226 C. ***** XRT END *****
0227 C.
0228 . C. ***** MDP 'uAÎaî»ô%ÿaÊÂa¹aèDCBC•x²è *****
0229 C. (%â°îÿÓÿÃÿÈÿPÿËÿâÿçÿèaÈ%¼aa¼A»Ûa¹aè)
0230 . S. DC-BC dcbc-402:DCBC
0231 (MDP_known_event)
0232 C.
0233 C.
0234 . C. ***** ÿDÿ¹•Ï Daily±;îÑaÈ´Øa¹aèDCBC•x²è *****
0235 . S. DC-BC dcbc-153:DCBC
0236 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0237 C.
0238 C.
0239 . C. ;ãLOSÿÃÿSÿËÿ¼A»Û;ã
0240 C.
0241 . C. ***** LOS *****
0242 C.
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*** OP Sequence for XRT ***

2012/08/30	10:26:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/30	10:27:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/08/30	10:29:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/30	10:29:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/30	10:29:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/30	10:29:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/30	10:29:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/30	10:29:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/30	10:29:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/08/30	10:29:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/30	10:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/30	11:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/30	11:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/30	11:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/30	11:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/30	11:52:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/30	11:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/30	13:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/30	13:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/30	13:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/30	13:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/30	13:30:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/30	13:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/30	13:59:54.0	XRT_CTRL_MANU_421_OG [0x1a5]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/30	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2012/08/30	14:14:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/30	14:14:52.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/30	14:14:54.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/30	14:14:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/30	14:14:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2012/08/30	14:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/30	15:59:54.0	XRT_CTRL_MANU_421_OG [0x1a5]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/30	16:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 54 00				
2012/08/30	16:14:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/30	16:14:52.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/30	16:14:54.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/30	16:14:56.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/30	16:14:58.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/08/30	16:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/30	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/30	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/08/30	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00 00				
2012/08/30	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/30	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/30	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/30	18:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/08/30	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2012/08/30	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/30	18:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]		5	02-76	00 e8 01 b3 1b			
			AOCU_NM						
2012/08/30	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/08/30	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/30	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/30	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/30	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/30	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/30	18:12:56.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2012/08/30	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/08/30	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/30	18:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/30	18:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/30	18:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/30	18:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/30	18:53:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/30	18:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/30	19:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/30	19:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/30	19:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/30	19:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/30	20:30:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/30	20:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/30	20:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/30	21:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]		5	02-76	04 00 00 00 00			
			AOCU_NM						
2012/08/30	21:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/08/30	21:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/08/30	21:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/08/30	21:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/08/30	21:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/08/30	21:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/30	21:02:56.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2012/08/30	21:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/08/30	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/30	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/30	21:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/30	21:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/30	21:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/30	22:07:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/30	22:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/30	23:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/30	23:13:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/08/30	23:13:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/08/30	23:16:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/08/30	23:35:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/30	23:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/08/31	00:07:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/08/31	00:07:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			

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2012/08/31	00:08:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/31	00:08:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/31	00:08:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	00:08:22.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2012/08/31	00:08:24.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	00:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	00:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	00:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	00:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	01:05:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	01:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	01:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	01:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/08/31	02:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	20	00	4f	4b
2012/08/31	02:00:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/31	02:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/31	02:00:20.5	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/31	02:00:22.5	XRT_ARS_DIS_409_OG [0x199]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	02:02:54.5	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	02:02:56.5	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2012/08/31	02:02:58.5	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/08/31	02:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	02:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	02:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	02:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	02:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	02:42:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	02:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	03:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	03:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	03:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	03:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	04:19:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	04:20:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	04:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/08/31	04:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/08/31	04:30:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/31	04:30:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/31	04:30:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	04:32:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2012/08/31	04:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	04:39:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	04:39:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/08/31	04:40:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04	00	00	00	00
2012/08/31	04:40:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/31	04:40:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/31	04:40:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2012/08/31	04:40:22.0	XRT_ARS_DIS_409_OG [0x199]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	04:42:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	04:42:56.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2012/08/31	04:42:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/08/31	04:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	05:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	05:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	05:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	05:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	05:56:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	05:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	07:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	07:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	07:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	07:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	07:34:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	07:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	08:50:00.0	XRT_CTRL_MANU_436_OG [0x1b4]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2012/08/31	09:11:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/08/31	09:11:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/31	09:11:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/31	09:11:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/31	09:11:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	09:11:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	09:11:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/08/31	09:11:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/08/31	09:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	10:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	10:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	10:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	10:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	10:38:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	10:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	10:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	10:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	10:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	10:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	10:49:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	10:50:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	12:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	12:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	12:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	12:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	12:28:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	12:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	13:59:54.0	XRT_CTRL_MANU_446_OG [0x1be]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	14:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04	00	00	00	00
2012/08/31	14:06:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	

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2012/08/31	14:07:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/31	14:07:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/31	14:07:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/31	14:07:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	14:07:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	14:07:26.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2012/08/31	14:07:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/08/31	14:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	14:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	14:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	14:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	14:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	14:27:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	14:28:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	15:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	15:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	15:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	15:44:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	15:45:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	15:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	15:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	15:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	15:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	16:16:30.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	16:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	16:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/08/31	16:50:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/08/31	16:50:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/08/31	16:50:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/08/31	16:50:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	16:52:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2012/08/31	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	16:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	17:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	54	00
2012/08/31	17:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/08/31	17:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/31	17:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/31	17:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/31	17:02:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	17:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	17:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/08/31	17:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/08/31	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	17:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	17:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	17:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2012/08/31	17:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	17:53:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	17:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	18:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	18:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	18:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	18:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	19:30:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	19:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	20:34:30.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	21:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/08/31	21:07:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/08/31	21:08:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/08/31	21:08:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/08/31	21:08:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/08/31	21:08:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/08/31	21:08:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	21:08:26.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/08/31	21:08:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/08/31	21:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	22:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	22:13:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	22:13:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	22:16:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/08/31	22:43:00.0	XRT_Custom_434_OG [0x1b2]							
2012/08/31	22:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/08/31	23:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/08/31	23:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/08/31	23:52:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/08/31	23:55:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	00:07:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	00:08:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	01:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	01:23:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	01:23:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	01:26:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	01:41:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	01:42:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	01:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	01:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/01	02:00:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/01	02:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/01	02:00:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/01	02:00:22.0	XRT_ARS_DIS_409_OG [0x199]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/01	02:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	02:02:56.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/09/01	02:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/01	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	02:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2012/09/01	02:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	02:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	03:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	03:18:30.5	XRT_Custom_434_OG [0x1b2]							
2012/09/01	03:19:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/01	04:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/01	04:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/01	04:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/01	04:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/01	04:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/09/01	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	04:09:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	04:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/01	04:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/09/01	04:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/01	04:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/01	04:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/01	04:10:22.0	XRT_ARS_DIS_409_OG [0x199]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/01	04:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	04:12:56.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/09/01	04:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/01	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	04:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	04:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	04:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	04:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	04:56:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	04:57:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	06:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	06:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	06:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	06:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	06:33:30.5	XRT_Custom_434_OG [0x1b2]							
2012/09/01	06:34:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	07:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	07:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	07:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	07:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	08:10:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	08:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	08:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/09/01	09:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/01	09:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/01	09:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/01	09:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/01	09:02:52.0	XRT_ARS_DIS_412_OG [0x19c]							

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2012/09/01	09:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	09:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/09/01	09:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/09/01	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	09:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00	00	00