

# XRT Timeline to be uploaded on 2012/09/01

Period: 2012/09/01 09:20:00 - 2012/09/06 09:43:00

\* \* \* \* \*

Normal mode

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## XOB #192A: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 150s cad

Term	Pointing (x, y)	Comment
09/01 09:47:30 - 09/01 13:59:54	Track ( -77.7, -108.5) <small>@ 09/01 09:30:00</small>	# OP start + 10min, HOP-213, AR 11560 observations.
09/02 09:03:00 - 09/02 13:59:54	Track ( 139.3, -107.9) <small>@ 09/02 09:00:00</small>	# HOP-213, AR 11560 observations.
09/03 09:03:00 - 09/03 13:59:54	Track ( 353.5, -100.8) <small>@ 09/03 09:00:00</small>	# HOP-213, AR 11560 observations.
09/03 18:19:30 - 09/04 05:53:54	Track ( -504.1, -373.5) <small>@ 09/03 18:16:30</small>	# AR 11564
09/04 06:11:00 - 09/04 08:52:00	Track ( -415.6, -380.3) <small>@ 09/04 06:04:00</small>	# AR 11564

**PROG= 19 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>	
└─ <b>Seqn= 19 2-time(s) 2.0sec</b>	
└─ Open/G-band	Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>	
└─ <b>Seqn= 18 1-time(s) 2.0sec</b>	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ <b>Seqn= 32 4-time(s) 2.0sec</b>	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ <b>Seqn= 33 30-time(s) 150.0sec</b>	
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1923: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 60s cad

Term	Pointing (x, y)	Comment
09/01 14:03:00 - 09/01 17:43:24	Track ( -805.0, -330.6) <small>@ 09/01 14:00:00</small>	# Flaring AR 11564 at the limb.
09/01 17:56:30 - 09/02 05:59:54	Track ( -788.8, -334.4) <small>@ 09/01 17:53:30</small>	# Return to flaring AR 11564.
09/02 06:13:00 - 09/02 08:59:54	Track ( -730.6, -345.8) <small>@ 09/02 06:10:00</small>	# AR 11564
09/02 14:03:00 - 09/02 18:01:24	Track ( -688.1, -352.6) <small>@ 09/02 14:00:00</small>	# AR 11564
09/02 18:14:30 - 09/03 06:19:54	Track ( -663.7, -356.1) <small>@ 09/02 18:11:30</small>	# AR 11564
09/03 06:33:00 - 09/03 08:59:54	Track ( -586.1, -365.6) <small>@ 09/03 06:30:00</small>	# AR 11564
09/03 14:03:00 - 09/03 17:43:00	Track ( -534.7, -370.8) <small>@ 09/03 14:00:00</small>	# AR 11564

**PROG= 17 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>	
└─ <b>Seqn= 19 2-time(s) 2.0sec</b>	
└─ Open/G-band	Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>	
└─ <b>Seqn= 18 1-time(s) 2.0sec</b>	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ <b>Seqn= 32 4-time(s) 2.0sec</b>	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ <b>Seqn= 33 30-time(s) 60.0sec</b>	
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
└─ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1926: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/1443) + Thin-Be(256)

Term	Pointing (x, y)	Comment
09/01 17:46:30 - 09/01 17:53:24	Fixed ( 0.0, 0.0)	synoptic, shifted -16.5 min
09/02 06:03:00 - 09/02 06:09:54	Fixed ( 0.0, 0.0)	synoptic
09/02 18:04:30 - 09/02 18:11:24	Fixed ( 0.0, 0.0)	synoptic, shifted 1.5 min
09/03 06:23:00 - 09/03 06:29:54	Fixed ( 0.0, 0.0)	synoptic, shifted 20.0 min
09/03 18:09:30 - 09/03 18:16:24	Fixed ( 0.0, 0.0)	synoptic, shifted 6.5 min
09/04 05:57:00 - 09/04 06:03:54	Fixed ( 0.0, 0.0)	synoptic, shifted -6.0 min

<b>PROG= 05 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 12.0sec</b>													
└─ <b>Seqn= 50 1-time(s) 4.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 6 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ <b>Seqn= 51 1-time(s) 4.0sec</b>													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 54 1-time(s) 2.0sec</b>													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 8 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
09/01 09:47:30 - 09/01 13:59:54	Track ( -77.7, -108.5) <sup>Ⓜ</sup> 09/01 09:30:00	# OP start + 10min, HOP-213, AR 11560 observations.
09/01 14:03:00 - 09/01 17:43:24	Track ( -805.0, -330.6) <sup>Ⓜ</sup> 09/01 14:00:00	# Flaring AR 11564 at the limb.
09/01 17:56:30 - 09/02 05:59:54	Track ( -788.8, -334.4) <sup>Ⓜ</sup> 09/01 17:53:30	# Return to flaring AR 11564.
09/02 06:13:00 - 09/02 08:59:54	Track ( -730.6, -345.8) <sup>Ⓜ</sup> 09/02 06:10:00	# AR 11564
09/02 09:03:00 - 09/02 13:59:54	Track ( 139.3, -107.9) <sup>Ⓜ</sup> 09/02 09:00:00	# HOP-213, AR 11560 observations.
09/02 14:03:00 - 09/02 18:01:24	Track ( -688.1, -352.6) <sup>Ⓜ</sup> 09/02 14:00:00	# AR 11564
09/02 18:14:30 - 09/03 06:19:54	Track ( -663.7, -356.1) <sup>Ⓜ</sup> 09/02 18:11:30	# AR 11564
09/03 06:33:00 - 09/03 08:59:54	Track ( -586.1, -365.6) <sup>Ⓜ</sup> 09/03 06:30:00	# AR 11564
09/03 09:03:00 - 09/03 13:59:54	Track ( 353.5, -100.8) <sup>Ⓜ</sup> 09/03 09:00:00	# HOP-213, AR 11560 observations.
09/03 14:03:00 - 09/03 17:43:00	Track ( -534.7, -370.8) <sup>Ⓜ</sup> 09/03 14:00:00	# AR 11564
09/03 18:19:30 - 09/04 05:53:54	Track ( -504.1, -373.5) <sup>Ⓜ</sup> 09/03 18:16:30	# AR 11564
09/04 06:11:00 - 09/04 08:52:00	Track ( -415.6, -380.3) <sup>Ⓜ</sup> 09/04 06:04:00	# AR 11564

<b>PROG= 16 15-time(s)</b>													
└─ <b>Subr= 1 45-time(s) 10.0sec</b>													
└─ <b>Seqn= 35 1-time(s) 2.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Subr= 2 1-time(s) 10.0sec</b>													
└─ <b>Seqn= 36 1-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Seqn= 37 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn= 38 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

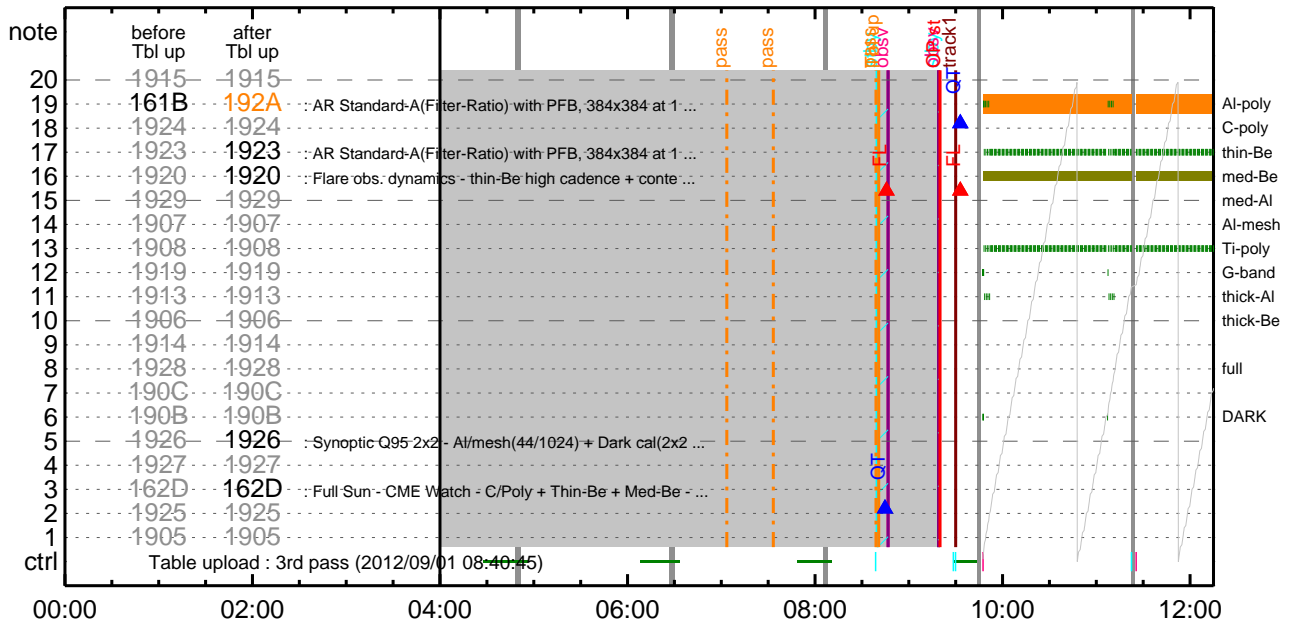
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### Flare Detection

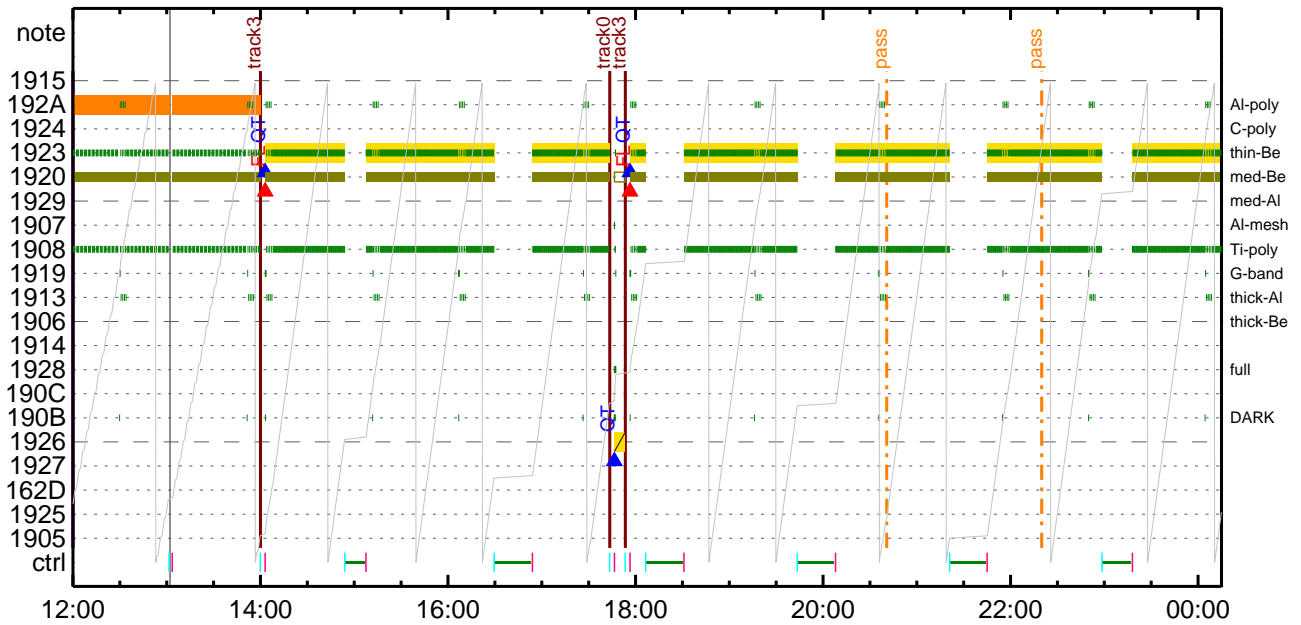
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<b>FLD Patrol</b>												
Term	Pointing (x, y)	Comment										
09/01 17:56:16 - 09/02 06:00:16	Track ( -788.8, -334.4) <sup>Ⓜ</sup> 09/01 17:53:30	# Return to flaring AR 11564.										
09/02 06:12:46 - 09/02 18:01:46	Track ( -730.6, -345.8) <sup>Ⓜ</sup> 09/02 06:10:00	# AR 11564										
09/02 18:14:16 - 09/03 06:20:16	Track ( -663.7, -356.1) <sup>Ⓜ</sup> 09/02 18:11:30	# AR 11564										
09/03 06:32:46 - 09/03 18:06:46	Track ( -586.1, -365.6) <sup>Ⓜ</sup> 09/03 06:30:00	# AR 11564										
09/03 18:19:16 - 09/04 05:54:16	Track ( -504.1, -373.5) <sup>Ⓜ</sup> 09/03 18:16:30	# AR 11564										
09/04 06:10:46 - 09/06 09:43:00	Track ( -415.6, -380.3) <sup>Ⓜ</sup> 09/04 06:04:00	# AR 11564										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

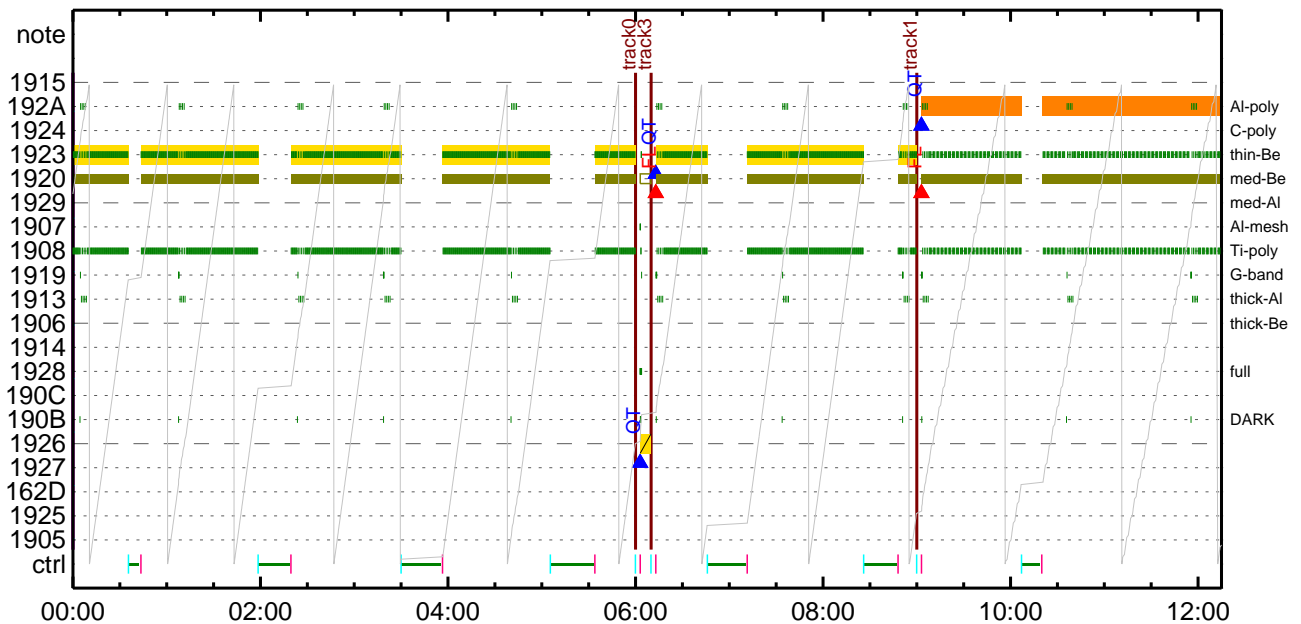
### CMDI #0888 2012/09/01



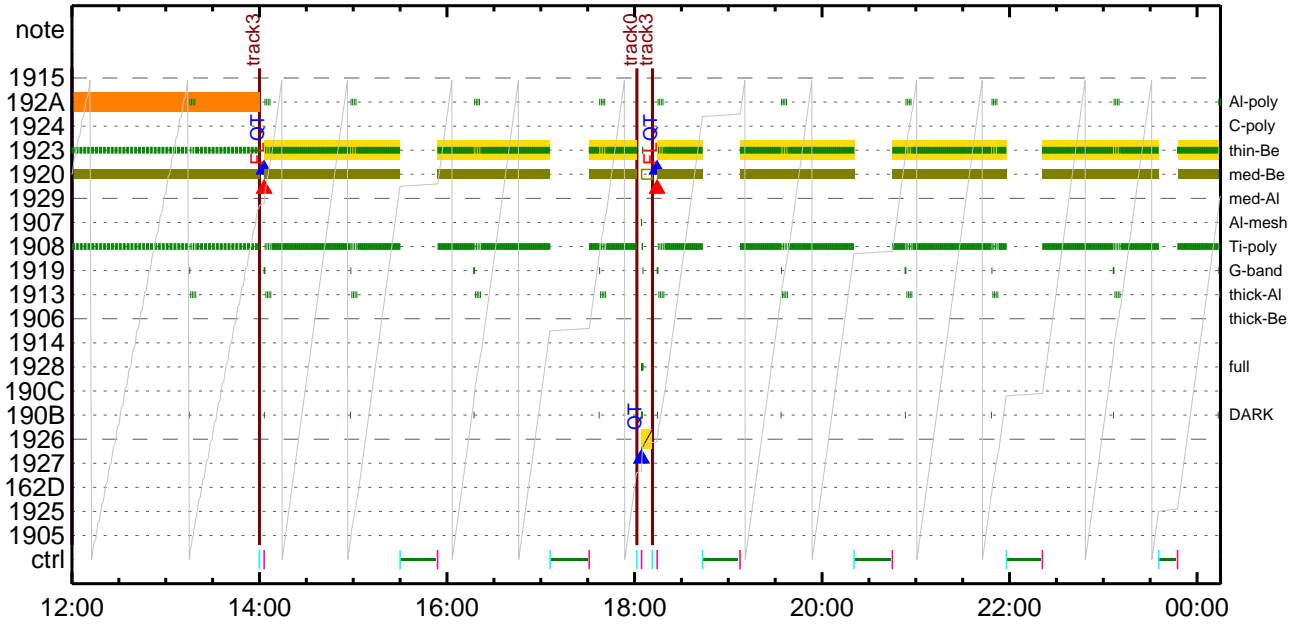
### CMDI #0888 2012/09/01



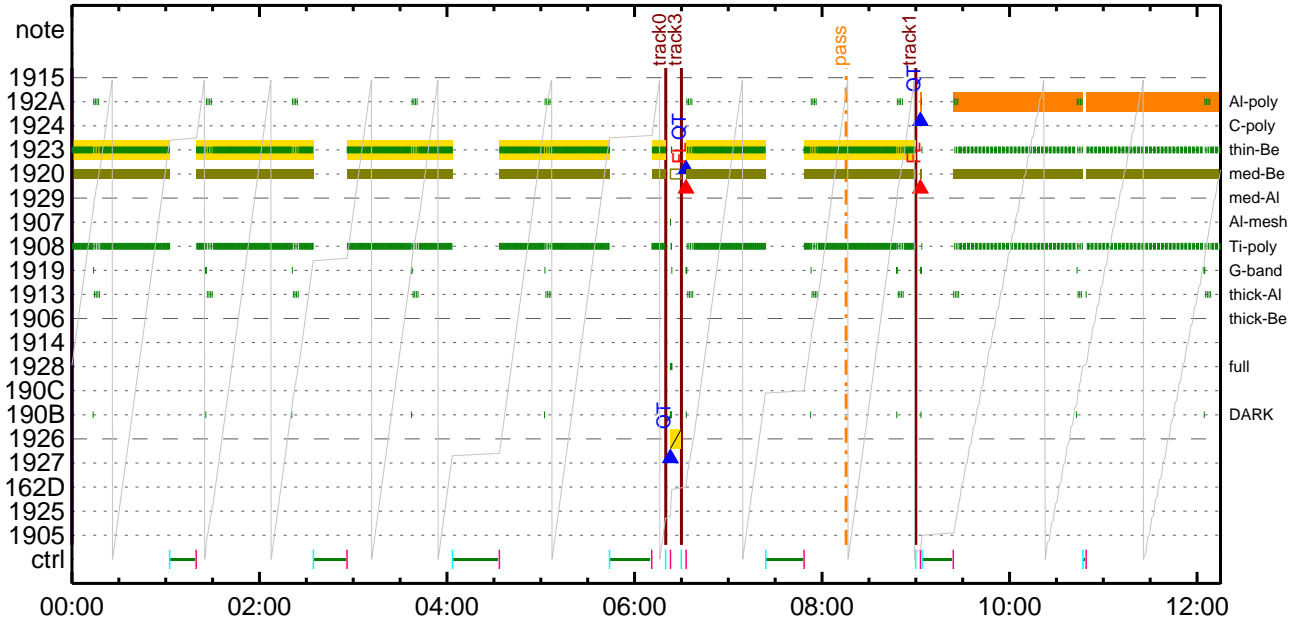
### CMDI #0888 2012/09/02



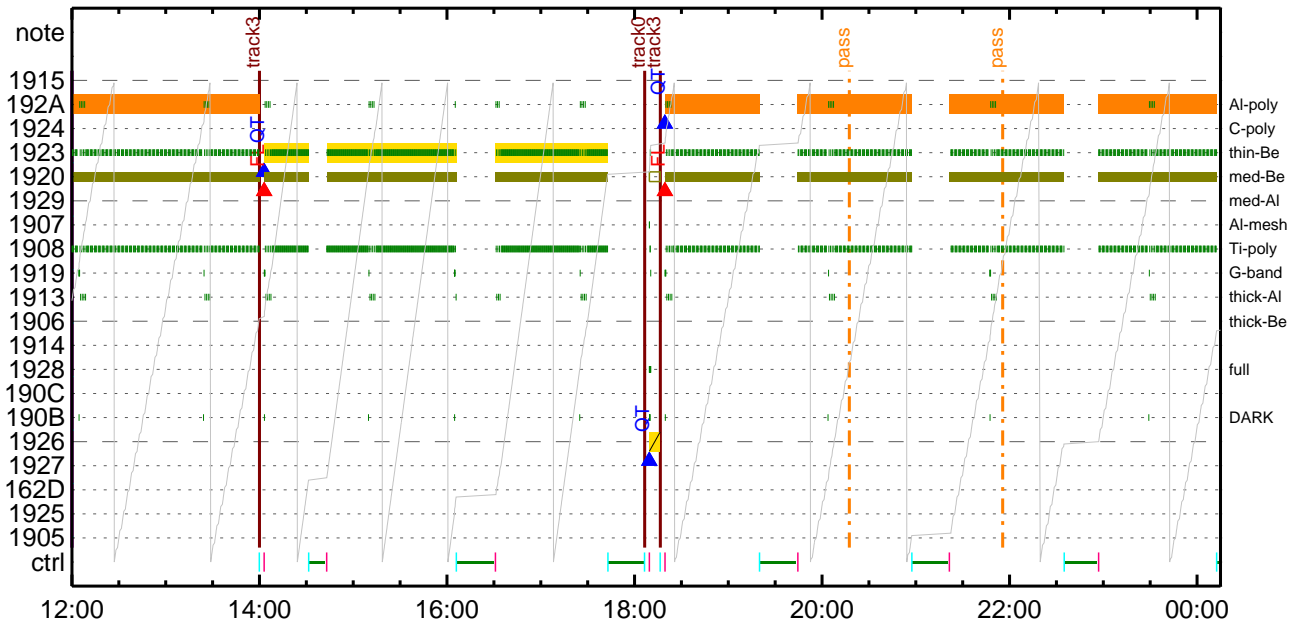
CMDI #0888 2012/09/02



CMDI #0888 2012/09/03

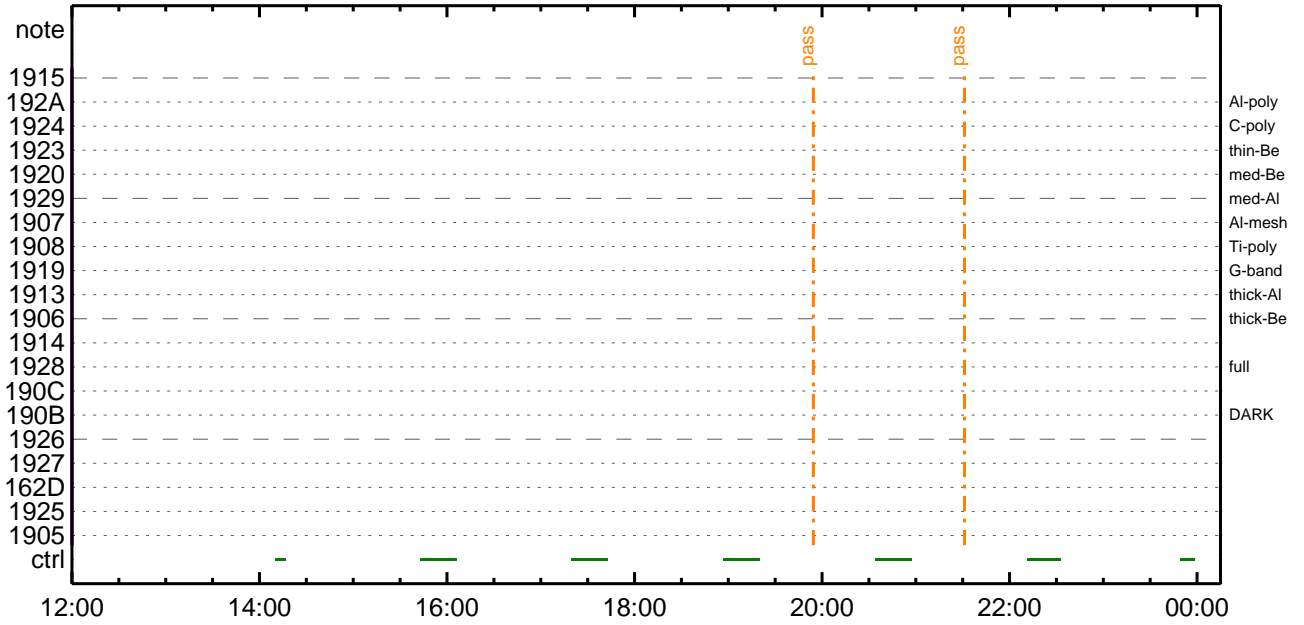


CMDI #0888 2012/09/03

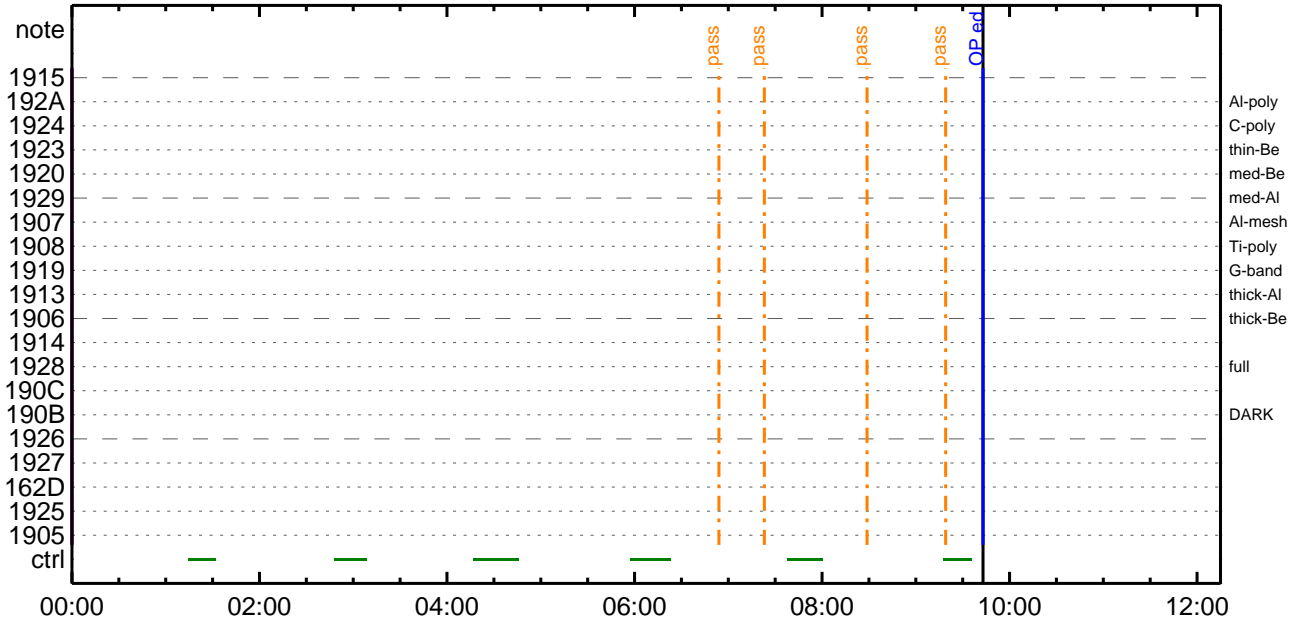




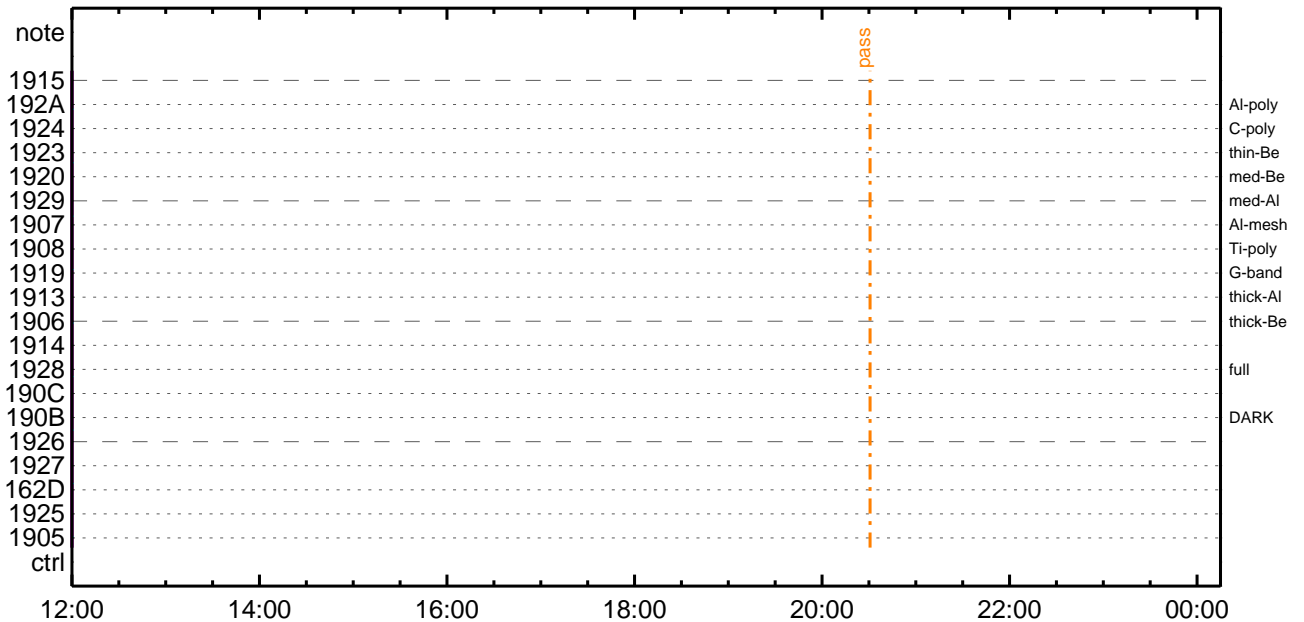
### CMDI #0888 2012/09/05



### CMDI #0888 2012/09/06



### CMDI #0888 2012/09/06









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0194 C.
0195 +. TI 2012-09-01 09:19:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼□îÄë%îíñ□îŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»□ð³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL□îŷ°èŷÄŷÖŷ×½ªî»□ð³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Ê□ðîâ□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-09-01 09:19:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-09-01 09:19:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-09-01 09:19:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-09-01 09:19:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîâî»ö¼ŷ□èÄð□¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷÉŷáŷçŷèè¼□□¼ª»Û□¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñèË´Ø□¹èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-266:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-283:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Resume FG table (auto mode) >
0138 +. DC 07-F0 MDP_FG_CTRL_AUTO
0139 BC (50)
0140 . C. -----
0141 C. MDP_FG_CTRL_MODE = AUTO [ ]
0142 C. -----
0143 C.
0144 C. *****
0145 C. SOT TI command set
0146 C. *****
0147 C. Execute, after the success of TBL upload.
0148 +. TI 2012-09-01 09:19:18.0
0149 DC 07-F0 MDP_SOT_MODE_OBSV
0150 BC (40)
0151 . C. -----
0152 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0153 C. -----
0154 C.
0155 C. Only when FG_CTRL_AUTO is used in RT.
0156 +. TI 2012-09-01 09:19:20.0
0157 DC 07-F0 MDP_FG_CTRL_AUTO
0158 BC (50)
0159 . C. -----
0160 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0161 C. -----
0162 C. ***** SOT END *****
0163 C.
0164 C. ***** XRT START *****
0165 C.
0166 +. DC 07-F0 MDP_XRT_CTRL_MANU
0167 BC (c1)
0168 + DC 07-F0 MDP_XRT_MODE_STBY
0169 BC (c3)
0170 . C. ----- Success Verify ? OK / NG____
0171 C.
0172 C. XRT Obs. Table Upload
0173 . S. RAM ram-291:MDP_OBS_X
0174 ( )
0175 C.
0176 +. DC 07-F0 MDP_DUMP_XRTTBL
0177 BC (84 07 00 00 00 3a d4)
0178 . C. ----- Comparison Check ? OK / ERR ____
0179 C.
0180 C.
0181 +. DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 01 b1 b1 04 04)
0183 + DC 07-F0 MDP_XRT_ROI_SET
0184 BC (cd 02 b1 b1 08 08)
0185 + DC 07-F0 MDP_XRT_ROI_SET
0186 BC (cd 03 b1 b1 08 08)
0187 + DC 07-F0 MDP_XRT_ROI_SET
0188 BC (cd 04 b1 b1 06 06)
0189 + DC 07-F0 MDP_XRT_ROI_SET
0190 BC (cd 05 85 83 06 06)
0191 + DC 07-F0 MDP_XRT_ROI_SET
0192 BC (cd 06 80 80 20 20)
0193 + DC 07-F0 MDP_XRT_ROI_SET

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\*\*\* OP Sequence for XRT \*\*\*

2012/09/01	09:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	09:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	09:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	09:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	09:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/09/01	09:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	09:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/01	09:32:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/01	09:32:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/01	09:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/01	09:32:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/01	09:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	09:32:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/09/01	09:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/01	09:46:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	09:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	11:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	11:22:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	11:22:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	11:24:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	11:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	11:25:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	13:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	13:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	13:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	13:02:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	13:03:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	13:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	13:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/09/01	14:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/01	14:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/01	14:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/01	14:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/01	14:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/01	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	14:02:56.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/09/01	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/01	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	14:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	14:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	14:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/01	14:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/01	15:06:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/01	15:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/01	16:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/01	16:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/01	16:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							

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2012/09/01	16:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/01	16:53:00.0	XRT_Custom_434_OG [0x1b2]						
2012/09/01	16:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/01	17:43:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/01	17:43:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/09/01	17:43:30.0	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2012/09/01	17:43:46.0	XRT_FLD_DIS_404_OG [0x194]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/09/01	17:43:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/09/01	17:43:50.0	XRT_ARS_DIS_406_OG [0x196]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/01	17:46:28.0	XRT_QT_PROG_SET_441_OG [0x1b9]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2012/09/01	17:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/01	17:53:24.0	XRT_CTRL_MANU_447_OG [0x1bf]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/01	17:53:30.0	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	03 00 00 00 00		
2012/09/01	17:55:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/09/01	17:56:16.0	XRT_FLD_ENA_428_OG [0x1ac]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/09/01	17:56:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/09/01	17:56:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/01	17:56:22.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/01	17:56:24.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/01	17:56:26.0	XRT_QT_PROG_SET_411_OG [0x19b]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2012/09/01	17:56:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/09/01	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/01	18:06:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/01	18:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/01	18:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/09/01	18:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/01	18:30:00.0	XRT_Custom_434_OG [0x1b2]						
2012/09/01	18:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/01	19:43:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/01	19:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/01	19:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/09/01	19:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/01	20:07:00.0	XRT_Custom_434_OG [0x1b2]						
2012/09/01	20:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/01	21:21:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/01	21:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/01	21:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/09/01	21:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/01	21:44:00.0	XRT_Custom_434_OG [0x1b2]						
2012/09/01	21:45:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/01	22:58:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/01	22:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/01	22:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/09/01	23:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/01	23:17:00.0	XRT_Custom_434_OG [0x1b2]						
2012/09/01	23:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/02	00:35:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/02	00:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/02	00:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						

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2012/09/02	00:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/02	00:42:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/02	00:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	01:58:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	01:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/02	01:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/02	02:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/02	02:18:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/02	02:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	03:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/02	03:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/02	03:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/02	03:55:30.5	XRT_Custom_434_OG [0x1b2]					
2012/09/02	03:56:30.5	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	05:05:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	05:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/02	05:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/02	05:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/02	05:33:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/02	05:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/09/02	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/09/02	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/02	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/02	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/02	06:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/09/02	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	06:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2012/09/02	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/09/02	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/02	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/02	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/02	06:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/02	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/02	06:12:56.0	XRT_QT_PROG_SET_411_OG [0x19b]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2012/09/02	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/09/02	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	06:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	06:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/02	06:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/02	06:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/02	07:10:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/02	07:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/02	08:26:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/02	08:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/02	08:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					



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2012/09/02	08:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/02	08:47:00.5	XRT_Custom_434_OG [0x1b2]								
2012/09/02	08:48:00.5	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/02	08:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/02	09:00:00.5	AOCS_Ore-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	01	00	00	00	00
2012/09/02	09:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/09/02	09:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/02	09:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/02	09:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/02	09:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/02	09:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/02	09:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/09/02	09:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/09/02	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/02	10:07:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/02	10:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/02	10:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/02	10:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/02	10:19:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/02	10:20:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/02	13:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/02	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	03	00	00	00	00
2012/09/02	14:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/09/02	14:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/02	14:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/02	14:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/02	14:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/02	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/02	14:02:56.0	XRT_QT_PROG_SET_411_OG [0x19b]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2012/09/02	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/09/02	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/02	15:30:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/02	15:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/02	15:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/02	15:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/02	15:53:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/02	15:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/02	17:06:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/02	17:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/02	17:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/02	17:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/02	17:30:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/02	17:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/02	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/02	18:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/09/02	18:01:30.0	AOCS_Ore-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00	00	00	00	00
2012/09/02	18:01:46.0	XRT_FLD_DIS_404_OG [0x194]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/02	18:01:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				

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2012/09/02	18:01:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/02	18:04:28.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/09/02	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/02	18:11:24.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/02	18:11:30.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/09/02	18:13:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/02	18:14:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/02	18:14:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/02	18:14:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/02	18:14:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/02	18:14:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/02	18:14:26.0	XRT_QT_PROG_SET_411_OG [0x19b]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2012/09/02	18:14:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/02	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/02	18:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/02	18:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/02	18:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/02	18:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/02	19:06:30.0	XRT_Custom_434_OG [0x1b2]			
2012/09/02	19:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/02	20:20:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/02	20:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/02	20:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/02	20:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/02	20:44:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/02	20:45:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/02	21:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/02	21:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/02	21:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/02	22:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/02	22:20:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/02	22:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/02	23:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/02	23:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/02	23:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/02	23:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/02	23:46:30.0	XRT_Custom_434_OG [0x1b2]			
2012/09/02	23:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/03	01:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/03	01:02:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/03	01:02:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/03	01:05:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/03	01:18:30.0	XRT_Custom_434_OG [0x1b2]			
2012/09/03	01:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/03	02:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/03	02:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/03	02:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/03	02:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/03	02:55:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/03	02:56:00.5	XRT_CTRL_AUTO_413_OG [0x19d]			

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2012/09/03	04:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/03	04:03:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/03	04:03:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/03	04:06:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/03	04:32:30.5	XRT_Custom_434_OG [0x1b2]	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/03	04:33:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/03	05:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/03	05:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/03	05:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/03	05:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/03	06:10:00.0	XRT_Custom_434_OG [0x1b2]	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/03	06:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/03	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/03	06:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/03	06:20:00.5	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
			AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/03	06:20:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/03	06:20:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/03	06:20:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/03	06:22:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/09/03	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/03	06:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/03	06:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00				
			AOCU_NM	5	02-76	03 00 00 00 00				
2012/09/03	06:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/03	06:32:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/03	06:32:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/03	06:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/03	06:32:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/03	06:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/03	06:32:56.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/09/03	06:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/03	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/03	07:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/03	07:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/03	07:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/03	07:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/03	07:47:30.0	XRT_Custom_434_OG [0x1b2]	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/03	07:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/03	08:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/03	09:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00				
			AOCU_NM	5	02-76	01 00 00 00 00				
2012/09/03	09:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/03	09:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/03	09:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/03	09:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/03	09:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/03	09:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/03	09:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/09/03	09:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/03	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				





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2012/09/04	05:09:30.5	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	05:10:30.5	XRT_CTRL_AUTO_413_OG [0x19d]					
2012/09/04	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	05:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	05:54:00.0	AOCS_Orе-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/09/04	05:54:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/09/04	05:54:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/04	05:54:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/04	05:56:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/04	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/09/04	06:03:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	06:04:00.0	AOCS_Orе-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	06:06:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8]	AOCU_NM	5	02-76	03 00 00 00 00	
2012/09/04	06:10:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_RECAL	2	07-F8	78 00	
2012/09/04	06:10:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/09/04	06:10:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/04	06:10:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/04	06:10:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/04	06:10:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/04	06:10:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	06:10:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2012/09/04	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/09/04	06:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	06:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	06:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	06:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/04	06:47:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	06:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
2012/09/04	08:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	08:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	08:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	08:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/04	08:24:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	08:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
2012/09/04	08:52:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	09:00:00.0	AOCS_Orе-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	10:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	01 00 00 00 00	
2012/09/04	10:17:00.0	AOCS_Orе-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			AOCU_NM	5	02-76	00 00 00 00 00	