

XRT Timeline to be uploaded on 2012/09/06

Period: 2012/09/06 09:43:00 - 2012/09/11 09:33:00

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Normal mode

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XOB #192A: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 150s cad

Term	Pointing (x, y)	Comment
09/06 09:56:00 - 09/06 13:59:54	Track (857.7, -47.6) @ 09/06 09:53:00	# OP start + 10min, HOP 213, AR 11560
09/07 09:03:00 - 09/07 11:59:54	Track (928.1, -22.2) @ 09/07 09:00:00	HOP 213, AR 11560

PROG= 19 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 19	2-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
Seqn= 18	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 33	30-time(s)	150.0sec										
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1926: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/1443) + Thin-Be(250)

Term	Pointing (x, y)	Comment
09/06 14:03:00 - 09/06 14:14:54	Fixed (0.0, 0.0)	synoptic, shifted 4 hrs before
09/07 06:28:00 - 09/07 06:34:54	Fixed (0.0, 0.0)	synoptic, shifted 25.0 min
09/07 14:03:00 - 09/07 14:09:54	Fixed (0.0, 0.0)	synoptic, shift 4 hrs before
09/08 06:13:00 - 09/08 06:19:54	Fixed (0.0, 0.0)	synoptic, shifted 10.0 min

PROG= 05 1-time(s)

Subr= 1	1-time(s)	12.0sec										
Seqn= 50	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 51	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 8	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2

Term	Pointing (x, y)	Comment
09/06 14:18:00 - 09/07 06:24:54	Fixed (-9.0, 885.0)	HOP 101
09/07 14:13:00 - 09/08 06:09:54	Fixed (-9.0, 885.0)	HOP 101

PROG= 03 Inf.-time(s)

Subr= 1	4-time(s)	480.0sec										
Seqn= 48	1-time(s)	60.0sec										
C-poly/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 46	1-time(s)	60.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 45	1-time(s)	60.0sec										
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2	1-time(s)	60.0sec										
Seqn= 49	1-time(s)	30.0sec										
C-poly/Open	C-poly/Open	close	Safe	Dark	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1923: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 60s cad

Term	Pointing (x, y)	Comment
09/07 07:01:30 - 09/07 08:59:54	Track (254.7, -365.0) @ 09/07 06:35:00	AR 11564
09/07 12:03:00 - 09/07 13:59:54	Track (300.3, -363.2) @ 09/07 12:00:00	AR 11564
09/08 06:23:00 - 09/08 10:36:00	Track (447.8, -354.8) @ 09/08 06:20:00	AR 11564
PROG= 17 Inf.-time(s)		
Subr= 1	1-time(s) 2.0sec	
└─ Seqn= 19	2-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2	2-time(s) 2.0sec	
└─ Seqn= 18	1-time(s) 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 32	4-time(s) 2.0sec	
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 33	30-time(s) 60.0sec	
└─ thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
└─ thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
└─ thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
└─ thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
09/06 09:56:00 - 09/06 13:59:54	Track (857.7, -47.6) @ 09/06 09:53:00	# OP start + 10min, HOP 213, AR 11560
09/06 14:18:00 - 09/07 06:24:54	Fixed (-9.0, 885.0)	HOP 101
09/07 07:01:30 - 09/07 08:59:54	Track (254.7, -365.0) @ 09/07 06:35:00	AR 11564
09/07 09:03:00 - 09/07 11:59:54	Track (928.1, -22.2) @ 09/07 09:00:00	HOP 213, AR 11560
09/07 12:03:00 - 09/07 13:59:54	Track (300.3, -363.2) @ 09/07 12:00:00	AR 11564
09/07 14:13:00 - 09/08 06:09:54	Fixed (-9.0, 885.0)	HOP 101
09/08 06:23:00 - 09/08 10:36:00	Track (447.8, -354.8) @ 09/08 06:20:00	AR 11564
PROG= 16 15-time(s)		
Subr= 1	45-time(s) 10.0sec	
└─ Seqn= 35	1-time(s) 2.0sec	
└─ thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2	1-time(s) 10.0sec	
└─ Seqn= 36	1-time(s) 2.0sec	
└─ med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 37	1-time(s) 2.0sec	
└─ Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Seqn= 38	1-time(s) 2.0sec	
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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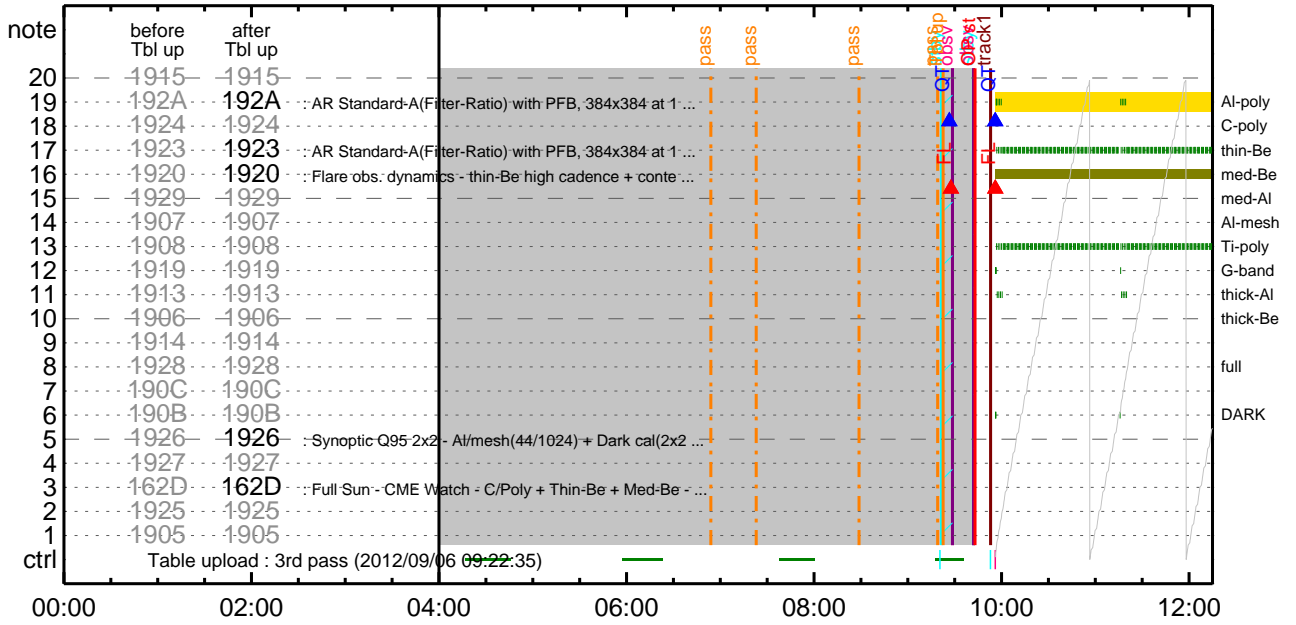
Flare Detection

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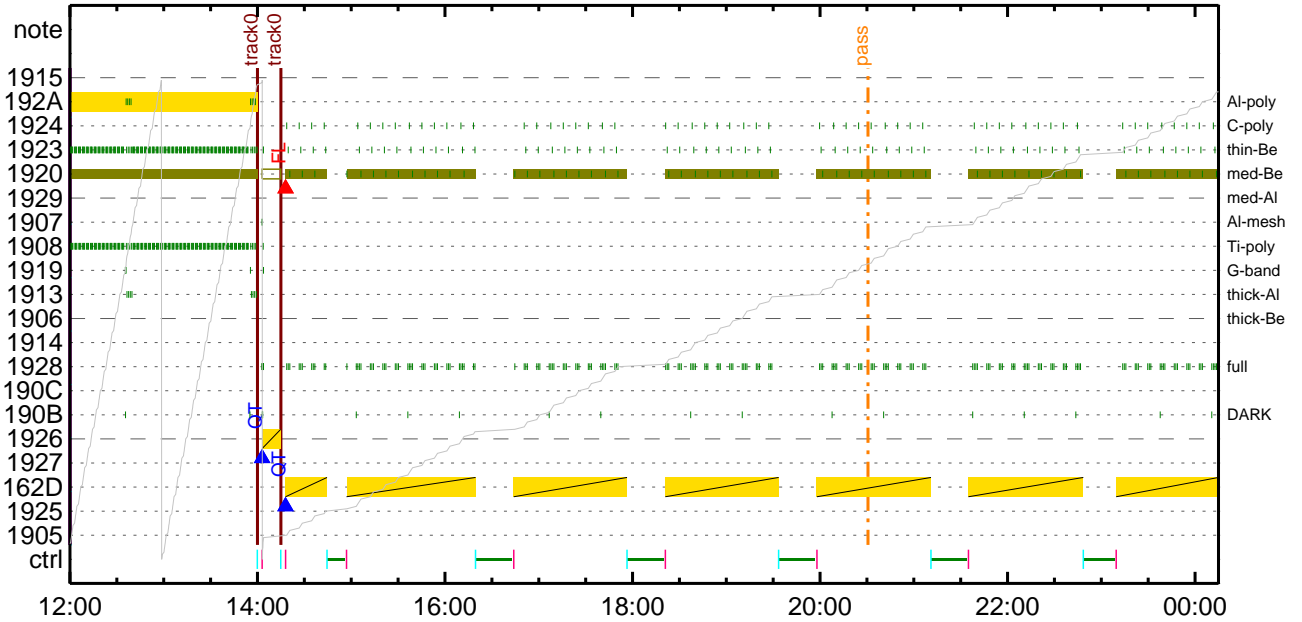
FLD Patrol

Term	Pointing (x, y)	Comment
09/06 14:17:46 - 09/07 06:25:16	Fixed (-9.0, 885.0)	HOP 101
09/07 06:37:46 - 09/07 14:00:16	Track (254.7, -365.0) @ 09/07 06:35:00	AR 11564
09/07 14:12:46 - 09/08 06:10:16	Fixed (-9.0, 885.0)	HOP 101
09/08 06:22:46 - 09/11 09:33:00	Track (447.8, -354.8) @ 09/08 06:20:00	AR 11564
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

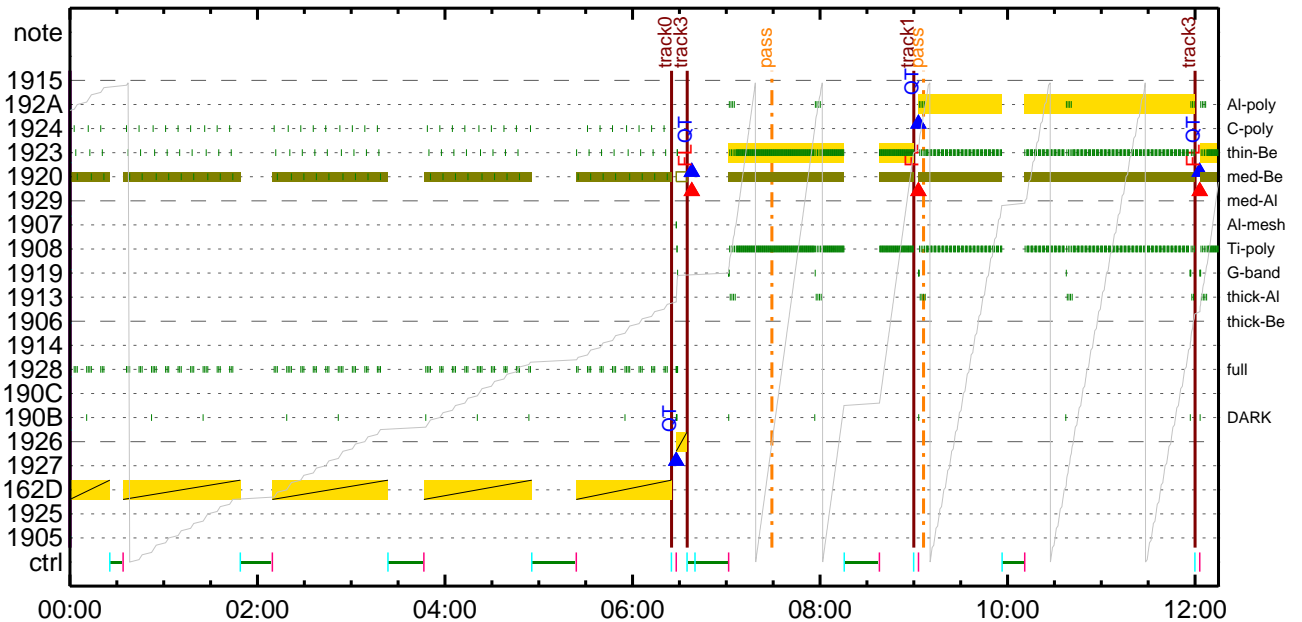
CMDI #0897 2012/09/06



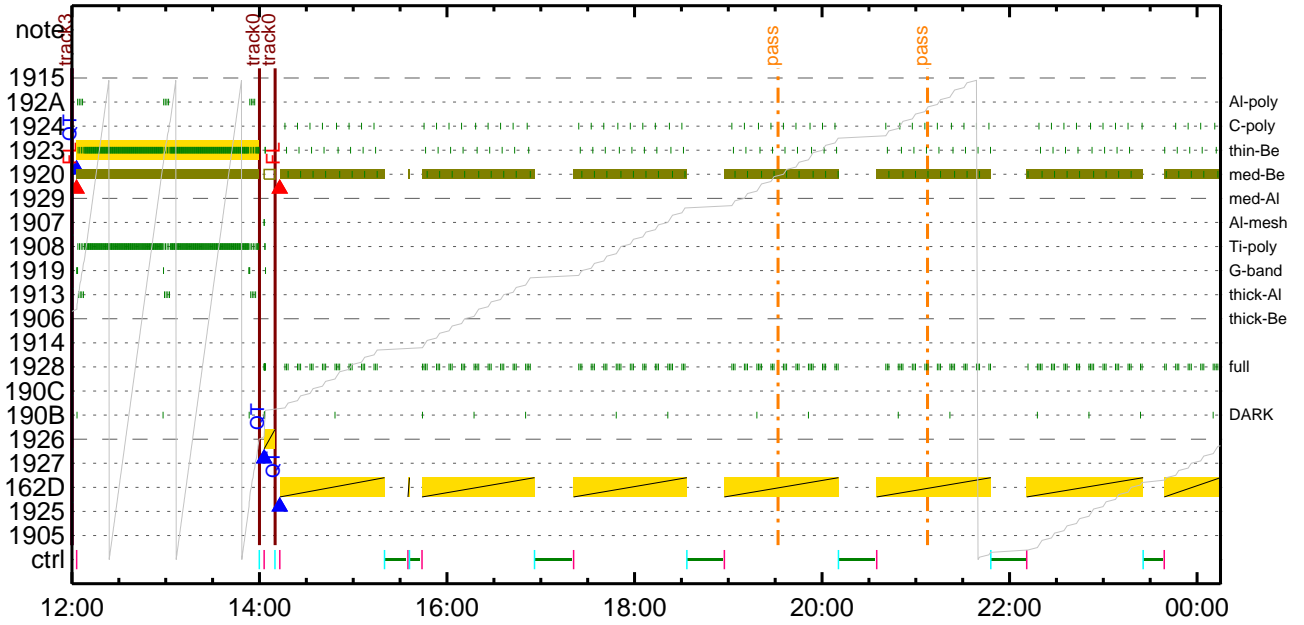
CMDI #0897 2012/09/06



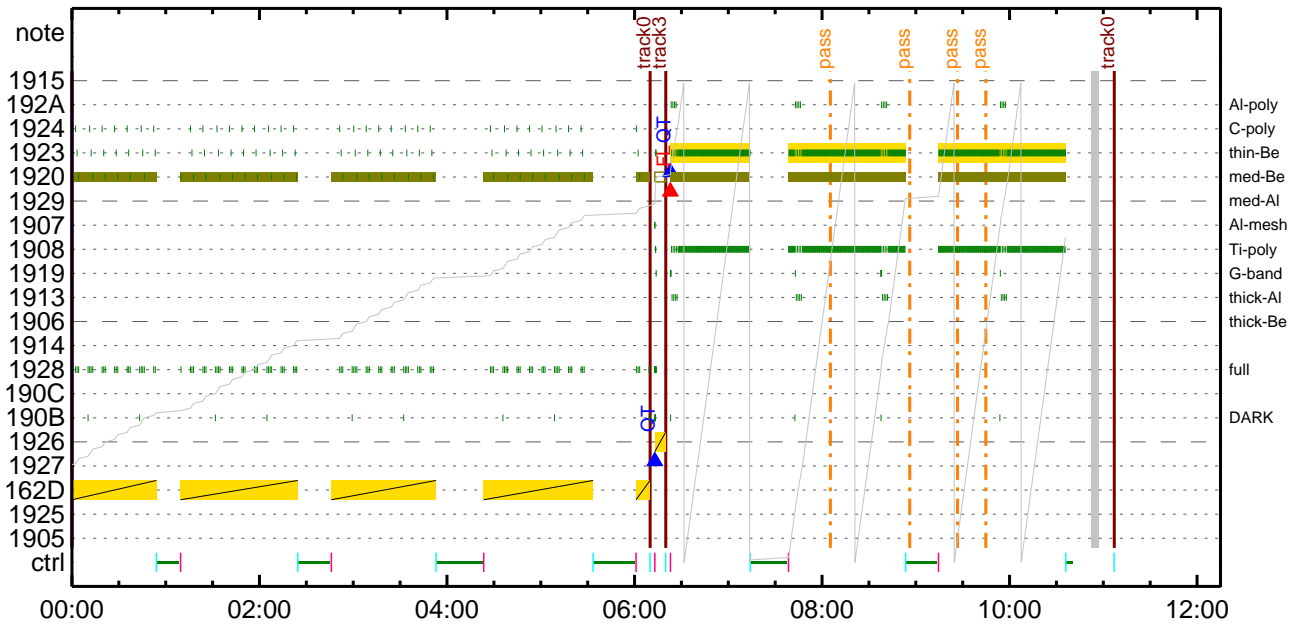
CMDI #0897 2012/09/07



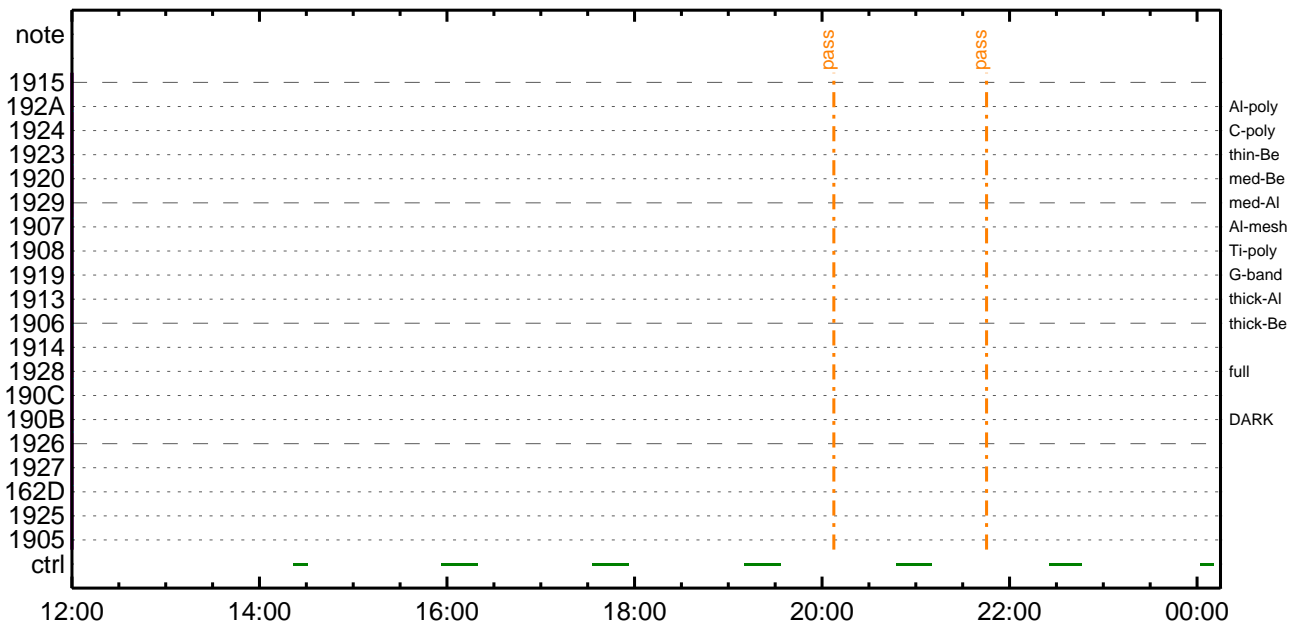
CMDI #0897 2012/09/07



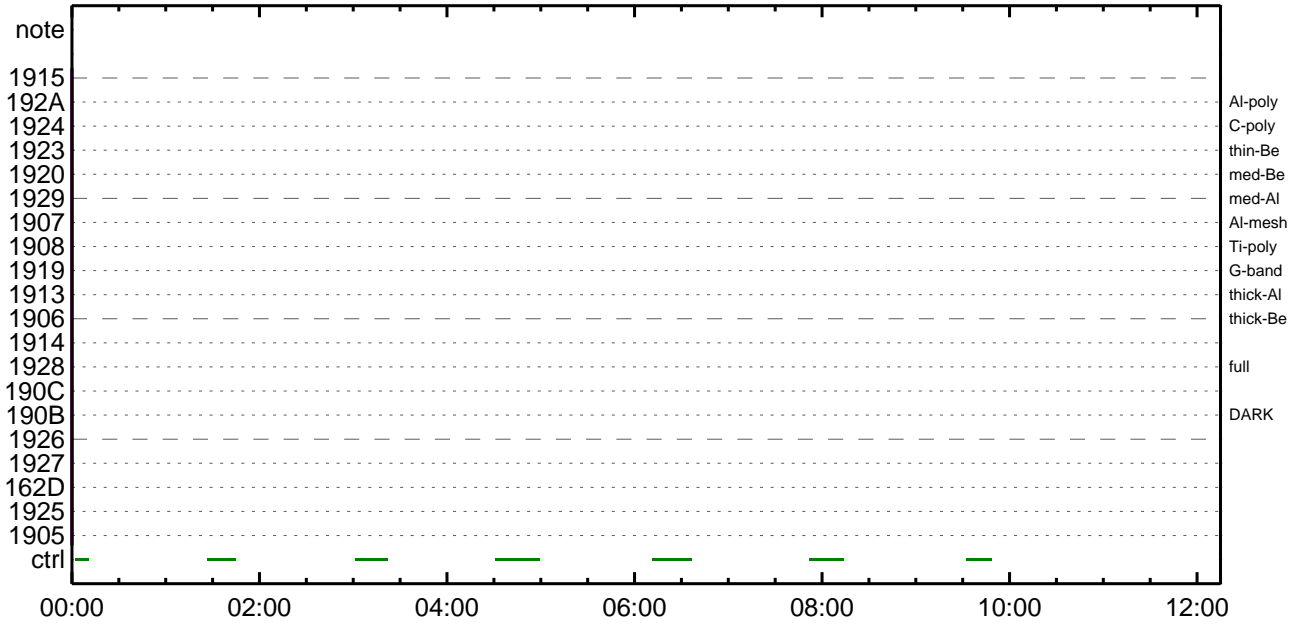
CMDI #0897 2012/09/08



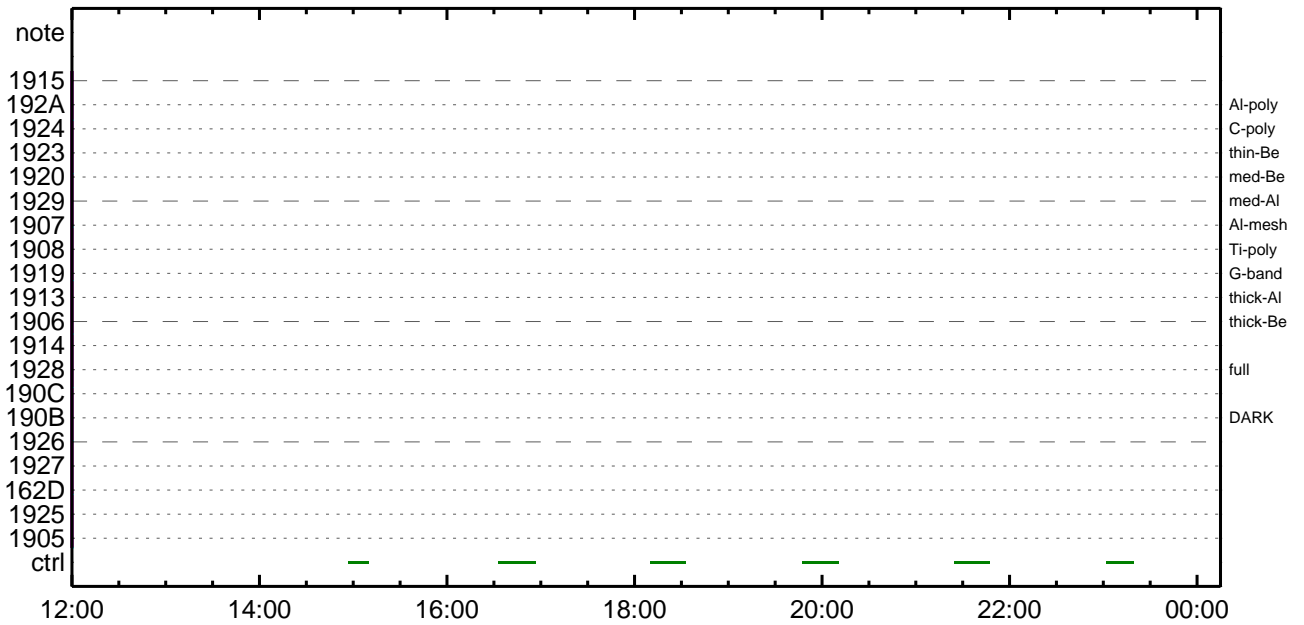
CMDI #0897 2012/09/08



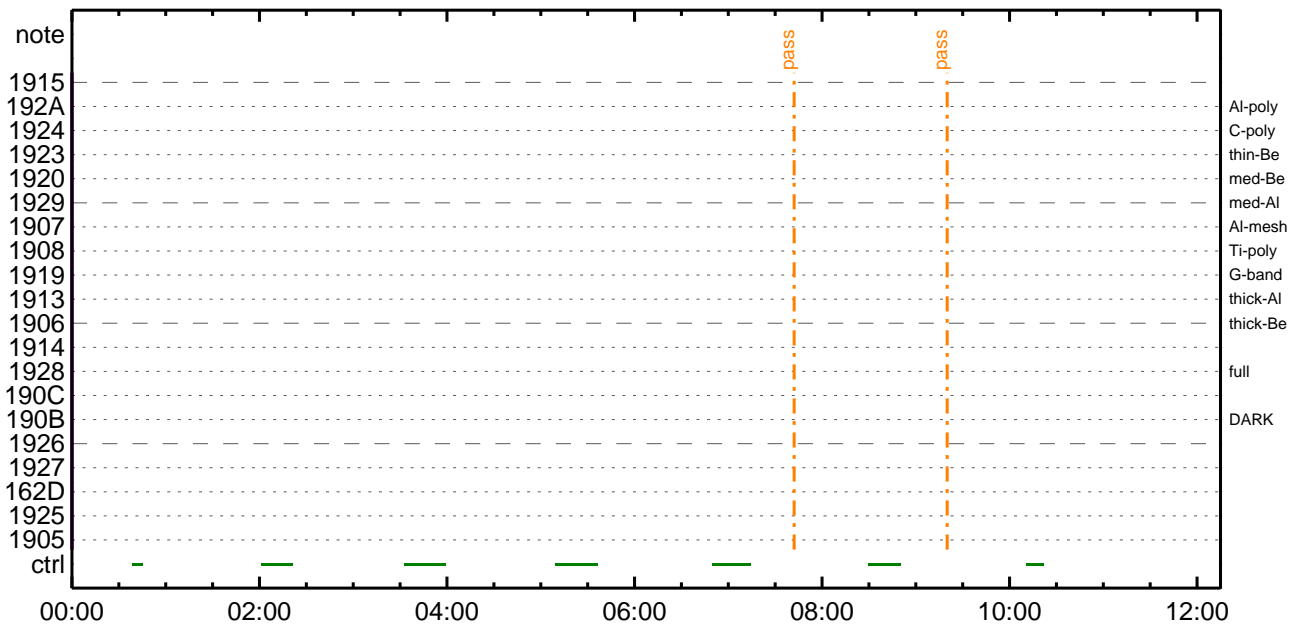
CMDI #0897 2012/09/09



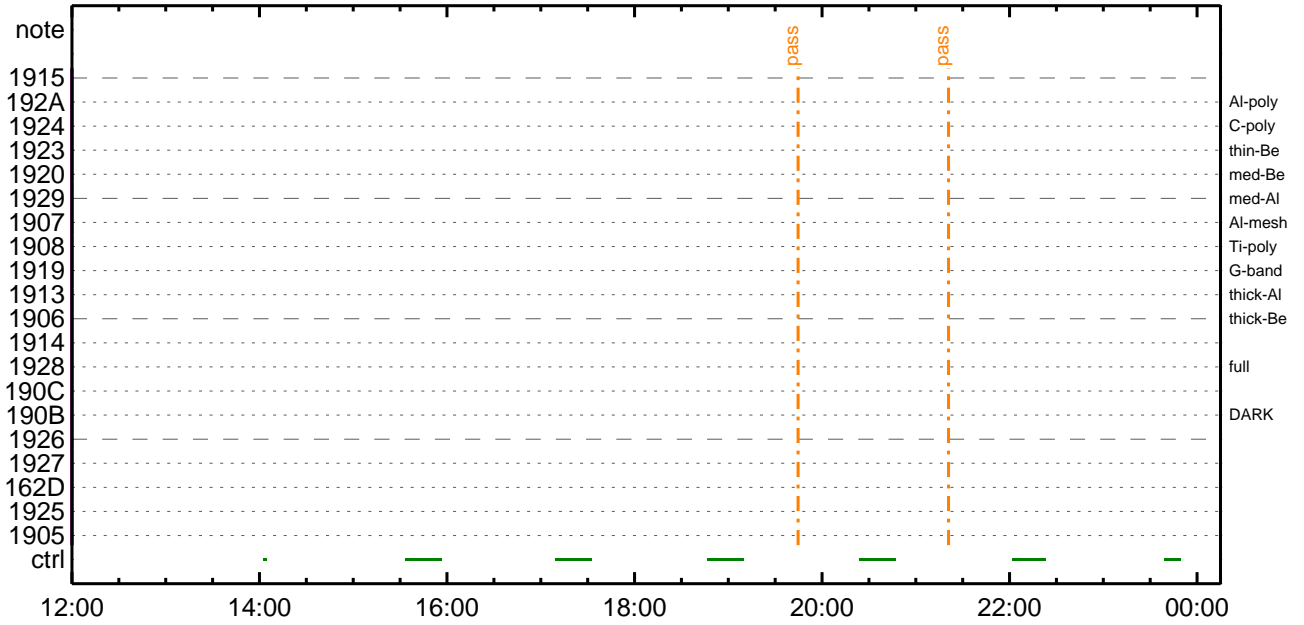
CMDI #0897 2012/09/09



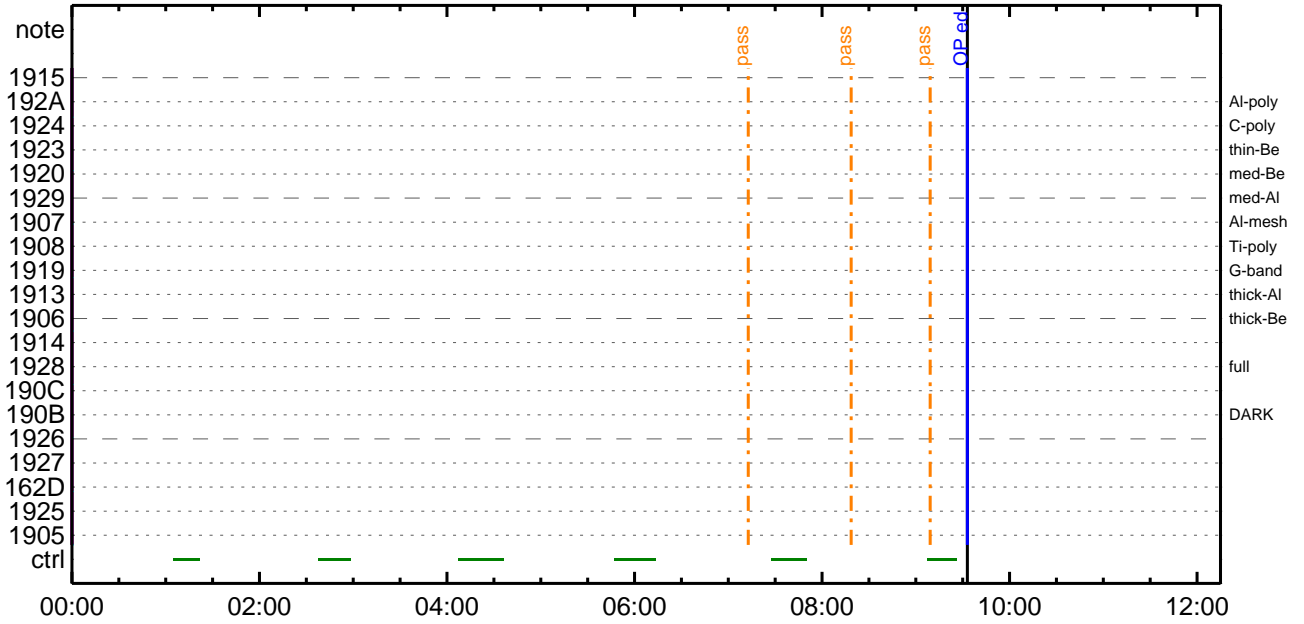
CMDI #0897 2012/09/10



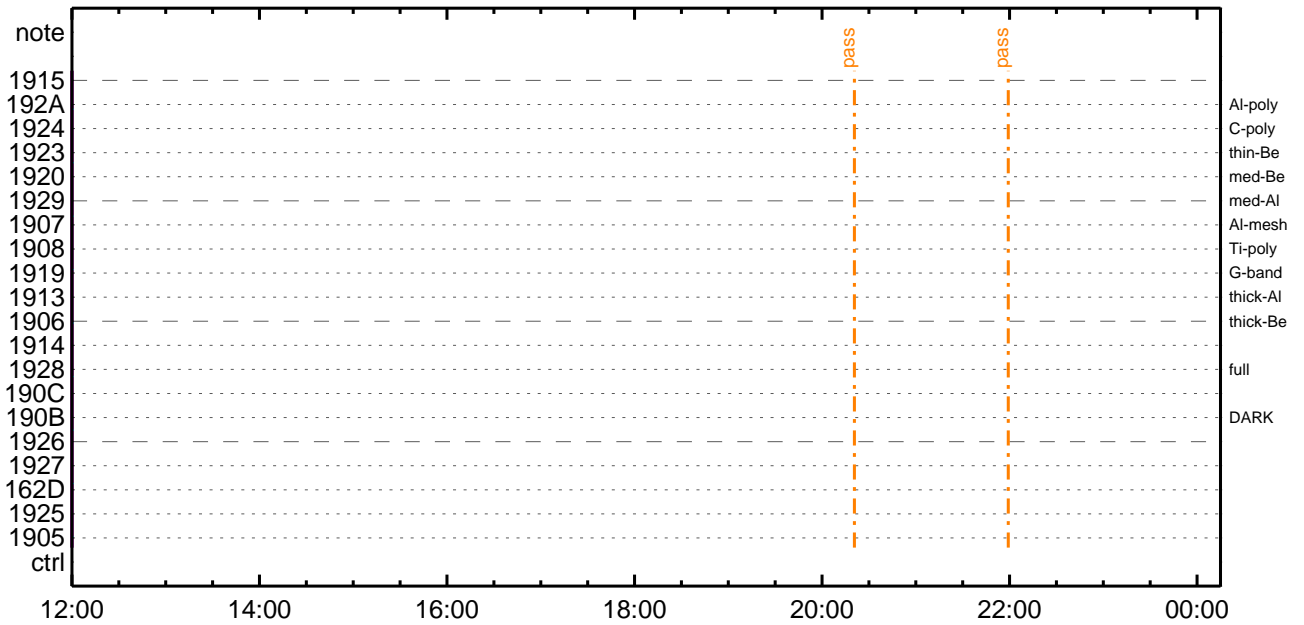
CMDI #0897 2012/09/10



CMDI #0897 2012/09/11



CMDI #0897 2012/09/11




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0096 C.      00000000; SET 0EDUMP 01A±0iYNY¹0Ç¹00|0³0E; f
0097 C.
0098 . C.  TIY³YBYÖYÉ00dADİ¿ (UT)
0099 +. TI 2012-09-06 09:38:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      0000 [HK1_TI_CMD_NUM] EQ      1COUNTUP
0102 C.
0103 +. TI 2012-09-06 09:38:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      0000 [HK1_TI_CMD_NUM] EQ      1COUNTUP
0106 C.
0107 +. TI 2012-09-06 09:38:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      0000 [HK1_TI_CMD_NUM] EQ      1COUNTUP
0110 C.
0111 +. TI 2012-09-06 09:42:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      0000 [HK1_TI_CMD_NUM] EQ      1COUNTUP
0114 C.
0115 C.      0E²¼0İÄè%îİÑ0İYÁY§YÁY-¹àİÜ
0116 C.      0000 [HK1_TI_CMD_ENA/DIS] EQ      ENA
0117 C.      0000 [HK1_TI_CMD_NUM] EQ      4
0118 C.      0000 [HK1_NEXT_EXEC_PIM] EQ      DHU
0119 C.      0000 [HK1_NEXT_EXEC_DC] EQ      0xB3
0120 C.
0121 . C.  *****
0122 C.  TIİİî°èYÁYÖY×
0123 C.  *****
0124 C.
0125 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      0000 [HK1_DMP_TOP_ADRS_1] EQ      07
0129 C.      0000 [HK1_DMP_TOP_ADRS_0] EQ      2B
0130 C.      0000 [HK1_DMP_BLOCK_NUM] EQ      3
0131 C.      0000 [HK1_DMP_REPEAT_NUM] EQ      0
0132 C.      0000 [HK1_DMA_DMP_PIM] EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      0000 [HK1_PKT_FORM_NO] EQ      7
0136 C.      0000 [HK1_PKT_GEN_TIME] EQ      0.25 s
0137 C.      0000 [HK1_S_TLM_BIT_RATE] EQ      32k
0138 C.      0000 [HK1_X_TLM_BIT_RATE] EQ      4M
0139 C.      0000 [HK1_DMP_CHK_FLG] EQ      EXEC
0140 C.
0141 . C.  YÁYÖY×¼²¹İ»0d³İÇ§
0142 C.      0000 [HK1_DMP_CHK_FLG] EQ      NON
0143 C.
0144 . C.  RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§
0145 C.
0146 . C.  DHUYâ;¼YÉ;È¼Y¼, Yî;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      0000 [HK1_PKT_FORM_NO] EQ      2
0150 C.      0000 [HK1_PKT_GEN_TIME] EQ      0.5S
0151 C.      0000 [HK1_S_TLM_BIT_RATE] EQ      32K
0152 C.      0000 [HK1_X_TLM_BIT_RATE] EQ      4M
0153 C.
0154 C.  *****
0155 C.  SOT TI command set
0156 C.  *****
0157 C.  Execute, after the success of OP upload.
0158 +. TI 2012-09-06 09:42:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 . C.  -----
0162 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0163 C.  -----
0164 C.  ***** SOT END *****
0165 . C.  Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C.  ***** Start EIS operation (TI set) *****
0169 C.  Execute, after the success of OP upload.
0170 C.  Set EIS TI-commands
0171 +. TI 2012-09-06 09:42:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC      (21 02)
0174 +. TI 2012-09-06 09:42:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC      (22)
0177 . C.      [ ] [HK1_TI_CMD_NUM] EQ      2 COUNTUP
0178 C.  ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C.  ***** XRT START *****
0183 C.  Execute, after the success of OP upload.
0184 +. TI 2012-09-06 09:42:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC      (c3)
0187 . C.      [ ] [HK1_TI_CMD_NUM] EQ      1COUNTUP
0188 C.
0189 C.  ***** XRT END *****
0190 C.
0191 . C.  ***** MDP `úÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C.  (¼â0İYÖYÁYÈYBÿYÁYÇYè0È¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 . C. < Dump RAMID=MDP_OBS_F >
0098 +. DC 07-F0 MDP_DUMP_FGTBL
0099 BC (82 07 00 00 00 38 b8)
0100 C. -----
0101 C. MDP_OBS_F verify = OK/NG [ ]
0102 C. -----
0103 C.
0104 C. *****
0105 C. SOT TI command set
0106 C. *****
0107 C. Execute, after the success of TBL upload.
0108 +. TI 2012-09-06 09:42:18.0
0109 DC 07-F0 MDP_SOT_MODE_OBSV
0110 BC (40)
0111 . C. -----
0112 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0113 C. -----
0114 C.
0115 C.
0116 . C. ***** MDP 'úÃîâî»ô¼ÝðËÂð¹æDCBC•x²è *****
0117 C. (¼áºîÿÓÿÄÿËÿÌÿÍÿÏÿÐÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿØÿÙÿºÿ»ÿ¼ÿ½ÿ¾ÿ¿ÿÀÿÁÿÂÿÃÿ
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑæ´Ø¹æDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿËÿÏÿÛÿ-¼Â»Û;ä
0128 C.
0129 . C. ***** LOS *****
0130 C.

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*** OP Sequence for XRT ***

2012/09/06	09:52:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	09:53:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/09/06	09:55:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/06	09:55:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/06	09:55:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/06	09:55:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/06	09:55:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/06	09:55:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	09:55:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/09/06	09:55:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/06	09:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	13:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/06	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/06	14:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/06	14:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/06	14:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/06	14:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/09/06	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	14:14:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	14:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 b1 59 00 cc				
2012/09/06	14:17:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/06	14:17:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/06	14:17:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/06	14:17:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/06	14:17:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/06	14:17:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	14:17:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/09/06	14:17:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/06	14:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	14:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	14:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	14:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/06	14:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/06	14:56:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/06	14:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	16:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	16:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	16:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/06	16:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/06	16:43:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/06	16:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	17:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	17:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	17:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/06	17:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/06	18:20:00.0	XRT_Custom_434_OG [0x1b2]							

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2012/09/06	18:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	19:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	19:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	19:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/06	19:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/06	19:57:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/06	19:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	21:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	21:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	21:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/06	21:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/06	21:34:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/06	21:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/06	22:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/06	22:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/06	22:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/06	22:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/06	23:08:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/06	23:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/07	00:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/07	00:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/07	00:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/07	00:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/07	00:33:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/07	00:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/07	01:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/07	01:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/07	01:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/07	01:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/07	02:08:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/07	02:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/07	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/07	03:23:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/07	03:23:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/07	03:26:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/07	03:45:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/07	03:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/07	04:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/07	04:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/07	04:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/07	04:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/07	05:23:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/07	05:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/07	06:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/07	06:24:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/07	06:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/07	06:25:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/07	06:25:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/07	06:25:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/07	06:27:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				

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2012/09/07	06:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/09/07	06:34:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/09/07	06:35:00.0	AOCS_OrE-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	03 00 00 00 00					
2012/09/07	06:37:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/09/07	06:37:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/09/07	06:37:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/09/07	06:37:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/09/07	06:37:52.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/09/07	06:37:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/09/07	06:37:56.0	XRT_QT_PROG_SET_411_OG [0x19b]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11					
2012/09/07	06:37:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2012/09/07	06:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/09/07	06:40:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/09/07	06:40:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/09/07	06:43:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/09/07	07:00:30.0	XRT_Custom_434_OG [0x1b2]								
2012/09/07	07:01:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/09/07	08:15:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/09/07	08:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/09/07	08:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/09/07	08:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/09/07	08:37:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/07	08:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/09/07	08:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/09/07	09:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	01 00 00 00 00					
2012/09/07	09:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/09/07	09:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/09/07	09:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/09/07	09:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/09/07	09:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/09/07	09:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/09/07	09:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13					
2012/09/07	09:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2012/09/07	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/09/07	09:56:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/09/07	09:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/09/07	09:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/09/07	09:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/09/07	10:10:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/07	10:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/09/07	11:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/09/07	12:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	03 00 00 00 00					
2012/09/07	12:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/09/07	12:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/09/07	12:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/09/07	12:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/09/07	12:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/09/07	12:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					

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2012/09/07	12:02:56.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2012/09/07	12:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/07	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	13:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/09/07	14:00:00.5	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2012/09/07	14:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/07	14:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/07	14:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/07	14:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2012/09/07	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	14:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	14:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 b1 59 00	cc
2012/09/07	14:12:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/09/07	14:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/07	14:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/07	14:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/07	14:12:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/07	14:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	14:12:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2012/09/07	14:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/07	14:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	15:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	15:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	15:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/07	15:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	15:34:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	15:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	15:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	15:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/07	15:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	15:43:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	15:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	16:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	16:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	16:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/07	16:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	17:20:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	17:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	18:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	18:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/07	18:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	18:56:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	18:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	20:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	20:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	20:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					

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2012/09/07	20:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	20:34:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	20:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	21:48:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	21:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	21:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/07	21:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	22:10:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	22:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/07	23:25:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/07	23:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/07	23:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/07	23:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/07	23:38:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/07	23:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/08	00:54:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/08	00:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/08	00:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/08	00:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/08	01:08:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/08	01:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/08	02:24:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/08	02:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/08	02:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/08	02:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/08	02:45:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/08	02:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/08	03:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/08	03:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/08	03:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/08	03:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/08	04:22:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/08	04:23:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/08	05:33:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/08	05:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/08	05:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/08	05:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/08	06:00:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/08	06:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/08	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/08	06:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/09/08	06:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/09/08	06:10:16.0	XRT_FLD_DIS_404_OG [0x194]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/08	06:10:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/08	06:10:20.0	XRT_ARS_DIS_406_OG [0x196]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/08	06:12:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/09/08	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/08	06:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/08	06:20:00.0	AOCs_OrE-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2012/09/08	06:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					

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2012/09/08	06:22:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/09/08	06:22:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/09/08	06:22:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/08	06:22:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/08	06:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/08	06:22:56.0	XRT_QT_PROG_SET_411_OG [0x19b]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2012/09/08	06:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/09/08	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/08	07:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/08	07:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/08	07:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/08	07:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/08	07:37:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/08	07:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/08	08:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/08	08:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/08	08:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/08	08:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/08	09:13:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/08	09:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/08	10:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/08	10:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/08	10:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/08	10:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/08	11:06:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/08	11:07:00.5	AOCS_ORe-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00	00	00	00