

# XRT Timeline to be uploaded on 2012/09/18

Period: 2012/09/18 10:37:00 - 2012/09/22 09:16:00

\* \* \* \* \*

Normal mode

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XOB #192E: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1)													
Term	Pointing (x, y)					Comment							
09/18 10:50:00 - 09/18 10:56:54	Fixed ( 0.0, 0.0)					# OP start + 10min, HOP186 and synoptic							
<b>PROG= 04 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 63 1-time(s) 4.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Seqn= 6 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Seqn= 70 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 69 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Subr= 2 1-time(s) 6.0sec													
└─ Seqn= 66 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Subr= 3 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>													

## XOB #1933: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 512x512 at 1064 1048, 10s-cad w/ G-Band VLS Closed Test-2

Term	Pointing (x, y)					Comment							
09/18 11:00:00 - 09/18 11:15:24	Fixed ( -910.0, -300.0)					AR obs at the S-E limb							
<b>PROG= 11 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 18 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
└─ Seqn= 65 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 72 150-time(s) 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 10.0sec													
└─ Seqn= 72 150-time(s) 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 10.0sec													
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>													

## XOB #1932: CME watch - 4x4 - AEC 2 - Ti/poly - 35s cad

Term	Pointing (x, y)					Comment							
09/18 11:18:30 - 09/18 11:40:24	Fixed ( -9.0, -945.0)					HOP79 (1/20)							
09/18 11:43:30 - 09/18 12:05:24	Fixed ( -9.0, -885.0)					(2/20)							
09/18 12:08:30 - 09/18 12:30:24	Fixed ( -9.0, -785.0)					(3/20)							
09/18 12:33:30 - 09/18 12:55:24	Fixed ( -9.0, -685.0)					(4/20)							
09/18 12:58:30 - 09/18 13:20:24	Fixed ( -9.0, -585.0)					(5/20)							
09/18 13:23:30 - 09/18 14:04:00	Fixed ( -9.0, -485.0)					(6/20)							
09/18 14:12:30 - 09/18 14:34:24	Fixed ( -9.0, -385.0)					(7/20)							
09/18 14:37:30 - 09/18 14:59:24	Fixed ( -9.0, -285.0)					(8/20)							
09/18 15:02:30 - 09/18 15:36:00	Fixed ( -9.0, -185.0)					(9/20)							
09/18 16:03:30 - 09/18 16:25:24	Fixed ( -9.0, -85.0)					(10/20)							
09/18 17:40:00 - 09/18 18:01:54	Fixed ( -9.0, 105.0)					(12/20)							
09/18 18:05:00 - 09/18 18:49:30	Fixed ( -9.0, 205.0)					(13/20)							
09/18 19:17:00 - 09/18 19:38:54	Fixed ( -9.0, 305.0)					(14/20)							
09/18 19:42:00 - 09/18 20:27:00	Fixed ( -9.0, 405.0)					(15/20)							
09/18 20:54:30 - 09/18 21:16:24	Fixed ( -9.0, 505.0)					(16/20)							
09/18 21:19:30 - 09/18 22:04:30	Fixed ( -9.0, 605.0)					(17/20)							

09/18 22:30:00 - 09/18 22:51:54 Fixed ( -9.0, 705.0) (18/20)  
 09/18 22:55:00 - 09/18 23:42:00 Fixed ( -9.0, 805.0) (19/20)  
 09/18 23:56:00 - 09/19 00:14:00 Fixed ( -9.0, 905.0) (20/20)

**PROG= 14 Inf.-time(s)**

Subr= 1	1-time(s)	35.0sec											
└─ Seqn= 34	1-time(s)	4.0sec											
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18**

Term	Pointing (x, y)	Comment
09/18 16:32:30 - 09/18 17:12:30	Fixed ( -9.0, 5.0)	(11/20)

**PROG= 13 1-time(s)**

Subr= 1	1-time(s)	14.0sec											
└─ Seqn= 64	1-time(s)	4.0sec											
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6	1-time(s)	2.0sec											
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 70	1-time(s)	4.0sec											
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 67	1-time(s)	2.0sec											
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 69	1-time(s)	2.0sec											
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
└─ Seqn= 68	1-time(s)	2.0sec											
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops**

Term	Pointing (x, y)	Comment
09/18 11:00:00 - 09/18 11:15:24	Fixed ( -910.0, -300.0)	AR obs at the S-E limb

**PROG= 16 15-time(s)**

Subr= 1	45-time(s)	10.0sec											
└─ Seqn= 35	1-time(s)	2.0sec											
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	10.0sec											
└─ Seqn= 36	1-time(s)	2.0sec											
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 37	1-time(s)	2.0sec											
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 38	1-time(s)	2.0sec											
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

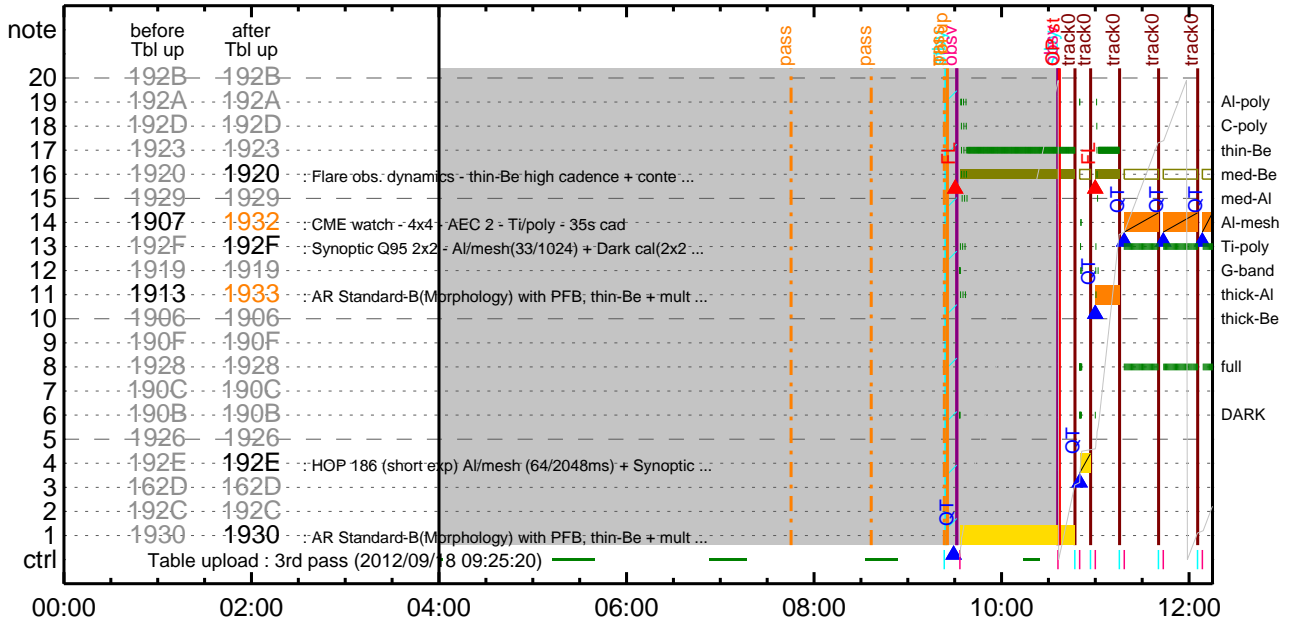
**Flare Detection**

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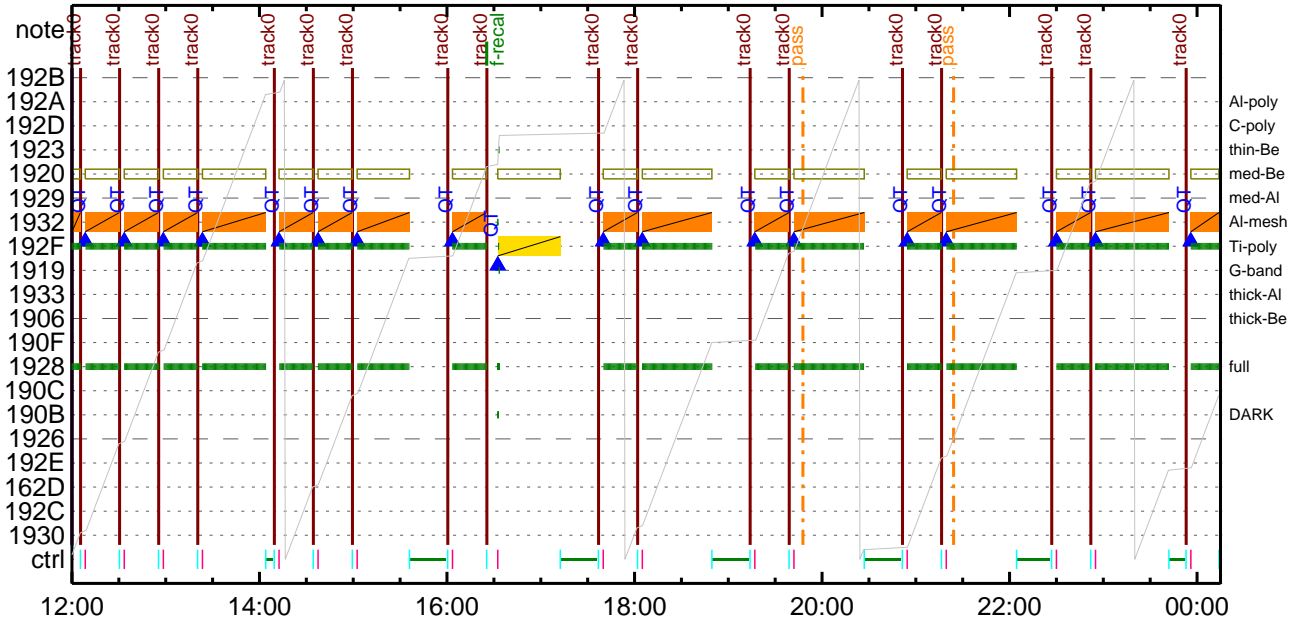
**FLD Patrol**

Term	Pointing (x, y)	Comment
09/18 10:59:46 - 09/18 11:18:18	Fixed ( -910.0, -300.0)	AR obs at the S-E limb
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

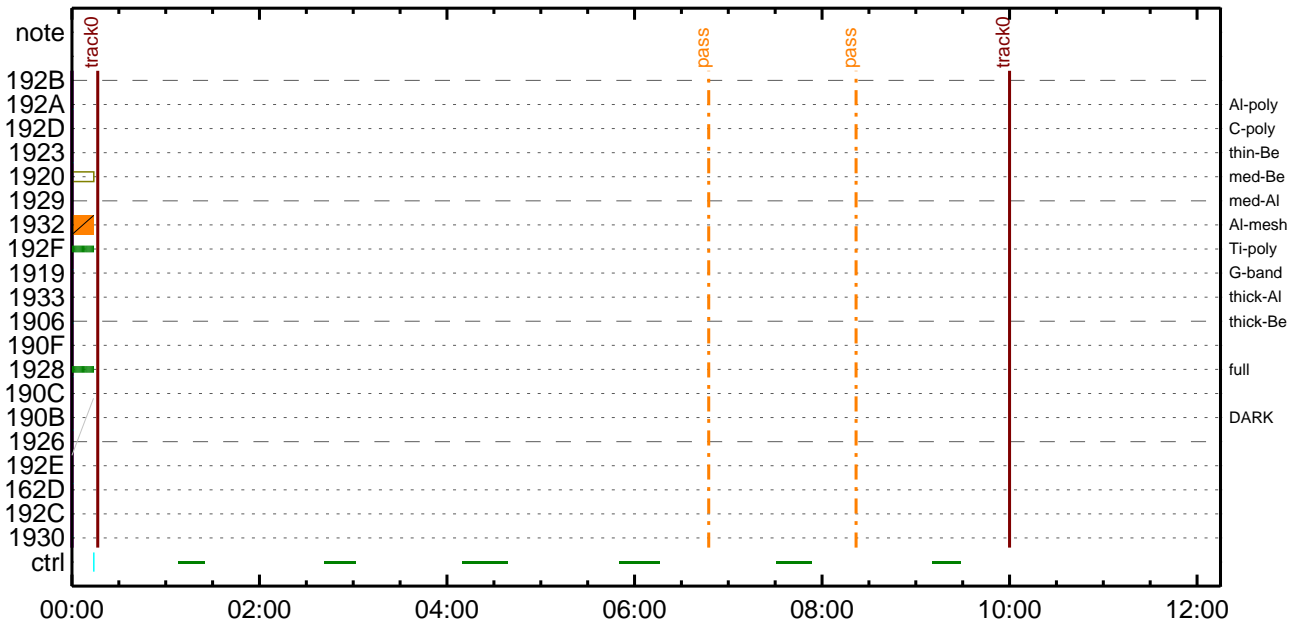
### CMDI #0924 2012/09/18



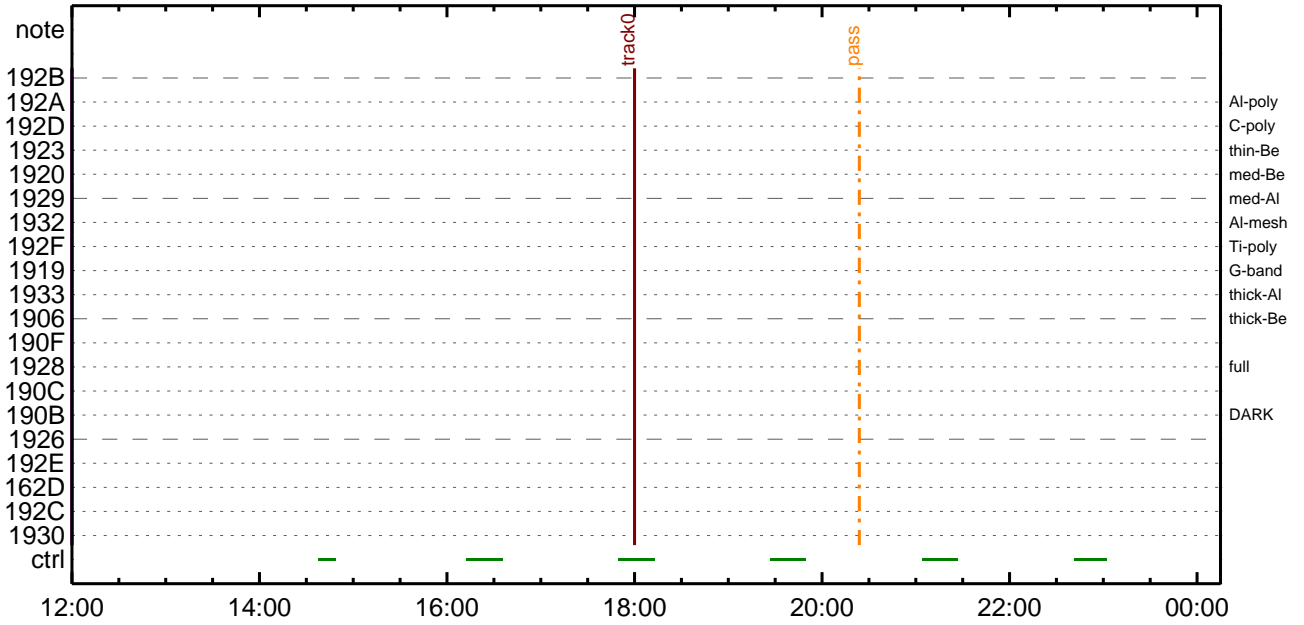
### CMDI #0924 2012/09/18



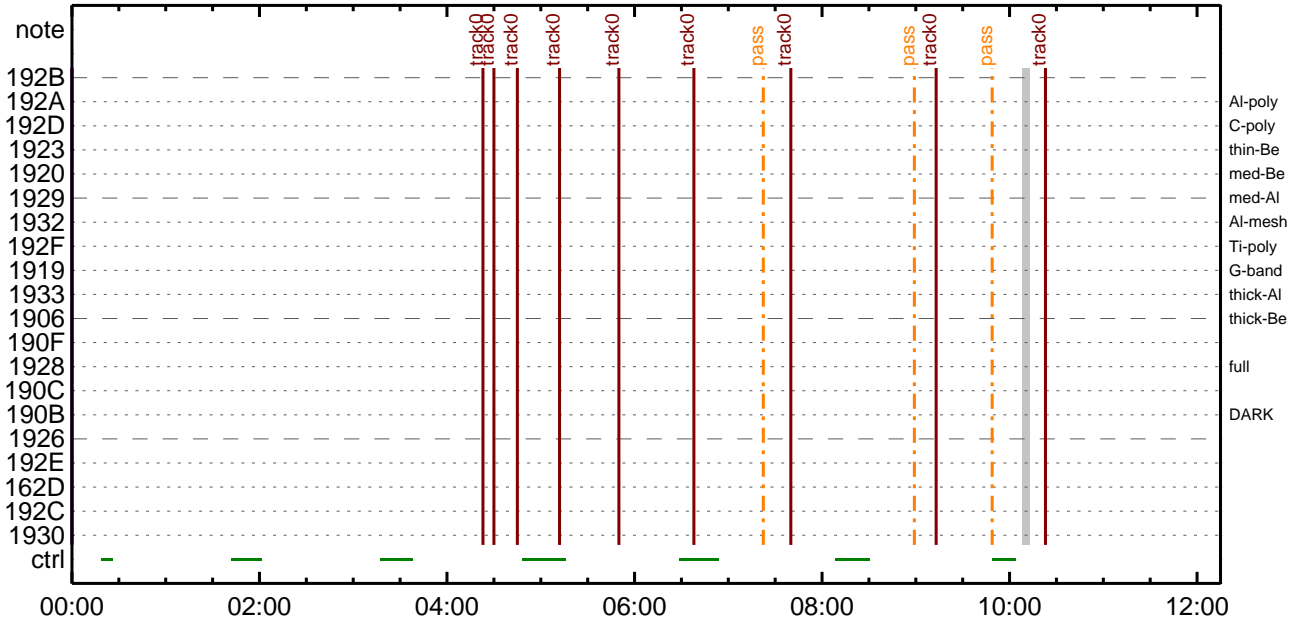
### CMDI #0924 2012/09/19



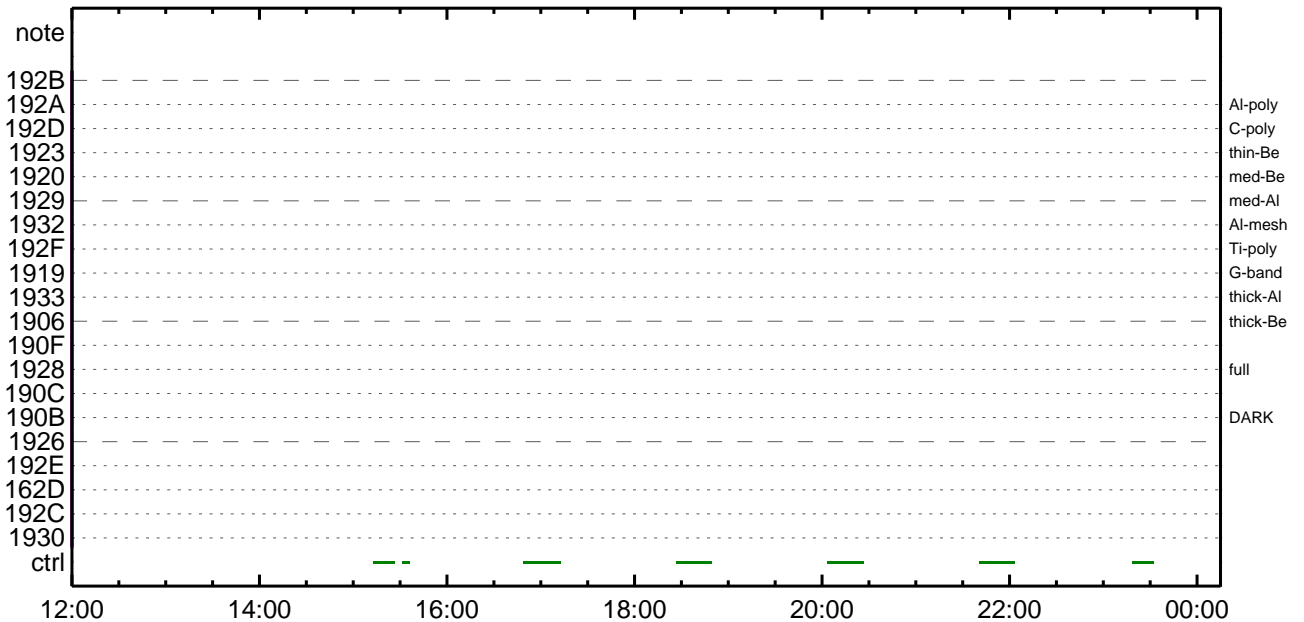
CMDI #0924 2012/09/19



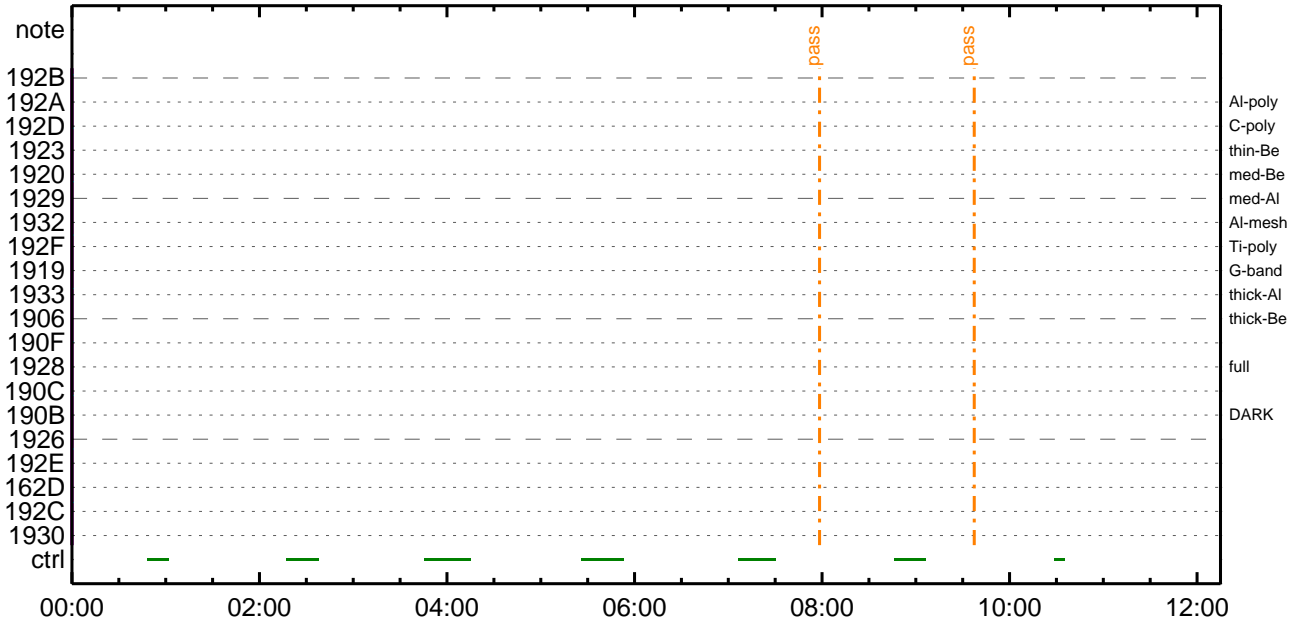
CMDI #0924 2012/09/20



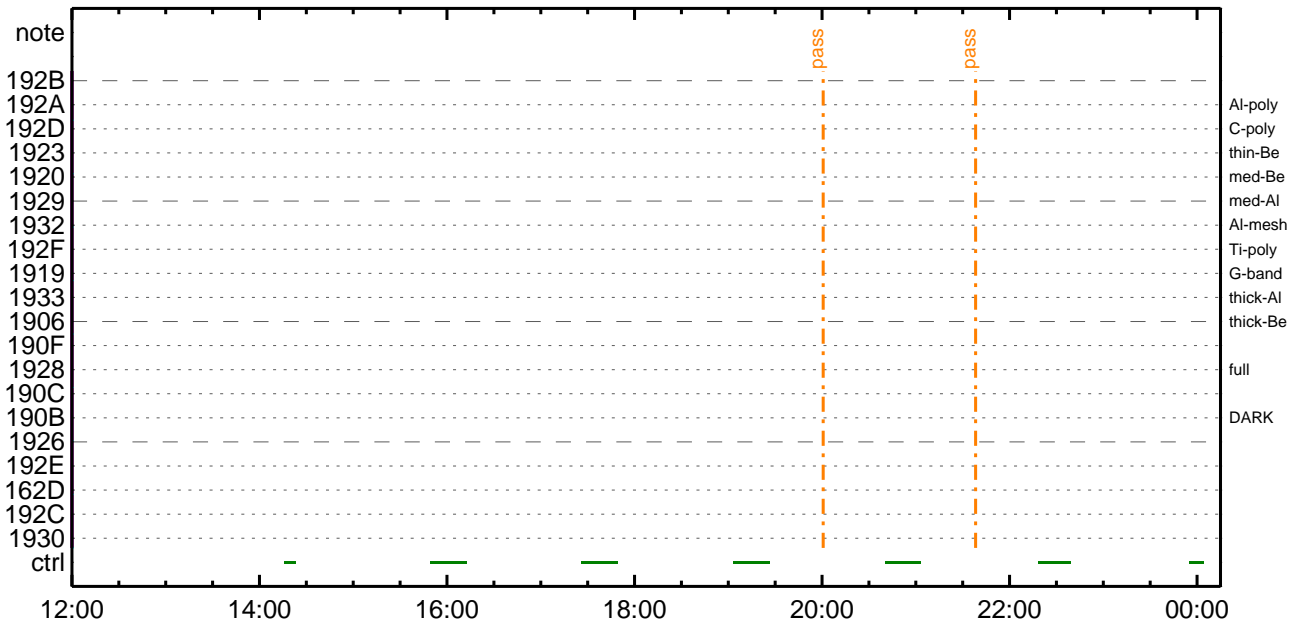
CMDI #0924 2012/09/20



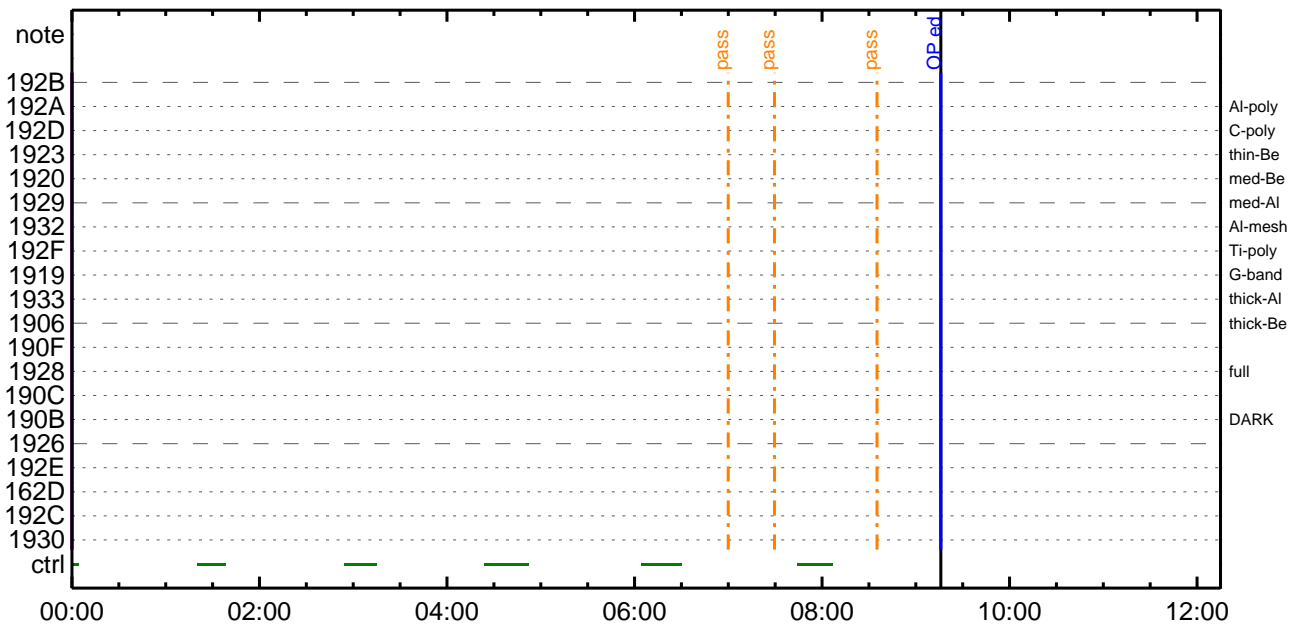
CMDI #0924 2012/09/21



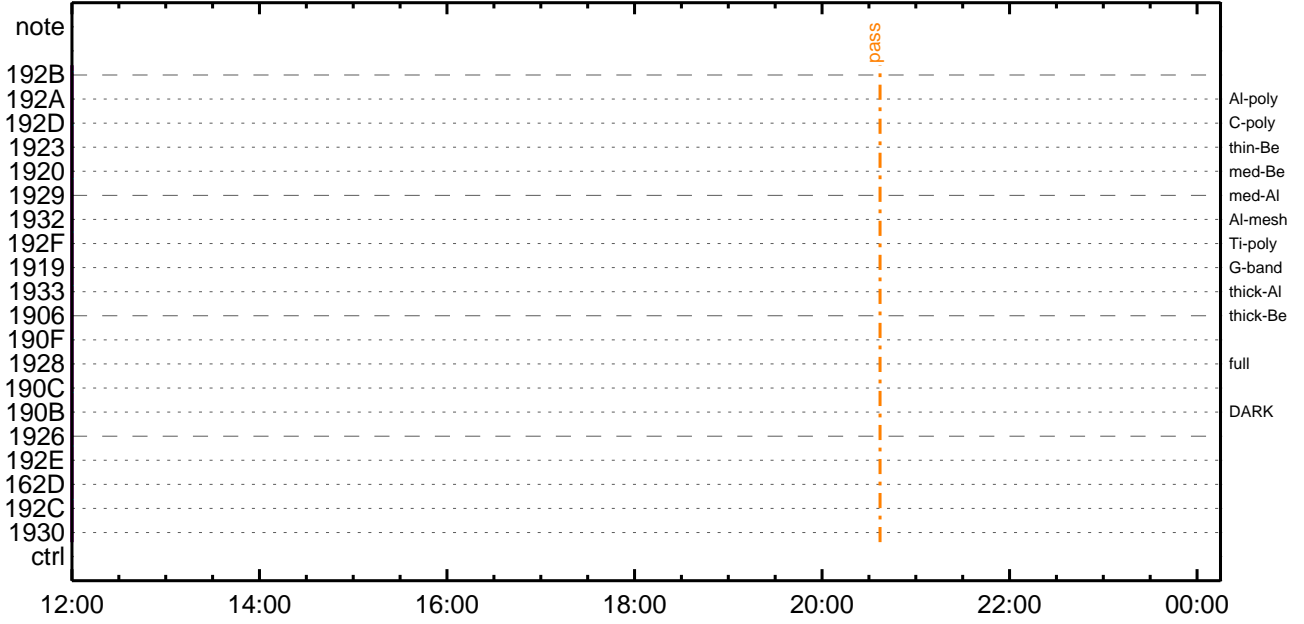
CMDI #0924 2012/09/21



CMDI #0924 2012/09/22



CMDI #0924 2012/09/22





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-151:OP
0104 ( )
0105 S. OG og-151:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ      40
0113 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0114 C.          çç[HK1_DMP_BLOCK_NUM]          EQ     127
0115 C.          çç[HK1_DMP_REPEAT_NUM]         EQ      0
0116 C.          çç[HK1_DMA_DMP_PIM]            EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.          çç[HK1_PKT_FORM_NO]            EQ      7
0120 C.          çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0121 C.          çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0122 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0123 C.          çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0124 C. YAYOXx½^a î»oð³ îÇ§
0125 C.          çç[HK1_DMP_CHK_FLG]            EQ     NON
0126 C. RAM ID=NMOG□î¼E¹ç•ë² îOKoð³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ     41
0132 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0133 C.          çç[HK1_DMP_BLOCK_NUM]          EQ     127
0134 C.          çç[HK1_DMP_REPEAT_NUM]         EQ      0
0135 C.          çç[HK1_DMA_DMP_PIM]            EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.          çç[HK1_PKT_FORM_NO]            EQ      7
0139 C.          çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0140 C.          çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0141 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0142 C.          çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0143 C. YAYOXx½^a î»oð³ îÇ§
0144 C.          çç[HK1_DMP_CHK_FLG]            EQ     NON
0145 C. RAM ID=NMOG□î¼E¹ç•ë² îOKoð³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ     42
0151 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0152 C.          çç[HK1_DMP_BLOCK_NUM]          EQ     65
0153 C.          çç[HK1_DMP_REPEAT_NUM]         EQ      0
0154 C.          çç[HK1_DMA_DMP_PIM]            EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.          çç[HK1_PKT_FORM_NO]            EQ      7
0158 C.          çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0159 C.          çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0160 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0161 C.          çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0162 C. YAYOXx½^a î»oð³ îÇ§
0163 C.          çç[HK1_DMP_CHK_FLG]            EQ     NON
0164 C. RAM ID=NMOG,RAM ID=OP□î¼E¹ç•ë² îOKoð³ îÇ§
0165 C.
0166 C. ***** oE²¼o î¼A´¶A°oEÉ¬oA÷¿@ (¼âµ-YAYOXx½^e¼çoðAÓAæoÇ¼^a°oE¼i¹çoçoâ) *****
0167 C. DHUYâ;4YE;E¼Y½;Yi;4YE;Eoðîâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.          çç[HK1_PKT_FORM_NO]            EQ      2
0171 C.          çç[HK1_PKT_GEN_TIME]           EQ     0.5S
0172 C.          çç[HK1_S_TLM_BIT_RATE]         EQ     32K
0173 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOAD¬-Á÷¿@NGuî¼i¹ç;ç°E²¼o îTI-CMDÁ÷¿@î¼A¹Ôo•oEoð³ oE;f
0180 C.          oEo¿;çSEToEDUMPaîE±°iYNY¹oç¹Ôo|o³oE;f
0181 C.
0182 C. TIY³YpYóYÉoðAÐî¿¿(UT)
0183 +. TI 2012-09-18 10:32:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.          çç[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186 C.
0187 +. TI 2012-09-18 10:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.          çç[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190 C.
0191 +. TI 2012-09-18 10:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.          çç[HK1_TI_CMD_NUM]              EQ     1COUNTUP

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0194 C.
0195 +. TI 2012-09-18 10:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄë%îíñòîŷÄŷ§ŷÄŷ-¹àîŷ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLîŷ°èŷÄŷÖŷ×½ªî»ò³îç§
0229 C.
0230 C. DHUŷä;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Êòðîäñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-09-18 10:36:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2012-09-18 10:36:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2012-09-18 10:36:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC          (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-09-18 10:36:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîñî»ö¼ŷ¼òËÄðñ¹òèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷÉŷÄŷçŷèò¼ªñ¼ª»ŷñ¹òè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹.İ Daily±çîñòË'Øñ¹òèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»ŷ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 01)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 10)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2012-09-18 10:36:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0165 BC (c0)
0166 +. TI 2012-09-18 10:36:04.0
0167 DC 07-F0 MDP_XRT_CTRL_AUTO
0168 BC (c0)
0169 . C. ----- Success Verify ? OK / NG ____
0170 C.
0171 C. ***** XRT END *****
0172 . C. *****
0173 C. SOT table upload
0174 C. *****
0175 . C. < Stop FG table >
0176 +. DC 07-F0 MDP_FG_CTRL_MANU
0177 BC (51)
0178 . C. -----
0179 C. MDP_FG_CTRL_MODE = MANU [ ]
0180 C. -----
0181 C.
0182 . C. <Upload FG Observation Table>
0183 . S. RAM ram-263:MDP_OBS_F
0184 ( )
0185 C.
0186 . C. < Dump RAMID=MDP_OBS_F >
0187 +. DC 07-F0 MDP_DUMP_FGTBL
0188 BC (82 07 00 00 00 38 b8)
0189 C. -----
0190 C. MDP_OBS_F verify = OK/NG [ ]
0191 C. -----
0192 C.
0193 . C. < Upload DPL table >

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0194 C.
0195 C. STS_CHK=OFF
0196 C.
0197 . S. RAM ram-271:MDP_DPL
0198 ( )
0199 C.
0200 . C. < Dump RAMID=MDP_DPL >
0201 +. DC 07-F0 MDP_DUMP_FGTBL
0202 BC (82 07 00 38 b8 00 40)
0203 C. -----
0204 C. MDP_DPL verify = OK [ ]
0205 C. -----
0206 C.
0207 C. STS_CHK=ON
0208 C.
0209 . C. < Update MDP DSC PAR1 >
0210 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0211 BC (4c)
0212 C. MDP_CMD_CODE = F04C0700[ ]
0213 C. MDP_CMD_CNT (count-up 1) [ ]
0214 C. -----
0215 C.
0216 . C.
0217 C. *****
0218 C. SOT TI command set
0219 C. *****
0220 C. Execute, after the success of TBL upload.
0221 +. TI 2012-09-18 10:36:18.0
0222 DC 07-F0 MDP_SOT_MODE_OBSV
0223 BC (40)
0224 C. -----
0225 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0226 C. -----
0227 C.
0228 C.
0229 . C. ***** MDP 'uAIAI'>YAEADDCBC*x2 *****
0230 C. (%a^iYAYEYpYAYcYeE%4a%A>Ua^e)
0231 . S. DC-BC dcbc-402:DCBC
0232 (MDP_known_event)
0233 C.
0234 C.
0235 . C. ***** YDY^I Daily+;INe'0a^eDCBC*x2 *****
0236 . S. DC-BC dcbc-153:DCBC
0237 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0238 C.
0239 C.
0240 . C. ;aLOSAYSYA^-A>U;a
0241 C.
0242 . C. ***** LOS *****
0243 C.

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\*\*\* OP Sequence for XRT \*\*\*

2012/09/18	10:46:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/18	10:46:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/09/18	10:47:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2012/09/18	10:47:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/09/18	10:47:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/09/18	10:47:20.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/18	10:49:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2012/09/18	10:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/18	10:56:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/18	10:57:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 1a a7 50 e5		
2012/09/18	10:59:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/09/18	10:59:46.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/09/18	10:59:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/09/18	10:59:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/18	10:59:52.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/18	10:59:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/18	10:59:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2012/09/18	10:59:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/09/18	11:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/18	11:15:24.0	XRT_CTRL_MANU_442_OG [0x1ba]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/18	11:15:30.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 54 00 00 cc		
2012/09/18	11:17:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/09/18	11:18:18.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/09/18	11:18:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/09/18	11:18:22.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/18	11:18:24.0	XRT_ARS_DIS_412_OG [0x19c]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/18	11:18:26.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/18	11:18:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2012/09/18	11:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/18	11:40:24.0	XRT_CTRL_MANU_442_OG [0x1ba]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/18	11:40:30.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 4e a7 00 cc		
2012/09/18	11:42:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/09/18	11:43:18.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/09/18	11:43:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/09/18	11:43:22.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/18	11:43:24.0	XRT_ARS_DIS_412_OG [0x19c]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/18	11:43:26.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/18	11:43:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2012/09/18	11:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/18	12:05:24.0	XRT_CTRL_MANU_442_OG [0x1ba]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/18	12:05:30.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	00 45 ca 00 cc		
2012/09/18	12:07:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/09/18	12:08:18.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/09/18	12:08:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/09/18	12:08:22.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/18	12:08:24.0	XRT_ARS_DIS_412_OG [0x19c]					

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2012/09/18	12:08:26.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	12:08:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2012/09/18	12:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	12:30:24.0	XRT_CTRL_MANU_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	12:30:30.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	3c	e5	00 cc
2012/09/18	12:32:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/09/18	12:33:18.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	12:33:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	12:33:22.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	12:33:24.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	12:33:26.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	12:33:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2012/09/18	12:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	12:55:24.0	XRT_CTRL_MANU_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	12:55:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	34	00	00 cc
2012/09/18	12:57:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/09/18	12:58:18.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	12:58:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	12:58:22.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	12:58:24.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	12:58:26.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	12:58:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2012/09/18	12:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	13:20:24.0	XRT_CTRL_MANU_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	13:20:30.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	2b	1a	00 cc
2012/09/18	13:22:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/09/18	13:23:18.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	13:23:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	13:23:22.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	13:23:24.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	13:23:26.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	13:23:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2012/09/18	13:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	14:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	14:04:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	14:04:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/18	14:07:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/18	14:09:24.0	XRT_CTRL_MANU_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	14:09:30.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	22	35	00 cc
2012/09/18	14:11:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/09/18	14:12:18.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	14:12:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	14:12:22.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	14:12:24.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	14:12:26.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	14:12:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		

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2012/09/18	14:12:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/18	14:34:24.0	XRT_CTRL_MANU_442_OG [0x1ba] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/18	14:34:30.0	AOCS_ORe-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00 19 58 00 cc
2012/09/18	14:36:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/09/18	14:37:18.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2012/09/18	14:37:20.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/09/18	14:37:22.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/18	14:37:24.0	XRT_ARS_DIS_412_OG [0x19c] MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/18	14:37:26.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/18	14:37:28.0	XRT_QT_PROG_SET_418_OG [0x1a2] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/09/18	14:37:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/18	14:59:24.0	XRT_CTRL_MANU_442_OG [0x1ba] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/18	14:59:30.0	AOCS_ORe-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00 10 72 00 cc
2012/09/18	15:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/09/18	15:02:18.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2012/09/18	15:02:20.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/09/18	15:02:22.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/18	15:02:24.0	XRT_ARS_DIS_412_OG [0x19c] MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/18	15:02:26.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/18	15:02:28.0	XRT_QT_PROG_SET_418_OG [0x1a2] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/09/18	15:02:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/18	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/18	15:36:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/18	15:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/18	15:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/18	16:00:24.0	XRT_CTRL_MANU_442_OG [0x1ba] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/18	16:00:30.0	AOCS_ORe-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00 07 8d 00 cc
2012/09/18	16:02:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/09/18	16:03:18.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2012/09/18	16:03:20.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/09/18	16:03:22.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/18	16:03:24.0	XRT_ARS_DIS_412_OG [0x19c] MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/18	16:03:26.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/18	16:03:28.0	XRT_QT_PROG_SET_418_OG [0x1a2] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/09/18	16:03:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/18	16:25:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/18	16:25:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8] XRT_FOCUS_RECAL	2	07-F8	78 00
2012/09/18	16:25:30.0	AOCS_ORe-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00 ff 8e 00 cc
2012/09/18	16:29:26.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/09/18	16:29:46.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2012/09/18	16:29:48.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/09/18	16:29:50.0	XRT_ARS_DIS_406_OG [0x196] MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/18	16:32:28.0	XRT_QT_PROG_SET_419_OG [0x1a3] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/09/18	16:32:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/18	17:12:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/18	17:12:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/18	17:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			



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2012/09/18	17:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/18	17:36:54.0	XRT_CTRL_MANU_442_OG [0x1ba]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	17:37:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]							
			AOCU_NM	5	02-76	00 f6 a8 00 cc			
2012/09/18	17:39:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/18	17:39:48.0	XRT_FLD_DIS_404_OG [0x194]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	17:39:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	17:39:52.0	XRT_AEC_RESET_423_OG [0x1a7]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	17:39:54.0	XRT_ARS_DIS_412_OG [0x19c]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	17:39:56.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	17:39:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2012/09/18	17:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	18:01:54.0	XRT_CTRL_MANU_442_OG [0x1ba]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	18:02:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]							
			AOCU_NM	5	02-76	00 ed cb 00 cc			
2012/09/18	18:04:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/18	18:04:48.0	XRT_FLD_DIS_404_OG [0x194]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	18:04:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	18:04:52.0	XRT_AEC_RESET_423_OG [0x1a7]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	18:04:54.0	XRT_ARS_DIS_412_OG [0x19c]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	18:04:56.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	18:04:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2012/09/18	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	18:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	18:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	18:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/18	18:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/18	19:13:54.0	XRT_CTRL_MANU_442_OG [0x1ba]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	19:14:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
			AOCU_NM	5	02-76	00 e4 e6 00 cc			
2012/09/18	19:16:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/18	19:16:48.0	XRT_FLD_DIS_404_OG [0x194]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	19:16:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	19:16:52.0	XRT_AEC_RESET_423_OG [0x1a7]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	19:16:54.0	XRT_ARS_DIS_412_OG [0x19c]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	19:16:56.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	19:16:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2012/09/18	19:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	19:38:54.0	XRT_CTRL_MANU_442_OG [0x1ba]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	19:39:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]							
			AOCU_NM	5	02-76	00 dc 00 00 cc			
2012/09/18	19:41:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/18	19:41:48.0	XRT_FLD_DIS_404_OG [0x194]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	19:41:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	19:41:52.0	XRT_AEC_RESET_423_OG [0x1a7]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	19:41:54.0	XRT_ARS_DIS_412_OG [0x19c]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	19:41:56.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	19:41:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2012/09/18	19:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/18	20:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2012/09/18	20:27:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/18	20:27:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/18	20:30:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/18	20:51:24.0	XRT_CTRL_MANU_442_OG [0x1ba]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/18	20:51:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 d3 1b 00 cc				
2012/09/18	20:53:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/18	20:54:18.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/18	20:54:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/18	20:54:22.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/18	20:54:24.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/18	20:54:26.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/18	20:54:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/09/18	20:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/18	21:16:24.0	XRT_CTRL_MANU_442_OG [0x1ba]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/18	21:16:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00 ca 36 00 cc				
2012/09/18	21:18:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/18	21:19:18.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/18	21:19:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/18	21:19:22.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/18	21:19:24.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/18	21:19:26.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/18	21:19:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/09/18	21:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/18	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/18	22:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/18	22:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/18	22:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/18	22:26:54.0	XRT_CTRL_MANU_442_OG [0x1ba]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/18	22:27:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00 c1 59 00 cc				
2012/09/18	22:29:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/18	22:29:48.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/18	22:29:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/18	22:29:52.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/18	22:29:54.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/18	22:29:56.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/18	22:29:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/09/18	22:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/18	22:51:54.0	XRT_CTRL_MANU_442_OG [0x1ba]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/18	22:52:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00 b8 73 00 cc				
2012/09/18	22:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/18	22:54:48.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/18	22:54:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/18	22:54:52.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/18	22:54:54.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/18	22:54:56.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/18	22:54:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/09/18	22:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2012/09/18	23:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	23:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	23:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/18	23:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/18	23:52:54.0	XRT_CTRL_MANU_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/18	23:53:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 af 8e 00	cc		
2012/09/18	23:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2012/09/18	23:55:48.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/18	23:55:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/18	23:55:52.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/18	23:55:54.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/18	23:55:56.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/18	23:55:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2012/09/18	23:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/19	00:14:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/19	00:15:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_439_OG [0x1b7]	TCIB_XRT_S_HTR_A_ENA	0	04-BC				
2012/09/19	00:16:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 1a a7 50	e5		
2012/09/19	10:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 b3 8e 00	cc		
2012/09/19	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 1a a7 50	e5		
2012/09/20	04:23:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00 f7 bf 14	5a		
2012/09/20	04:30:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00 f7 bf 07	74		
2012/09/20	04:45:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00 f7 bf f7	e8		
2012/09/20	05:12:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00 f7 bf e8	5b		
2012/09/20	05:50:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]	AOCU_NM	5	02-76	00 f7 bf d9	40		
2012/09/20	06:38:00.0	AOCS_ORe-point_Start_29_OG [0x0b3]	AOCU_NM	5	02-76	00 f7 bf ca	25		
2012/09/20	07:40:00.0	AOCS_ORe-point_Start_30_OG [0x0b4]	AOCU_NM	5	02-76	00 f7 bf ba	25		
2012/09/20	09:13:00.0	AOCS_ORe-point_Start_31_OG [0x0b5]	AOCU_NM	5	02-76	00 f7 bf b0	cd		
2012/09/20	10:23:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00	00		