

XRT Timeline to be uploaded on 2012/09/20

Period: 2012/09/20 10:13:00 - 2012/09/25 10:03:00

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Normal mode

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XOB #1905: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long-2 - w leak image													
Term	Pointing (x, y)							Comment					
09/21 04:18:00 - 09/21 04:24:54	Fixed (-528.4, -528.4)							Post-bakeout four-quadrant pointing.					
PROG= 07 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 1 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 7 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 15 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1906: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long-w leak image													
Term	Pointing (x, y)							Comment					
09/21 04:28:00 - 09/21 04:34:54	Fixed (528.4, -528.4)												
PROG= 10 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 2 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 7 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 15 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1907: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long-w leak image													
Term	Pointing (x, y)							Comment					
09/21 04:38:00 - 09/21 04:44:54	Fixed (528.4, 528.4)												
PROG= 06 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 3 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 7 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 15 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1908: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long-w leak image													
Term	Pointing (x, y)							Comment					
09/21 04:48:00 - 09/21 04:54:54	Fixed (-528.4, 528.4)												
PROG= 08 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 4 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 7	2-time(s)		2.0sec									
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	1-time(s)		2.0sec										
	Seqn= 15	1-time(s)		2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 8	1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1934: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 50s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
09/21 04:58:00 - 09/21 09:59:54	Track (-734.5, -467.1) @ 09/21 04:55:00	# AR 11576, in SE.
09/21 10:13:00 - 09/21 10:28:30	Track (-711.6, -471.6) @ 09/21 10:10:00	* Cont.
09/21 14:24:30 - 09/21 18:05:24	Track (-692.5, -474.9) @ 09/21 14:15:00	* Track AR 11576.
09/21 18:18:30 - 09/21 23:55:00	Track (-63.4, 151.1) @ 09/21 18:15:30	# Track AR 11573, EIS obs of returning AR.
09/22 00:05:00 - 09/22 08:32:00	Track (-643.0, -482.5) @ 09/22 00:00:00	* AR 11576, in SE.

PROG= 15 Inf.-time(s)

Subr= 1	1-time(s)		2.0sec										
	Seqn= 18	1-time(s)		2.0sec									
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Seqn= 19	1-time(s)		2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Seqn= 65	4-time(s)		2.0sec									
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 73	70-time(s)		2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #192E: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1)

Term	Pointing (x, y)	Comment
09/21 10:03:00 - 09/21 10:09:54	Fixed (0.0, 0.0)	* HOP 186, with synoptic, shifted manually.

PROG= 04 1-time(s)

Subr= 1	1-time(s)		12.0sec										
	Seqn= 63	1-time(s)		4.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Seqn= 6	1-time(s)		2.0sec									
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 70	1-time(s)		4.0sec									
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 69	1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)		6.0sec										
	Seqn= 66	1-time(s)		2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	2.00s	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 3	1-time(s)		2.0sec										
	Seqn= 68	1-time(s)		2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

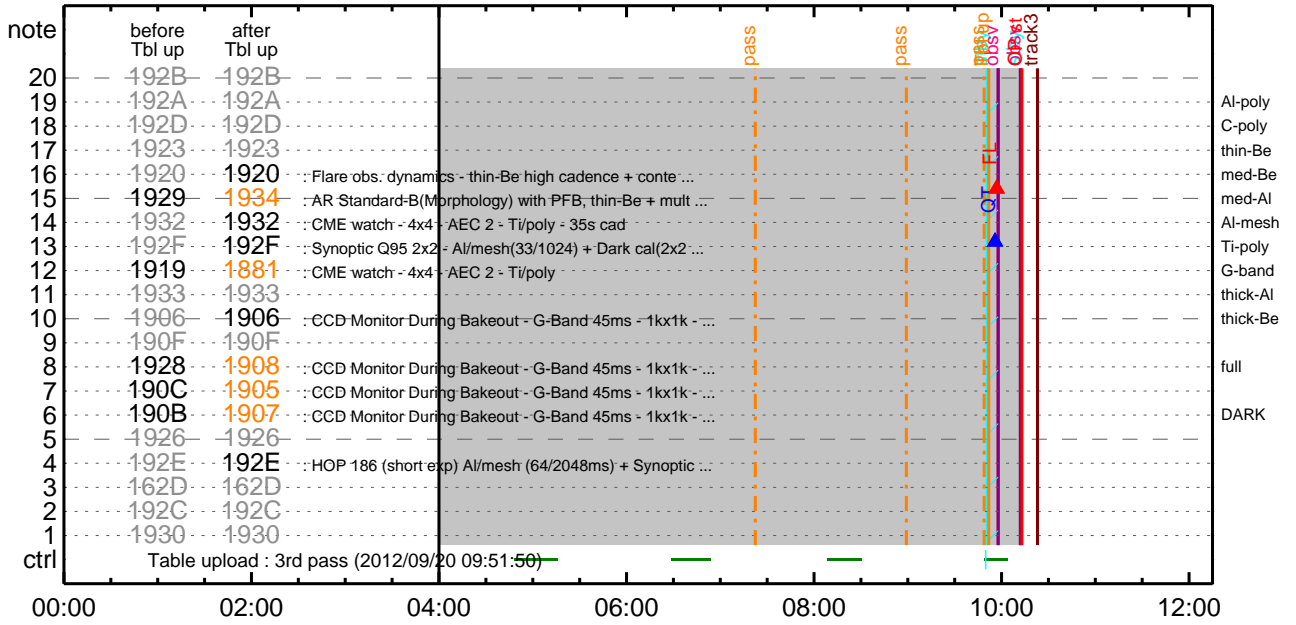
XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
09/21 10:36:00 - 09/21 14:14:54	Fixed (0.0, 0.0)	* Disc center pointing, for EIS engineering study. Also, SOT flat field.

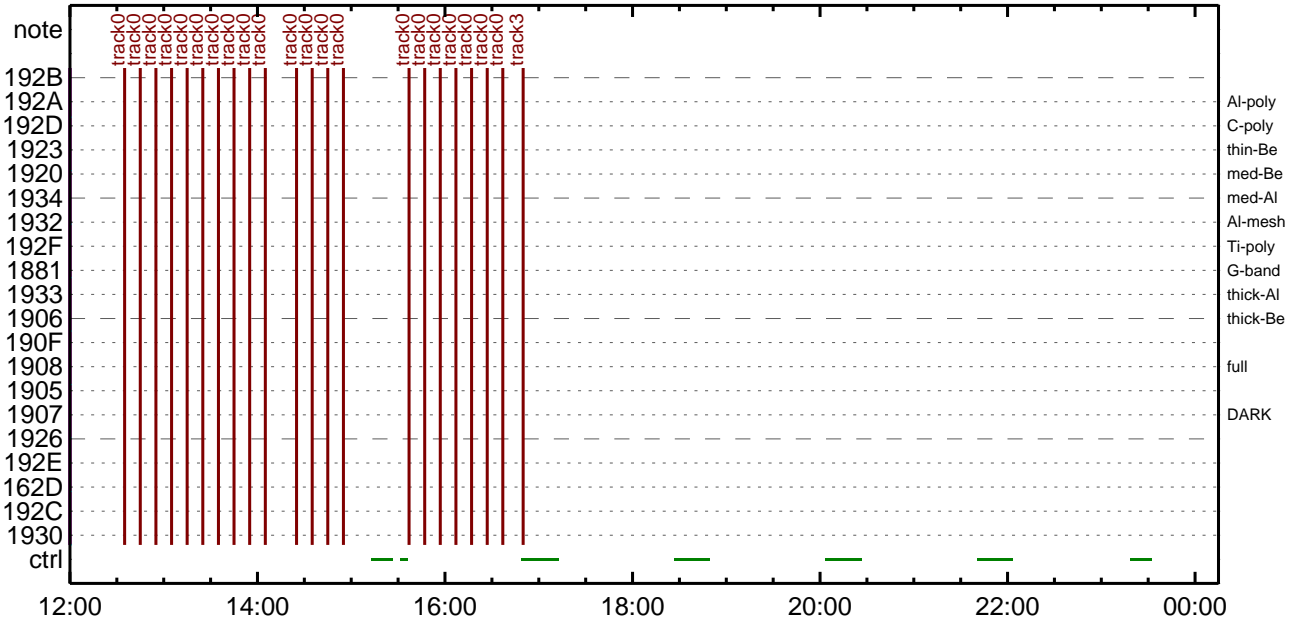
PROG= 12 Inf.-time(s)

Subr= 1	1-time(s)		120.0sec										
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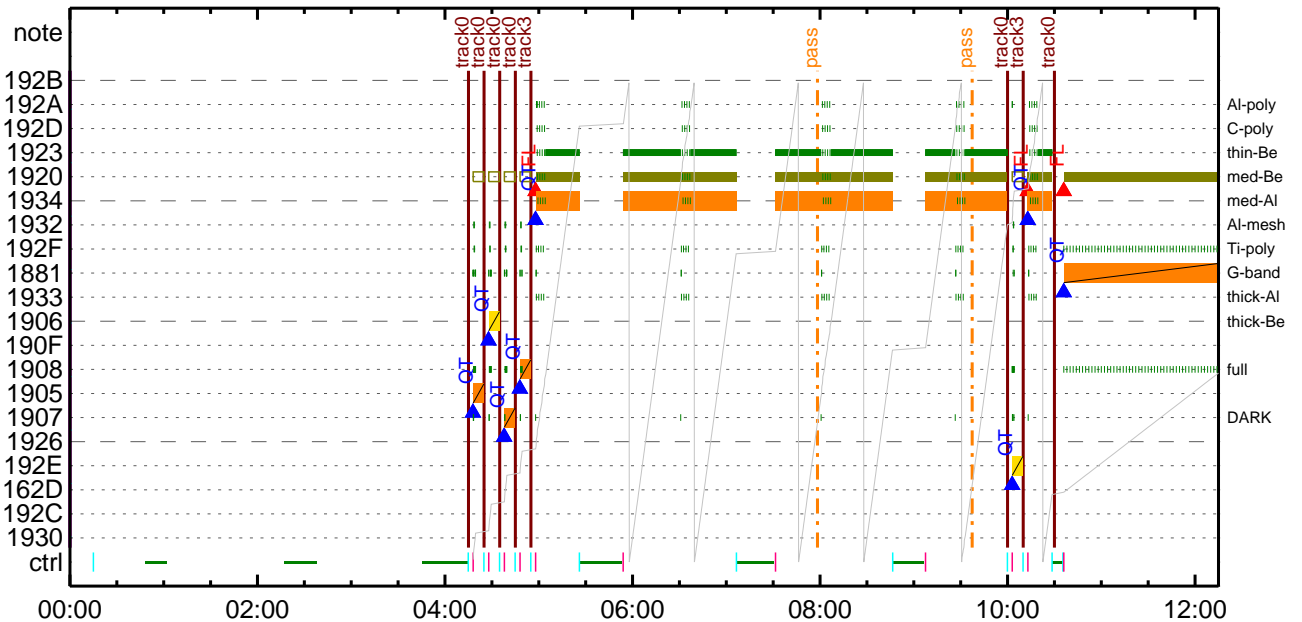
CMDI #0928 2012/09/20



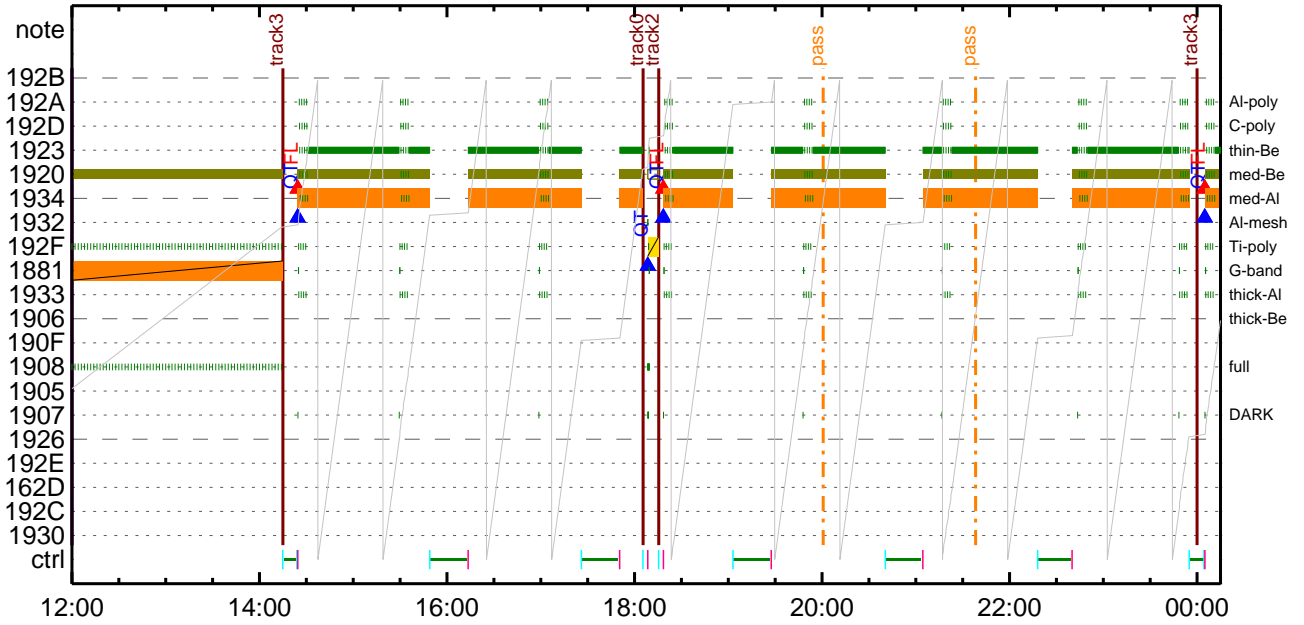
CMDI #0928 2012/09/20



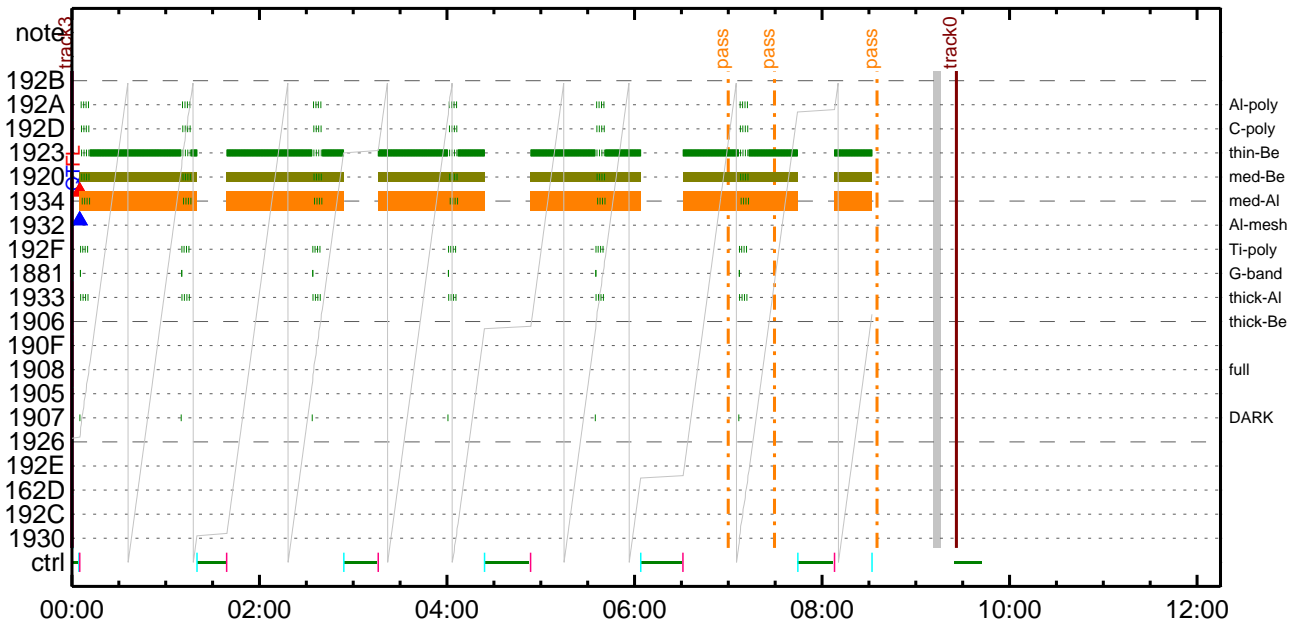
CMDI #0928 2012/09/21



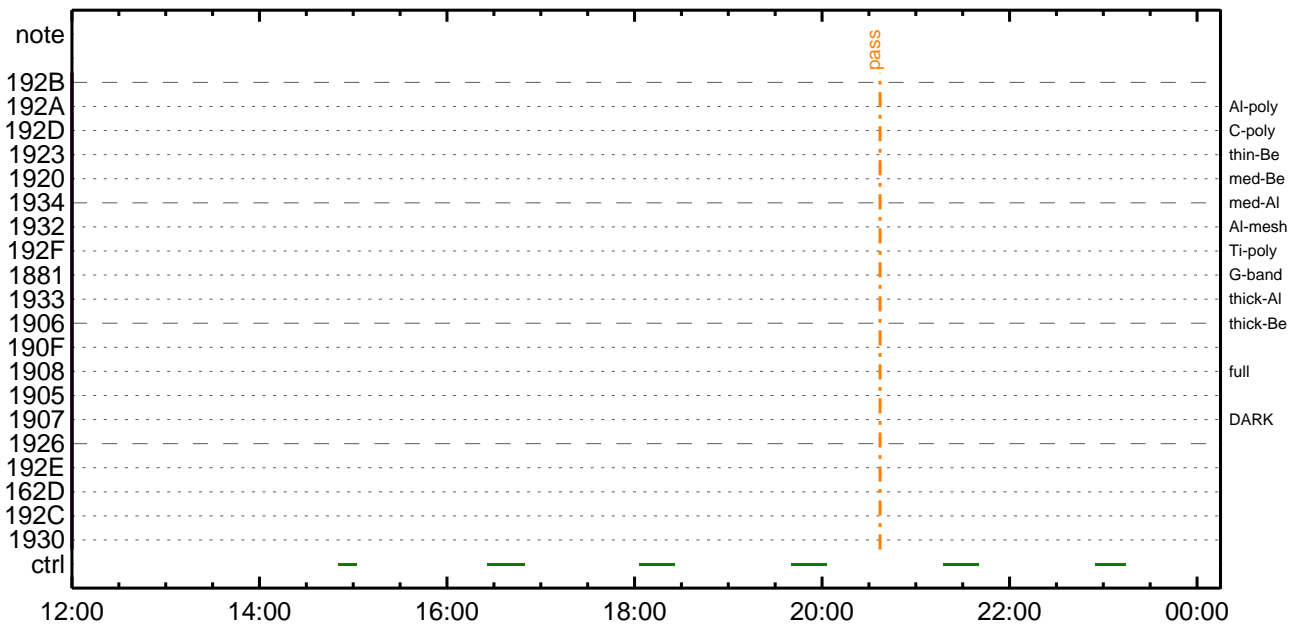
CMDI #0928 2012/09/21



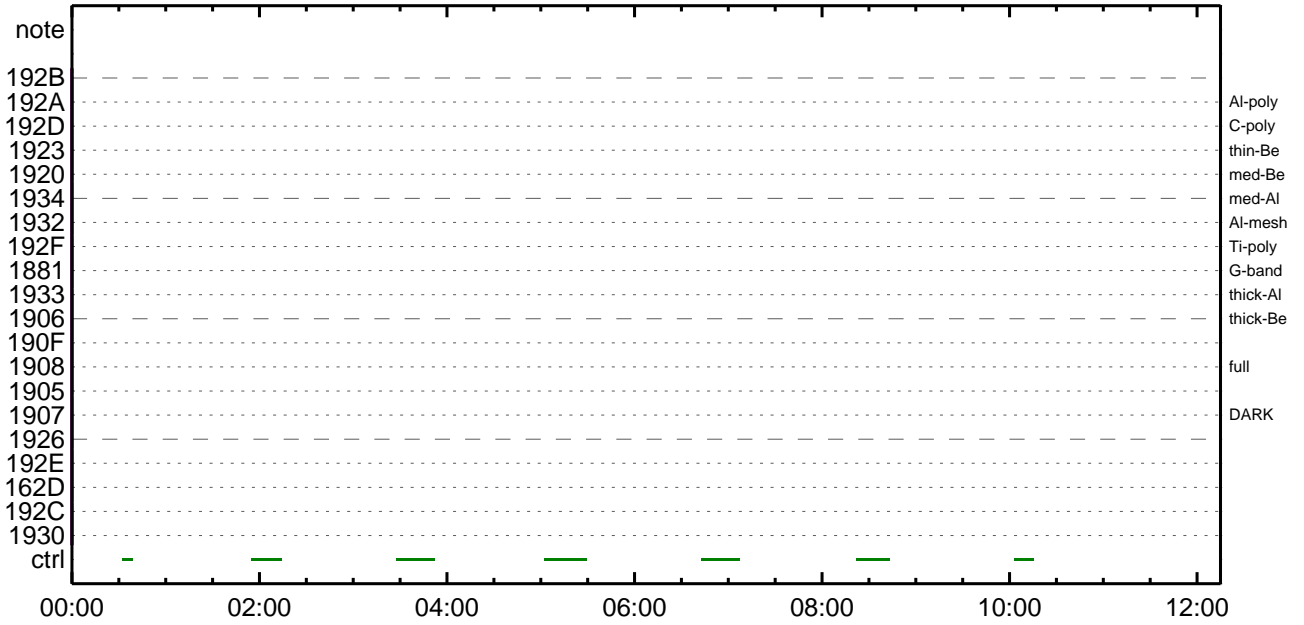
CMDI #0928 2012/09/22



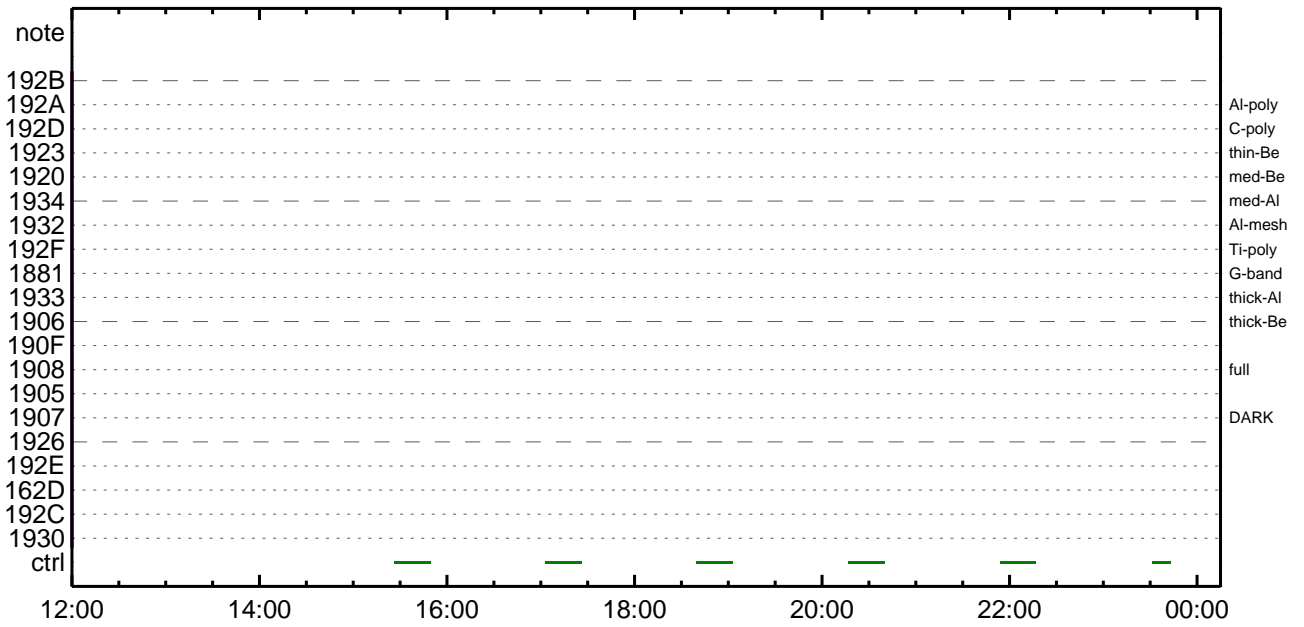
CMDI #0928 2012/09/22



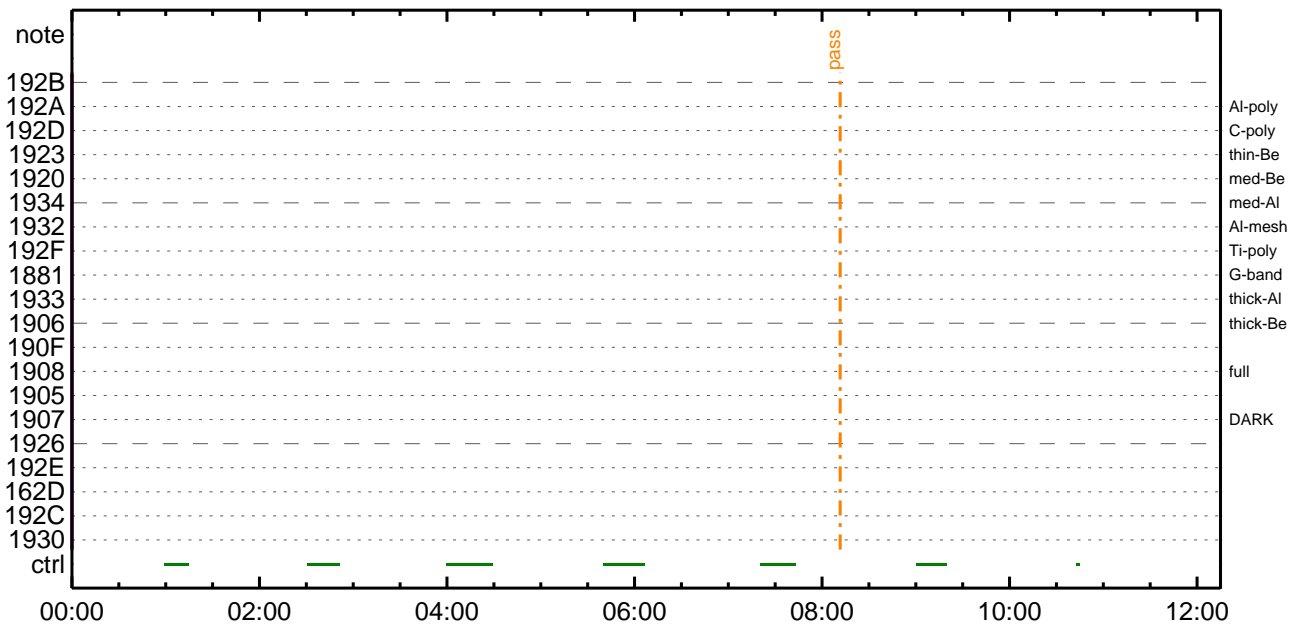
CMDI #0928 2012/09/23



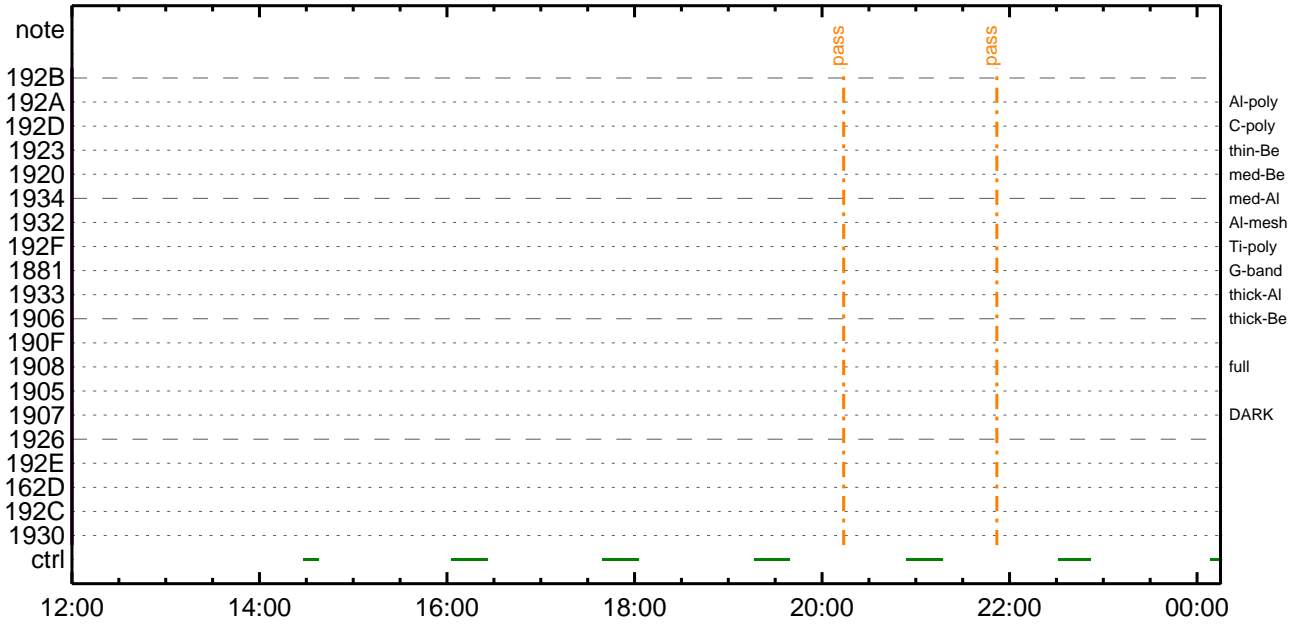
CMDI #0928 2012/09/23



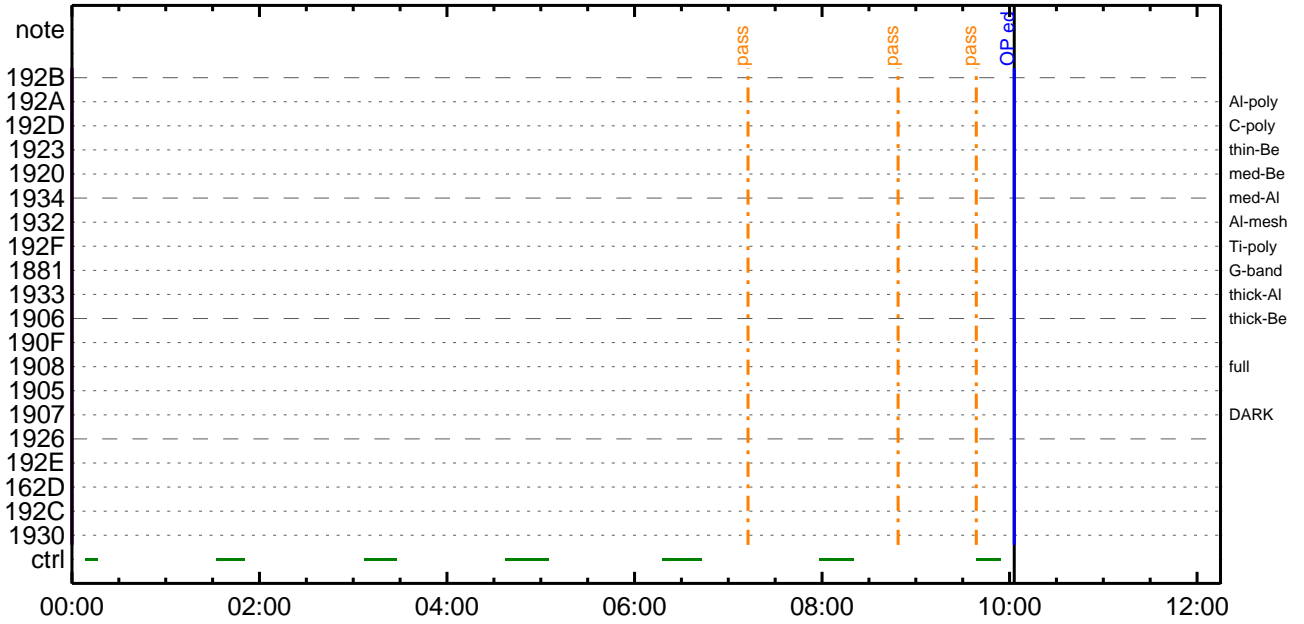
CMDI #0928 2012/09/24



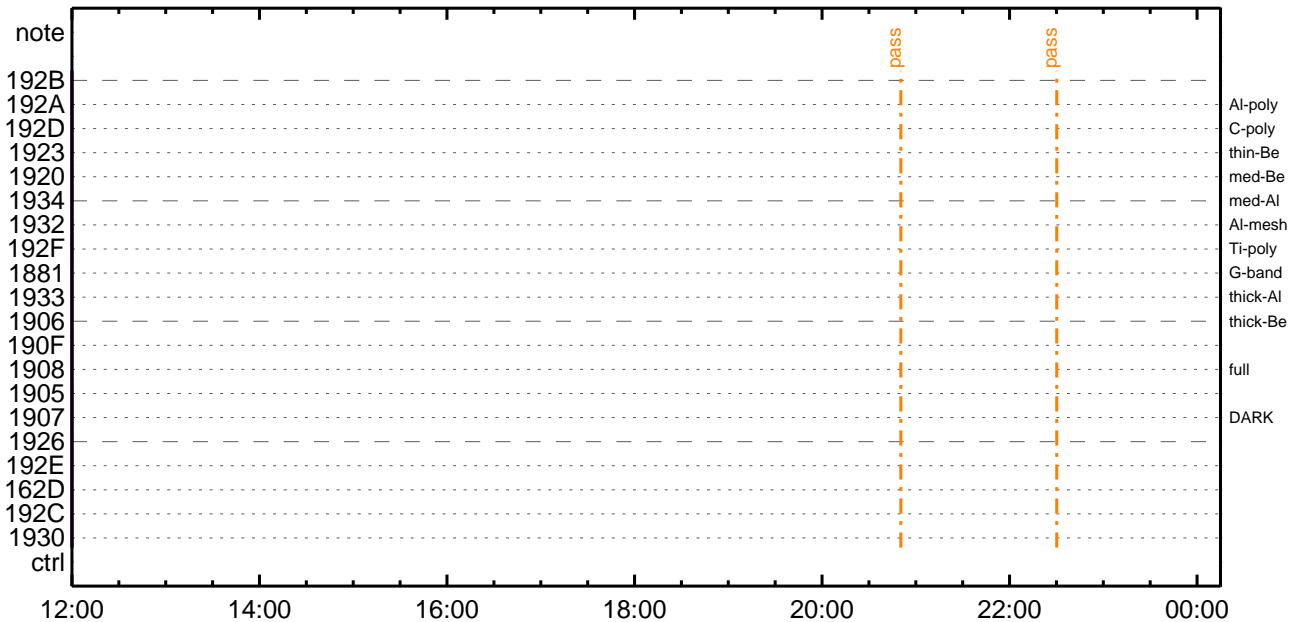
CMDI #0928 2012/09/24



CMDI #0928 2012/09/25



CMDI #0928 2012/09/25




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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;YÉ;YAYóYx
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGY1;YÉ;ã
0103 . S. OP      op-155:OP
0104 . C. (
0105 . S. OG      og-155:OG
0106 . C. (
0107 . C.
0108 . C. ;ãNMOG&OPíî°èYAYóYx;ã
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 . C.          çç[HK1_DMP_TOP_ADRS_1]             EQ      40
0113 . C.          çç[HK1_DMP_TOP_ADRS_0]             EQ          0
0114 . C.          çç[HK1_DMP_BLOCK_NUM]              EQ     127
0115 . C.          çç[HK1_DMP_REPEAT_NUM]             EQ          0
0116 . C.          çç[HK1_DMA_DMP_PIM]                 EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 . C.          çç[HK1_PKT_FORM_NO]                 EQ          7
0120 . C.          çç[HK1_PKT_GEN_TIME]                 EQ     0.25 s
0121 . C.          çç[HK1_S_TLM_BIT_RATE]               EQ     32k
0122 . C.          çç[HK1_X_TLM_BIT_RATE]               EQ         4M
0123 . C.          çç[HK1_DMP_CHK_FLG]                 EQ     EXEC
0124 . C.          YAYóYx½ª î»oð³îç§
0125 . C.          çç[HK1_DMP_CHK_FLG]                 EQ     NON
0126 . C. RAM ID=NMOGçî½è¹ç•ë²îOKçð³îç§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 . C.          çç[HK1_DMP_TOP_ADRS_1]             EQ     41
0132 . C.          çç[HK1_DMP_TOP_ADRS_0]             EQ          0
0133 . C.          çç[HK1_DMP_BLOCK_NUM]              EQ     127
0134 . C.          çç[HK1_DMP_REPEAT_NUM]             EQ          0
0135 . C.          çç[HK1_DMA_DMP_PIM]                 EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 . C.          çç[HK1_PKT_FORM_NO]                 EQ          7
0139 . C.          çç[HK1_PKT_GEN_TIME]                 EQ     0.25 s
0140 . C.          çç[HK1_S_TLM_BIT_RATE]               EQ     32k
0141 . C.          çç[HK1_X_TLM_BIT_RATE]               EQ         4M
0142 . C.          çç[HK1_DMP_CHK_FLG]                 EQ     EXEC
0143 . C.          YAYóYx½ª î»oð³îç§
0144 . C.          çç[HK1_DMP_CHK_FLG]                 EQ     NON
0145 . C. RAM ID=NMOGçî½è¹ç•ë²îOKçð³îç§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 . C.          çç[HK1_DMP_TOP_ADRS_1]             EQ     42
0151 . C.          çç[HK1_DMP_TOP_ADRS_0]             EQ          0
0152 . C.          çç[HK1_DMP_BLOCK_NUM]              EQ     65
0153 . C.          çç[HK1_DMP_REPEAT_NUM]             EQ          0
0154 . C.          çç[HK1_DMA_DMP_PIM]                 EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 . C.          çç[HK1_PKT_FORM_NO]                 EQ          7
0158 . C.          çç[HK1_PKT_GEN_TIME]                 EQ     0.25 s
0159 . C.          çç[HK1_S_TLM_BIT_RATE]               EQ     32k
0160 . C.          çç[HK1_X_TLM_BIT_RATE]               EQ         4M
0161 . C.          çç[HK1_DMP_CHK_FLG]                 EQ     EXEC
0162 . C.          YAYóYx½ª î»oð³îç§
0163 . C.          çç[HK1_DMP_CHK_FLG]                 EQ     NON
0164 . C. RAM ID=NMOG,RAM ID=OPçî½è¹ç•ë²îOKçð³îç§
0165 . C.
0166 . C. ***** çç²¼çî½Á'¶¼°èÈ¬çºÀ÷ç® (¼áµ-YAYóYx½é½ççðÁÖæç½ª°çè½¹¹çççç) *****
0167 . C. DHUYâ;YÉ;È½Y½;Yî;YÉ;Èçðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 . C.          çç[HK1_PKT_FORM_NO]                 EQ          2
0171 . C.          çç[HK1_PKT_GEN_TIME]                 EQ     0.5S
0172 . C.          çç[HK1_S_TLM_BIT_RATE]               EQ     32K
0173 . C.          çç[HK1_X_TLM_BIT_RATE]               EQ         4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ;§ OPOG UPLOADç-Á÷ç®NGçî½¹¹ç;ç°è²¼çî½¹¹ç-CMDÁ÷ç®çî½¹¹ç°çèççççççç;ç
0180 . C.          ççççç;çSETçèEDUMPCçî½±°ç¹Y¹ç¹¹ççççççç;ç
0181 . C.
0182 . C. TTY³YÏYóYÉçððÁDíç (UT)
0183 +. TI 2012-09-20 10:08:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C.          çç[HK1_TI_CMD_NUM]                   EQ     1COUNTUP
0186 . C.
0187 +. TI 2012-09-20 10:08:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C.          çç[HK1_TI_CMD_NUM]                   EQ     1COUNTUP
0190 . C.
0191 +. TI 2012-09-20 10:08:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C.          çç[HK1_TI_CMD_NUM]                   EQ     1COUNTUP

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0194 C.
0195 +. TI 2012-09-20 10:12:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ãîÄë%îíñòîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÍŷŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñîŷÊ¹ç•è²îOKòð³îÇ§
0229 C.
0230 C. DHUŷã;¼ŷÊ;Êŷ¼ŷ, ŷî;¼ŷÊ;Ëòðîãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-09-20 10:12:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2012-09-20 10:12:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2012-09-20 10:12:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîñî»ò¼ŷòÊÄðñ¹èDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷËŷŷŷËŷáŷçŷèè²¼ã¼ª»Ûñ¹è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷĐŷ¹.İ Daily±;îñòË´Øñ¹èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷŷÄŷ-¼ª»Û;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-157 2012-09-20 12:40:31 104 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁÍ;Èð¿ðÁð•µ°ÈÍ×ÁÇóÍYçYÁY×YÍ;¼YÉ;ÈÈèµ•íÍÉ;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÀ®, ùð¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 c0 c0 10 10)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 40 c0 10 10)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 40 40 10 10)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a c0 40 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 80 20 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 80 80 08 20)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 + DC 07-F0 MDP_XRT_FLD_DIS
0063 BC (d9)
0064 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0065 BC (c9)
0066 + DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 + DC 07-F0 MDP_XRT_ARS_DIS
0069 BC (d5)
0070 + DC 07-F0 MDP_XRT_FLD_RESET
0071 BC (da)
0072 + DC 07-F0 MDP_XRT_QT_PROG_SET
0073 BC (c4 0e)
0074 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0075 BC (c5 10)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C.
0079 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0080 C.
0081 +. DC 07-F0 MDP_XRT_MODE_OBSV
0082 BC (c2)
0083 +. TI 2012-09-20 10:12:02.0
0084 DC 07-F0 MDP_XRT_MODE_OBSV
0085 BC (c2)
0086 . C. ----- Success Verify ? OK / NG ____
0087 C.
0088 C. ***** XRT END *****
0089 C.
0090 . C. ***** MDP `úÁÍðÍ»ö¼YðÈÁðð¹ðÈDCBC•x²è *****
0091 C. (¼á°íYÓYÁYÈY¥YÉYáYçYÈE¼¼ð¼Á»Û¹ðé)
0092 . S. DC-BC dcbc-402:DCBC
0093 (MDP_known_event)
0094 C.
0095 C.
```

```
0096 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹σēDCBC•x²è *****
0097 . S. DC-BC dcbc-153:DCBC
0098 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0099 C.
0100 C.
0101 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0102 C.
0103 . C. ***** LOS *****
0104 C.
```

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*** OP Sequence for XRT ***

2012/09/20	10:23:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2012/09/20	12:35:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	03	8d	4f	be
2012/09/20	12:45:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	03	8d	3e	0c
2012/09/20	12:55:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	03	8d	2c	5a
2012/09/20	13:05:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	03	8d	1a	a7
2012/09/20	13:15:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	03	8d	08	fd
2012/09/20	13:25:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	03	8d	f7	4c
2012/09/20	13:35:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	03	8d	e5	9a
2012/09/20	13:45:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	03	8d	d3	e8
2012/09/20	13:55:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	03	8d	c2	25
2012/09/20	14:05:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	03	8d	b0	73
2012/09/20	14:25:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	14	e5	51	0e
2012/09/20	14:35:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	1f	ce	40	cc
2012/09/20	14:45:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	21	ca	30	41
2012/09/20	14:55:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	22	f1	1f	8d
2012/09/20	15:37:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	23	8d	0e	97
2012/09/20	15:47:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	23	a5	fd	27
2012/09/20	15:57:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	23	8d	f3	03
2012/09/20	16:07:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	22	f1	e2	0d
2012/09/20	16:17:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	21	ca	d1	59
2012/09/20	16:27:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	1f	ce	c0	cd
2012/09/20	16:37:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	18	72	af	bf
2012/09/20	16:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2012/09/21	00:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/09/21	00:15:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_449_OG [0x1c1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2012/09/21	04:14:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/09/21	04:15:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2012/09/21	04:17:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/09/21	04:17:52.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/09/21	04:17:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/09/21	04:17:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/09/21	04:17:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/09/21	04:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/09/21	04:24:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/09/21	04:25:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2012/09/21	04:27:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/09/21	04:27:52.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0a
2012/09/21	04:27:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/09/21	04:27:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/09/21	04:27:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/09/21	04:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/09/21	04:34:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/09/21	04:35:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2012/09/21	04:37:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/09/21	04:37:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 06
2012/09/21	04:37:54.0	XRT_FLD_DIS_404_OG [0x194]							

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2012/09/21	04:37:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/21	04:37:58.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/21	04:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	04:44:54.0	XRT_CTRL_MANU_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	04:45:00.0	AOCS_OrE-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00 d1 07 2e f9			
2012/09/21	04:47:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/21	04:47:52.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2012/09/21	04:47:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/21	04:47:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/21	04:47:58.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/21	04:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	04:54:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	04:55:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00			
2012/09/21	04:57:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/09/21	04:57:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/09/21	04:57:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/09/21	04:57:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/21	04:57:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/21	04:57:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	04:57:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2012/09/21	04:57:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/09/21	04:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	05:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	05:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	05:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	05:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	05:53:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/21	05:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	07:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	07:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	07:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	07:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	07:30:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/21	07:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	08:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	08:46:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	08:46:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	08:49:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	09:06:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/21	09:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/21	10:00:00.0	AOCS_OrE-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/09/21	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/21	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/21	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/21	10:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2012/09/21	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2012/09/21	10:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/21	10:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]		5	02-76	03 00 00 00 00
			AOCU_NM			
2012/09/21	10:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
			XRT_FOCUS_POSITION			
2012/09/21	10:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLD_ENA			
2012/09/21	10:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_FLRCTRL_ENA			
2012/09/21	10:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_AEC_RESET			
2012/09/21	10:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_ARS_DIS			
2012/09/21	10:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET			
2012/09/21	10:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	XRT_QT_PROG_SET	2	07-F0	c4 0f
			MDP_XRT_QT_PROG_SET			
2012/09/21	10:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	XRT_FL_PROG_SET	2	07-F0	c5 10
			MDP_XRT_FL_PROG_SET			
2012/09/21	10:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO			
2012/09/21	10:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU			
2012/09/21	10:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET			
2012/09/21	10:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT			
2012/09/21	10:30:00.0	AOCS_OrE-point_Start_27_OG [0x0b1]		5	02-76	00 00 00 00 00
			AOCU_NM			
2012/09/21	10:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP			
2012/09/21	10:35:24.0	XRT_CTRL_MANU_448_OG [0x1c0]	XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU			
2012/09/21	10:35:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			XRT_FOCUS_POSITION			
2012/09/21	10:35:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLD_ENA			
2012/09/21	10:35:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_FLRCTRL_ENA			
2012/09/21	10:35:50.0	XRT_AEC_RESET_423_OG [0x1a7]	XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_AEC_RESET			
2012/09/21	10:35:52.0	XRT_ARS_DIS_438_OG [0x1b6]	XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_ARS_DIS			
2012/09/21	10:35:54.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET			
2012/09/21	10:35:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	XRT_QT_PROG_SET	2	07-F0	c4 0c
			MDP_XRT_QT_PROG_SET			
2012/09/21	10:35:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	XRT_FL_PROG_SET	2	07-F0	c5 10
			MDP_XRT_FL_PROG_SET			
2012/09/21	10:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO			
2012/09/21	14:14:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU			
2012/09/21	14:14:56.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET			
2012/09/21	14:14:58.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT			
2012/09/21	14:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]		5	02-76	03 00 00 00 00
			AOCU_NM			
2012/09/21	14:18:08.0	XRT_PREFLR_STOP_433_OG [0x1b1]	XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP			
2012/09/21	14:23:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU			
2012/09/21	14:23:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
			XRT_FOCUS_POSITION			
2012/09/21	14:24:16.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLD_ENA			
2012/09/21	14:24:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_FLRCTRL_ENA			
2012/09/21	14:24:20.0	XRT_AEC_RESET_423_OG [0x1a7]	XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_AEC_RESET			
2012/09/21	14:24:22.0	XRT_ARS_DIS_438_OG [0x1b6]	XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_ARS_DIS			
2012/09/21	14:24:24.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET			
2012/09/21	14:24:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	XRT_QT_PROG_SET	2	07-F0	c4 0f
			MDP_XRT_QT_PROG_SET			
2012/09/21	14:24:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	XRT_FL_PROG_SET	2	07-F0	c5 10
			MDP_XRT_FL_PROG_SET			
2012/09/21	14:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO			
2012/09/21	15:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU			
2012/09/21	15:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]	XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET			
2012/09/21	15:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT			
2012/09/21	15:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP			
2012/09/21	16:12:30.0	XRT_Custom_434_OG [0x1b2]				
2012/09/21	16:13:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				

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2012/09/21	17:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	17:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	17:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	17:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	17:49:30.5	XRT_Custom_434_OG [0x1b2]							
2012/09/21	17:50:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	18:05:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/21	18:05:30.0	AOCS_ORe-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/09/21	18:05:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/21	18:05:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/21	18:05:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/21	18:08:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2012/09/21	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	18:15:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	18:15:30.0	AOCS_ORe-point_Start_28_OG [0x0b2]	AOCU_NM	5	02-76	02 00 00 00 00			
2012/09/21	18:17:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/09/21	18:18:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/09/21	18:18:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/09/21	18:18:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/21	18:18:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/21	18:18:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	18:18:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2012/09/21	18:18:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/09/21	18:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	19:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	19:03:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	19:03:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	19:06:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	19:26:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/21	19:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	20:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	20:40:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	20:40:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	20:43:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	21:03:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/21	21:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	22:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	22:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	22:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	22:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/21	22:39:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/21	22:40:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/21	23:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/21	23:55:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/21	23:55:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/21	23:58:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/22	00:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00			

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2012/09/22	00:04:24.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	00:04:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/09/22	00:04:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/22	00:04:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/22	00:04:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/22	00:04:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/22	00:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/22	00:04:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2012/09/22	00:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/09/22	00:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/22	01:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	01:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/22	01:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/22	01:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/22	01:38:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/22	01:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/22	02:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	02:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/22	02:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/22	02:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/22	03:15:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/22	03:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/22	04:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	04:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/22	04:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/22	04:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/22	04:52:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/22	04:53:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/22	06:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	06:04:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/22	06:04:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/22	06:07:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/22	06:30:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/22	06:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/22	07:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	07:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/22	07:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/22	07:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/22	08:07:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/22	08:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/22	08:32:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/22	09:26:00.0	AOCS_Ore-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00 00 00 00 00	