

XRT Timeline to be uploaded on 2012/09/25

Period: 2012/09/25 10:03:00 - 2012/09/29 11:23:00

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Normal mode

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XOB #192E: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1

Term	Pointing (x, y)	Comment
09/25 10:20:00 - 09/25 10:22:54	Fixed (0.0, 0.0)	# OP start + 10min, Synoptic (HOP 186)
09/26 10:03:00 - 09/26 10:17:30	Fixed (0.0, 0.0)	synoptic (HOP 186)

PROG= 04 1-time(s)

Subr= 1 1-time(s) 12.0sec	Seqn= 63 1-time(s) 4.0sec	Seqn= 6 1-time(s) 2.0sec	Seqn= 70 1-time(s) 4.0sec	Seqn= 69 1-time(s) 2.0sec	Subr= 2 1-time(s) 6.0sec	Subr= 3 1-time(s) 2.0sec	Seqn= 68 1-time(s) 2.0sec
Al-poly/Open Al-poly/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec	Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec	Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
09/25 10:26:00 - 09/25 12:44:54	Track (87.5, -533.2) @ 09/25 10:23:00	EIS returning AR 11576 obs.
09/25 12:48:00 - 09/25 14:29:54	Track (161.8, 5.8) @ 09/25 12:45:00	HOP220
09/25 18:06:00 - 09/25 22:59:54	Track (209.8, 7.1) @ 09/25 18:03:00	#HOP 220
09/26 05:43:06 - 09/26 09:59:54	Track (311.3, 10.8) @ 09/26 05:30:00	*HOP 220
09/26 10:27:36 - 09/26 14:29:54	Track (351.7, 12.7) @ 09/26 10:10:00	#HOP220
09/26 22:30:06 - 09/27 08:38:00	Track (451.5, 18.6) @ 09/26 22:07:00	HOP 220

PROG= 03 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec	Seqn= 18 1-time(s) 2.0sec	Seqn= 19 1-time(s) 2.0sec	Seqn= 65 4-time(s) 2.0sec	Subr= 2 1-time(s) 2.0sec	Seqn= 73 70-time(s) 120.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec	thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec	thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec	Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval			

XOB #1936: HOP 215 - AR Standard-B(Morphology) with PFB, Al/Poly + thin-Be + multifilter context, 384x384 at 1064 1048, 40s-cad w/ G-Band VLS Closed

Term	Pointing (x, y)	Comment
09/25 14:33:00 - 09/25 17:52:54	Track (-227.6, 4.5) @ 09/25 14:30:00	HOP 215
09/26 14:33:00 - 09/26 17:56:54	Track (101.1, 11.8) @ 09/26 14:30:00	HOP 215

PROG= 20 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec	Seqn= 18 1-time(s) 2.0sec	Seqn= 19 1-time(s) 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec

Subr= 2	1-time(s)	2.0sec																		
Seqn= 74	40-time(s)	40.0sec																		
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	DPCM	0	0	2.0sec							
Seqn= 73	1-time(s)	2.0sec																		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	12.5sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	12.5sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	12.5sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	12.5sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)																				
Term	Pointing (x, y)										Comment									
09/25 17:56:00 - 09/25 18:02:54	Fixed (0.0, 0.0)										synoptic, shifted -7.0 min									
09/26 18:00:00 - 09/26 18:06:54	Fixed (0.0, 0.0)										synoptic, shifted -3.0 min									

PROG= 13	1-time(s)																			
Subr= 1	1-time(s)	14.0sec																		
Seqn= 64	1-time(s)	4.0sec																		
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 6	1-time(s)	2.0sec																		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec							
Seqn= 70	1-time(s)	4.0sec																		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 67	1-time(s)	2.0sec																		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 69	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Subr= 2	1-time(s)	2.0sec																		
Seqn= 68	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms																				
Term	Pointing (x, y)										Comment									
09/25 23:03:00 - 09/26 05:15:30	Fixed (-12.0, 878.0)										HOP 206 (N-pole)									

PROG= 09	Inf.-time(s)																			
Subr= 1	1-time(s)	2.0sec																		
Seqn= 61	2-time(s)	2.0sec																		
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512	(1064, 1048)	DPCM	0	0	2.0sec							
Subr= 2	1-time(s)	2.0sec																		
Seqn= 62	1-time(s)	30.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec							
Subr= 3	30-time(s)	2.0sec																		
Seqn= 60	2-time(s)	30.0sec																		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2																				
Term	Pointing (x, y)										Comment									
09/26 18:22:00 - 09/26 20:06:54	Fixed (0.0, 945.0)										# Co-alignment for N-limb									

PROG= 05	1-time(s)																			
Subr= 1	1-time(s)	360.0sec																		
Seqn= 16	24-time(s)	300.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x1536	(1024, 768)	Q=90	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2																				
Term	Pointing (x, y)										Comment									
09/26 20:22:00 - 09/26 22:07:30	Fixed (-945.0, 0.0)										Co-alignment for E-limb									

PROG= 19	1-time(s)																			
Subr= 1	1-time(s)	360.0sec																		
Seqn= 17	15-time(s)	480.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

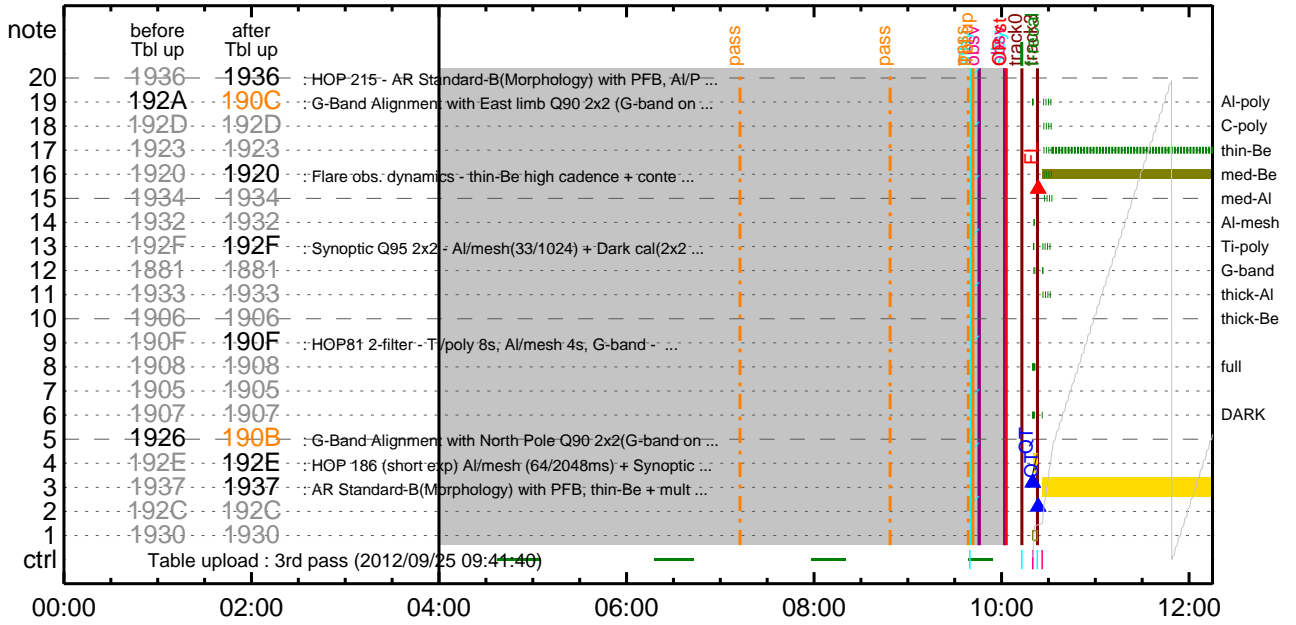
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Flare mode

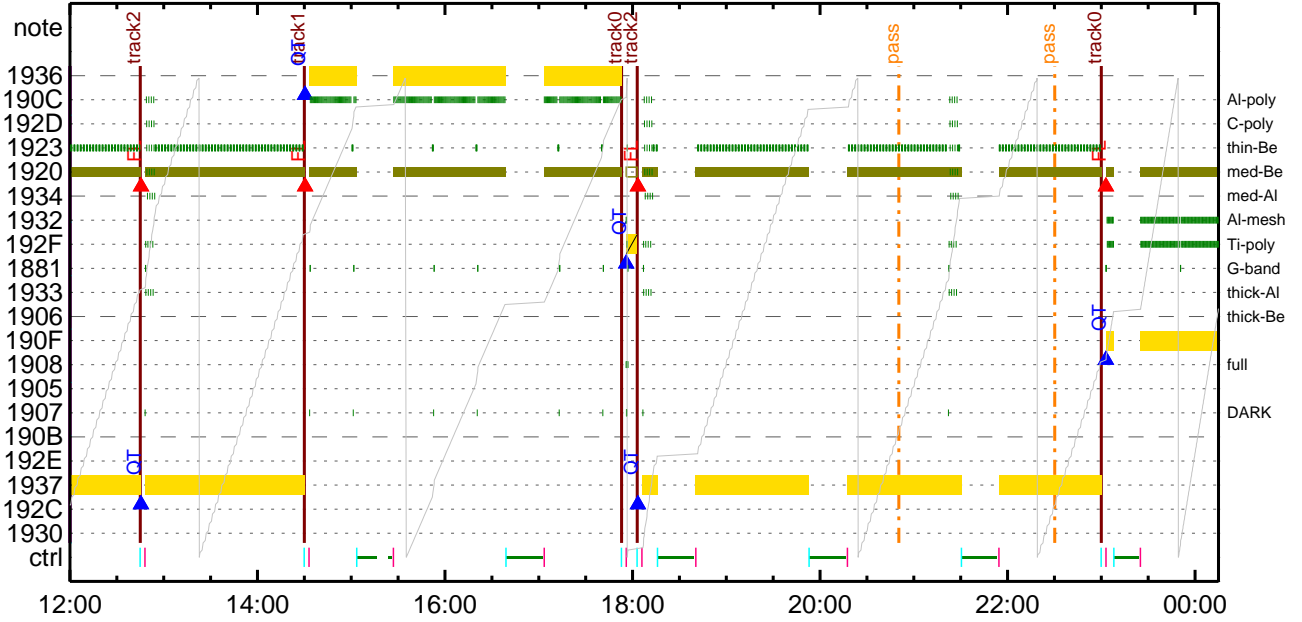
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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

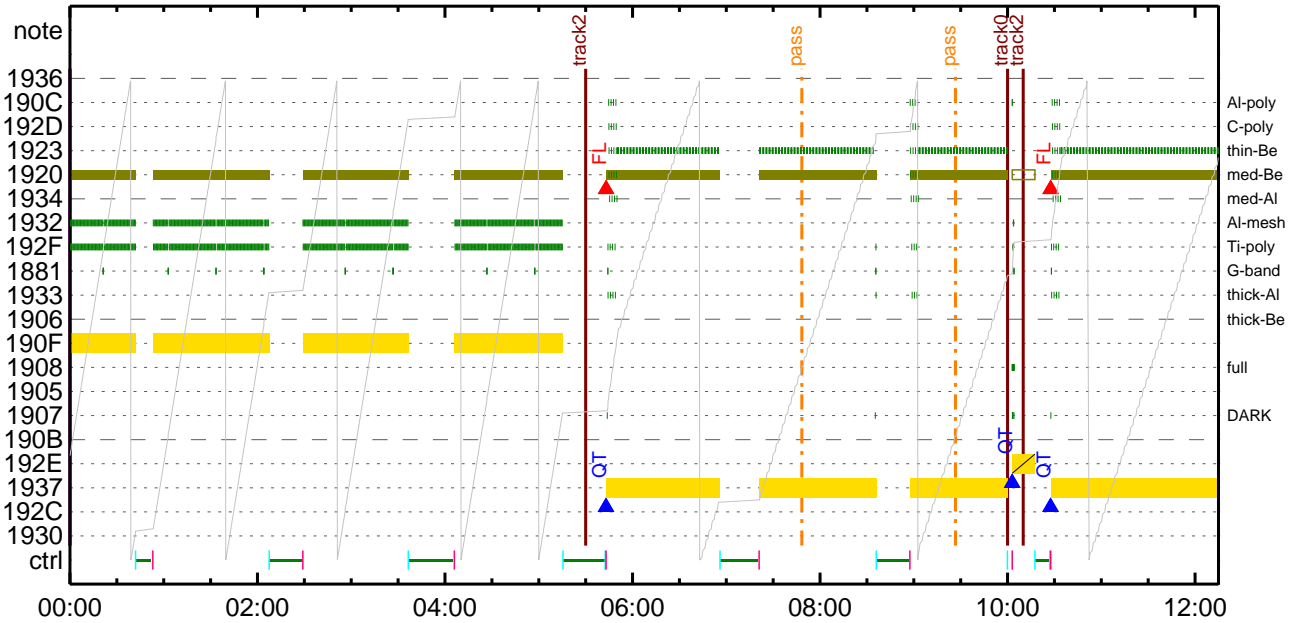
CMDI #0936 2012/09/25



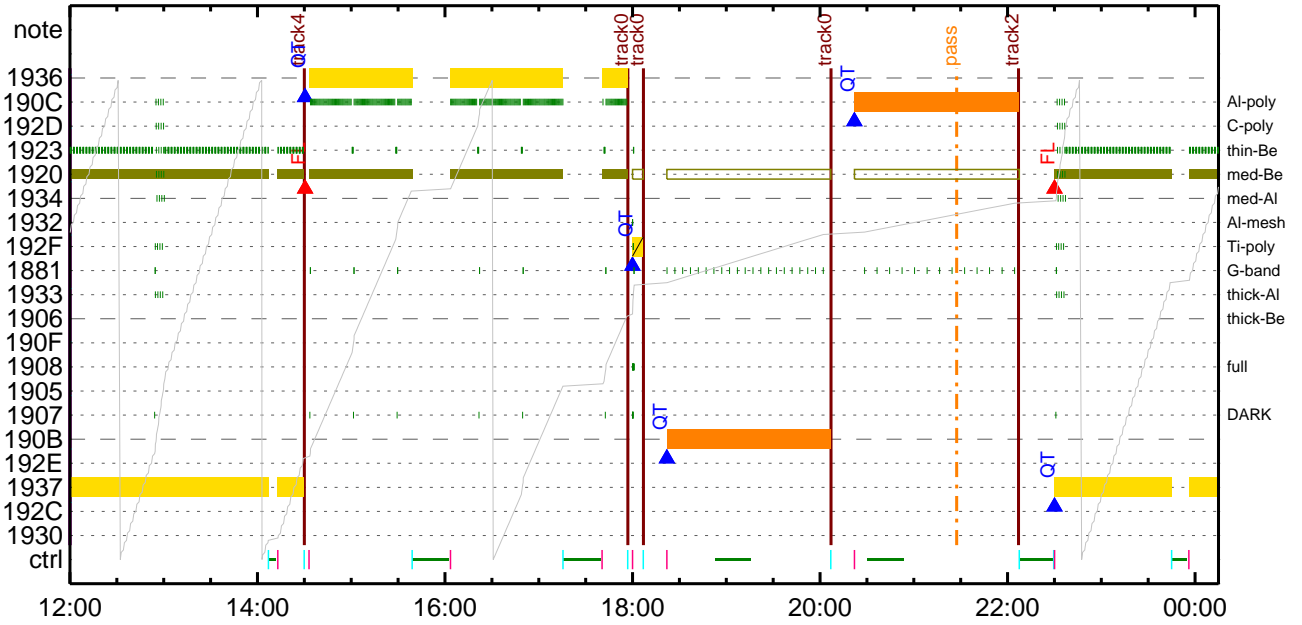
CMDI #0936 2012/09/25



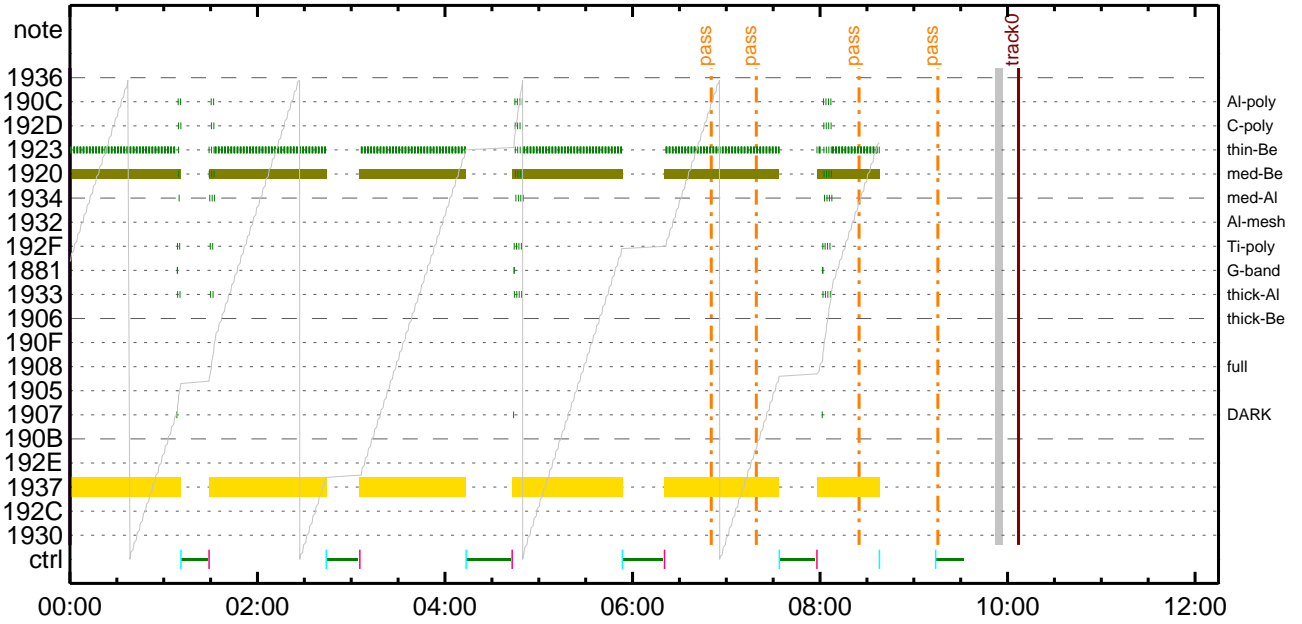
CMDI #0936 2012/09/26



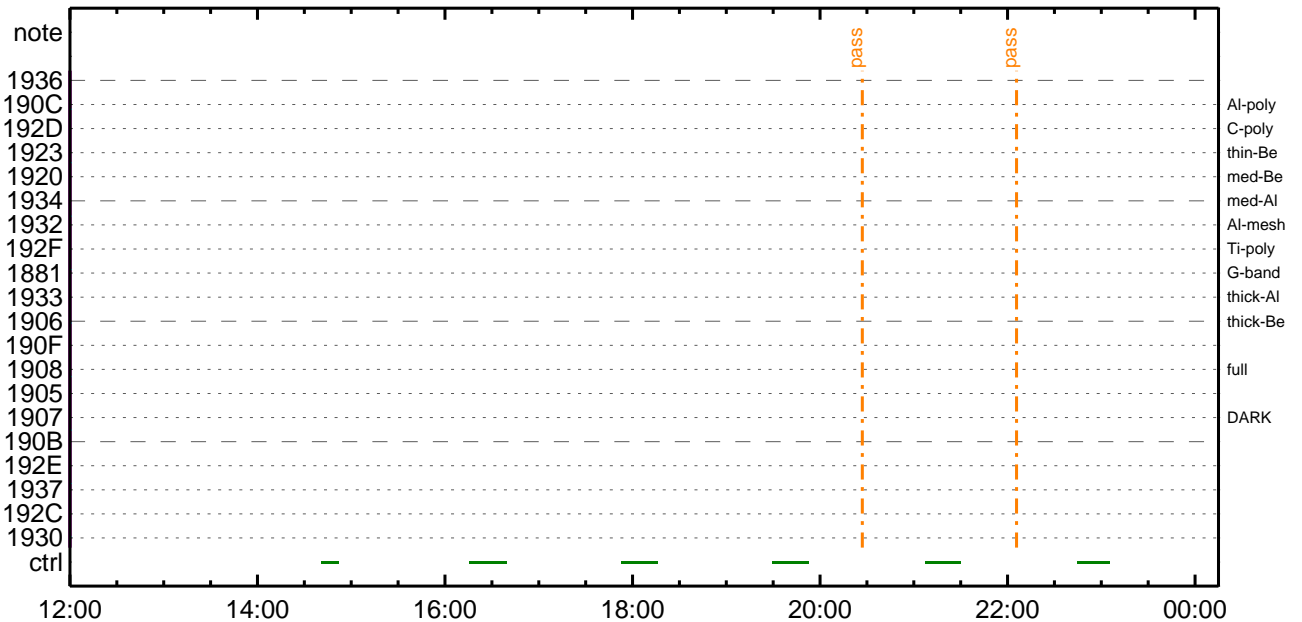
CMDI #0936 2012/09/26



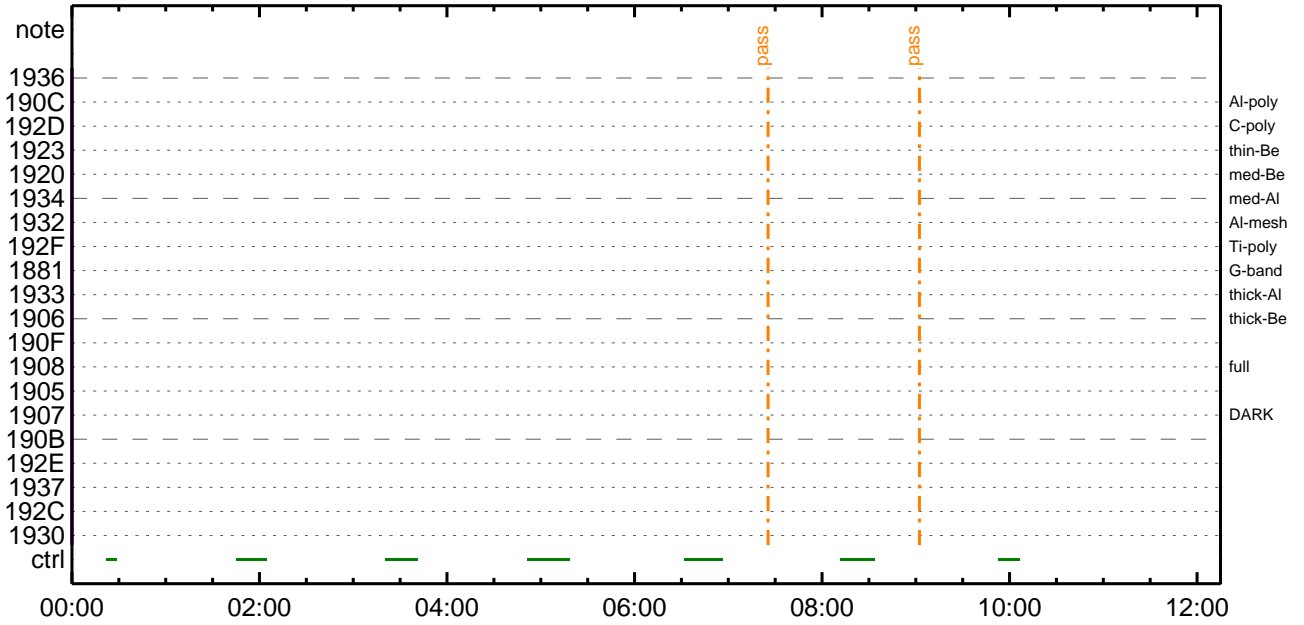
CMDI #0936 2012/09/27



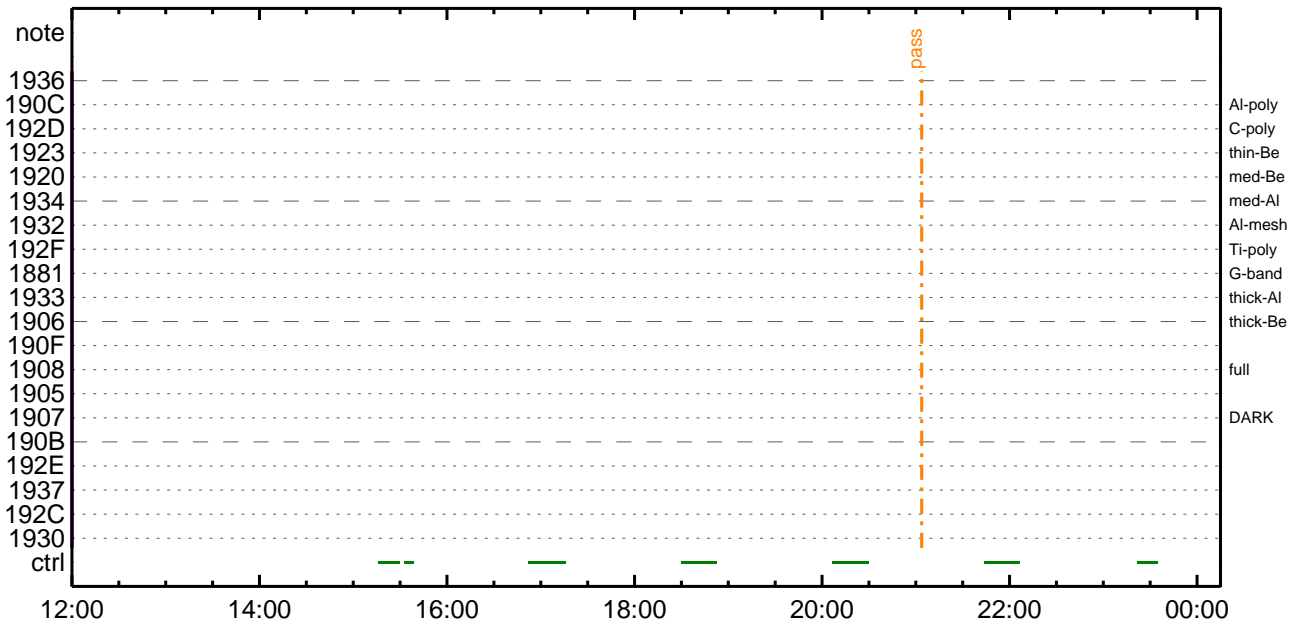
CMDI #0936 2012/09/27



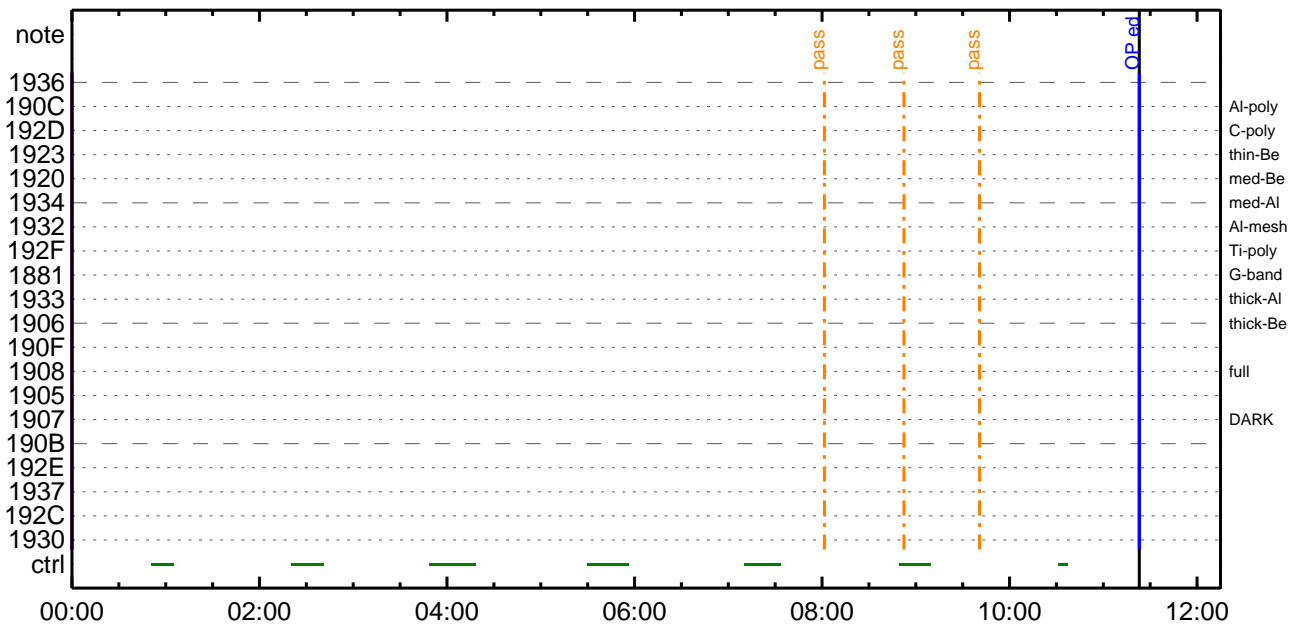
CMDI #0936 2012/09/28



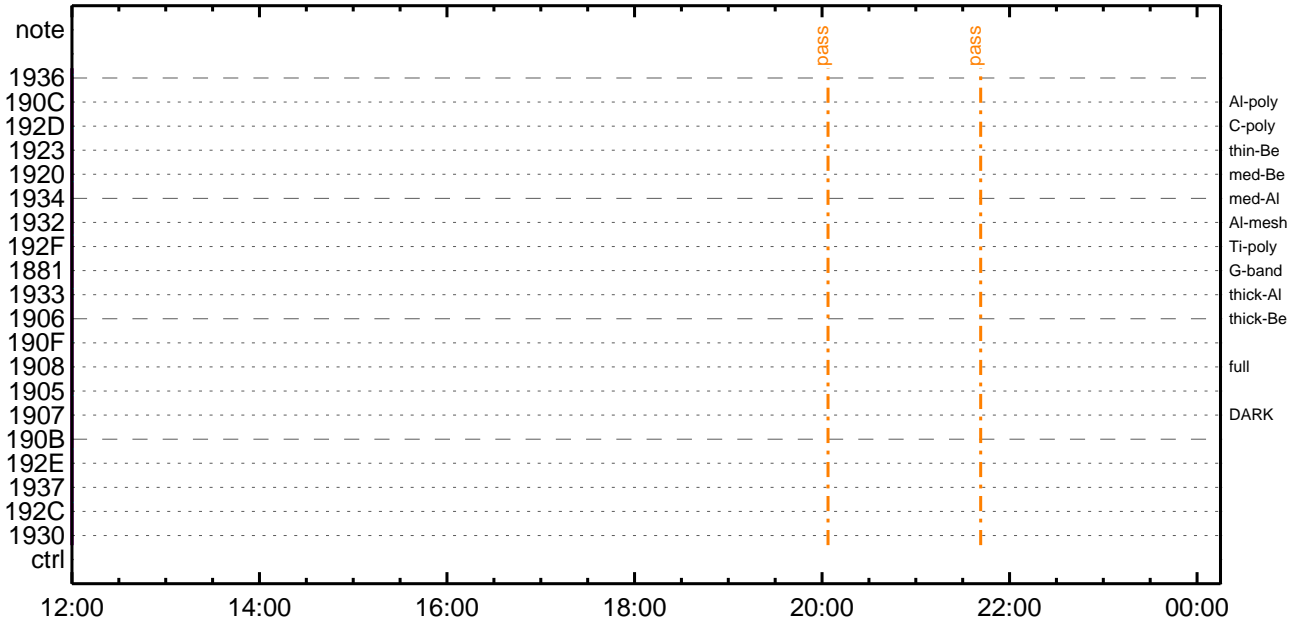
CMDI #0936 2012/09/28



CMDI #0936 2012/09/29



CMDI #0936 2012/09/29




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0096 C.
0097 C.
0098 . C. *****
0099 . C. UFSS-A USE Iãð•
0100 . C. *****
0101 . C.
0102 . C. ***** UFSS-A USEÀÛÄÊ *****
0103 +. DC 02-50 AOCU_UFSS-A_USE
0104 BC (05)
0105 . C. [ ] [ADS UPD] UFSA = USE
0106 . C. [ ] [ADS UPD] UFSA = USE
0107 . C. [ ] [ADS UPD] NSAS = NO_USE
0108 . C. [ ] [ADS UPD] STT = USE
0109 . C.
0110 . C.
0111 . C. *****
0112 C. OP/OGÿí;¼ÿÉ;|ÿÄÿóÿ×
0113 C. *****
0114 C.
0115 . C. ;ãOP/OGÿí;¼ÿÉ;ã
0116 . S. OP op-164:OP
0117 ( )
0118 . S. OG og-164:OG
0119 ( )
0120 C.
0121 . C. ;ãNMOG&OPÿî°èÿÄÿóÿ×;ã
0122 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0123 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0124 BC (20 00 7f 01 02)
0125 C. ðç[HK1_DMP_TOP_ADRS_1] EQ 40
0126 C. ðç[HK1_DMP_TOP_ADRS_0] EQ 0
0127 C. ðç[HK1_DMP_BLOCK_NUM] EQ 127
0128 C. ðç[HK1_DMP_REPEAT_NUM] EQ 0
0129 C. ðç[HK1_DMA_DMP_PIM] EQ DHU
0130 +. DC 01-22 DHU_MODE_CHNG
0131 BC (07 0b f8)
0132 C. ðç[HK1_PKT_FORM_NO] EQ 7
0133 C. ðç[HK1_PKT_GEN_TIME] EQ 0.25 s
0134 C. ðç[HK1_S_TLM_BIT_RATE] EQ 32k
0135 C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0136 C. ðç[HK1_DMP_CHK_FLG] EQ EXEC
0137 . C. ÿÄÿóÿ×¼ªªî»ðð³îç§
0138 C. ðç[HK1_DMP_CHK_FLG] EQ NON
0139 . C. RAM ID=NMOGðî¼ÿË¹ç•è²îOKðð³îç§
0140 C.
0141 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0142 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0143 BC (20 80 7f 01 02)
0144 C. ðç[HK1_DMP_TOP_ADRS_1] EQ 41
0145 C. ðç[HK1_DMP_TOP_ADRS_0] EQ 0
0146 C. ðç[HK1_DMP_BLOCK_NUM] EQ 127
0147 C. ðç[HK1_DMP_REPEAT_NUM] EQ 0
0148 C. ðç[HK1_DMA_DMP_PIM] EQ DHU
0149 +. DC 01-22 DHU_MODE_CHNG
0150 BC (07 0b f8)
0151 C. ðç[HK1_PKT_FORM_NO] EQ 7
0152 C. ðç[HK1_PKT_GEN_TIME] EQ 0.25 s
0153 C. ðç[HK1_S_TLM_BIT_RATE] EQ 32k
0154 C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0155 C. ðç[HK1_DMP_CHK_FLG] EQ EXEC
0156 . C. ÿÄÿóÿ×¼ªªî»ðð³îç§
0157 C. ðç[HK1_DMP_CHK_FLG] EQ NON
0158 . C. RAM ID=NMOGðî¼ÿË¹ç•è²îOKðð³îç§
0159 C.
0160 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0161 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0162 BC (21 00 41 01 02)
0163 C. ðç[HK1_DMP_TOP_ADRS_1] EQ 42
0164 C. ðç[HK1_DMP_TOP_ADRS_0] EQ 0
0165 C. ðç[HK1_DMP_BLOCK_NUM] EQ 65
0166 C. ðç[HK1_DMP_REPEAT_NUM] EQ 0
0167 C. ðç[HK1_DMA_DMP_PIM] EQ DHU
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (07 0b f8)
0170 C. ðç[HK1_PKT_FORM_NO] EQ 7
0171 C. ðç[HK1_PKT_GEN_TIME] EQ 0.25 s
0172 C. ðç[HK1_S_TLM_BIT_RATE] EQ 32k
0173 C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C. ðç[HK1_DMP_CHK_FLG] EQ EXEC
0175 . C. ÿÄÿóÿ×¼ªªî»ðð³îç§
0176 C. ðç[HK1_DMP_CHK_FLG] EQ NON
0177 . C. RAM ID=NMOG,RAM ID=OPðî¼ÿË¹ç•è²îOKðð³îç§
0178 C.
0179 . C. ***** òÈ²¼ðî¼ÿË¹ç•è²îOKðð³îç§ *****
0180 C. DHUÿã;¼ÿÉ;È¼ÿ¼;ÿì;¼ÿË;Èððîã¹
0181 +. DC 01-22 DHU_MODE_CHNG
0182 BC (02 0a f8)
0183 C. ðç[HK1_PKT_FORM_NO] EQ 2
0184 C. ðç[HK1_PKT_GEN_TIME] EQ 0.5S
0185 C. ðç[HK1_S_TLM_BIT_RATE] EQ 32K
0186 C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0187 C.
0188 . C. *****
0189 C. TI-CMD SET (OPOG STOP/COPY/START)
0190 C. *****
0191 C.
0192 . C. NOTICE |§ OPOG UPLOADð-Á÷¿ðNGðî¼ÿË¹ç;ç•è²¼ðî¼ÿË¹ç•è²îOKðð³îç§
0193 C. ððð¿;çSETðÈDUMPðî¼ÿË¹ç•è²¼ðî¼ÿË¹ç;

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0194 C.
0195 . C. TIY³¥P¥6¥EòðÀDİç (UT)
0196 +. TI 2012-09-25 09:58:00.0
0197 DC 01-B3 DHU_OP_STOP
0198 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0199 C.
0200 +. TI 2012-09-25 09:58:01.0
0201 DC 01-B4 DHU_OP_COPY
0202 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0203 C.
0204 +. TI 2012-09-25 09:58:01.0
0205 DC 01-B5 DHU_OPOG_COPY
0206 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0207 C.
0208 +. TI 2012-09-25 10:02:59.5
0209 DC 01-B2 DHU_OP_START
0210 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0211 C.
0212 C. °Ê²¼òİÄê%îíÑòî¥Ä¥$¥Ä¥-¹àîÛ
0213 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0214 C. çç[HK1_TI_CMD_NUM] EQ 4
0215 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0216 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0217 C.
0218 . *****
0219 C. TIîî°è¥Ä¥6¥×
0220 C. *****
0221 C.
0222 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0223 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0224 BC (03 ab 03 01 02)
0225 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0226 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0227 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0228 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0229 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0230 +. DC 01-22 DHU_MODE_CHNG
0231 BC (07 0b f8)
0232 C. çç[HK1_PKT_FORM_NO] EQ 7
0233 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0234 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0235 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0236 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0237 C.
0238 . ¥Ä¥6¥×¼ª¹î»òð³îç$
0239 C. çç[HK1_DMP_CHK_FLG] EQ NON
0240 C.
0241 . C. RAM ID=TI_TBLòîî³È¹ç•è²îOKòð³îç$
0242 C.
0243 . C. DHU¥â;¼¥È;È¼¥½;¥î;¼¥È;Èòðîáò¹
0244 +. DC 01-22 DHU_MODE_CHNG
0245 BC (02 0a f8)
0246 C. çç[HK1_PKT_FORM_NO] EQ 2
0247 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0248 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0249 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0250 C.
0251 C. *****
0252 C. SOT TI command set
0253 C. *****
0254 C. Execute, after the success of OP upload.
0255 +. TI 2012-09-25 10:02:16.0
0256 DC 07-F0 MDP_SOT_MODE_STBY
0257 BC (41)
0258 . C. -----
0259 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0260 C. -----
0261 C. ***** SOT END *****
0262 . C. Stop EIS observation and temporarily disable EIS mode changes
0263 C.
0264 C.
0265 C. ***** Start EIS operation (TI set) *****
0266 C. Execute, after the success of OP upload.
0267 C. Set EIS TI-commands
0268 +. TI 2012-09-25 10:02:30.0
0269 DC 07-FC EIS_MODE_MANU
0270 BC (21 02)
0271 +. TI 2012-09-25 10:02:40.0
0272 DC 07-FC EIS_MODE_CHG_DIS
0273 BC (22)
0274 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0275 C. ***** End EIS operation (TI set) *****
0276 C.
0277 C.
0278 C.
0279 C. ***** XRT START *****
0280 C. Execute, after the success of OP upload.
0281 +. TI 2012-09-25 10:02:00.0
0282 DC 07-F0 MDP_XRT_MODE_STBY
0283 BC (c3)
0284 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0285 C.
0286 C. ***** XRT END *****
0287 C.
0288 . C. ***** MDP ´ÜÄîó¹ò¼ÝòÈÄò¹òðDCBC•×²è *****
0289 C. (¼ª°î¥6¥Ä¥È¥P¥È¥Ä¥ç¥èòÈ¼¼ò¼Ä»Üò¹òè)
0290 . S. DC-BC dcbc-402:DCBC
0291 (MDP_known_event)

```

0292 C.
0293 C.
0294 . C. ***** ¥Ð¥¹•ï Daily±¿íÑ±È´Ø±±èDCBC•x²è *****
0295 . S. DC-BC dcbc-153:DCBC
0296 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0297 C.
0298 C.
0299 . C. ;ãLOS¥Á¥\$¥Ã¥-¼Á»Û;ã
0300 C.
0301 . C. ***** LOS *****
0302 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-166 2012-09-25 12:22:10 156 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Á»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YFÿÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÀø•µ°È»Í×ÁÇøÍYçYÄY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÀ®, ùø¹øèøÈøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG_____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 08 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 85 83 06 06)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 60 20 18)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d a0 80 18 20)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 + DC 07-F0 MDP_XRT_FLD_DIS
0063 BC (d9)
0064 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0065 BC (c9)
0066 + DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 + DC 07-F0 MDP_XRT_ARS_DIS
0069 BC (d5)
0070 + DC 07-F0 MDP_XRT_FLD_RESET
0071 BC (da)
0072 . C. ----- Success Verify ? OK / NG _____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2012-09-25 10:02:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG _____
0083 C.
0084 C. ***** XRT END *****
0085 . C. *****
0086 C. SOT table upload
0087 C. *****
0088 . C. < Stop FG table >
0089 +. DC 07-F0 MDP_FG_CTRL_MANU
0090 BC (51)
0091 . C. -----
0092 C. MDP_FG_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 . C. <Upload FG Observation Table>
```

```

0096 . S. RAM ram-265:MDP_OBS_F
0097 ( )
0098 C.
0099 . C. < Dump RAMID=MDP_OBS_F >
0100 +. DC 07-F0 MDP_DUMP_FGTBL
0101 BC (82 07 00 00 00 38 b8)
0102 C. -----
0103 C. MDP_OBS_F verify = OK/NG [ ]
0104 C. -----
0105 C.
0106 . C. < Upload DPL table >
0107 C.
0108 C. ¥ç¥Ã¥×¥í;¼¥É°âÊ°°ÊSTS_CHK°ðOFF°Ê°¹°è
0109 C.
0110 . S. RAM ram-271:MDP_DPL
0111 ( )
0112 C.
0113 . C. < Dump RAMID=MDP_DPL >
0114 +. DC 07-F0 MDP_DUMP_FGTBL
0115 BC (82 07 00 38 b8 00 40)
0116 C. -----
0117 C. MDP_DPL verify = OK [ ]
0118 C. -----
0119 C.
0120 C. STS_CHK°ðON°Ê°¹°è
0121 C.
0122 . C. < Update MDP DSC PAR1 >
0123 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0124 BC (4c)
0125 C. MDP_CMD_CODE = F04C0700[ ]
0126 C. MDP_CMD_CNT (count-up 1) [ ]
0127 C. -----
0128 C.
0129 . C.
0130 C. *****
0131 C. SOT TI command set
0132 C. *****
0133 C. Execute, after the success of TBL upload.
0134 +. TI 2012-09-25 10:02:18.0
0135 DC 07-F0 MDP_SOT_MODE_OBSV
0136 BC (40)
0137 C. -----
0138 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0139 C. -----
0140 C.
0141 C.
0142 . C. ***** MDP `ûÃîâî»ò¼¥°Ê°¹°èDCBC•x²è *****
0143 C. (¼â°î¥Ö¥Ã¥È¥P¥È¥â¥ç¥è°Ê¼°¼¼¼»Û°¹°è)
0144 . S. DC-BC dcbc-402:DCBC
0145 (MDP_known_event)
0146 C.
0147 C.
0148 . C. ***** ¥D¥¹.İ Daily±;îÑ°Ê´Ø°¹°èDCBC•x²è *****
0149 . S. DC-BC dcbc-153:DCBC
0150 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0151 C.
0152 C.
0153 . C. ;ãLOS¥Ã¥S¥Ã¥~¼¼¼»Û;ã
0154 C.
0155 . C. ***** LOS *****
0156 C.

```

*** OP Sequence for XRT ***

2012/09/25	10:12:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/25	10:12:56.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2012/09/25	10:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00
2012/09/25	10:16:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/09/25	10:17:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/09/25	10:17:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/09/25	10:17:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/25	10:19:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/09/25	10:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/25	10:22:54.0	XRT_CTRL_MANU_448_OG [0x1c0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/25	10:22:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/25	10:23:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00
2012/09/25	10:23:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/25	10:23:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/25	10:23:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/25	10:23:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/25	10:23:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/25	10:23:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/09/25	10:23:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/25	10:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/25	12:44:54.0	XRT_CTRL_MANU_448_OG [0x1c0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/25	12:44:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/25	12:45:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	02 00 00 00
2012/09/25	12:45:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/25	12:45:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/25	12:45:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/25	12:45:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/25	12:45:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/25	12:45:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/09/25	12:45:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/25	12:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/25	14:29:54.0	XRT_CTRL_MANU_448_OG [0x1c0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/25	14:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/25	14:30:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	01 00 00 00
2012/09/25	14:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/25	14:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/25	14:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/25	14:30:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/25	14:30:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/25	14:30:26.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/09/25	14:30:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/25	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/25	15:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/25	15:03:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/25	15:03:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/25	15:06:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			

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2012/09/25	15:26:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/25	15:27:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/25	16:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	16:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	16:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/25	16:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/25	17:02:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/25	17:03:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/25	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	17:52:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	17:53:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/09/25	17:53:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/09/25	17:53:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/09/25	17:53:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/09/25	17:55:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/25	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2012/09/25	18:02:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	18:02:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	18:03:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/09/25	18:03:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	02 00 00 00 00			
2012/09/25	18:03:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/09/25	18:03:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/09/25	18:03:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/09/25	18:03:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/09/25	18:03:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/25	18:03:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2012/09/25	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/09/25	18:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	18:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	18:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/25	18:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/25	18:39:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/25	18:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/25	19:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	19:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	19:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/25	19:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	da			
2012/09/25	19:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	da			
2012/09/25	19:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/25	19:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/25	20:16:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/25	20:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/25	21:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	21:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	21:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/25	21:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/09/25	21:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/25	21:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/09/25	21:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/09/25	21:53:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/25	21:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
2012/09/25	22:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/09/25	23:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/09/25	23:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 b1 f4 01 0e			
2012/09/25	23:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/09/25	23:02:46.0	XRT_FLD_ENA_437_OG [0x1b5]							

2012/09/25	23:02:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/25	23:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/25	23:02:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/25	23:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/25	23:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/25	23:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2012/09/25	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/25	23:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/25	23:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/25	23:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/25	23:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/25	23:24:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/25	23:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG				
2012/09/26	00:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	00:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	00:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	00:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	00:52:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	00:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG				
2012/09/26	02:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	02:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	02:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	02:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	02:28:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	02:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG				
2012/09/26	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	03:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	03:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	03:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	04:05:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	04:06:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG				
2012/09/26	05:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	05:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	05:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	05:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	05:30:00.0	AOCs_OrE-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	05:42:30.0	XRT_CTRL_MANU_448_OG [0x1c0]	AOCU_NM	5	02-76	02 00 00 00 00	
2012/09/26	05:42:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	05:42:52.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/09/26	05:42:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/26	05:42:56.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/26	05:42:58.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/26	05:43:00.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/26	05:43:02.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	05:43:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2012/09/26	05:43:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/26	06:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	06:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26			MDP_XRT_FLD_RESET	1	07-F0	da	

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2012/09/26	06:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/26	06:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/26	07:20:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/26	07:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/26	08:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/26	08:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/26	08:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/26	08:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/26	08:56:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/26	08:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/26	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/26	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/26	10:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/26	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/26	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/26	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/26	10:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/09/26	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/26	10:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/09/26	10:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/26	10:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/26	10:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/26	10:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/26	10:27:00.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/26	10:27:02.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/26	10:27:22.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/26	10:27:24.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/26	10:27:26.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/26	10:27:28.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/26	10:27:30.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/26	10:27:32.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/09/26	10:27:34.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/26	10:27:36.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/26	14:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/26	14:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/26	14:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/26	14:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/26	14:12:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/26	14:13:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/26	14:29:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/26	14:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/26	14:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/09/26	14:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/26	14:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/26	14:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/26	14:30:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/26	14:30:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/26	14:30:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				

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2012/09/26	14:30:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/26	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	15:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	15:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	15:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	16:02:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/26	16:03:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	17:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	17:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	17:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	17:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	17:39:30.5	XRT_Custom_434_OG [0x1b2]					
2012/09/26	17:40:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	17:56:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/09/26	17:57:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00	00
2012/09/26	17:57:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/26	17:57:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/26	17:57:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/26	17:59:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/09/26	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	18:06:54.0	XRT_CTRL_MANU_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	18:07:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 ac 00 00	00
2012/09/26	18:21:28.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/09/26	18:21:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/26	18:21:50.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/26	18:21:52.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/26	18:21:54.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/26	18:21:56.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	18:21:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/09/26	18:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	20:06:54.0	XRT_CTRL_MANU_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	20:07:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00 54	00
2012/09/26	20:21:28.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/09/26	20:21:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/09/26	20:21:50.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/09/26	20:21:52.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/26	20:21:54.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/26	20:21:56.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	20:21:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2012/09/26	20:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/26	22:07:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00	00
2012/09/26	22:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/26	22:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/26	22:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/26	22:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/26	22:29:30.0	XRT_CTRL_MANU_448_OG [0x1c0]					

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2012/09/26	22:29:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2012/09/26	22:29:52.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/26	22:29:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/26	22:29:56.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/26	22:29:58.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/26	22:30:00.5	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/26	22:30:02.5	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/09/26	22:30:04.5	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/26	22:30:06.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/26	23:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/26	23:45:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/26	23:45:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/26	23:48:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/26	23:55:00.0	XRT_Custom_434_OG [0x1b2]				
2012/09/26	23:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/27	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	01:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/27	01:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/27	01:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/27	01:28:00.0	XRT_Custom_434_OG [0x1b2]				
2012/09/27	01:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/27	02:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	02:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/27	02:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/27	02:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/27	03:04:30.5	XRT_Custom_434_OG [0x1b2]				
2012/09/27	03:05:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/27	04:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	04:13:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/27	04:13:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/27	04:16:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/27	04:42:00.5	XRT_Custom_434_OG [0x1b2]				
2012/09/27	04:43:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/27	05:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	05:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/27	05:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/27	05:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/27	06:19:30.0	XRT_Custom_434_OG [0x1b2]				
2012/09/27	06:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/27	07:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	07:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/27	07:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/27	07:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/27	07:57:00.0	XRT_Custom_434_OG [0x1b2]				
2012/09/27	07:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/27	08:38:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	09:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/27	09:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/27	09:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

2012/09/27	09:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/09/27	10:07:00.0	AOCS_ORe-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	00	00	00	00	00	00