

XRT Timeline to be uploaded on 2012/09/29

Period: 2012/09/29 11:23:00 - 2012/10/04 10:43:00

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Normal mode

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XOB #192E: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1

Term	Pointing (x, y)	Comment
09/29 11:36:00 - 09/29 11:42:54	Fixed (0.0, 0.0)	# OP start + 10min : HOP 186
09/30 10:03:00 - 09/30 10:11:00	Fixed (0.0, 0.0)	synoptic, HOP 186 + SOT H-alpha flat field
10/01 09:58:00 - 10/01 10:20:00	Fixed (0.0, 0.0)	synoptic - HOP 186

Subr= 04	1-time(s)	12.0sec
Subr= 1	1-time(s)	12.0sec
Seqn= 63	1-time(s)	4.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 6	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 70	1-time(s)	4.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 69	1-time(s)	2.0sec
Open/G-band	Open/G-band	open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2	1-time(s)	6.0sec
Seqn= 66	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Subr= 3	1-time(s)	2.0sec
Seqn= 68	1-time(s)	2.0sec
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1934: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 50s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
09/29 11:46:00 - 09/29 14:18:30	Track (-595.3, -304.7) @ 09/29 11:43:00	Track AR 11582

Subr= 15	Inf.-time(s)	2.0sec
Subr= 1	1-time(s)	2.0sec
Seqn= 18	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 19	1-time(s)	2.0sec
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 65	4-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 73	70-time(s)	2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1936: HOP 215 - AR Standard-B(Morphology) with PFB, Al/Poly + thin-Be + multifilter context, 384x384 at 1064 1048, 40s-cad w/ G-Band VLS Closed

Term	Pointing (x, y)	Comment
09/29 14:33:00 - 09/29 17:29:00	Track (865.3, 67.4) @ 09/29 14:30:00	HOP 215 - AR 11575
09/30 14:33:00 - 09/30 17:42:24	Track (2.2, -292.6) @ 09/30 14:30:00	HOP 215 - AR 11579

Subr= 20	Inf.-time(s)	2.0sec
Subr= 1	1-time(s)	2.0sec
Seqn= 18	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 19	1-time(s)	2.0sec
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 74	40-time(s)	40.0sec
Al-poly/Open	Al-poly/thick-Be	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 73	1-time(s)	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18

Term	Pointing (x, y)	Comment
09/29 17:57:30 - 09/29 18:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
09/30 17:45:30 - 09/30 17:52:24	Fixed (0.0, 0.0)	synoptic, shifted -17.5 min
10/01 18:07:30 - 10/01 18:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min

PROG= 13 1-time(s)

- Subr= 1 1-time(s) 14.0sec
 - Seqn= 64 1-time(s) 4.0sec
 - Open/Al-mesh Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 6 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 70 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 67 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 69 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 68 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
09/29 18:07:30 - 09/30 09:59:54	Track (-550.3, -308.7) @ 09/29 18:04:30	# track AR 11582
09/30 10:58:00 - 09/30 14:29:54	Track (-421.4, -317.7) @ 09/30 10:55:00	# track AR 11582
09/30 17:55:30 - 10/01 09:54:54	Track (-364.7, -320.7) @ 09/30 17:52:30	# track AR 11582
10/01 16:08:00 - 10/01 18:00:24	Track (-173.8, -327.0) @ 10/01 16:05:00	track AR 11582
10/01 18:13:30 - 10/02 09:03:00	Track (-155.3, -327.3) @ 10/01 18:10:30	# track AR 11582

PROG= 03 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 18 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 19 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Seqn= 65 4-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 73 70-time(s) 120.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
09/30 10:14:06 - 09/30 10:54:54	Fixed (0.0, 0.0)	synoptic, HOP 186 + SOT H-alpha flat field

PROG= 12 Inf.-time(s)

- Subr= 1 1-time(s) 120.0sec
 - Seqn= 34 1-time(s) 4.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms

Term	Pointing (x, y)	Comment
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PROG= 09 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 61	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 62	1-time(s)	30.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	30-time(s)	2.0sec										
└─ Seqn= 60	2-time(s)	30.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
09/29 11:46:00 - 09/29 14:18:30	Track (-595.3, -304.7) ^{Ⓜ 09/29 11:43:00}	Track AR 11582
09/29 14:33:00 - 09/29 17:29:00	Track (865.3, 67.4) ^{Ⓜ 09/29 14:30:00}	HOP 215 - AR 11575
09/29 18:07:30 - 09/30 09:59:54	Track (-550.3, -308.7) ^{Ⓜ 09/29 18:04:30}	# track AR 11582
09/30 10:14:06 - 09/30 10:54:54	Fixed (0.0, 0.0)	synoptic, HOP 186 + SOT H-alpha flat field
09/30 10:58:00 - 09/30 14:29:54	Track (-421.4, -317.7) ^{Ⓜ 09/30 10:55:00}	# track AR 11582
09/30 14:33:00 - 09/30 17:42:24	Track (2.2, -292.6) ^{Ⓜ 09/30 14:30:00}	HOP 215 - AR 11579
09/30 17:55:30 - 10/01 09:54:54	Track (-364.7, -320.7) ^{Ⓜ 09/30 17:52:30}	# track AR 11582
10/01 10:24:00 - 10/01 16:04:54	Fixed (-14.0, 881.0)	HOP 206 - North pole
10/01 16:08:00 - 10/01 18:00:24	Track (-173.8, -327.0) ^{Ⓜ 10/01 16:05:00}	track AR 11582
10/01 18:13:30 - 10/02 09:03:00	Track (-155.3, -327.3) ^{Ⓜ 10/01 18:10:30}	# track AR 11582

PROG= 16 15-time(s)

Subr= 1	45-time(s)	10.0sec										
└─ Seqn= 35	1-time(s)	2.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	10.0sec										
└─ Seqn= 36	1-time(s)	2.0sec										
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 37	1-time(s)	2.0sec										
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 38	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Active Region Search** * * * * *

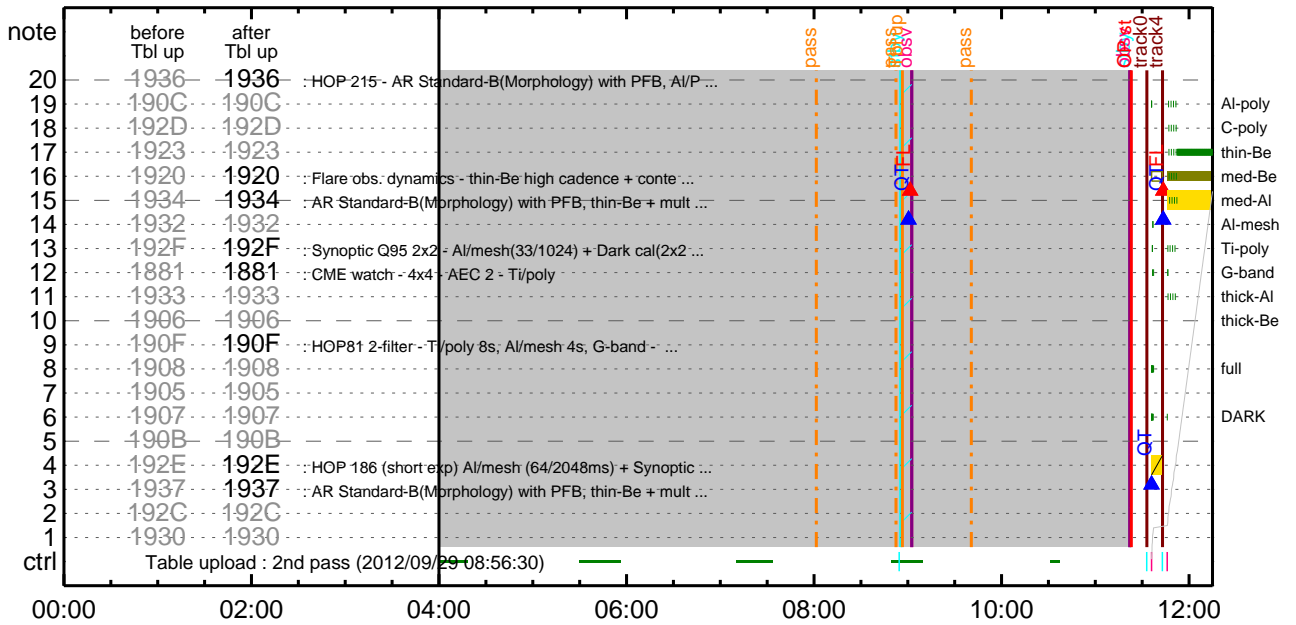
NOT USED

* * * * * **Flare Detection** * * * * *

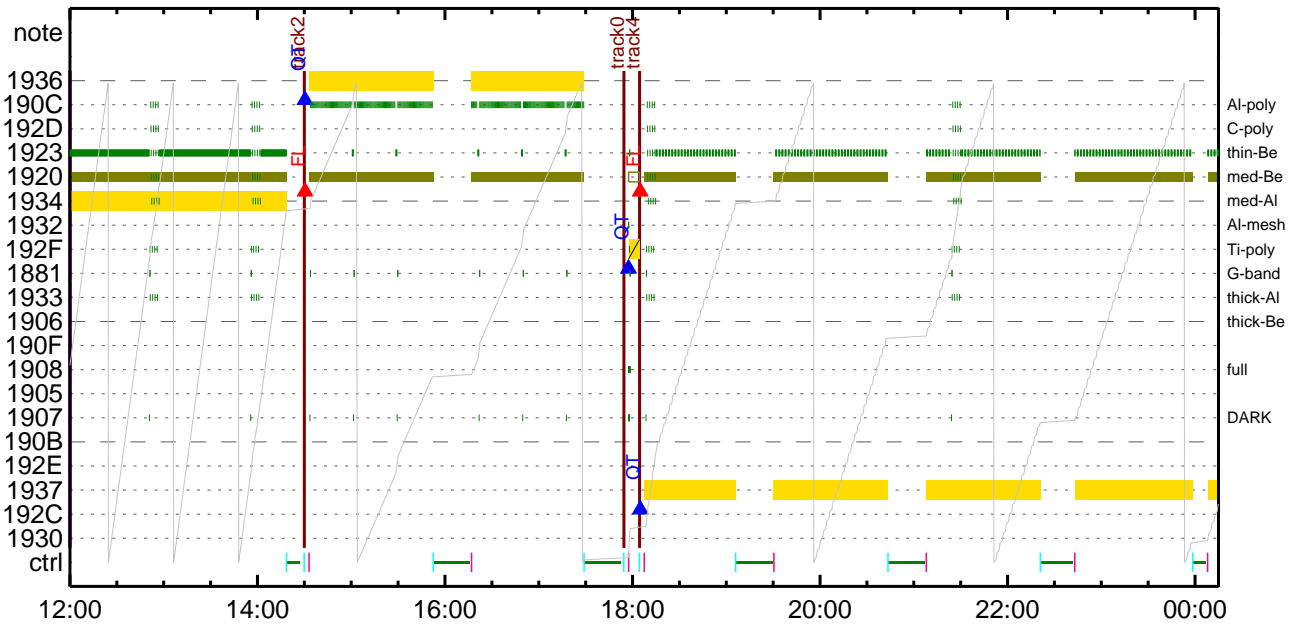
FLD Patrol

Term	Pointing (x, y)	Comment									
09/29 11:43:16 - 09/29 17:54:46	Track (-595.3, -304.7) ^{Ⓜ 09/29 11:43:00}	Track AR 11582									
09/29 18:04:46 - 09/30 10:00:16	Track (-550.3, -308.7) ^{Ⓜ 09/29 18:04:30}	# track AR 11582									
09/30 10:13:52 - 09/30 17:42:46	Fixed (0.0, 0.0)	synoptic, HOP 186 + SOT H-alpha flat field									
09/30 17:52:46 - 10/01 09:55:16	Track (-364.7, -320.7) ^{Ⓜ 09/30 17:52:30}	# track AR 11582									
10/01 10:22:52 - 10/01 18:04:46	Fixed (-14.0, 881.0)	HOP 206 - North pole									
10/01 18:10:46 - 10/04 10:43:00	Track (-155.3, -327.3) ^{Ⓜ 10/01 18:10:30}	# track AR 11582									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

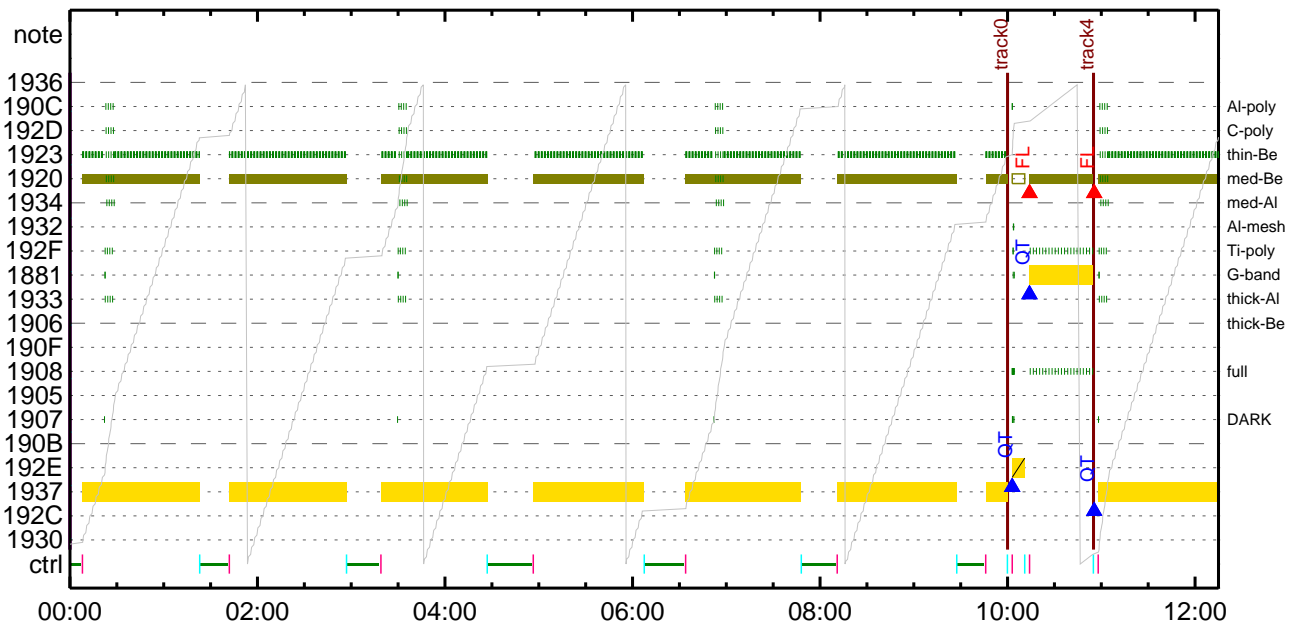
CMDI #0945 2012/09/29



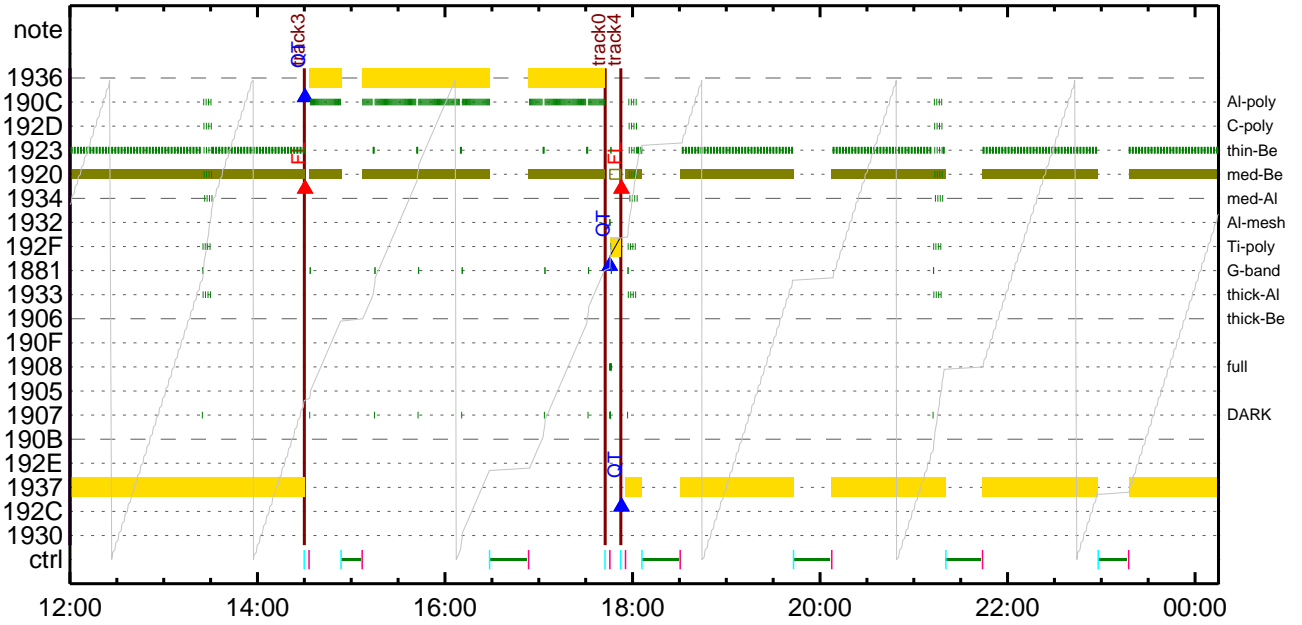
CMDI #0945 2012/09/29



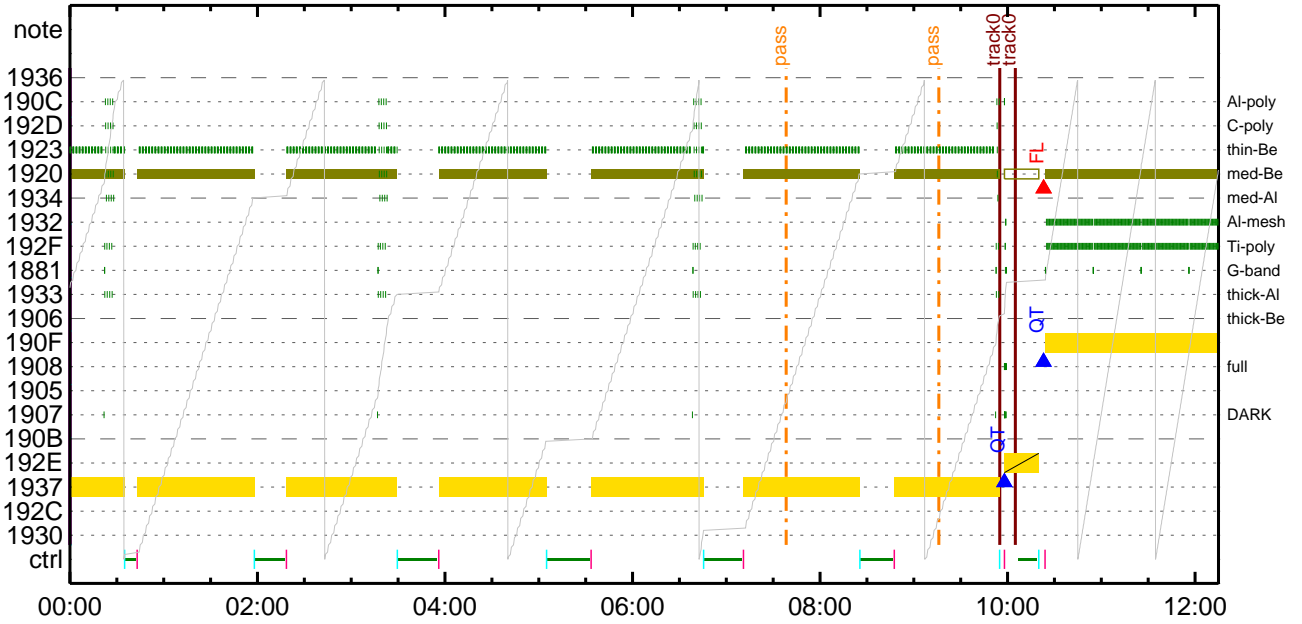
CMDI #0945 2012/09/30



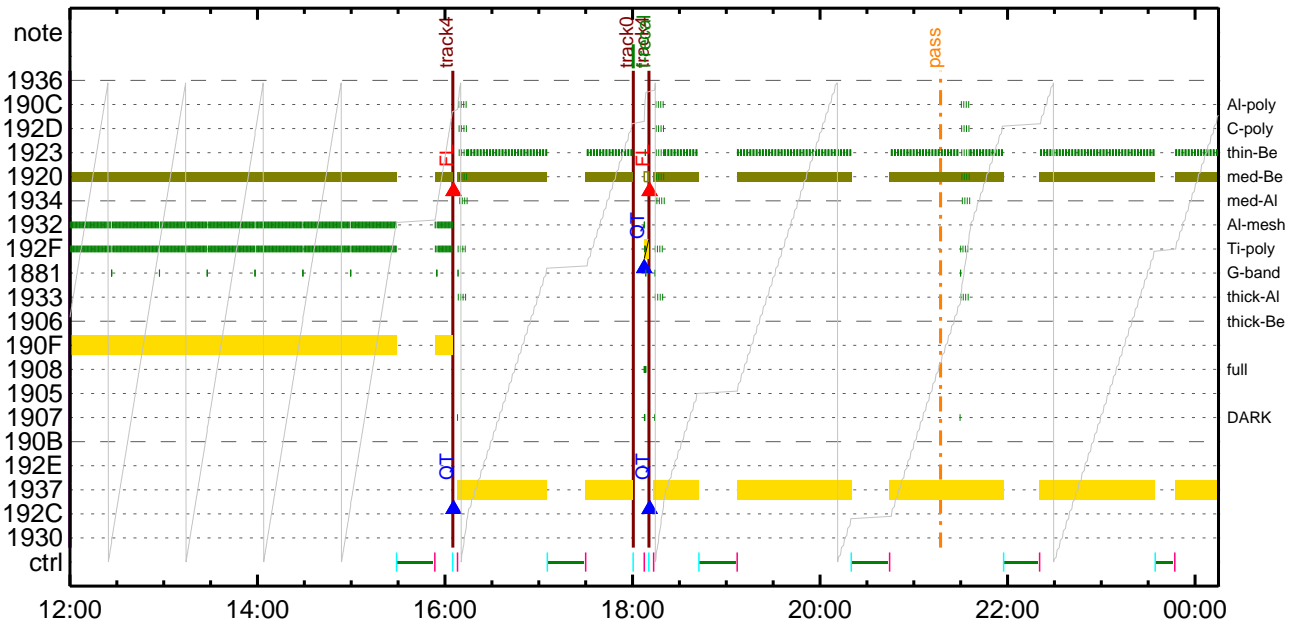
CMDI #0945 2012/09/30



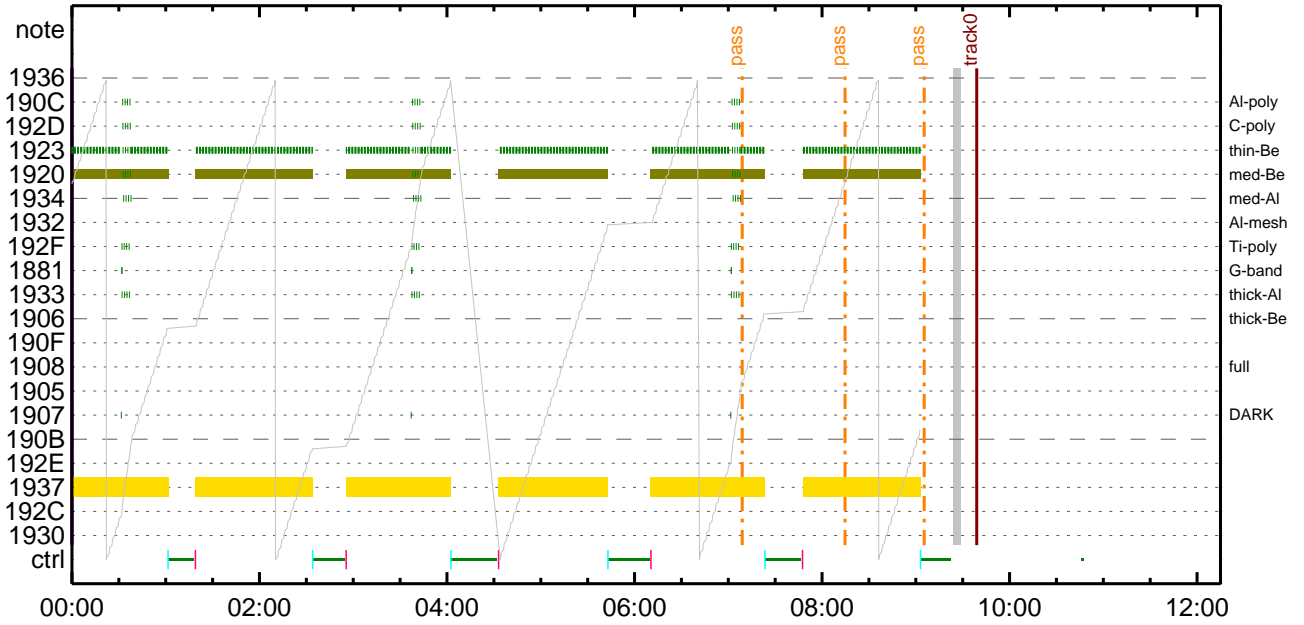
CMDI #0945 2012/10/01



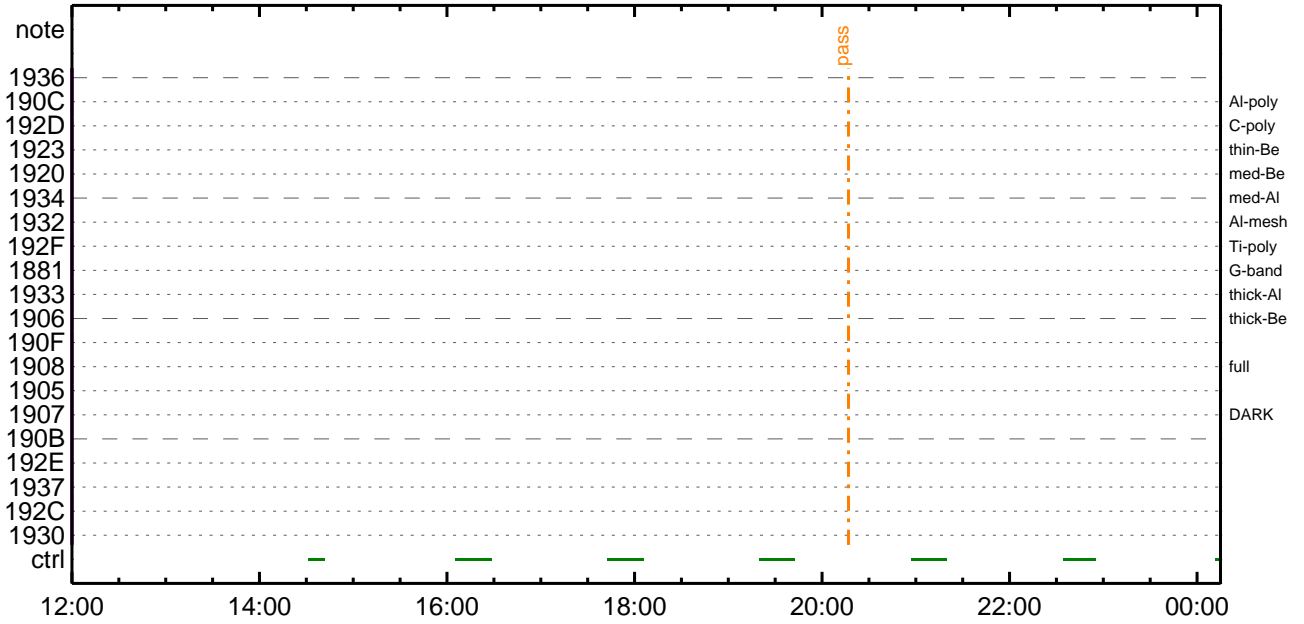
CMDI #0945 2012/10/01



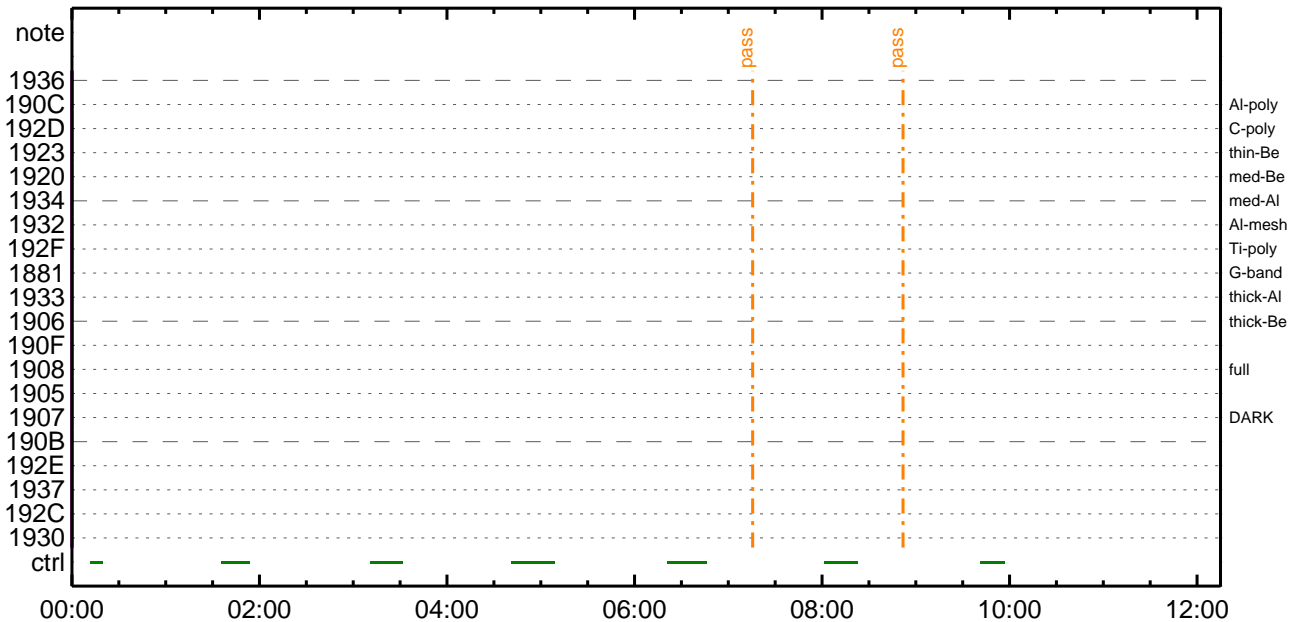
CMDI #0945 2012/10/02



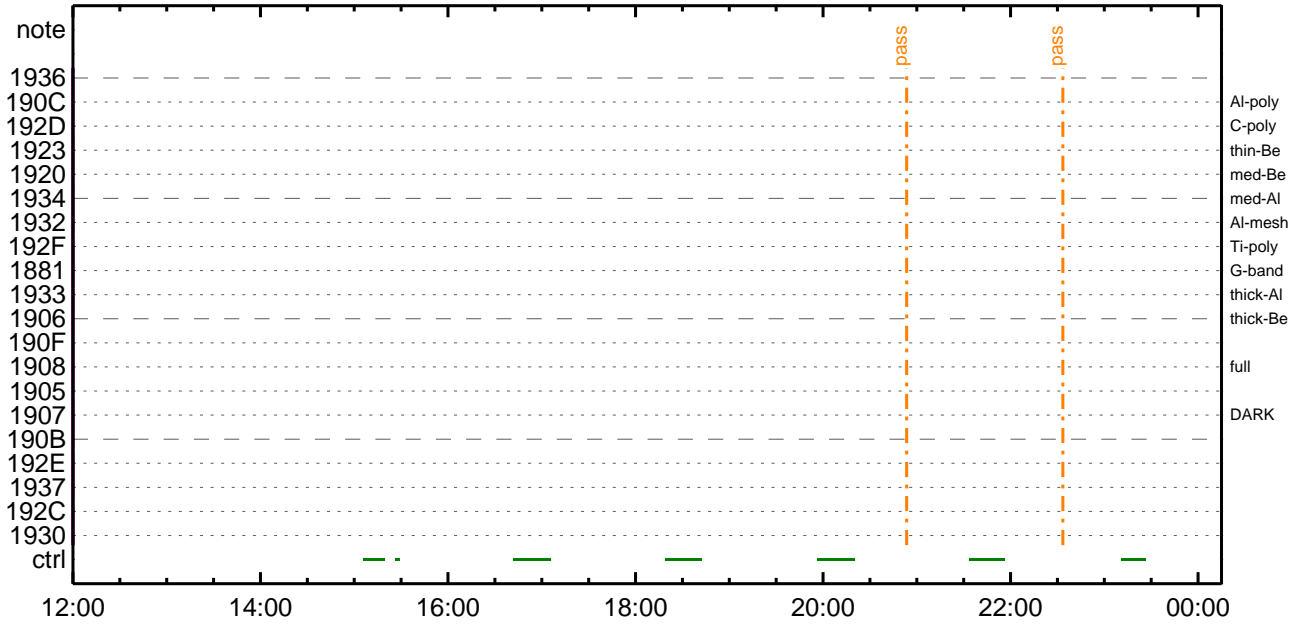
CMDI #0945 2012/10/02



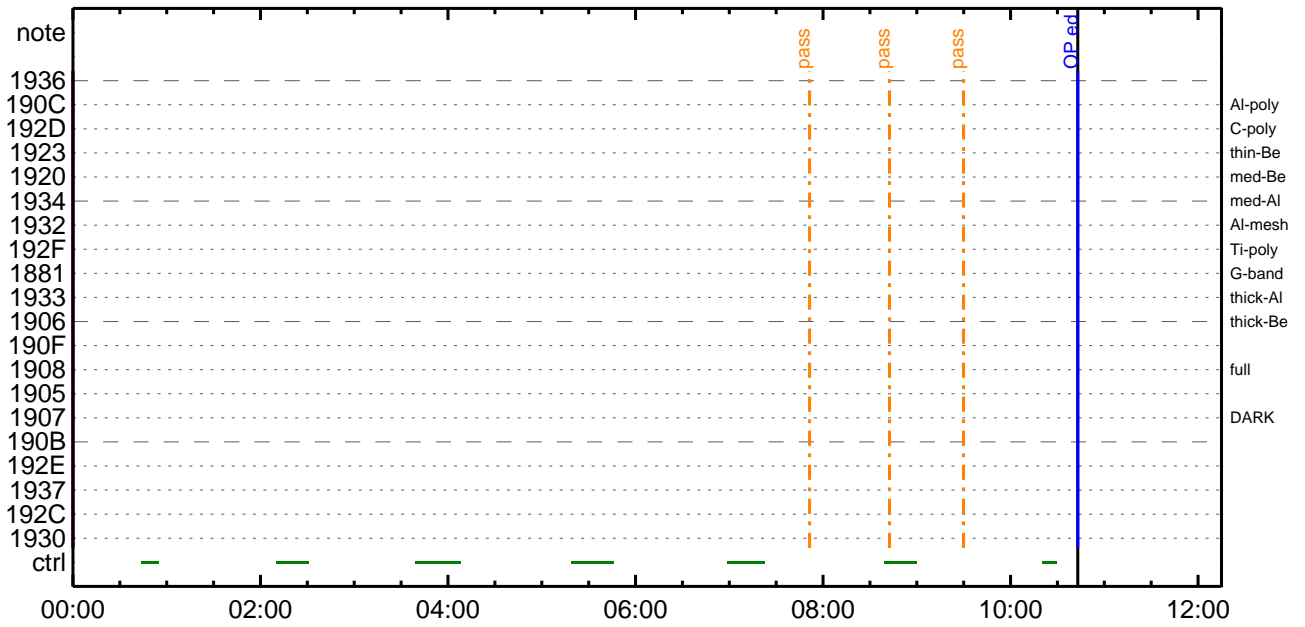
CMDI #0945 2012/10/03



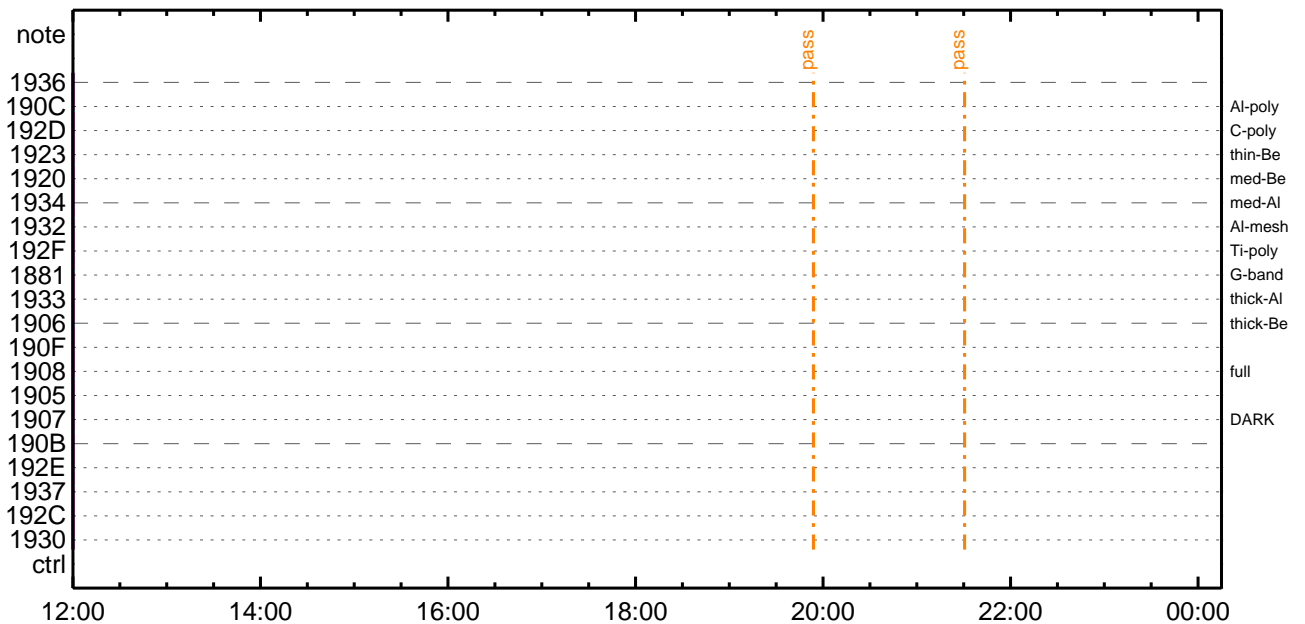
CMDI #0945 2012/10/03



CMDI #0945 2012/10/04



CMDI #0945 2012/10/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-174:OP
0104 ( )
0105 S. OG og-174:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYA6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YA6Yx½ªî»ò³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½Ë¹ç•è²îOKò³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YA6Yx½ªî»ò³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½Ë¹ç•è²îOKò³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YA6Yx½ªî»ò³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½Ë¹ç•è²îOKò³îç§
0165 C.
0166 C. ***** òË²¼òî½Ë¹ç•è²îOKò³îç§ *****
0167 C. DHUªâ;½YE;Ë½Y½;Yi;½YE;Ëòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î½Ë¹ç•è²îOKò³îç§
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0182 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0183 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0184 +. TI 2012-09-29 11:18:00.0
0185 DC 01-B3 DHU_OP_STOP
0186 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0187 C.
0188 +. TI 2012-09-29 11:18:01.0
0189 DC 01-B4 DHU_OP_COPY
0190 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0191 C.
0192 +. TI 2012-09-29 11:18:01.0
0193 DC 01-B5 DHU_OPOG_COPY
0194 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2012-09-29 11:22:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%íîñαîŷÄŷ§ŷÄŷ-¹âiü
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÖŷx½ªî»αð³îÇ§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾Ê¹ç•è²îOKαð³îÇ§
0229 C.
0230 C. DHUŷä;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÈ;Êαðîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-09-29 11:22:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-09-29 11:22:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2012-09-29 11:22:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-09-29 11:22:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÄîαî»ö¼ŷαÉÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷÈŷÄŷçŷèè¼αα¼Ä»Üα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÈŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Ü;ä
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-175 2012-09-29 13:41:09 153 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È±¿±Á±·µ°È»Í×ÁÇ±ÍYçYÁY×Yí;¼YÉ;ÈÈ±µ·íÉ;È±È¼°ÇÔ±·±¿¼l¹ç±Í;çÀ®, ù±¹±±±±±±±ÇÁ+¿®±·±È±±±±±±È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 50s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-09-29 11:22:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 C. ***** XRT START *****
0069 C.
0070 +. DC 07-F0 MDP_XRT_CTRL_MANU
0071 BC (c1)
0072 + DC 07-F0 MDP_XRT_MODE_STBY
0073 BC (c3)
0074 . C. ----- Success Verify ? OK / NG____
0075 C.
0076 C. XRT Obs. Table Upload
0077 . S. RAM ram-291:MDP_OBS_X
0078 ( )
0079 C.
0080 +. DC 07-F0 MDP_DUMP_XRTTBL
0081 BC (84 07 00 00 00 3a d4)
0082 . C. ----- Comparison Check ? OK / ERR ____
0083 C.
0084 C.
0085 +. DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 01 b1 b1 04 04)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 02 b1 b1 08 08)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 03 b1 b1 08 08)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 04 b1 b1 06 06)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 05 85 83 06 06)
0095 + DC 07-F0 MDP_XRT_ROI_SET
```

```

0096 BC (cd 06 85 83 06 06)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 07 80 80 20 20)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 08 80 80 20 08)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 09 80 80 08 20)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 0a 80 80 06 06)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 0b 85 83 08 08)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108 BC (cd 0f 80 80 06 06)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110 BC (cd 10 80 80 08 08)
0111 + DC 07-F0 MDP_XRT_FLD_ENA
0112 BC (d8)
0113 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0114 BC (c8)
0115 + DC 07-F0 MDP_XRT_AEC_RESET
0116 BC (d0)
0117 + DC 07-F0 MDP_XRT_ARS_DIS
0118 BC (d5)
0119 + DC 07-F0 MDP_XRT_FLD_RESET
0120 BC (da)
0121 + DC 07-F0 MDP_XRT_QT_PROG_SET
0122 BC (c4 0f)
0123 + DC 07-F0 MDP_XRT_FL_PROG_SET
0124 BC (c5 10)
0125 . C. ----- Success Verify ? OK / NG ____
0126 C.
0127 C.
0128 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0129 C.
0130 + DC 07-F0 MDP_XRT_MODE_OBSV
0131 BC (c2)
0132 + TI 2012-09-29 11:22:02.0
0133 DC 07-F0 MDP_XRT_MODE_OBSV
0134 BC (c2)
0135 . C. ----- Success Verify ? OK / NG ____
0136 C.
0137 C. ***** XRT END *****
0138 C.
0139 . C. ***** MDP `uAÎaÎ»ô%ÿnÈÂa¹aèDCBC•x²è *****
0140 C. (%â°iÿÓÿÁÿÈÿPÿËÿâÿçÿèaÈ%¼aa¼A»Ûa¹aè)
0141 . S. DC-BC dcbc-402:DCBC
0142 (MDP_known_event)
0143 C.
0144 C.
0145 . C. ***** ÿDÿ¹.Ï Daily±;îÑaÈ´Øa¹aèDCBC•x²è *****
0146 . S. DC-BC dcbc-153:DCBC
0147 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148 C.
0149 C.
0150 . C. ;ãLOSÿÁÿSÿÿÿ¼A»Û;ã
0151 C.
0152 . C. ***** LOS *****
0153 C.

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*** OP Sequence for XRT ***

```

2012/09/29 11:32:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 11:32:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/09/29 11:33:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/09/29 11:33:16.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/09/29 11:33:18.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/09/29 11:33:20.0 XRT_ARS_DIS_406_OG [0x196]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/09/29 11:35:58.0 XRT_QT_PROG_SET_416_OG [0x1a0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2012/09/29 11:36:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/09/29 11:42:54.0 XRT_CTRL_MANU_448_OG [0x1c0]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 11:42:56.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/09/29 11:43:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 04 00 00 00 00
2012/09/29 11:43:16.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/09/29 11:43:18.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/09/29 11:43:20.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/09/29 11:43:22.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/09/29 11:43:24.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/09/29 11:43:26.0 XRT_QT_PROG_SET_435_OG [0x1b3]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0f
2012/09/29 11:43:28.0 XRT_FL_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2012/09/29 11:46:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/09/29 14:18:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 14:18:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/09/29 14:18:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/09/29 14:21:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/09/29 14:29:54.0 XRT_CTRL_MANU_448_OG [0x1c0]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 14:29:56.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/09/29 14:30:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 02 00 00 00 00
2012/09/29 14:30:16.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/09/29 14:30:18.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/09/29 14:30:20.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/09/29 14:30:22.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/09/29 14:30:24.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/09/29 14:30:26.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 14
2012/09/29 14:30:28.0 XRT_FL_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2012/09/29 14:33:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/09/29 15:52:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 15:52:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/09/29 15:52:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/09/29 15:55:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/09/29 16:16:00.0 XRT_Custom_434_OG [0x1b2]
2012/09/29 16:17:00.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/09/29 17:29:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 17:29:02.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/09/29 17:29:04.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/09/29 17:32:14.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/09/29 17:54:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/09/29 17:54:26.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00

```

2012/09/29	17:54:30.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	00	00	00	00	00
2012/09/29	17:54:46.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/29	17:54:48.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/29	17:54:50.0	XRT_ARS_DIS_406_OG [0x196] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/29	17:57:28.0	XRT_QT_PROG_SET_419_OG [0x1a3] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2012/09/29	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/29	18:04:24.0	XRT_CTRL_MANU_448_OG [0x1c0] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/29	18:04:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/09/29	18:04:30.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	04	00	00	00	00
2012/09/29	18:04:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/29	18:04:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/29	18:04:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/29	18:04:52.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/29	18:04:54.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/29	18:04:56.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/09/29	18:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/09/29	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/29	19:06:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/29	19:06:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/29	19:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/29	19:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/29	19:29:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/29	19:30:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/29	20:43:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/29	20:43:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/29	20:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/29	20:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/29	21:07:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/29	21:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/29	22:21:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/29	22:21:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/29	22:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/29	22:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/29	22:42:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/29	22:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/29	23:58:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/29	23:58:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/29	23:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/30	00:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/30	00:07:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/30	00:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	01:23:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/30	01:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/30	01:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/30	01:41:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/30	01:42:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	02:57:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	02:57:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/09/30	02:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/30	03:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/30	03:18:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/30	03:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	04:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	04:27:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/30	04:27:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/30	04:30:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/30	04:55:30.0	XRT_Custom_434_OG [0x1b2]			
2012/09/30	04:56:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	06:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	06:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/30	06:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/30	06:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/30	06:33:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/30	06:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	07:48:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	07:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/30	07:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/30	07:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/30	08:10:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/30	08:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	09:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	09:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/30	09:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/30	09:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/30	09:45:00.0	XRT_Custom_434_OG [0x1b2]			
2012/09/30	09:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/09/30	10:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/09/30	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/09/30	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/09/30	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/30	10:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/09/30	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	10:11:00.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	10:13:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/30	10:13:52.0	XRT_FLD_ENA_437_OG [0x1b5]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/30	10:13:54.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/30	10:13:56.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/30	10:13:58.0	XRT_ARS_DIS_412_OG [0x19c]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/30	10:14:00.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/30	10:14:02.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/09/30	10:14:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/30	10:14:06.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/30	10:54:54.0	XRT_CTRL_MANU_448_OG [0x1c0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/30	10:54:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/30	10:55:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	04 00 00 00 00

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2012/09/30	10:55:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/30	10:55:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/30	10:55:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/30	10:55:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/30	10:55:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/30	10:55:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/09/30	10:55:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/30	10:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	14:29:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	14:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/30	14:30:00.0	AOCS_Orе-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/09/30	14:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/30	14:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/30	14:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/30	14:30:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/30	14:30:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/30	14:30:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2012/09/30	14:30:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/30	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	14:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	14:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/30	14:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/30	14:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/30	15:06:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/30	15:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	16:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	16:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/30	16:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/30	16:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/30	16:52:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/30	16:53:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	17:42:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	17:42:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/30	17:42:30.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/30	17:42:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/30	17:42:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/30	17:42:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/30	17:45:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/09/30	17:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/30	17:52:24.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/30	17:52:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/30	17:52:30.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/09/30	17:52:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/30	17:52:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/30	17:52:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/30	17:52:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/30	17:52:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/30	17:52:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							

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2012/09/30	17:52:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/30	17:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/30	18:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/30	18:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/30	18:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/30	18:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/30	18:29:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/30	18:30:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/30	19:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/30	19:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/30	19:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/30	19:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/30	20:06:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/30	20:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/30	21:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/30	21:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/30	21:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/30	21:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/30	21:43:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/30	21:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/30	22:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/30	22:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/30	22:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/30	23:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/30	23:16:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/30	23:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/01	00:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/01	00:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/01	00:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/01	00:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/01	00:42:00.0	XRT_Custom_434_OG [0x1b2]					
2012/10/01	00:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/01	01:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/01	01:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/01	01:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/01	02:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/01	02:17:30.0	XRT_Custom_434_OG [0x1b2]					
2012/10/01	02:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/01	03:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/01	03:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/01	03:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/01	03:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/01	03:55:00.0	XRT_Custom_434_OG [0x1b2]					
2012/10/01	03:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/01	05:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/01	05:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/01	05:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/01	05:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/01	05:32:30.0	XRT_Custom_434_OG [0x1b2]					
2012/10/01	05:33:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2012/10/01	06:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/01	06:45:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/01	06:45:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/10/01	06:48:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/10/01	07:10:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	07:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/01	08:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/01	08:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/01	08:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/10/01	08:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/10/01	08:46:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	08:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/01	09:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/01	09:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/10/01	09:55:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/10/01	09:55:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/10/01	09:55:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/10/01	09:55:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/01	09:57:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2012/10/01	09:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/01	10:05:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 b1 b3 01 3f			
2012/10/01	10:20:00.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/01	10:22:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/10/01	10:22:52.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/10/01	10:22:54.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/10/01	10:22:56.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/01	10:22:58.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/01	10:23:00.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/01	10:23:02.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2012/10/01	10:23:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/10/01	10:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/01	15:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/01	15:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/01	15:29:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/10/01	15:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/10/01	15:52:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	15:53:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/01	16:04:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/01	16:04:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/10/01	16:05:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00			
2012/10/01	16:05:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/10/01	16:05:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/10/01	16:05:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/01	16:05:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/01	16:05:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/01	16:05:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2012/10/01	16:05:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2012/10/01	16:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2012/10/01	17:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	17:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/01	17:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/01	17:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/01	17:29:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	17:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/01	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	18:00:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/10/01	18:00:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/01	18:04:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/01	18:04:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/01	18:04:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/01	18:04:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/01	18:07:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/10/01	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/01	18:10:24.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	18:10:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/01	18:10:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/10/01	18:10:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/01	18:10:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/01	18:10:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/01	18:10:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/01	18:10:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/01	18:10:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/10/01	18:10:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/01	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/01	18:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	18:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/01	18:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/01	18:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/01	19:06:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	19:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/01	20:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	20:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/01	20:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/01	20:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/01	20:43:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	20:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/01	21:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	21:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/01	21:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/01	22:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/01	22:19:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/01	22:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/01	23:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/01	23:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/01	23:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/01	23:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/01	23:46:00.0	XRT_Custom_434_OG [0x1b2]							

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2012/10/01	23:47:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	01:01:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	01:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	01:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	01:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	01:18:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	01:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	02:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	02:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	02:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	02:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	02:54:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	02:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	04:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	04:02:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	04:02:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	04:05:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	04:32:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	04:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	05:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	05:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	05:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	05:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	06:09:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	06:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	07:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	07:23:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	07:23:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	07:26:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	07:46:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	07:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	09:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	09:03:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	09:03:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	09:06:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	09:39:00.0	AOCS_Re-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00