

# XRT Timeline to be uploaded on 2012/10/02

Period: 2012/10/02 09:29:00 - 2012/10/06 10:19:00

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Normal mode

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XOB #192E: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x												
Term	Pointing (x, y)		Comment									
10/02 09:42:00 - 10/02 09:48:54	Fixed (	0.0, 0.0)	# OP start + 10min: a synoptic including HOP186									
10/03 10:03:00 - 10/03 10:09:54	Fixed (	0.0, 0.0)	synoptic with HOP186									
10/04 10:03:00 - 10/04 10:20:30	Fixed (	0.0, 0.0)	synoptic with HOP186									
<b>PROG= 04 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 63 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 6 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 70 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 69 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 1-time(s) 6.0sec												
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Subr= 3 1-time(s) 2.0sec												
└─ Seqn= 68 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1934: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 50s-cad w/ G-Band VLS Closed Test												
Term	Pointing (x, y)		Comment									
10/02 09:52:00 - 10/02 10:59:54	Track (	16.3, -323.8)	AR 11582									
10/02 15:03:00 - 10/02 17:42:30	Track (	267.7, 176.3)	AR 11580									
10/03 10:13:00 - 10/03 13:09:54	Track (	233.5, -319.6)	AR 11582									
10/03 13:13:00 - 10/03 17:55:54	Track (	78.1, -482.3)	EFR in south (AR11584)									
<b>PROG= 15 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 18 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Seqn= 65 4-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 73 70-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly												
Term	Pointing (x, y)		Comment									
10/02 11:03:00 - 10/02 11:15:54	Fixed (	936.0, 0.0)	HOP130 (1/15)									
10/02 11:19:00 - 10/02 11:31:54	Fixed (	468.0, 0.0)	(2/15)									
10/02 11:51:00 - 10/02 12:03:54	Fixed (	-468.0, 0.0)	(4/15)									
10/02 12:07:00 - 10/02 12:19:54	Fixed (	-936.0, 0.0)	(5/15)									
10/02 12:23:00 - 10/02 12:29:54	Fixed (	815.0, 470.0)	(6/15)									
10/02 12:33:00 - 10/02 12:45:54	Fixed (	411.0, 847.0)	(7/15)									
10/02 12:49:00 - 10/02 13:01:54	Fixed (	0.0, 941.0)	(8/15)									
10/02 13:05:00 - 10/02 13:17:54	Fixed (	-411.0, 847.0)	(9/15)									
10/02 13:21:00 - 10/02 13:27:54	Fixed (	-815.0, 470.0)	(10/15)									
10/02 13:31:00 - 10/02 13:37:54	Fixed (	815.0, -470.0)	(11/15)									

10/02 13:41:00 - 10/02 13:53:54	Fixed ( 411.0, -847.0)	(12/15)
10/02 13:57:00 - 10/02 14:09:54	Fixed ( 0.0, -941.0)	(13/15)
10/02 14:13:00 - 10/02 14:46:54	Fixed ( -411.0, -847.0)	(14/15)
10/02 14:50:00 - 10/02 14:59:54	Fixed ( -815.0, -471.0)	(15/15)

**PROG= 12 Inf.-time(s)**

<b>Subr= 1 1-time(s) 120.0sec</b>												
<b>Seqn= 34 1-time(s) 4.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1874: Full Sun 6 Filter- 1x1 Q98 -FW1/FW2-short exposures-2**

Term	Pointing (x, y)	Comment
10/02 11:35:00 - 10/02 11:47:54	Fixed ( 0.0, 0.0)	(3/15)

**PROG= 02 1-time(s)**

<b>Subr= 1 1-time(s) 180.0sec</b>												
<b>Seqn= 80 1-time(s) 30.0sec</b>												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 78 1-time(s) 30.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 79 1-time(s) 30.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 81 1-time(s) 30.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 82 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/Open	close	Safe	Norm	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 76 1-time(s) 2.0sec</b>												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 2 1-time(s) 360.0sec</b>												
<b>Seqn= 77 1-time(s) 4.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 75 1-time(s) 4.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)**

Term	Pointing (x, y)	Comment
10/02 18:09:00 - 10/02 18:15:54	Fixed ( 0.0, 0.0)	synoptic, shifted 6.0 min
10/03 17:59:00 - 10/03 18:05:54	Fixed ( 0.0, 0.0)	synoptic, shifted -4.0 min

**PROG= 13 1-time(s)**

<b>Subr= 1 1-time(s) 14.0sec</b>												
<b>Seqn= 64 1-time(s) 4.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 70 1-time(s) 4.0sec</b>												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 67 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 69 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 68 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test**

Term	Pointing (x, y)	Comment
10/02 18:19:00 - 10/03 09:41:30	Fixed ( 920.0, 120.0)	AR on the W-limb
10/03 18:09:00 - 10/04 09:59:54	Track ( 118.9, -481.6) @ 10/03 18:06:00	(cont.)

**PROG= 03 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 18 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 19 1-time(s) 2.0sec</b>												

Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Seqn= 65 4-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 73 70-time(s) 120.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
10/02 09:52:00 - 10/02 10:59:54	Track ( 16.3, -323.8) <sup>Ⓜ 10/02 09:49:00</sup>	AR 11582
10/02 11:03:00 - 10/02 11:15:54	Fixed ( 936.0, 0.0)	HOP130 (1/15)
10/02 11:19:00 - 10/02 11:31:54	Fixed ( 468.0, 0.0)	(2/15)
10/02 11:51:00 - 10/02 12:03:54	Fixed ( -468.0, 0.0)	(4/15)
10/02 12:07:00 - 10/02 12:19:54	Fixed ( -936.0, 0.0)	(5/15)
10/02 12:23:00 - 10/02 12:29:54	Fixed ( 815.0, 470.0)	(6/15)
10/02 12:33:00 - 10/02 12:45:54	Fixed ( 411.0, 847.0)	(7/15)
10/02 12:49:00 - 10/02 13:01:54	Fixed ( 0.0, 941.0)	(8/15)
10/02 13:05:00 - 10/02 13:17:54	Fixed ( -411.0, 847.0)	(9/15)
10/02 13:21:00 - 10/02 13:27:54	Fixed ( -815.0, 470.0)	(10/15)
10/02 13:31:00 - 10/02 13:37:54	Fixed ( 815.0, -470.0)	(11/15)
10/02 13:41:00 - 10/02 13:53:54	Fixed ( 411.0, -847.0)	(12/15)
10/02 13:57:00 - 10/02 14:09:54	Fixed ( 0.0, -941.0)	(13/15)
10/02 14:13:00 - 10/02 14:46:54	Fixed ( -411.0, -847.0)	(14/15)
10/02 14:50:00 - 10/02 14:59:54	Fixed ( -815.0, -471.0)	(15/15)
10/02 15:03:00 - 10/02 17:42:30	Track ( 267.7, 176.3) <sup>Ⓜ 10/02 15:00:00</sup>	AR 11580
10/02 18:19:00 - 10/03 09:41:30	Fixed ( 920.0, 120.0)	AR on the W-limb
10/03 10:13:00 - 10/03 13:09:54	Track ( 233.5, -319.6) <sup>Ⓜ 10/03 10:10:00</sup>	AR 11582
10/03 13:13:00 - 10/03 17:55:54	Track ( 78.1, -482.3) <sup>Ⓜ 10/03 13:10:00</sup>	EFR in south (AR11584)
10/03 18:09:00 - 10/04 09:59:54	Track ( 118.9, -481.6) <sup>Ⓜ 10/03 18:06:00</sup>	(cont.)

#### PROG= 16 15-time(s)

<b>Subr= 1 45-time(s) 10.0sec</b>												
<b>Seqn= 35 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>												
<b>Seqn= 36 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 37 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 38 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

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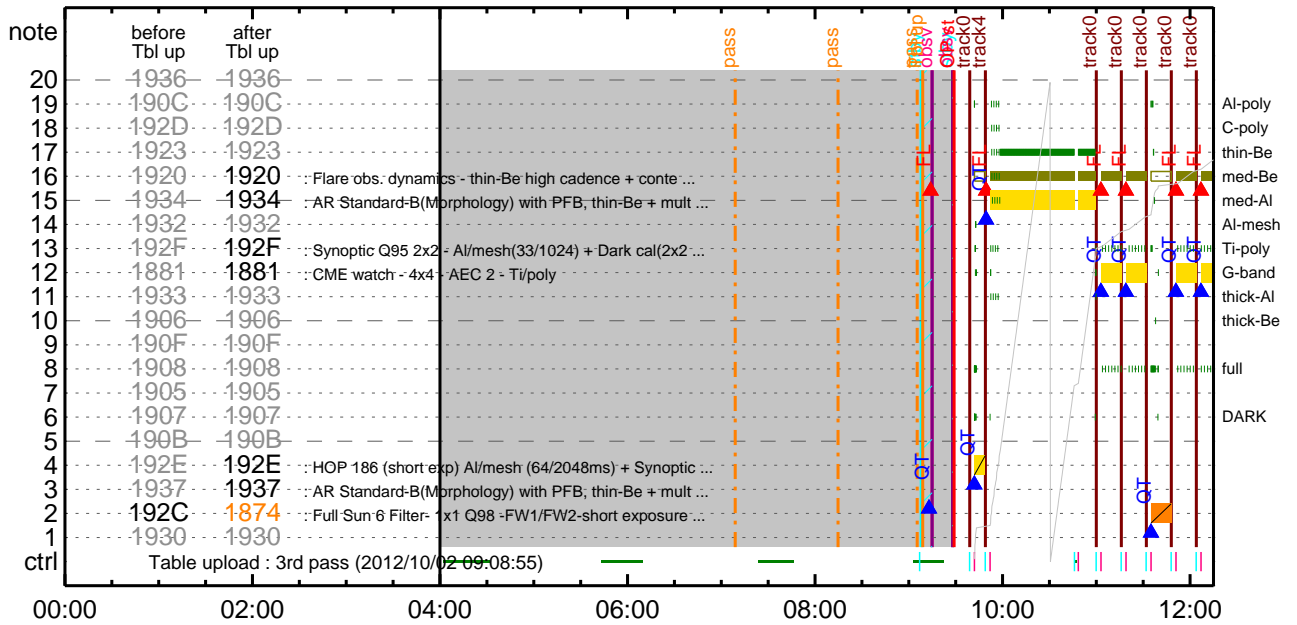
### Flare Detection

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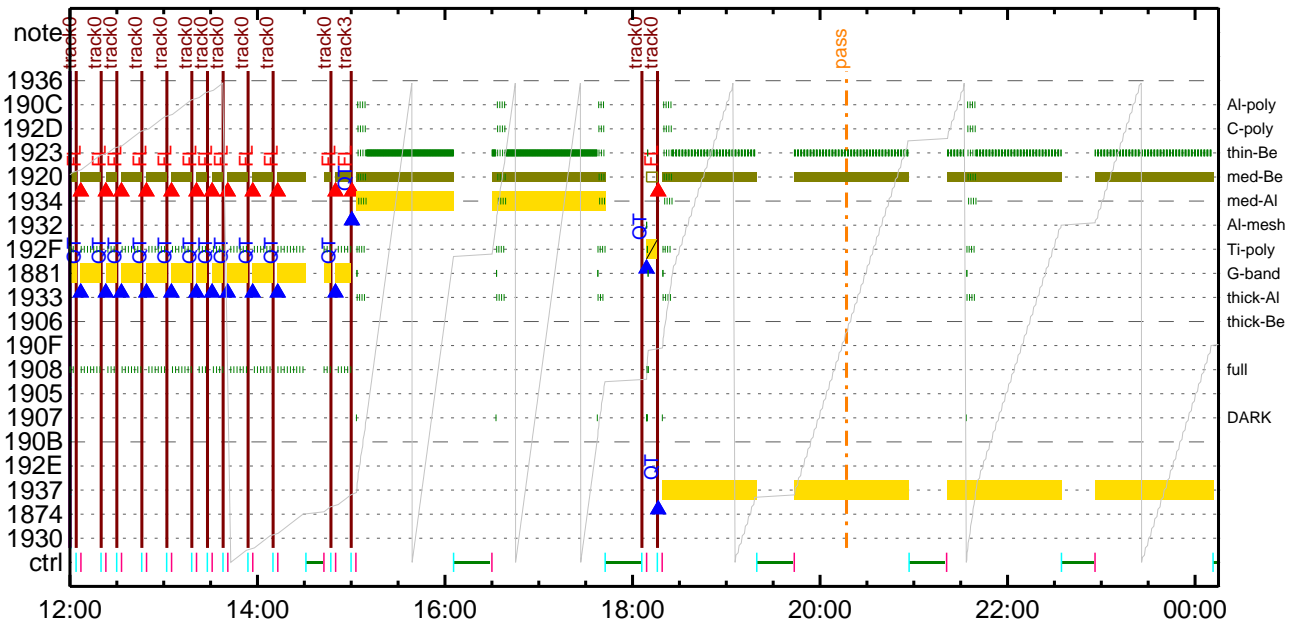
#### FLD Patrol

Term	Pointing (x, y)	Comment									
10/02 09:49:16 - 10/02 11:32:16	Track ( 16.3, -323.8) <sup>Ⓜ 10/02 09:49:00</sup>	AR 11582									
10/02 11:50:46 - 10/02 18:06:16	Fixed ( -468.0, 0.0)	(4/15)									
10/02 18:16:16 - 10/03 10:00:16	Fixed ( 920.0, 120.0)	AR on the W-limb									
10/03 10:10:16 - 10/03 17:56:16	Track ( 233.5, -319.6) <sup>Ⓜ 10/03 10:10:00</sup>	AR 11582									
10/03 18:06:16 - 10/04 10:00:16	Track ( 118.9, -481.6) <sup>Ⓜ 10/03 18:06:00</sup>	(cont.)									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

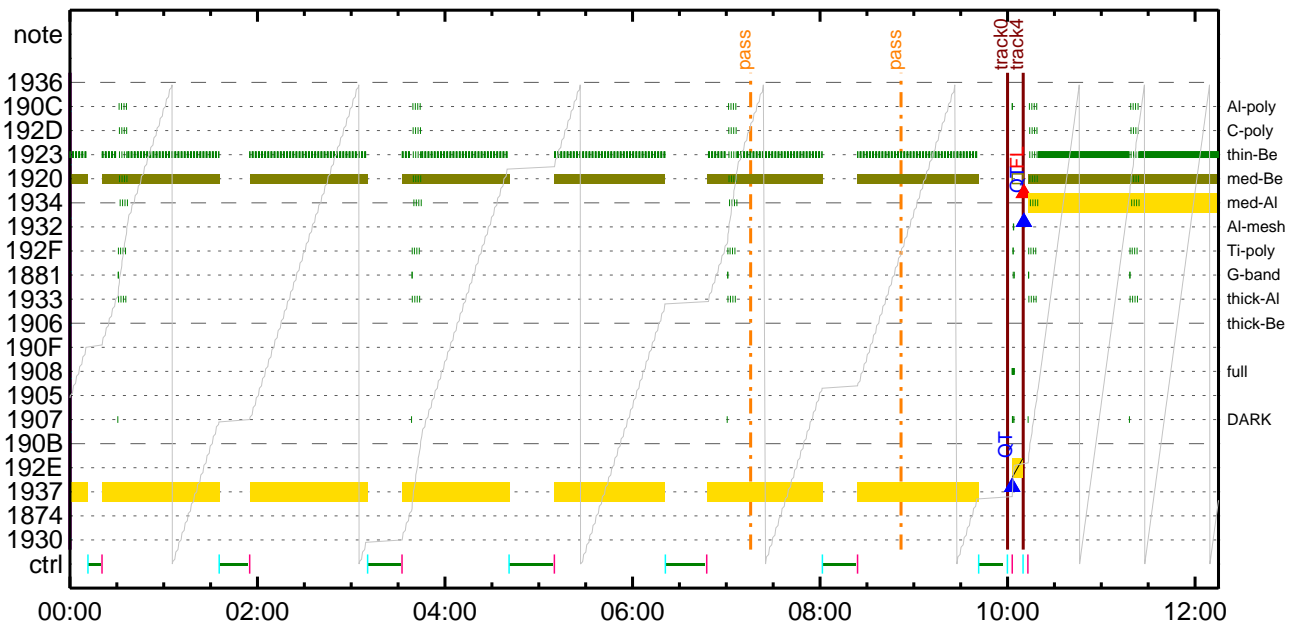
### CMDI #0950 2012/10/02



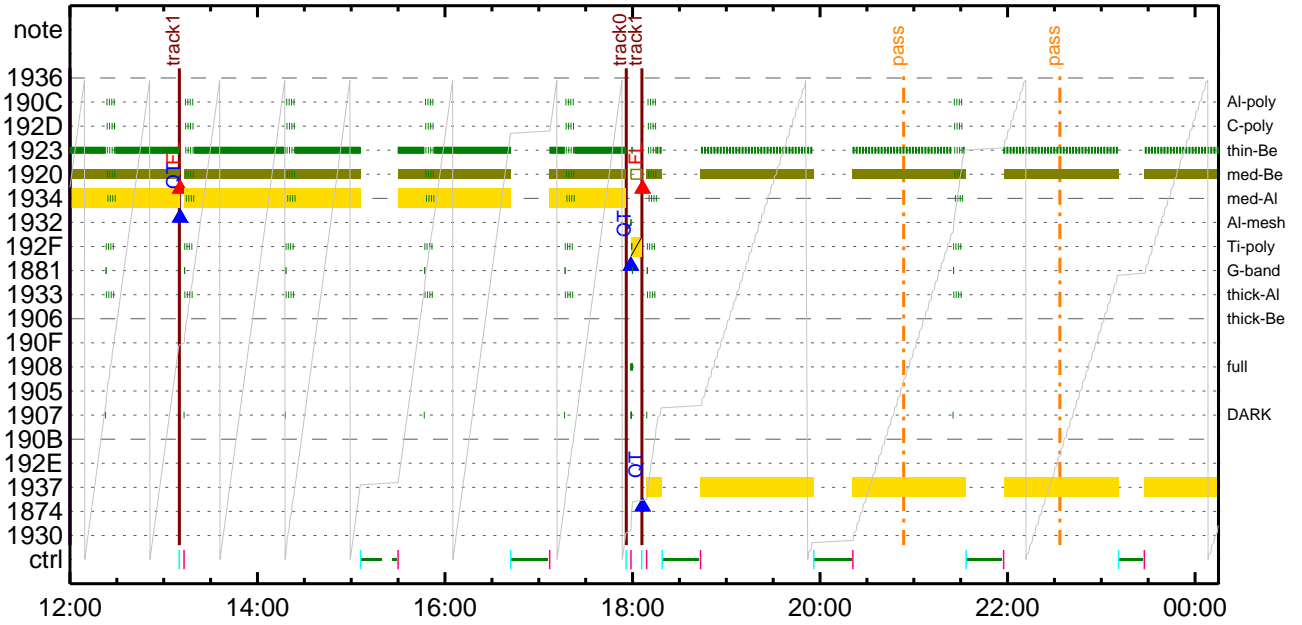
### CMDI #0950 2012/10/02



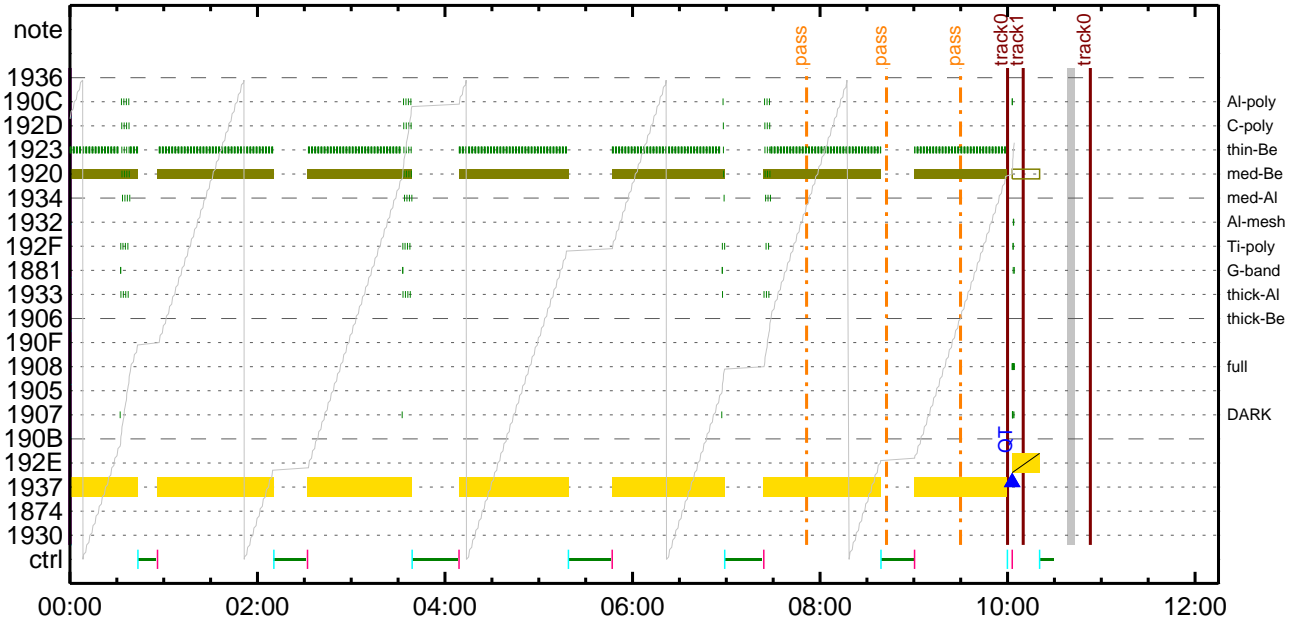
### CMDI #0950 2012/10/03



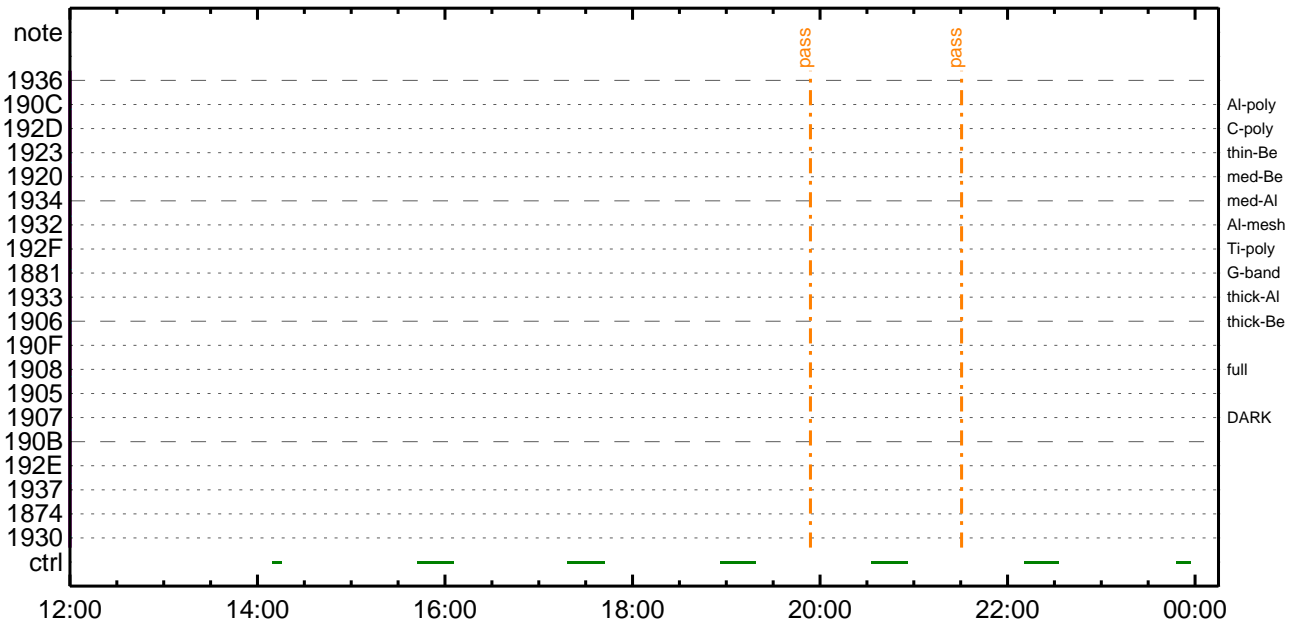
### CMDI #0950 2012/10/03



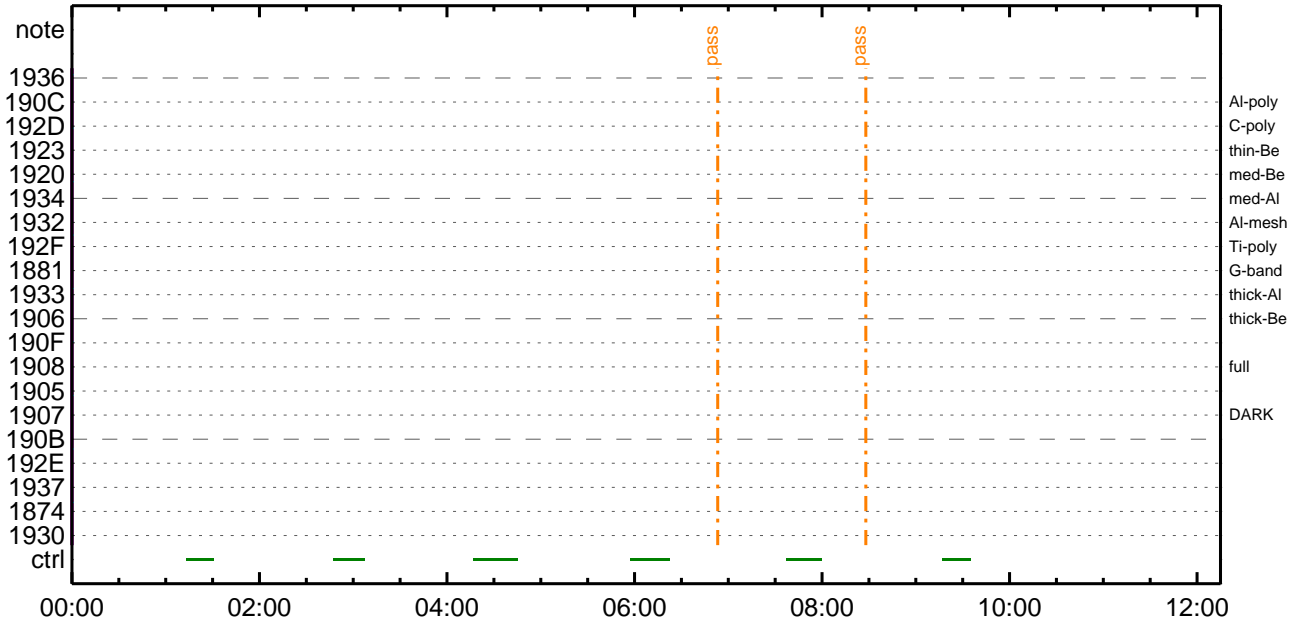
### CMDI #0950 2012/10/04



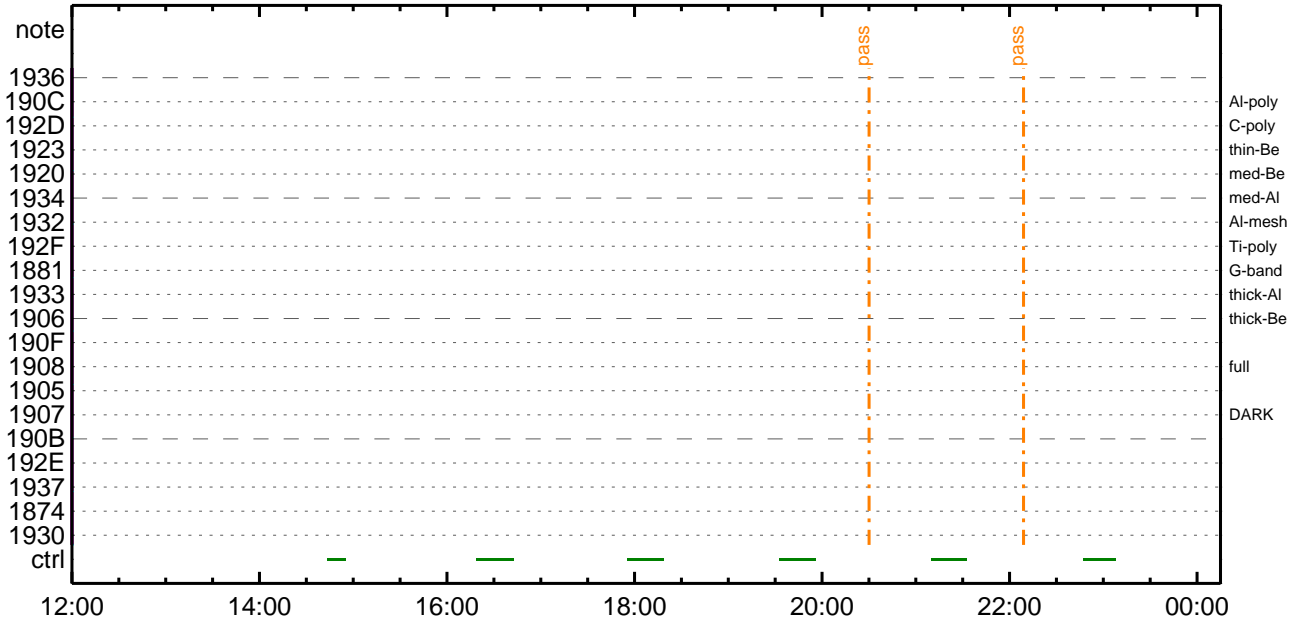
### CMDI #0950 2012/10/04



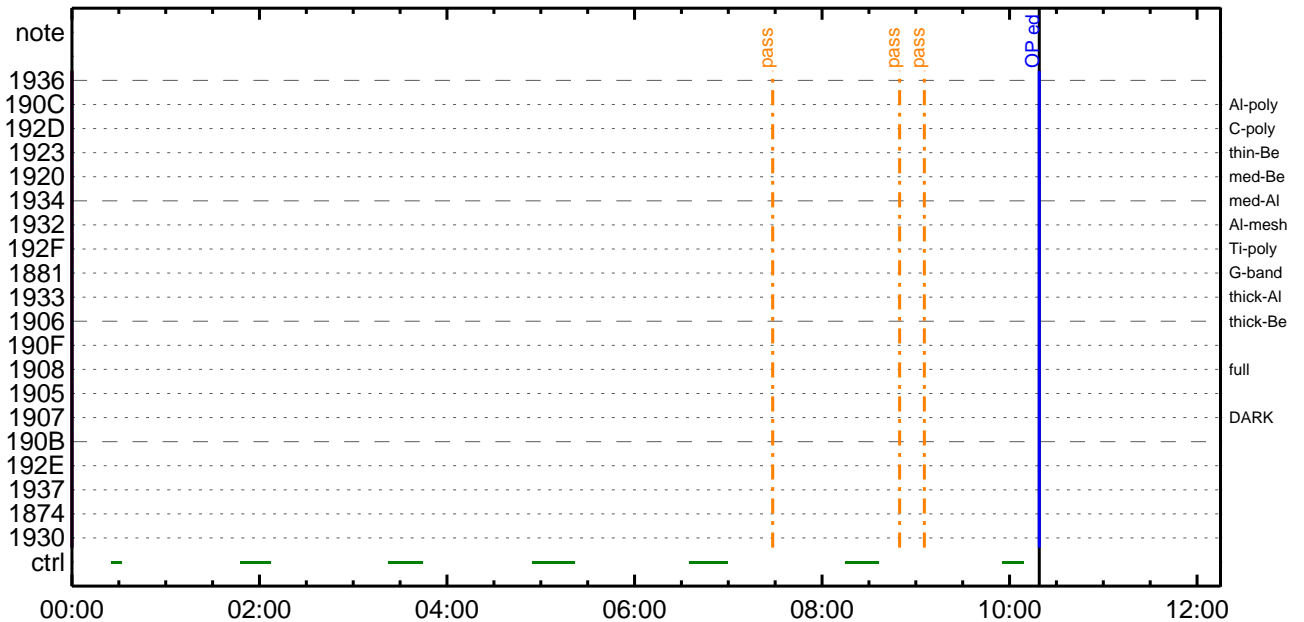
### CMDI #0950 2012/10/05



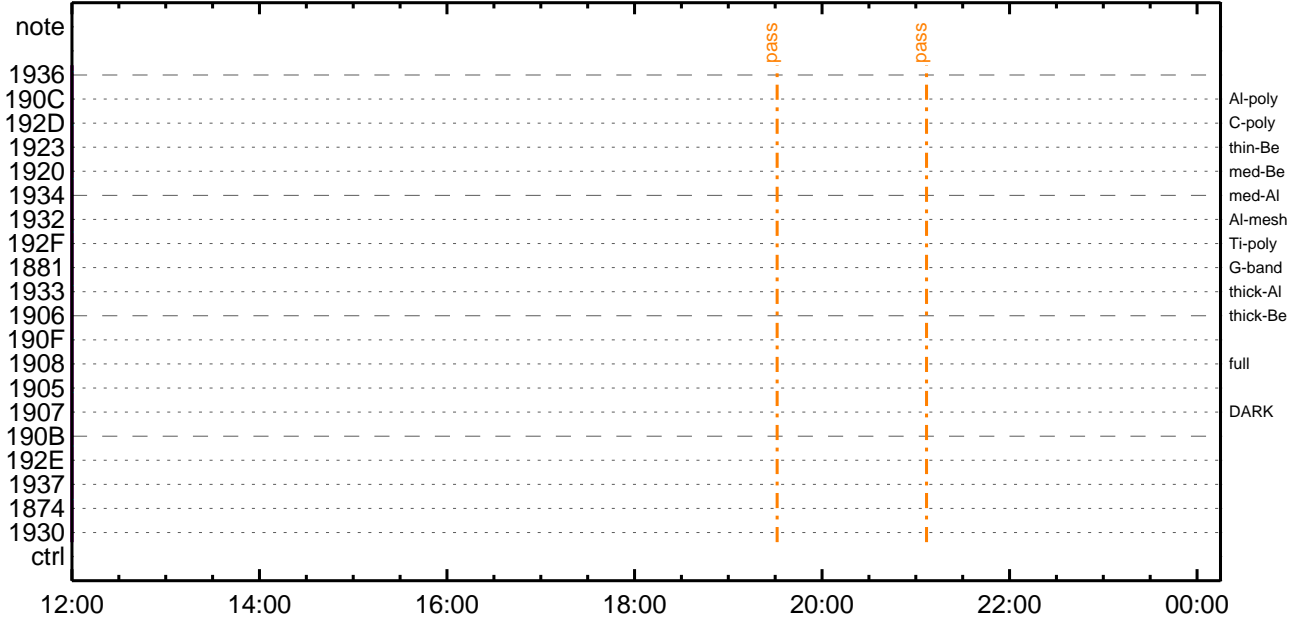
### CMDI #0950 2012/10/05



### CMDI #0950 2012/10/06



CMDI #0950 2012/10/06







```

0096 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0097 C.
0098 C. TI 2012-10-02 09:24:00.0
0099 +. TI 2012-10-02 09:24:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0102 C.
0103 +. TI 2012-10-02 09:24:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0106 C.
0107 +. TI 2012-10-02 09:24:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0110 C.
0111 +. TI 2012-10-02 09:28:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0114 C.
0115 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0116 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0117 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0118 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0119 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0120 C.
0121 C. *****
0122 C. TI 2012-10-02 09:28:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0129 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0130 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0131 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0132 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0136 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0137 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0138 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0139 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0140 C.
0141 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0142 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0143 C.
0144 C. RAM ID=TI_TBL 01-02 01-02 01-02 01-02
0145 C.
0146 C. DHU 01-02 01-02 01-02 01-02
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0150 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0151 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0152 C.                01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2012-10-02 09:28:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2012-10-02 09:28:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2012-10-02 09:28:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2012-10-02 09:28:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 01-02 01-02 01-02 01-02 *****
0192 C. (01-02 01-02 01-02 01-02)
0193 C. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```







```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_DIS
0111 BC (d9)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0113 BC (c9)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 03)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 10)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 + DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 + TI 2012-10-02 09:28:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0139 C. (%á°îÿÓÿÃÿËÿËÿËÿáÿçÿèñÊ¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ÿDÿ¹•Ï Daily±¿îññÊ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOSÿÁÿ§ÿÃÿ¬¼Ã»Û;ã
0150 C.
0151 . C. ***** LOS *****
0152 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2012/10/02	09:38:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/02	09:38:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/10/02	09:39:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2012/10/02	09:39:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/10/02	09:39:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/10/02	09:39:20.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/02	09:41:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2012/10/02	09:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/02	09:48:54.0	XRT_CTRL_MANU_448_OG [0x1c0]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/02	09:48:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/10/02	09:49:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2012/10/02	09:49:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/10/02	09:49:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/10/02	09:49:20.5	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/10/02	09:49:22.5	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/02	09:49:24.5	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/02	09:49:26.5	XRT_QT_PROG_SET_435_OG [0x1b3]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2012/10/02	09:49:28.5	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/10/02	09:52:00.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/02	10:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/02	10:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/02	10:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/02	10:47:30.0	XRT_Custom_434_OG [0x1b2]					
2012/10/02	10:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/02	10:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/02	10:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/02	11:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 ac cd		
2012/10/02	11:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/10/02	11:02:46.0	XRT_FLD_ENA_437_OG [0x1b5]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/10/02	11:02:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/10/02	11:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/10/02	11:02:52.0	XRT_ARS_DIS_412_OG [0x19c]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/02	11:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/02	11:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2012/10/02	11:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/10/02	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/02	11:15:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/02	11:16:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 00 d6 67		
2012/10/02	11:18:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/10/02	11:18:46.0	XRT_FLD_ENA_437_OG [0x1b5]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/10/02	11:18:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/10/02	11:18:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/10/02	11:18:52.0	XRT_ARS_DIS_412_OG [0x19c]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/02	11:18:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/02	11:18:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2012/10/02	11:18:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		

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2012/10/02	11:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/02	11:31:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/02	11:31:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/10/02	11:32:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00	00 00
2012/10/02	11:32:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/10/02	11:32:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/10/02	11:32:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/02	11:34:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2012/10/02	11:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/02	11:47:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/02	11:48:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00 00	29 99
2012/10/02	11:50:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/10/02	11:50:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/10/02	11:50:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/10/02	11:50:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/10/02	11:50:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/02	11:50:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/02	11:50:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/10/02	11:50:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/10/02	11:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/02	12:03:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/02	12:04:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 00 00 00	53 33
2012/10/02	12:06:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/10/02	12:06:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/10/02	12:06:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/10/02	12:06:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/10/02	12:06:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/02	12:06:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/02	12:06:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/10/02	12:06:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/10/02	12:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/02	12:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/02	12:20:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 d6 36 b7	8e
2012/10/02	12:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/10/02	12:22:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/10/02	12:22:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/10/02	12:22:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/10/02	12:22:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/02	12:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/02	12:22:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/10/02	12:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/10/02	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/02	12:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/02	12:30:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 b4 b5 db	75
2012/10/02	12:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/10/02	12:32:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/10/02	12:32:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]					



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2012/10/02	12:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/02	12:32:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/02	12:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/02	12:32:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/10/02	12:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/10/02	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/02	12:45:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/02	12:46:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	ac 5b 00 00		
2012/10/02	12:48:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2012/10/02	12:48:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/10/02	12:48:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/10/02	12:48:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/02	12:48:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/02	12:48:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/02	12:48:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/10/02	12:48:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/10/02	12:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/02	13:01:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/02	13:02:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	b4 b5 24 8b		
2012/10/02	13:04:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2012/10/02	13:04:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/10/02	13:04:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/10/02	13:04:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/02	13:04:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/02	13:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/02	13:04:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/10/02	13:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/10/02	13:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/02	13:17:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/02	13:18:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	d6 36 48 72		
2012/10/02	13:20:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2012/10/02	13:20:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/10/02	13:20:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/10/02	13:20:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/02	13:20:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/02	13:20:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/10/02	13:20:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/10/02	13:20:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/10/02	13:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/02	13:27:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/02	13:28:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	29 ca b7 8e		
2012/10/02	13:30:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2012/10/02	13:30:46.0	XRT_FLD_ENA_437_OG [0x1b5]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/10/02	13:30:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/10/02	13:30:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/02	13:30:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2012/10/02	13:30:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/02	13:30:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/10/02	13:30:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/02	13:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/02	13:37:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/02	13:38:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 4b 4b db 75				
2012/10/02	13:40:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/02	13:40:46.0	XRT_FLD_ENA_437_OG [0x1b5]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/02	13:40:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/02	13:40:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/02	13:40:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/02	13:40:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/02	13:40:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/10/02	13:40:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/02	13:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/02	13:53:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/02	13:54:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2012/10/02	13:56:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/02	13:56:46.0	XRT_FLD_ENA_437_OG [0x1b5]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/02	13:56:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/02	13:56:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/02	13:56:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/02	13:56:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/02	13:56:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/10/02	13:56:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/02	13:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/02	14:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/02	14:10:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2012/10/02	14:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/02	14:12:46.0	XRT_FLD_ENA_437_OG [0x1b5]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/02	14:12:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/02	14:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/02	14:12:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/02	14:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/02	14:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/10/02	14:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/02	14:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/02	14:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/02	14:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/02	14:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/02	14:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/02	14:41:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/02	14:42:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/02	14:46:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/02	14:47:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2012/10/02	14:49:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/02	14:49:46.0	XRT_FLD_ENA_437_OG [0x1b5]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				

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2012/10/02	14:49:48.0	XRT_FLRCTRL_ENA_443_OG [0x1bb] MDP_XRT_FLRCTRL_ENA	1 07-F0 c8
2012/10/02	14:49:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1 07-F0 d0
2012/10/02	14:49:52.0	XRT_ARS_DIS_412_OG [0x19c] MDP_XRT_ARS_DIS	1 07-F0 d5
2012/10/02	14:49:54.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1 07-F0 da
2012/10/02	14:49:56.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2 07-F0 c4 0c
2012/10/02	14:49:58.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2 07-F0 c5 10
2012/10/02	14:50:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2012/10/02	14:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0] MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/10/02	14:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2012/10/02	15:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7] AOCU_NM	5 02-76 03 00 00 00 00
2012/10/02	15:00:16.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1 07-F0 d8
2012/10/02	15:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1 07-F0 c8
2012/10/02	15:00:20.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1 07-F0 d0
2012/10/02	15:00:22.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1 07-F0 d5
2012/10/02	15:00:24.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1 07-F0 da
2012/10/02	15:00:26.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2 07-F0 c4 0f
2012/10/02	15:00:28.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2 07-F0 c5 10
2012/10/02	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2012/10/02	16:05:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/10/02	16:05:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1 07-F0 da
2012/10/02	16:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2012/10/02	16:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2012/10/02	16:29:00.0	XRT_Custom_434_OG [0x1b2]	
2012/10/02	16:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2012/10/02	17:42:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/10/02	17:42:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1 07-F0 da
2012/10/02	17:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2012/10/02	17:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2012/10/02	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/10/02	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2012/10/02	18:06:00.0	AOCS_Ore-point_Start_1_OG [0x097] AOCU_NM	5 02-76 00 00 00 00 00
2012/10/02	18:06:16.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1 07-F0 d9
2012/10/02	18:06:18.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2012/10/02	18:06:20.0	XRT_ARS_DIS_406_OG [0x196] MDP_XRT_ARS_DIS	1 07-F0 d5
2012/10/02	18:08:58.0	XRT_QT_PROG_SET_419_OG [0x1a3] MDP_XRT_QT_PROG_SET	2 07-F0 c4 0d
2012/10/02	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2012/10/02	18:15:54.0	XRT_CTRL_MANU_448_OG [0x1c0] MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/10/02	18:15:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2012/10/02	18:16:00.0	AOCS_Ore-point_Start_18_OG [0x0a8] AOCU_NM	5 02-76 00 f5 59 ae 36
2012/10/02	18:16:16.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1 07-F0 d8
2012/10/02	18:16:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1 07-F0 c8
2012/10/02	18:16:20.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1 07-F0 d0
2012/10/02	18:16:22.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1 07-F0 d5
2012/10/02	18:16:24.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1 07-F0 da
2012/10/02	18:16:26.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2 07-F0 c4 03
2012/10/02	18:16:28.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2 07-F0 c5 10
2012/10/02	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1 07-F0 c0

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2012/10/02	19:19:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	19:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	19:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	19:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	19:42:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	19:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	20:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	20:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	20:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	21:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	21:20:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	21:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/02	22:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/02	22:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/02	22:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/02	22:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/02	22:55:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/02	22:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	00:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	00:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	00:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	00:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	00:19:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	00:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	01:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	01:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	01:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	01:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	01:54:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	01:55:00.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	03:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	03:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	03:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	03:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	03:31:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	03:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	04:41:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	04:41:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	04:41:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	04:44:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	05:09:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	05:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	06:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	06:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	06:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	06:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	06:46:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	06:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	08:01:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	08:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	08:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			

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2012/10/03	08:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/10/03	08:23:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/03	08:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/03	09:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/03	09:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/03	09:41:34.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/10/03	09:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/03	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/03	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/03	10:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/03	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/03	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/03	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/03	10:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/10/03	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/03	10:09:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/03	10:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/03	10:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00				
2012/10/03	10:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/03	10:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/03	10:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/03	10:10:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/03	10:10:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/03	10:10:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/10/03	10:10:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/03	10:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/03	13:09:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/03	13:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/03	13:10:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	01 00 00 00 00				
2012/10/03	13:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/03	13:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/03	13:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/03	13:10:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/03	13:10:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/03	13:10:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/10/03	13:10:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/03	13:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/03	15:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/03	15:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/03	15:06:04.0	XRT_PREFLR_STOP_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/10/03	15:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/03	15:29:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/10/03	15:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/03	16:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/03	16:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/03	16:42:04.0	XRT_PREFLR_STOP_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/10/03	16:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2012/10/03	17:06:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	17:07:00.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	17:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/10/03	17:56:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/10/03	17:56:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/10/03	17:56:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/10/03	17:56:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/03	17:58:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/10/03	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	18:05:54.0	XRT_CTRL_MANU_448_OG [0x1c0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	18:05:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/10/03	18:06:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]			
		AOCU_NM	5	02-76	01 00 00 00 00
2012/10/03	18:06:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/10/03	18:06:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/10/03	18:06:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/10/03	18:06:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/03	18:06:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	18:06:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/10/03	18:06:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/10/03	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	18:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	18:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	18:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	18:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	18:42:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	18:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	19:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	19:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	19:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	19:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	20:20:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	20:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	21:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	21:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	21:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	21:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	21:56:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	21:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/03	23:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/03	23:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/03	23:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/03	23:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/03	23:26:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/03	23:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/04	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/04	00:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/04	00:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/04	00:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/10/04	00:55:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/04	00:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	02:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	02:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/04	02:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/04	02:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/04	02:31:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/04	02:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	03:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/04	03:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/04	03:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/04	04:08:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/04	04:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	05:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	05:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/04	05:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/04	05:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/04	05:46:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/04	05:47:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	06:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	06:59:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/04	06:59:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/04	07:02:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/04	07:23:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/04	07:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	08:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	08:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/04	08:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/04	08:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/04	08:59:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/04	09:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/04	10:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/04	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/04	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/04	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/04	10:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/10/04	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/04	10:10:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/10/04	10:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/04	10:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/04	10:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/04	10:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/04	10:53:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				