

XRT Timeline to be uploaded on 2012/10/04

Period: 2012/10/04 10:43:00 - 2012/10/09 10:32:00

* * * * *

Normal mode

* * * * *

| XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms | | | | | | | | | | | | | |
|---|-----------------------|----------------|-------|------|-------|-------|----------------------------|-----|----------------------|-------|------------|----------|--------|
| Term | Pointing (x, y) | | | | | | Comment | | | | | | |
| 10/04 10:56:00 - 10/04 17:59:54 | Fixed (-14.0, 881.0) | | | | | | # OP start + 10min, HOP206 | | | | | | |
| PROG= 09 Inf.-time(s) | | | | | | | | | | | | | |
| ┌ Subr= 1 1-time(s) 2.0sec | | | | | | | | | | | | | |
| └ Seqn= 61 2-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | close | Safe | Norm | 63ms | Obs | 1x1 | 512x512 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| ┌ Subr= 2 1-time(s) 2.0sec | | | | | | | | | | | | | |
| └ Seqn= 62 1-time(s) 30.0sec | | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | open | Safe | Norm | 44ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=90 | 0 | 0 | 2.0sec |
| ┌ Subr= 3 30-time(s) 2.0sec | | | | | | | | | | | | | |
| └ Seqn= 60 2-time(s) 30.0sec | | | | | | | | | | | | | |
| | Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 4.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=90 | 0 | 0 | 2.0sec |
| | Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 4.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=90 | 0 | 0 | 2.0sec |
| | Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

| XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18 | | | | | | | | | | | | |
|---|-------------------|--|--|--|--|--|----------------------------|--|--|--|--|--|
| Term | Pointing (x, y) | | | | | | Comment | | | | | |
| 10/04 18:03:00 - 10/04 18:09:54 | Fixed (0.0, 0.0) | | | | | | synoptic | | | | | |
| 10/05 18:22:00 - 10/05 18:28:54 | Fixed (0.0, 0.0) | | | | | | synoptic, shifted 19.0 min | | | | | |

| | | | | | | | | | | | | | |
|-----------------------------|----------------|----------------|-------|------|-------|-------|-----|-----|------------------------|-------|------------|----------|--------|
| PROG= 13 1-time(s) | | | | | | | | | | | | | |
| ┌ Subr= 1 1-time(s) 14.0sec | | | | | | | | | | | | | |
| └ Seqn= 64 1-time(s) 4.0sec | | | | | | | | | | | | | |
| | Open/Al-mesh | Open/Ti-poly | close | Safe | Norm | 32ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| | Open/Al-mesh | Open/Ti-poly | close | Safe | Norm | 1.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| └ Seqn= 6 1-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| └ Seqn= 70 1-time(s) 4.0sec | | | | | | | | | | | | | |
| | Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 63ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| | Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| └ Seqn= 67 1-time(s) 2.0sec | | | | | | | | | | | | | |
| | thin-Be/Open | thin-Be/Open | close | Safe | Norm | 177ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| | thin-Be/Open | thin-Be/Open | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| └ Seqn= 69 1-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | open | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| ┌ Subr= 2 1-time(s) 2.0sec | | | | | | | | | | | | | |
| └ Seqn= 68 1-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | close | Safe | Norm | 32ms | Obs | 1x1 | 2048x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| | Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

| Term | Pointing (x, y) | | | | | | Comment | | | | | | |
|---------------------------------|---|------------------|-------|------|-------|-------|--------------|-----|----------------------|-------|------------|----------|---------|
| 10/04 18:13:00 - 10/04 21:09:54 | Track (664.1, 205.7) @ 10/04 18:10:00 | | | | | | Obs. AR11580 | | | | | | |
| 10/04 21:13:00 - 10/05 09:59:54 | Track (520.4, -303.8) @ 10/04 21:10:00 | | | | | | Obs. AR11582 | | | | | | |
| 10/05 13:13:00 - 10/05 17:55:30 | Track (633.7, -293.2) @ 10/05 13:10:00 | | | | | | Obs. AR11582 | | | | | | |
| PROG= 03 Inf.-time(s) | | | | | | | | | | | | | |
| ┌ Subr= 1 1-time(s) 2.0sec | | | | | | | | | | | | | |
| └ Seqn= 18 1-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| | Open/G-band | Open/G-band | open | Safe | Norm | 44ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| └ Seqn= 19 1-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | close | Safe | Norm | 63ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| └ Seqn= 65 4-time(s) 2.0sec | | | | | | | | | | | | | |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| | Open/thick-Al | Open/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| | Al-poly/Open | Al-poly/thick-Be | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| | C-poly/Open | C-poly/thick-Al | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| | med-Be/Open | med-Be/Open | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| | med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| ┌ Subr= 2 1-time(s) 2.0sec | | | | | | | | | | | | | |
| └ Seqn= 73 70-time(s) 120.0sec | | | | | | | | | | | | | |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 12.5sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 12.5sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 12.5sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 3 | 12.5sec |
| | Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

XOB #192E: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1

| Term | Pointing (x, y) | Comment |
|---------------------------------|-------------------|----------------------------------|
| 10/05 10:03:00 - 10/05 10:09:54 | Fixed (0.0, 0.0) | synoptic, shifted @ 10UT, HOP186 |
| 10/06 09:43:00 - 10/06 09:55:30 | Fixed (0.0, 0.0) | synoptic, shifted @ 9:40, HOP186 |

PROG= 04 1-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 63 1-time(s) 4.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Seqn= 6 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 70 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 69 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 1-time(s) 6.0sec
 - Seqn= 66 1-time(s) 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
- Subr= 3 1-time(s) 2.0sec
 - Seqn= 68 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1938: BP dynamics - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms-120s

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|------------------|
| 10/05 10:13:00 - 10/05 13:09:54 | Track (307.4, -398.3) @ 10/05 10:10:00 | Obs plage region |
| 10/06 07:04:08 - 10/06 09:39:54 | Track (0.0, 0.0) @ 10/06 06:40:00 | DC Obs. |

PROG= 18 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 61 2-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 62 1-time(s) 30.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
- Subr= 3 30-time(s) 2.0sec
 - Seqn= 83 2-time(s) 120.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 4 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 4 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1934: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 50s-cad w/ G-Band VLS Closed Test

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|--------------|
| 10/05 18:32:00 - 10/05 21:10:00 | Track (792.7, 225.4) @ 10/05 18:29:00 | Obs. AR11580 |
| 10/05 21:34:01 - 10/06 06:35:00 | Track (686.8, -287.0) @ 10/05 21:29:00 | Obs. AR11582 |

PROG= 15 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 18 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 19 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Seqn= 65 4-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 73 70-time(s) 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|----------------------------|
| 10/04 10:56:00 - 10/04 17:59:54 | Fixed (-14.0, 881.0) | # OP start + 10min, HOP206 |
| 10/04 18:13:00 - 10/04 21:09:54 | Track (664.1, 205.7) @ 10/04 18:10:00 | Obs. AR11580 |
| 10/04 21:13:00 - 10/05 09:59:54 | Track (520.4, -303.8) @ 10/04 21:10:00 | Obs. AR11582 |
| 10/05 10:13:00 - 10/05 13:09:54 | Track (307.4, -398.3) @ 10/05 10:10:00 | Obs plage region |
| 10/05 13:13:00 - 10/05 17:55:30 | Track (633.7, -293.2) @ 10/05 13:10:00 | Obs. AR11582 |
| 10/05 18:32:00 - 10/05 21:10:00 | Track (792.7, 225.4) @ 10/05 18:29:00 | Obs. AR11580 |
| 10/05 21:34:01 - 10/06 06:35:00 | Track (686.8, -287.0) @ 10/05 21:29:00 | Obs. AR11582 |
| 10/06 07:04:08 - 10/06 09:39:54 | Track (0.0, 0.0) @ 10/06 06:40:00 | DC Obs. |

PROG= 16 15-time(s)

| | | | | | | | | | | | | | | | | | | | |
|-----------------------------------|------------------|-------|------|-------|-------|-----|-----|----------------------|-------|------------|----------|--------|--|--|--|--|--|--|--|
| Subr= 1 45-time(s) 10.0sec | | | | | | | | | | | | | | | | | | | |
| Seqn= 35 1-time(s) 2.0sec | | | | | | | | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec | | | | | | | |
| Subr= 2 1-time(s) 10.0sec | | | | | | | | | | | | | | | | | | | |
| Seqn= 36 1-time(s) 2.0sec | | | | | | | | | | | | | | | | | | | |
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec | | | | | | | |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 2.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec | | | | | | | |
| Seqn= 37 1-time(s) 2.0sec | | | | | | | | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec | | | | | | | |
| Seqn= 38 1-time(s) 2.0sec | | | | | | | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 44ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec | | | | | | | |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec | | | | | | | |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 2x2 | 512x512 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec | | | | | | | |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | | | | | | | | |

* * * * *

Active Region Search

* * * * *

NOT USED

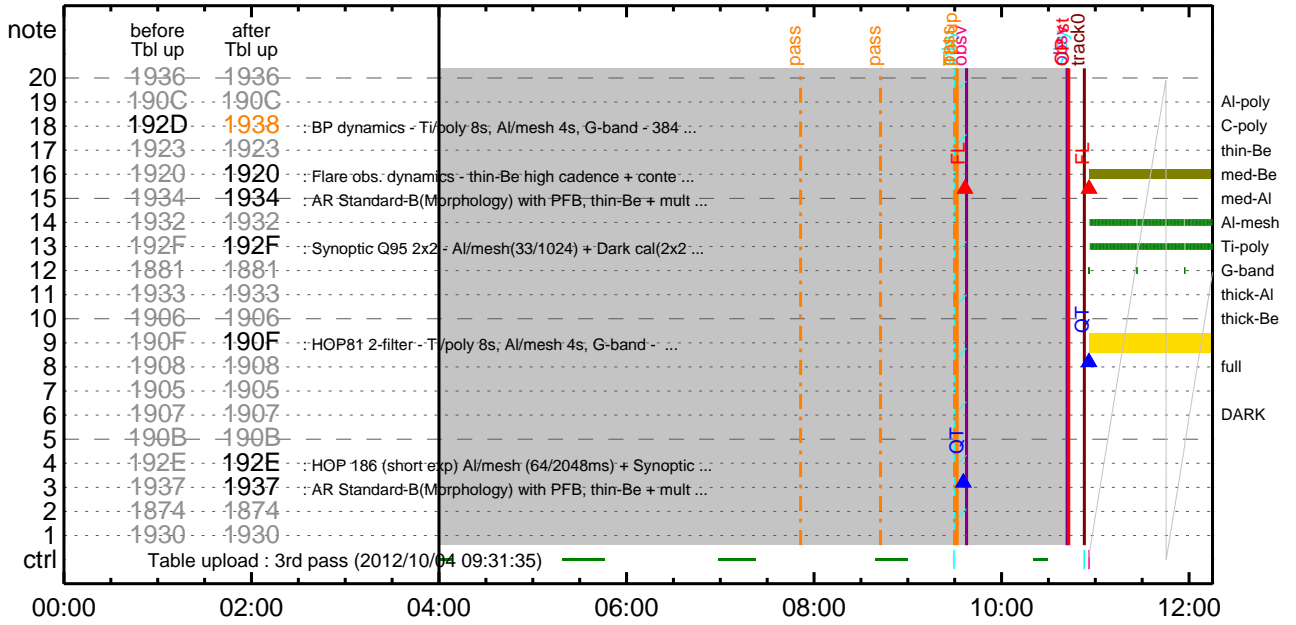
* * * * *

Flare Detection

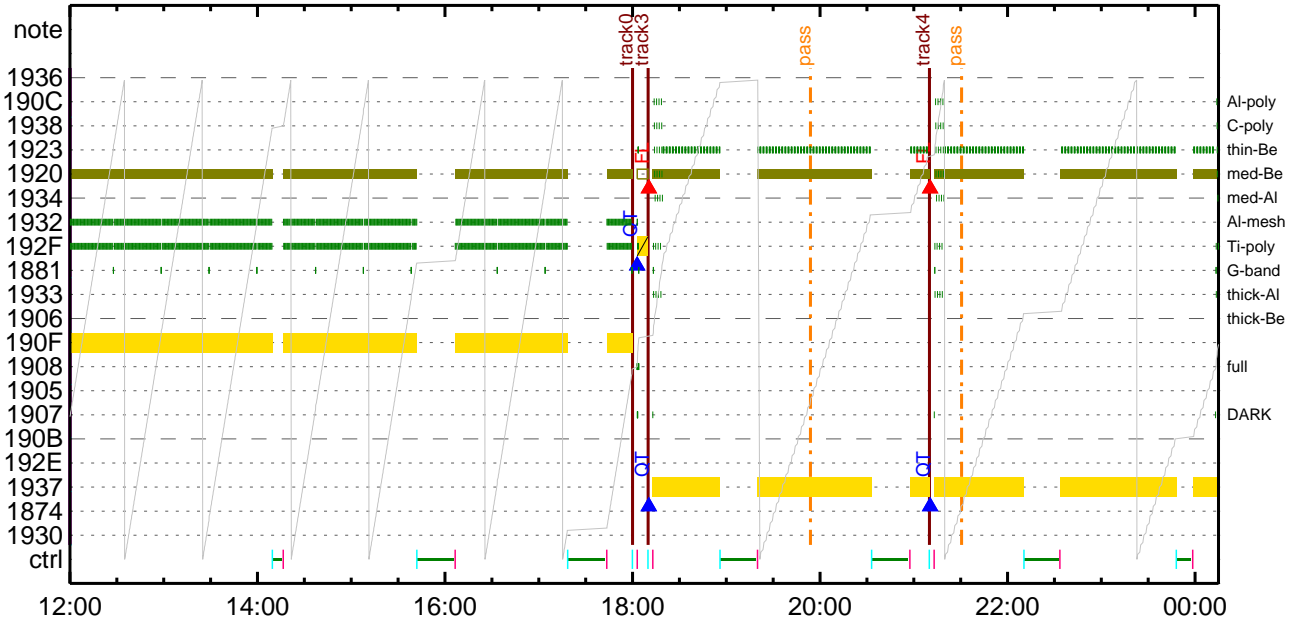
* * * * *

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|--|
| 10/04 10:55:46 - 10/04 18:00:16 | Fixed (-14.0, 881.0) | # OP start + 10min, HOP206 |
| 10/04 18:10:16 - 10/05 10:00:16 | Track (664.1, 205.7) @ 10/04 18:10:00 | Obs. AR11580 |
| 10/05 10:12:46 - 10/05 18:19:16 | Track (307.4, -398.3) @ 10/05 10:10:00 | Obs plage region |
| 10/05 18:29:16 - 10/06 09:40:16 | Track (792.7, 225.4) @ 10/05 18:29:00 | Obs. AR11580 |
| Open/Ti-poly | Open/thick-Al | close Safe Norm 8ms Obs 8x8 Q=50 30sec |
| Default Filter | Thicker Filter | VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval |

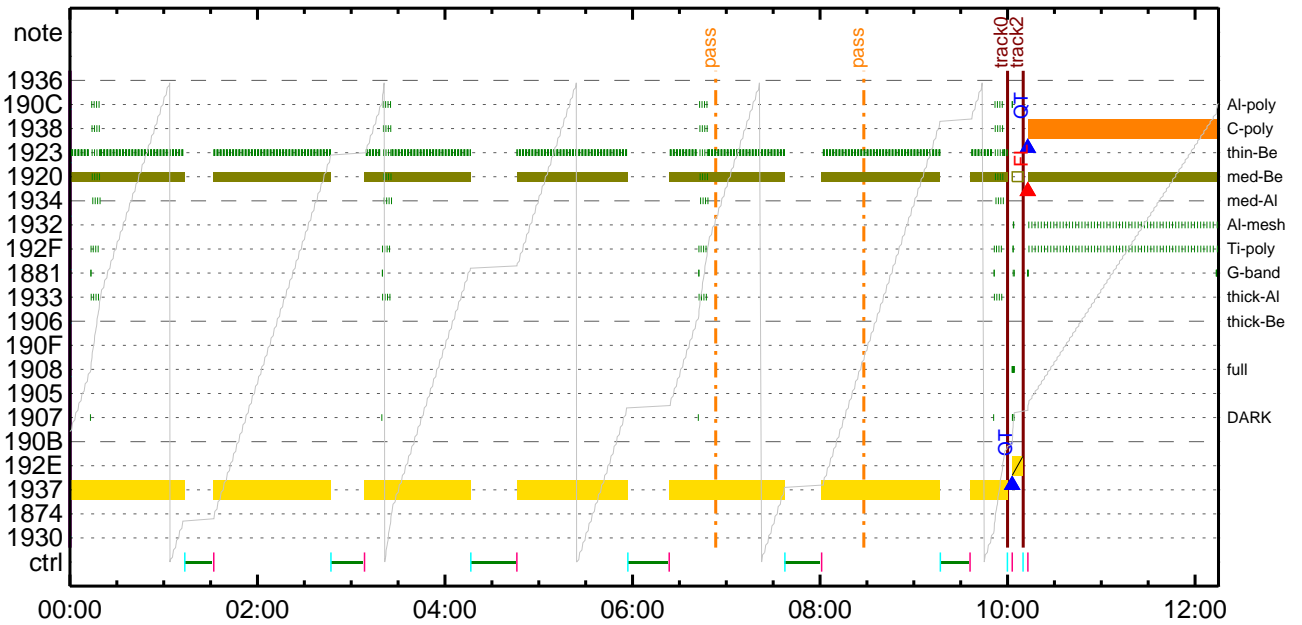
CMDI #0954 2012/10/04



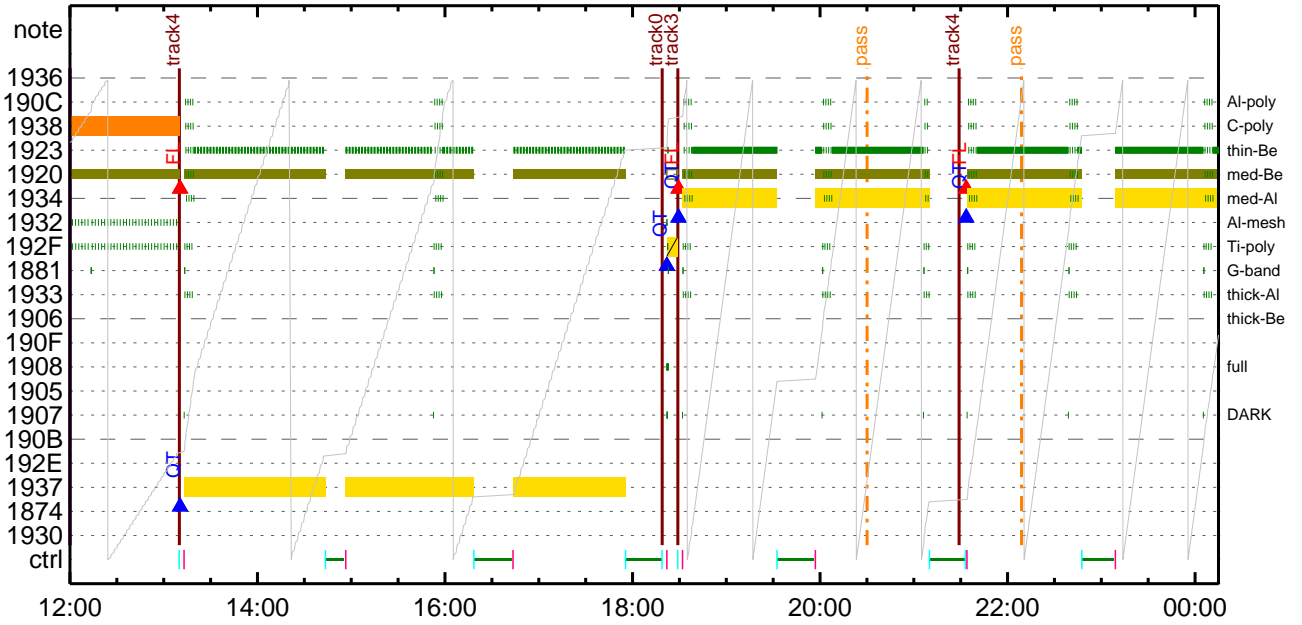
CMDI #0954 2012/10/04



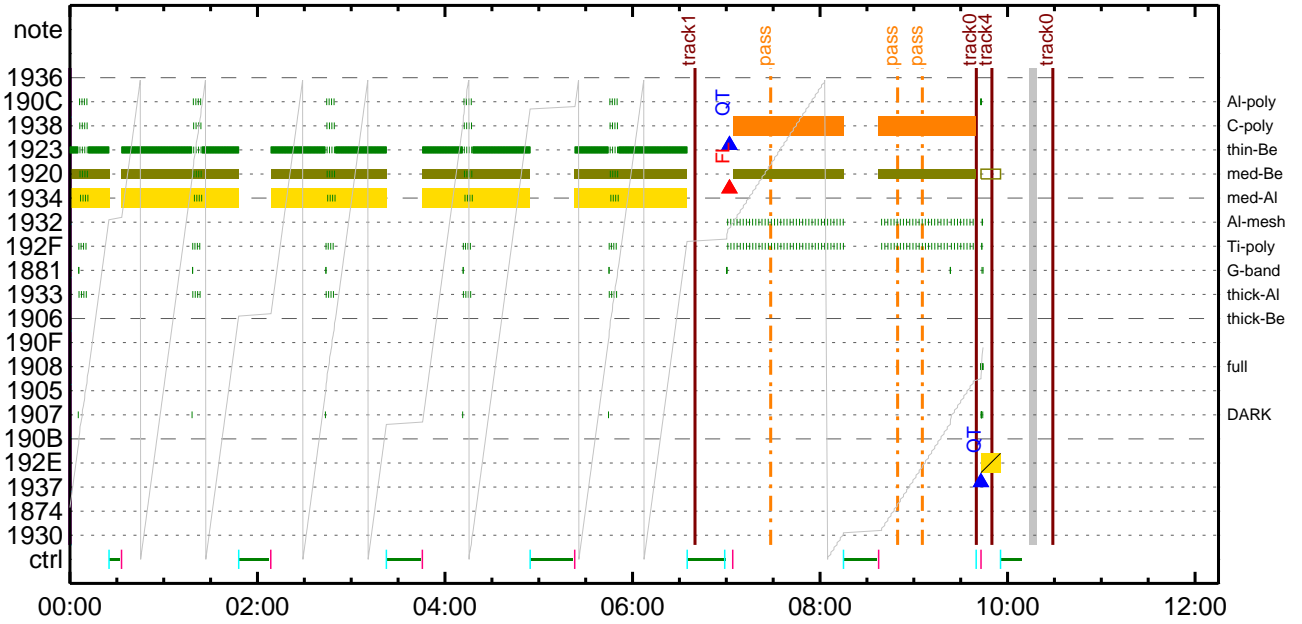
CMDI #0954 2012/10/05



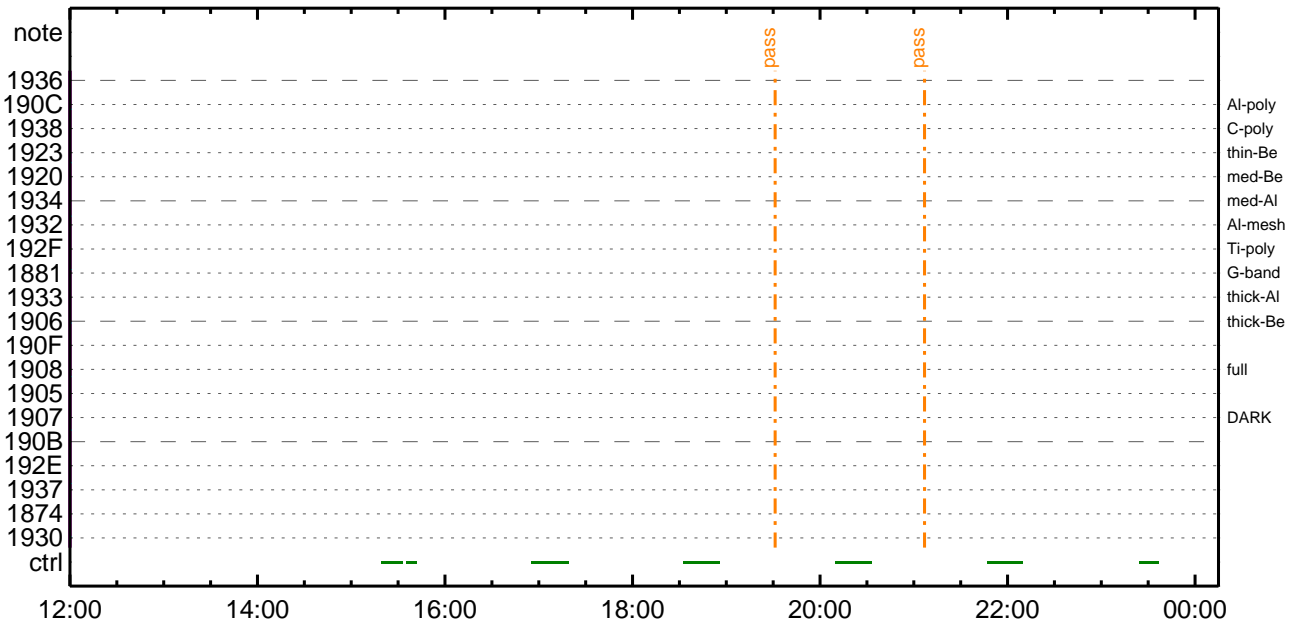
CMDI #0954 2012/10/05



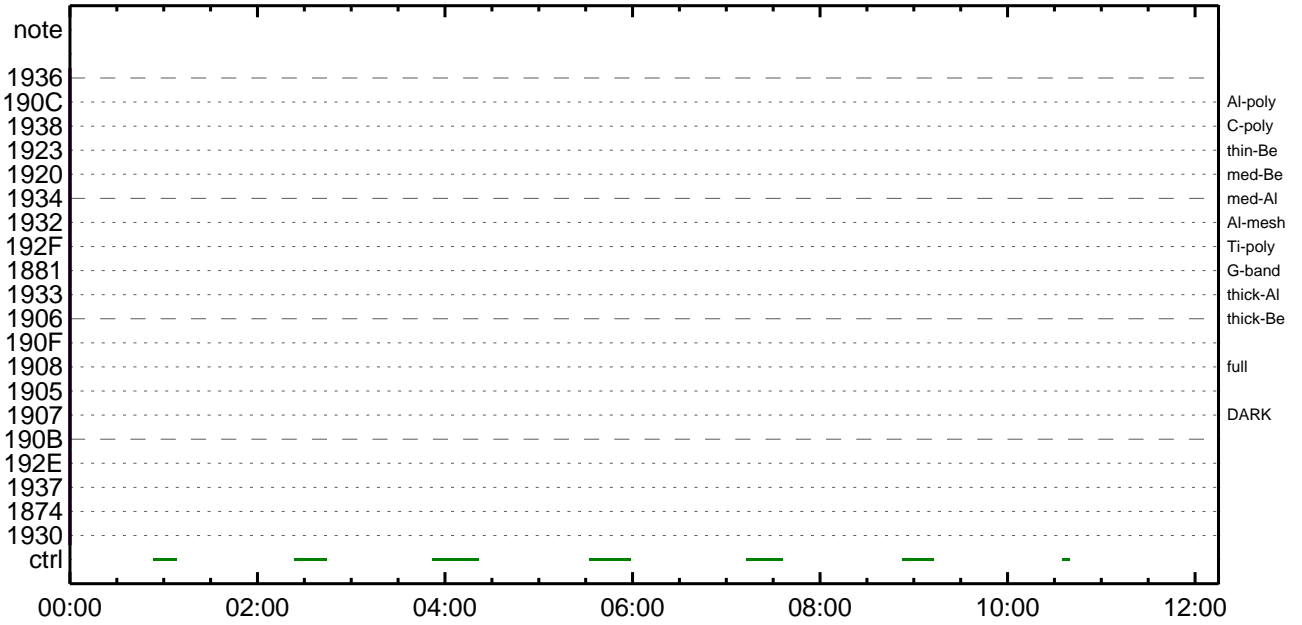
CMDI #0954 2012/10/06



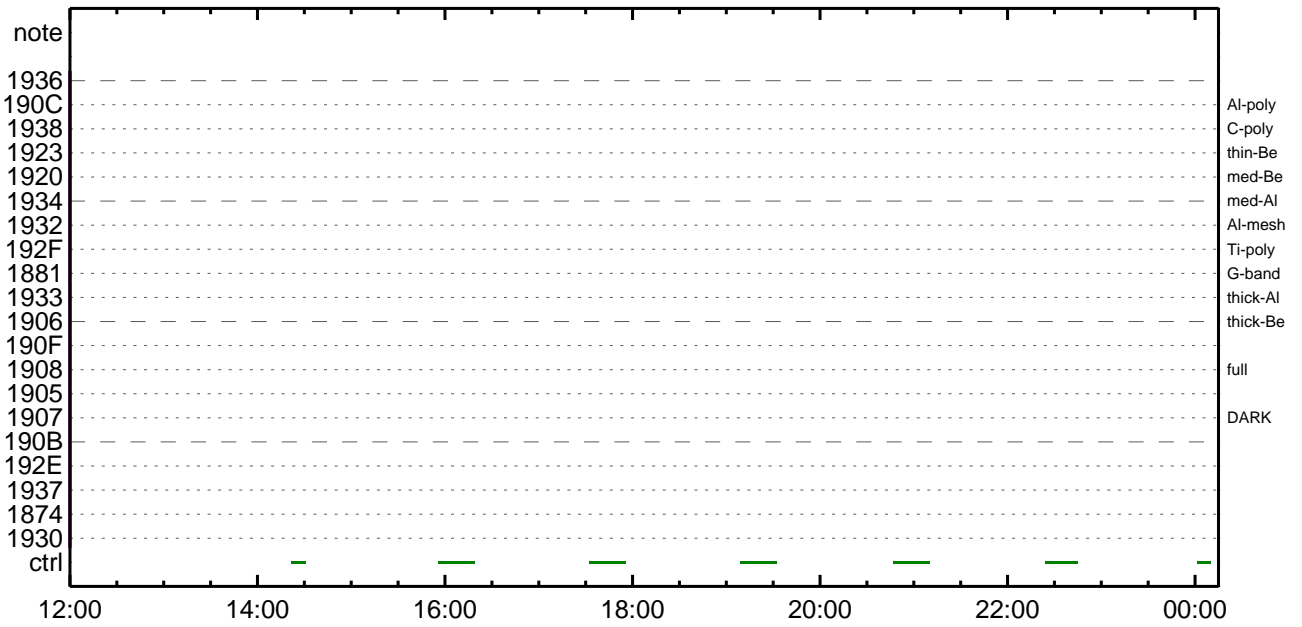
CMDI #0954 2012/10/06



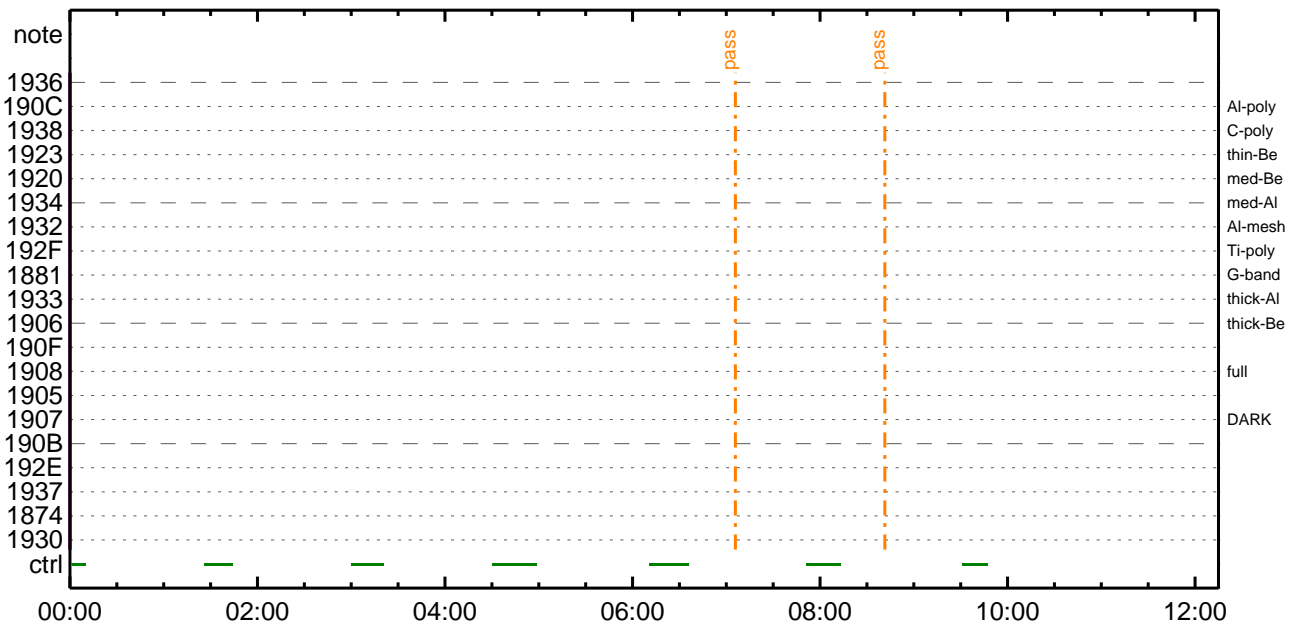
CMDI #0954 2012/10/07



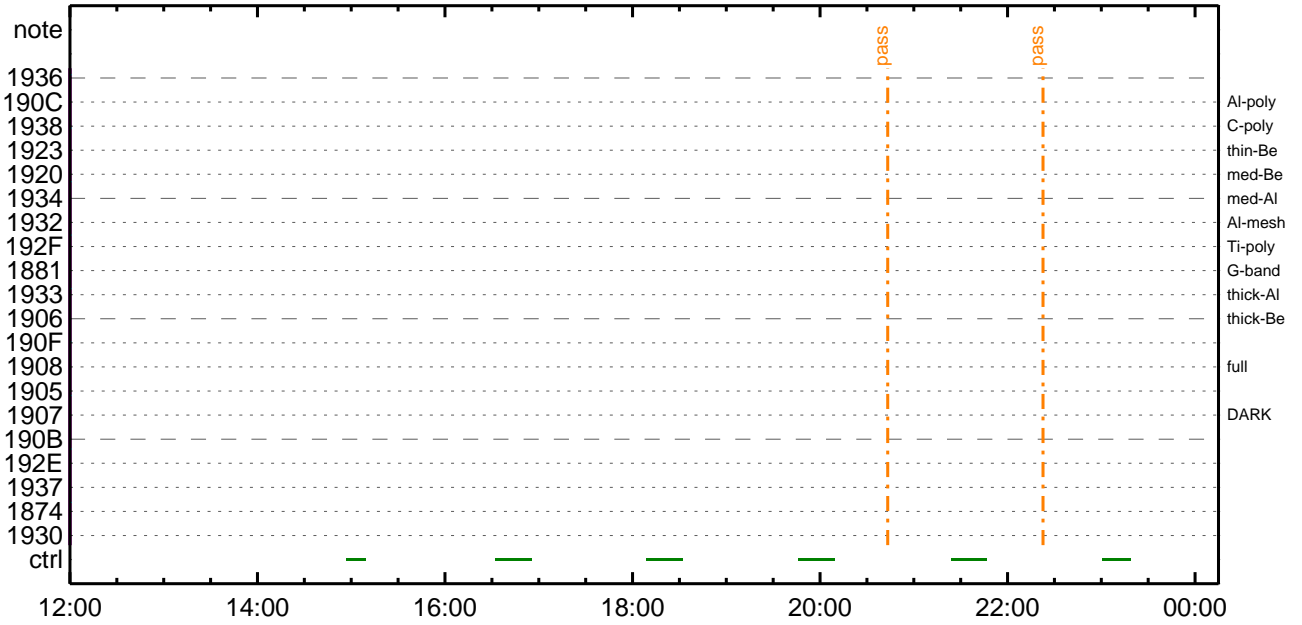
CMDI #0954 2012/10/07



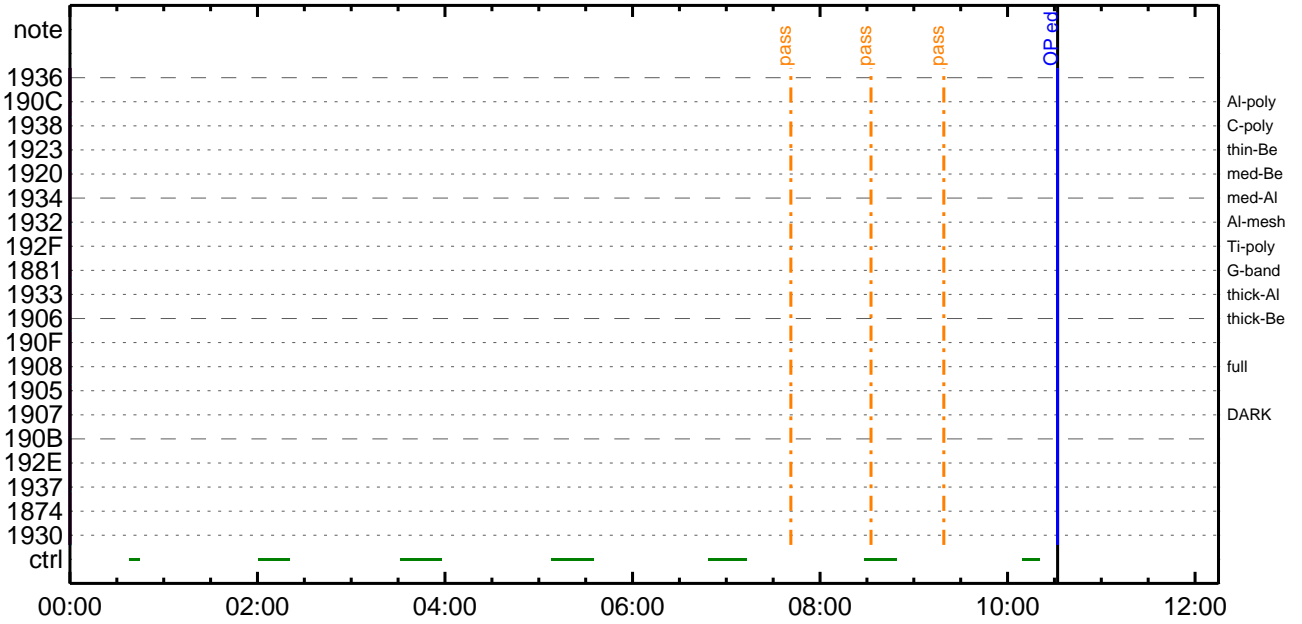
CMDI #0954 2012/10/08



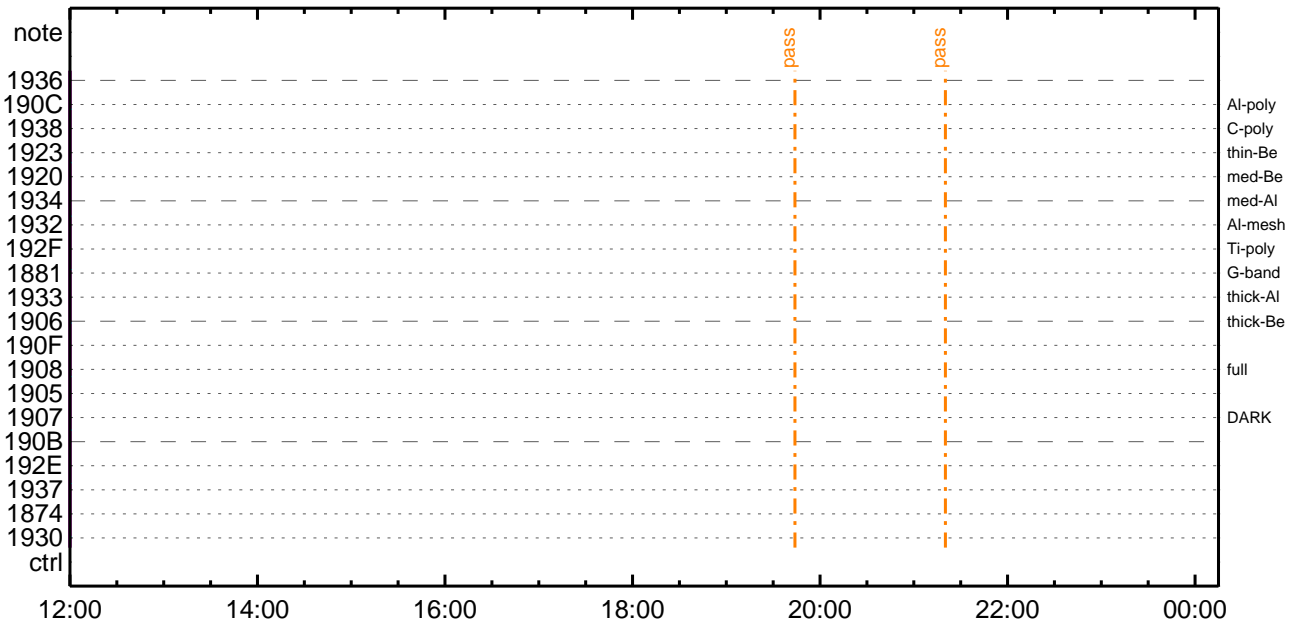
CMDI #0954 2012/10/08



CMDI #0954 2012/10/09



CMDI #0954 2012/10/09




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-184:OP
0104 ( )
0105 . S. OG og-184:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYOXx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGqî½E¹ç.ë²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYOXx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGqî½E¹ç.ë²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYOXx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPqî½E¹ç.ë²îOKð³îÇ§
0165 C.
0166 . C. ***** òÊ²¼qî½E¹ç.ë²îOKð³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eððîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOADq-Á÷çîNGuî½E¹ç;ç°Ê²¼qî½E¹ç.ë²îOKð³îÇ§ *****
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 . C. TIY³YpYOXEððîã¹ç(UT)
0183 +. TI 2012-10-04 10:38:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-10-04 10:38:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-10-04 10:38:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

0194 C.
0195 +. TI 2012-10-04 10:42:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîîŸÄŸ§ŸÄŸ-¹àîÛ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ªî»αò³îÇ§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŸâ;¼ŸÉ;È¼Ÿ¼. Ÿî;¼ŸÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-10-04 10:42:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-10-04 10:42:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2012-10-04 10:42:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-10-04 10:42:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŸαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŸÖŸÄŸÈŸŸŸŸáŸçŸèèÈ¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŸĐŸ¹•î Daily±çîñèË'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŸÄŸ§ŸÄŸ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.


```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 08 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 85 83 08 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_DIS
0141 BC (d9)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0143 BC (c9)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 + DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 04)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 10)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2012-10-04 10:42:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-269:MDP_OBS_F
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 . C. < Stop SP table >
0189 +. DC 07-F0 MDP_SP_CTRL_MANU
0190 BC (61)
0191 C. -----
0192 C. MDP_SP_CTRL_MODE = MANU [ ]
0193 C. -----

```

```
0194 C.
0195 . C. <Upload SP Observation Table>
0196 . S. RAM ram-287:MDP_OBS_S
0197 ( )
0198 C.
0199 . C. < Dump RAMID=MDP_OBS_S >
0200 +. DC 07-F0 MDP_DUMP_SPTBL
0201 BC (83 07 00 00 00 38 b8)
0202 C. -----
0203 C. MDP_OBS_S verify = OK/NG [ ]
0204 C. -----
0205 C.
0206 C. *****
0207 C. SOT TI command set
0208 C. *****
0209 C. Execute, after the success of TBL upload.
0210 +. TI 2012-10-04 10:42:18.0
0211 DC 07-F0 MDP_SOT_MODE_OBSV
0212 BC (40)
0213 . C. -----
0214 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0215 C. -----
0216 C.
0217 C.
0218 . C. ***** MDP `úÃîî»ö%ÝðËÂð¹ñèDCBC•x²è *****
0219 C. (%Á°îÝÓYÁYÈYÏYËYáYçYèñE½¼ñ¼Á»Û¹ñè)
0220 . S. DC-BC dcbc-402:DCBC
0221 (MDP_known_event)
0222 C.
0223 C.
0224 . C. ***** YDÝ!•İ Daily±¿İÑñE´Ø¹ñèDCBC•x²è *****
0225 . S. DC-BC dcbc-153:DCBC
0226 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0227 C.
0228 C.
0229 . C. ;ãLOS¥Á¥$¥Ã¥-¼Á»Û;ã
0230 C.
0231 . C. ***** LOS *****
0232 C.
```

*** OP Sequence for XRT ***

| | | | | | | | | |
|------------|------------|-----------------------------------|---|-------|----------------|--|--|--|
| 2012/10/04 | 10:52:54.0 | XRT_CTRL_MANU_447_OG [0x1bf] | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2012/10/04 | 10:53:00.0 | AOCS_ORe-point_Start_1_OG [0x097] | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 b1 b3 01 3f | | | |
| 2012/10/04 | 10:55:26.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | |
| 2012/10/04 | 10:55:46.0 | XRT_FLD_ENA_437_OG [0x1b5] | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | |
| 2012/10/04 | 10:55:48.0 | XRT_FLRCTRL_ENA_443_OG [0x1bb] | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | |
| 2012/10/04 | 10:55:50.0 | XRT_AEC_RESET_423_OG [0x1a7] | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | |
| 2012/10/04 | 10:55:52.0 | XRT_ARS_DIS_412_OG [0x19c] | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | |
| 2012/10/04 | 10:55:54.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2012/10/04 | 10:55:56.0 | XRT_QT_PROG_SET_445_OG [0x1bd] | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 09 | | | |
| 2012/10/04 | 10:55:58.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 10 | | | |
| 2012/10/04 | 10:56:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2012/10/04 | 14:09:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2012/10/04 | 14:09:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2012/10/04 | 14:09:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2012/10/04 | 14:12:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2012/10/04 | 14:15:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | |
| 2012/10/04 | 14:16:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2012/10/04 | 15:42:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2012/10/04 | 15:42:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2012/10/04 | 15:42:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2012/10/04 | 15:45:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2012/10/04 | 16:05:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | |
| 2012/10/04 | 16:06:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2012/10/04 | 17:18:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2012/10/04 | 17:18:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2012/10/04 | 17:18:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2012/10/04 | 17:21:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2012/10/04 | 17:42:30.5 | XRT_Custom_434_OG [0x1b2] | | | | | | |
| 2012/10/04 | 17:43:30.5 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2012/10/04 | 17:59:54.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2012/10/04 | 17:59:56.0 | XRT_FOCUS_POSITION_403_OG [0x193] | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | | | |
| 2012/10/04 | 18:00:00.0 | AOCS_ORe-point_Start_2_OG [0x098] | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | | | |
| 2012/10/04 | 18:00:16.0 | XRT_FLD_DIS_404_OG [0x194] | | | | | | |
| | | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | |
| 2012/10/04 | 18:00:18.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | | | | | | |
| | | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | |
| 2012/10/04 | 18:00:20.0 | XRT_ARS_DIS_406_OG [0x196] | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | |
| 2012/10/04 | 18:02:58.0 | XRT_QT_PROG_SET_419_OG [0x1a3] | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 0d | | | |
| 2012/10/04 | 18:03:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2012/10/04 | 18:09:54.0 | XRT_CTRL_MANU_448_OG [0x1c0] | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2012/10/04 | 18:09:56.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | |
| 2012/10/04 | 18:10:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 03 00 00 00 00 | | | |
| 2012/10/04 | 18:10:16.0 | XRT_FLD_ENA_428_OG [0x1ac] | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | |
| 2012/10/04 | 18:10:18.0 | XRT_FLRCTRL_ENA_429_OG [0x1ad] | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | |
| 2012/10/04 | 18:10:20.0 | XRT_AEC_RESET_423_OG [0x1a7] | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | |
| 2012/10/04 | 18:10:22.0 | XRT_ARS_DIS_438_OG [0x1b6] | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | |
| 2012/10/04 | 18:10:24.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2012/10/04 | 18:10:26.0 | XRT_QT_PROG_SET_427_OG [0x1ab] | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 03 | | | |
| 2012/10/04 | 18:10:28.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 10 | | | |

Oct 04, 12 12:22

XRT_OGLIST_0954.chk

| | | | | | | |
|------------|------------|------------------------------------|---------------------|---|-------|----------------|
| 2012/10/04 | 18:13:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/04 | 18:56:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/04 | 18:56:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/04 | 18:56:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/10/04 | 18:59:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/10/04 | 19:19:00.0 | XRT_Custom_434_OG [0x1b2] | | | | |
| 2012/10/04 | 19:20:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/04 | 20:33:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/04 | 20:33:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/04 | 20:33:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/10/04 | 20:36:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/10/04 | 20:56:30.0 | XRT_Custom_434_OG [0x1b2] | | | | |
| 2012/10/04 | 20:57:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/04 | 21:09:54.0 | XRT_CTRL_MANU_448_OG [0x1c0] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/04 | 21:09:56.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 |
| 2012/10/04 | 21:10:00.0 | AOCS_Or-e-point_Start_4_OG [0x09a] | AOCU_NM | 5 | 02-76 | 04 00 00 00 00 |
| 2012/10/04 | 21:10:16.0 | XRT_FLD_ENA_428_OG [0x1ac] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 |
| 2012/10/04 | 21:10:18.0 | XRT_FLRCTRL_ENA_429_OG [0x1ad] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 |
| 2012/10/04 | 21:10:20.0 | XRT_AEC_RESET_423_OG [0x1a7] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 |
| 2012/10/04 | 21:10:22.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2012/10/04 | 21:10:24.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/04 | 21:10:26.0 | XRT_QT_PROG_SET_427_OG [0x1ab] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 03 |
| 2012/10/04 | 21:10:28.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 10 |
| 2012/10/04 | 21:13:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/04 | 22:10:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/04 | 22:10:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/04 | 22:10:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/10/04 | 22:13:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/10/04 | 22:32:30.0 | XRT_Custom_434_OG [0x1b2] | | | | |
| 2012/10/04 | 22:33:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/04 | 23:48:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/04 | 23:48:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/04 | 23:48:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/10/04 | 23:51:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/10/04 | 23:57:30.0 | XRT_Custom_434_OG [0x1b2] | | | | |
| 2012/10/04 | 23:58:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/05 | 01:13:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/05 | 01:13:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/05 | 01:13:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/10/05 | 01:16:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/10/05 | 01:31:00.0 | XRT_Custom_434_OG [0x1b2] | | | | |
| 2012/10/05 | 01:32:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/05 | 02:47:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/05 | 02:47:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/10/05 | 02:47:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/10/05 | 02:50:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/10/05 | 03:07:30.0 | XRT_Custom_434_OG [0x1b2] | | | | |
| 2012/10/05 | 03:08:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/10/05 | 04:16:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/10/05 | 04:16:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | |

Oct 04, 12 12:22

XRT_OGLIST_0954.chk

Page 3/6

| | | | | | | | | | | |
|------------|------------|-----------------------------------|---------------------|---|-------|----------------|--|--|--|--|
| 2012/10/05 | 04:16:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| | | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/05 | 04:19:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/05 | 04:45:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | | |
| 2012/10/05 | 04:46:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 05:57:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 05:57:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/05 | 05:57:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/05 | 06:00:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/05 | 06:22:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | | |
| 2012/10/05 | 06:23:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 07:37:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 07:37:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/05 | 07:37:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/05 | 07:40:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/05 | 08:00:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | | |
| 2012/10/05 | 08:01:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 09:17:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 09:17:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/05 | 09:17:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/05 | 09:20:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/05 | 09:35:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | | |
| 2012/10/05 | 09:36:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 09:59:54.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 09:59:56.0 | XRT_FOCUS_POSITION_403_OG [0x193] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | | | | |
| 2012/10/05 | 10:00:00.0 | AOCS_ORe-point_Start_2_OG [0x098] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | | | | |
| 2012/10/05 | 10:00:16.0 | XRT_FLD_DIS_404_OG [0x194] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | | |
| 2012/10/05 | 10:00:18.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | | |
| 2012/10/05 | 10:00:20.0 | XRT_ARS_DIS_406_OG [0x196] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/10/05 | 10:02:58.0 | XRT_QT_PROG_SET_416_OG [0x1a0] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 04 | | | | |
| 2012/10/05 | 10:03:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 10:09:54.0 | XRT_CTRL_MANU_447_OG [0x1bf] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 10:10:00.0 | AOCS_ORe-point_Start_5_OG [0x09b] | AOCU_NM | 5 | 02-76 | 02 00 00 00 00 | | | | |
| 2012/10/05 | 10:12:26.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |
| 2012/10/05 | 10:12:46.0 | XRT_FLD_ENA_437_OG [0x1b5] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/10/05 | 10:12:48.0 | XRT_FLRCTRL_ENA_443_OG [0x1bb] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/10/05 | 10:12:50.0 | XRT_AEC_RESET_423_OG [0x1a7] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/10/05 | 10:12:52.0 | XRT_ARS_DIS_412_OG [0x19c] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/10/05 | 10:12:54.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/05 | 10:12:56.0 | XRT_QT_PROG_SET_415_OG [0x19f] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 12 | | | | |
| 2012/10/05 | 10:12:58.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 10 | | | | |
| 2012/10/05 | 10:13:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 13:09:54.0 | XRT_CTRL_MANU_448_OG [0x1c0] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 13:09:56.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |
| 2012/10/05 | 13:10:00.0 | AOCS_ORe-point_Start_4_OG [0x09a] | AOCU_NM | 5 | 02-76 | 04 00 00 00 00 | | | | |
| 2012/10/05 | 13:10:16.0 | XRT_FLD_ENA_428_OG [0x1ac] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/10/05 | 13:10:18.0 | XRT_FLRCTRL_ENA_429_OG [0x1ad] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/10/05 | 13:10:20.0 | XRT_AEC_RESET_423_OG [0x1a7] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/10/05 | 13:10:22.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |

Oct 04, 12 12:22

XRT_OGLIST_0954.chk

Page 4/6

| | | | | | | | | |
|------------|------------|-----------------------------------|---------------------|---|-------|----|-------------|--|
| 2012/10/05 | 13:10:24.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 13:10:26.0 | XRT_QT_PROG_SET_427_OG [0x1ab] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 03 | |
| 2012/10/05 | 13:10:28.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 10 | |
| 2012/10/05 | 13:13:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/10/05 | 14:43:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 14:43:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 14:43:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/10/05 | 14:46:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/10/05 | 14:55:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | |
| 2012/10/05 | 14:56:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/10/05 | 16:18:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 16:18:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 16:18:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/10/05 | 16:21:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/10/05 | 16:42:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | |
| 2012/10/05 | 16:43:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/10/05 | 17:55:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 17:55:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 17:55:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/10/05 | 17:58:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/10/05 | 18:18:54.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 18:18:56.0 | XRT_FOCUS_POSITION_403_OG [0x193] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff aa 00 | |
| 2012/10/05 | 18:19:00.0 | AOCS_ORe-point_Start_2_OG [0x098] | AOCU_NM | 5 | 02-76 | 00 | 00 00 00 00 | |
| 2012/10/05 | 18:19:16.0 | XRT_FLD_DIS_404_OG [0x194] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | |
| 2012/10/05 | 18:19:18.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | |
| 2012/10/05 | 18:19:20.0 | XRT_ARS_DIS_406_OG [0x196] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | |
| 2012/10/05 | 18:21:58.0 | XRT_QT_PROG_SET_419_OG [0x1a3] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 0d | |
| 2012/10/05 | 18:22:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/10/05 | 18:28:54.0 | XRT_CTRL_MANU_448_OG [0x1c0] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 18:28:56.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | fe 97 00 | |
| 2012/10/05 | 18:29:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 03 | 00 00 00 00 | |
| 2012/10/05 | 18:29:16.0 | XRT_FLD_ENA_428_OG [0x1ac] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | |
| 2012/10/05 | 18:29:18.0 | XRT_FLRCTRL_ENA_429_OG [0x1ad] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | |
| 2012/10/05 | 18:29:20.0 | XRT_AEC_RESET_423_OG [0x1a7] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | |
| 2012/10/05 | 18:29:22.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | |
| 2012/10/05 | 18:29:24.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 18:29:26.0 | XRT_QT_PROG_SET_435_OG [0x1b3] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 0f | |
| 2012/10/05 | 18:29:28.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 10 | |
| 2012/10/05 | 18:32:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/10/05 | 19:32:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 19:32:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 19:32:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/10/05 | 19:35:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/10/05 | 19:56:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | |
| 2012/10/05 | 19:57:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/10/05 | 21:10:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/10/05 | 21:10:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/10/05 | 21:10:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |

Oct 04, 12 12:22

XRT_OGLIST_0954.chk

Page 5/6

| | | | | | | | | | |
|------------|------------|-----------------------------------|---|-------|----------------|--|--|--|--|
| 2012/10/05 | 21:13:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/05 | 21:29:00.0 | AOCS_Ore-point_Start_4_OG [0x09a] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 04 00 00 00 00 | | | | |
| 2012/10/05 | 21:33:01.0 | XRT_CTRL_MANU_448_OG [0x1c0] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 21:33:03.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |
| 2012/10/05 | 21:33:23.0 | XRT_FLD_ENA_428_OG [0x1ac] | | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/10/05 | 21:33:25.0 | XRT_FLRCTRL_ENA_429_OG [0x1ad] | | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/10/05 | 21:33:27.0 | XRT_AEC_RESET_423_OG [0x1a7] | | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/10/05 | 21:33:29.0 | XRT_ARS_DIS_438_OG [0x1b6] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/10/05 | 21:33:31.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/05 | 21:33:33.0 | XRT_QT_PROG_SET_435_OG [0x1b3] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 0f | | | | |
| 2012/10/05 | 21:33:35.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 10 | | | | |
| 2012/10/05 | 21:34:01.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/05 | 22:47:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/05 | 22:47:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/05 | 22:47:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/05 | 22:50:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/05 | 23:08:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | |
| 2012/10/05 | 23:09:00.5 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 00:25:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 00:25:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 00:25:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 00:28:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 00:32:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | |
| 2012/10/06 | 00:33:00.0 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 01:48:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 01:48:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 01:48:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 01:51:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 02:07:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | |
| 2012/10/06 | 02:08:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 03:22:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 03:22:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 03:22:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 03:25:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 03:44:30.5 | XRT_Custom_434_OG [0x1b2] | | | | | | | |
| 2012/10/06 | 03:45:30.5 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 04:54:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 04:54:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 04:54:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 04:57:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 05:22:00.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | |
| 2012/10/06 | 05:23:00.5 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 06:35:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 06:35:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 06:35:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 06:38:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 06:40:00.0 | AOCS_Ore-point_Start_6_OG [0x09c] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | | | | |
| 2012/10/06 | 06:59:00.0 | XRT_CTRL_MANU_447_OG [0x1bf] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 07:01:32.0 | XRT_FOCUS_POSITION_420_OG [0x1a4] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |

Oct 04, 12 12:22

XRT_OGLIST_0954.chk

Page 6/6

| | | | | | | | | | |
|------------|------------|-----------------------------------|---|-------|----|----|----|----|----|
| 2012/10/06 | 07:01:52.0 | XRT_FLD_ENA_437_OG [0x1b5] | | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/10/06 | 07:01:54.0 | XRT_FLRCTRL_ENA_443_OG [0x1bb] | | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/10/06 | 07:01:56.0 | XRT_AEC_RESET_423_OG [0x1a7] | | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/10/06 | 07:01:58.0 | XRT_ARS_DIS_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/10/06 | 07:02:00.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 07:02:02.0 | XRT_QT_PROG_SET_415_OG [0x19f] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 12 | | | |
| 2012/10/06 | 07:02:04.0 | XRT_FL_PROG_SET_444_OG [0x1bc] | | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 10 | | | |
| 2012/10/06 | 07:04:08.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 08:15:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 08:15:02.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 08:15:04.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 08:18:14.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 08:36:30.0 | XRT_Custom_434_OG [0x1b2] | | | | | | | |
| 2012/10/06 | 08:37:30.0 | XRT_CTRL_AUTO_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 09:39:54.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 09:39:56.0 | XRT_FOCUS_POSITION_403_OG [0x193] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff | aa | 00 | |
| 2012/10/06 | 09:40:00.0 | AOCS_OrE-point_Start_2_OG [0x098] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 | 00 |
| 2012/10/06 | 09:40:16.0 | XRT_FLD_DIS_404_OG [0x194] | | | | | | | |
| | | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | | |
| 2012/10/06 | 09:40:18.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | | | | | | | |
| | | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | | |
| 2012/10/06 | 09:40:20.0 | XRT_ARS_DIS_406_OG [0x196] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/10/06 | 09:42:58.0 | XRT_QT_PROG_SET_416_OG [0x1a0] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 04 | | | |
| 2012/10/06 | 09:43:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/10/06 | 09:50:00.0 | AOCS_OrE-point_Start_4_OG [0x09a] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 04 | 00 | 00 | 00 | 00 |
| 2012/10/06 | 09:55:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/10/06 | 09:55:32.0 | XRT_FLD_RESET_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/10/06 | 09:55:34.0 | XRT_PREFLR_STRT_432_OG [0x1b0] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/10/06 | 09:58:44.0 | XRT_PREFLR_STOP_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/10/06 | 10:29:00.0 | AOCS_OrE-point_Start_2_OG [0x098] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 | 00 |