

XRT Timeline to be uploaded on 2012/10/13

Period: 2012/10/13 10:17:00 - 2012/10/18 09:35:00

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Normal mode

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XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
10/13 10:30:00 - 10/13 13:59:54	Track (-466.3, 105.5) @ 10/13 10:27:00	# OP start + 10min, track AR 11589.
10/13 18:35:00 - 10/14 09:46:54	Track (-401.0, 102.4) @ 10/13 18:32:00	# Track 11589.
10/14 10:13:00 - 10/14 13:59:54	Track (-269.8, 98.0) @ 10/14 09:57:00	# Cont.
10/14 18:13:00 - 10/15 09:59:54	Track (-197.2, 96.5) @ 10/14 18:10:00	# Track AR 11589.
10/15 10:17:00 - 10/15 17:59:54	Track (-52.6, 95.4) @ 10/15 10:10:00	# Cont.
10/15 18:13:00 - 10/16 05:45:54	Track (20.3, 95.7) @ 10/15 18:10:00	# Cont.
10/16 05:59:00 - 10/16 09:35:54	Track (127.4, 97.2) @ 10/16 05:56:00	# Cont.

PROG= 03 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 18 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 19 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 65 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 73 70-time(s) 120.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1939: HOP073 prominence multi-filter with G-band(45ms)-DPCM 1x1-512x512-FOV -AEC2-3min cadence with G-band Test

Term	Pointing (x, y)	Comment
10/13 14:03:00 - 10/13 17:58:30	Fixed (722.0, -614.0)	* HOP 219, prominence.
10/14 14:03:00 - 10/14 17:59:54	Fixed (722.0, -614.0)	* HOP 219, prominence.

PROG= 17 Inf.-time(s)

Subr= 3	1-time(s)	2.0sec										
Seqn= 88 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 1 20-time(s) 180.0sec												
Seqn= 85 1-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 86 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 89 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	11.3s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 90 1-time(s) 2.0sec												
C-poly/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 91 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18

Term	Pointing (x, y)	Comment
10/13 18:25:00 - 10/13 18:31:54	Fixed (0.0, 0.0)	synoptic, shifted 22.0 min
10/14 18:03:00 - 10/14 18:09:54	Fixed (0.0, 0.0)	synoptic
10/15 18:03:00 - 10/15 18:09:54	Fixed (0.0, 0.0)	synoptic
10/16 05:49:00 - 10/16 05:55:54	Fixed (0.0, 0.0)	synoptic, shifted -14.0 min

PROG= 13 1-time(s)

Subr= 1	1-time(s)	14.0sec										
Seqn= 64 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 70		1-time(s)		4.0sec								
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67		1-time(s)		2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 68		1-time(s)		2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #193A: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1)

Term	Pointing (x, y)	Comment
10/14 09:50:00 - 10/14 09:56:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
10/15 10:03:00 - 10/15 10:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
PROG= 01 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 84 1-time(s) 4.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 70 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly	close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 69 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 6.0sec		
Seqn= 66 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Subr= 3 1-time(s) 2.0sec		
Seqn= 68 1-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
10/13 10:30:00 - 10/13 13:59:54	Track (-466.3, 105.5) @ 10/13 10:27:00	# OP start + 10min, track AR 11589.
10/13 14:03:00 - 10/13 17:58:30	Fixed (722.0, -614.0)	* HOP 219, prominence.
10/13 18:35:00 - 10/14 09:46:54	Track (-401.0, 102.4) @ 10/13 18:32:00	# Track 11589.
10/14 10:13:00 - 10/14 13:59:54	Track (-269.8, 98.0) @ 10/14 09:57:00	# Cont.
10/14 14:03:00 - 10/14 17:59:54	Fixed (722.0, -614.0)	* HOP 219, prominence.
10/14 18:13:00 - 10/15 09:59:54	Track (-197.2, 96.5) @ 10/14 18:10:00	# Track AR 11589.
10/15 10:17:00 - 10/15 17:59:54	Track (-52.6, 95.4) @ 10/15 10:10:00	# Cont.
10/15 18:13:00 - 10/16 05:45:54	Track (20.3, 95.7) @ 10/15 18:10:00	# Cont.
10/16 05:59:00 - 10/16 09:35:54	Track (127.4, 97.2) @ 10/16 05:56:00	# Cont.

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec		
Seqn= 35 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
Seqn= 36 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 37 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 38 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

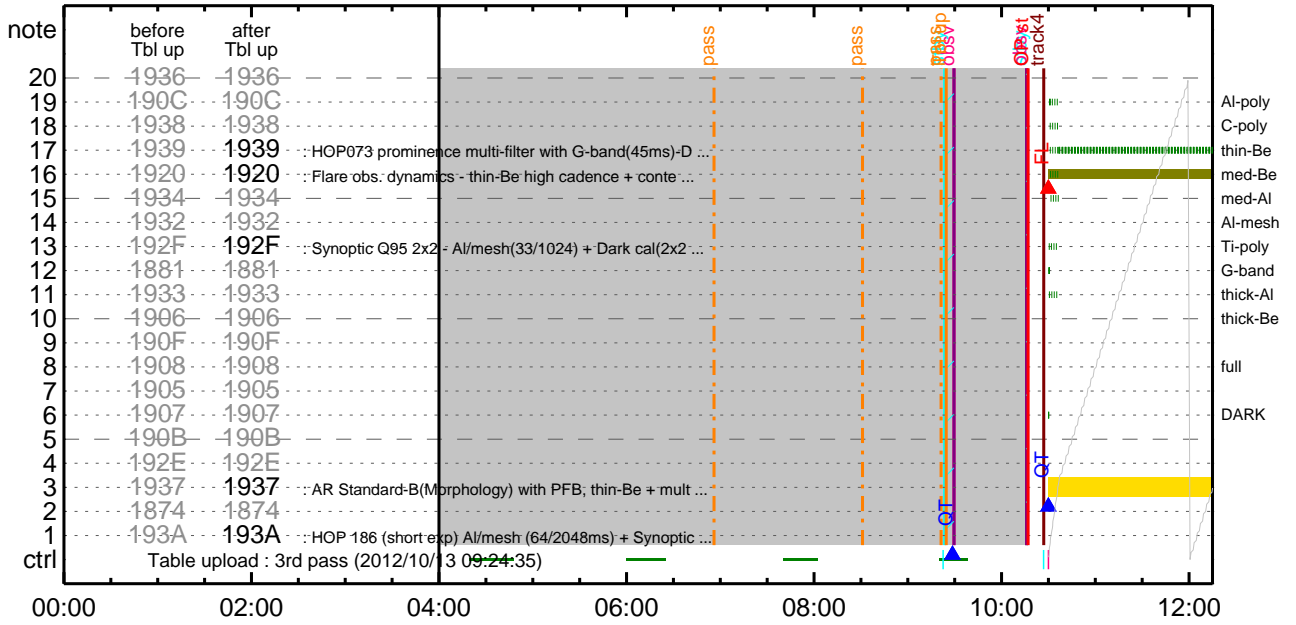
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Flare Detection

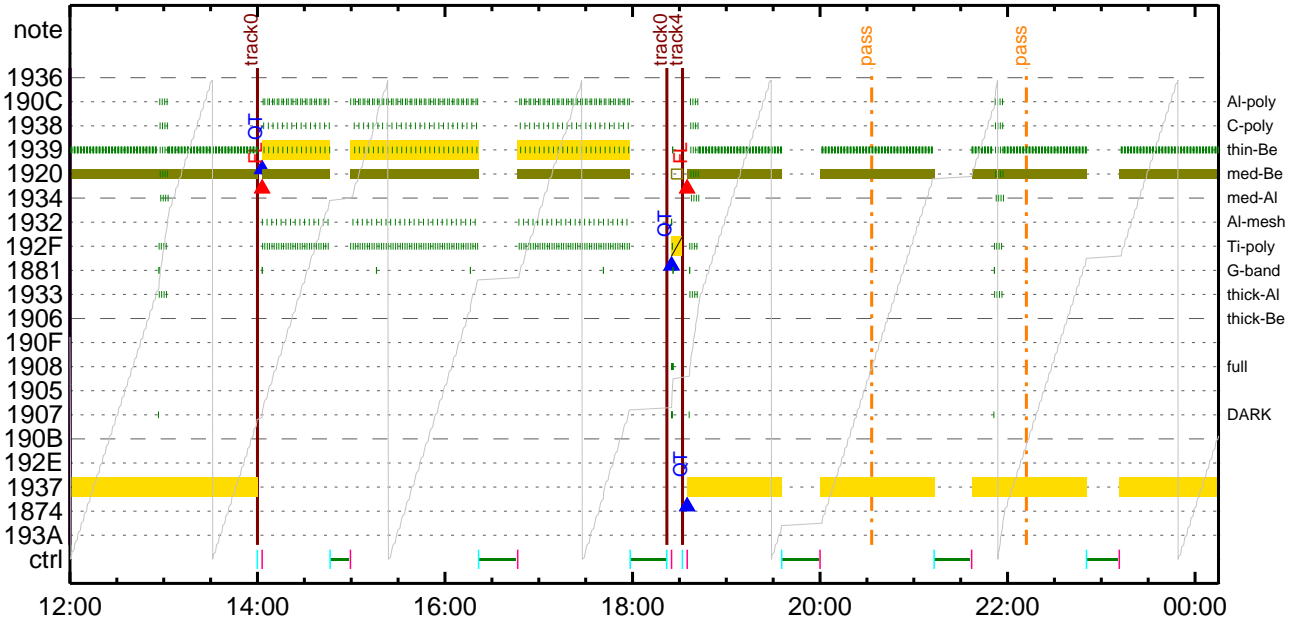
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FLD Patrol												
Term		Pointing (x, y)					Comment					
10/13 10:29:46 - 10/13 18:22:16		Track (-466.3, 105.5) @ 10/13 10:27:00					# OP start + 10min, track AR 11589.					
10/13 18:34:46 - 10/14 09:47:16		Track (-401.0, 102.4) @ 10/13 18:32:00					# Track 11589.					
10/14 09:59:46 - 10/14 18:00:16		Track (-269.8, 98.0) @ 10/14 09:57:00					# Cont.					
10/14 18:12:46 - 10/15 10:00:16		Track (-197.2, 96.5) @ 10/14 18:10:00					# Track AR 11589.					
10/15 10:16:46 - 10/15 18:00:16		Track (-52.6, 95.4) @ 10/15 10:10:00					# Cont.					
10/15 18:12:46 - 10/16 05:46:16		Track (20.3, 95.7) @ 10/15 18:10:00					# Cont.					
10/16 05:58:46 - 10/18 09:35:00		Track (127.4, 97.2) @ 10/16 05:56:00					# Cont.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

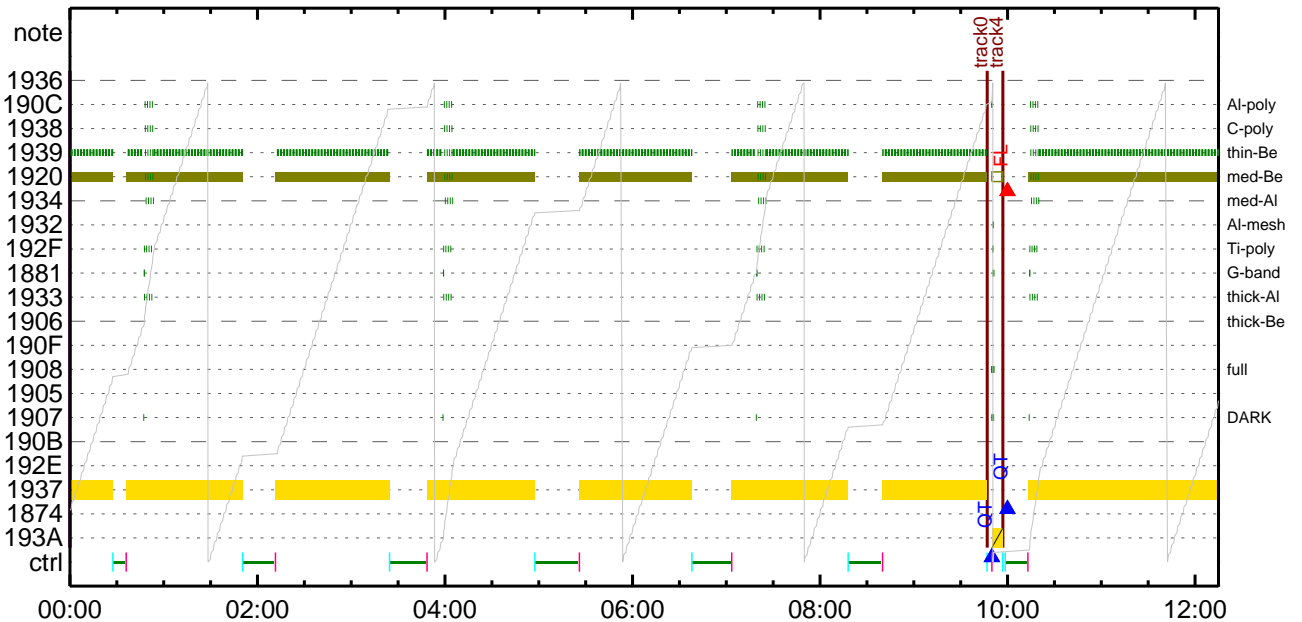
CMDI #0974 2012/10/13



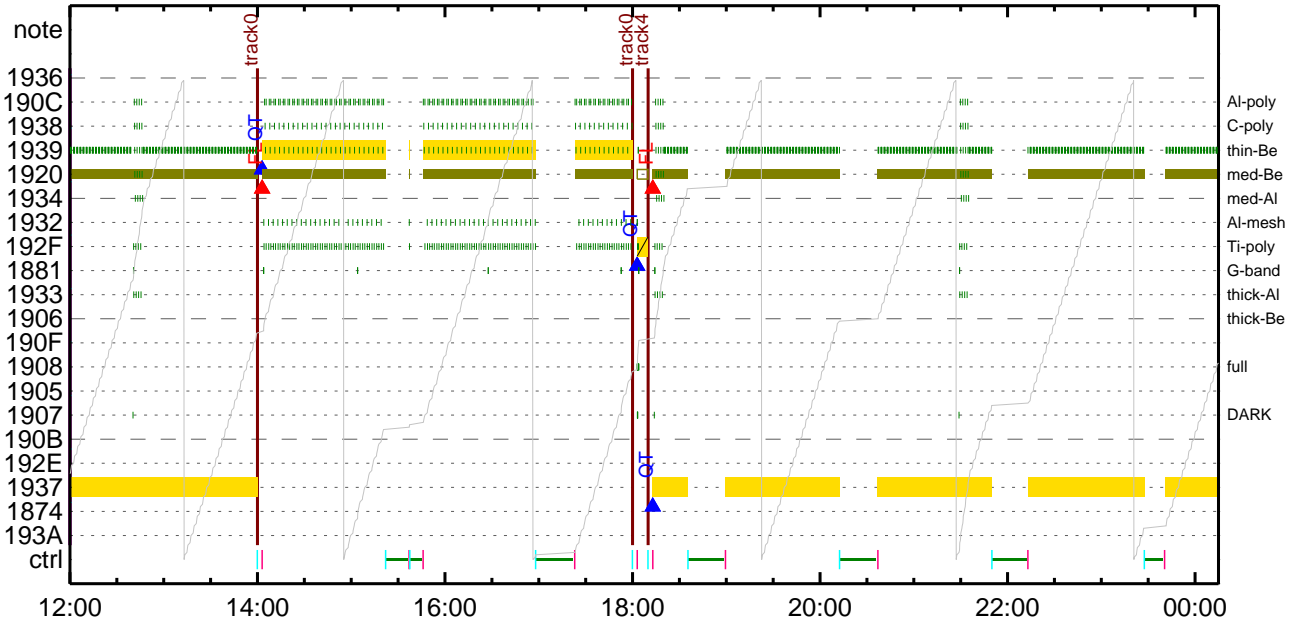
CMDI #0974 2012/10/13



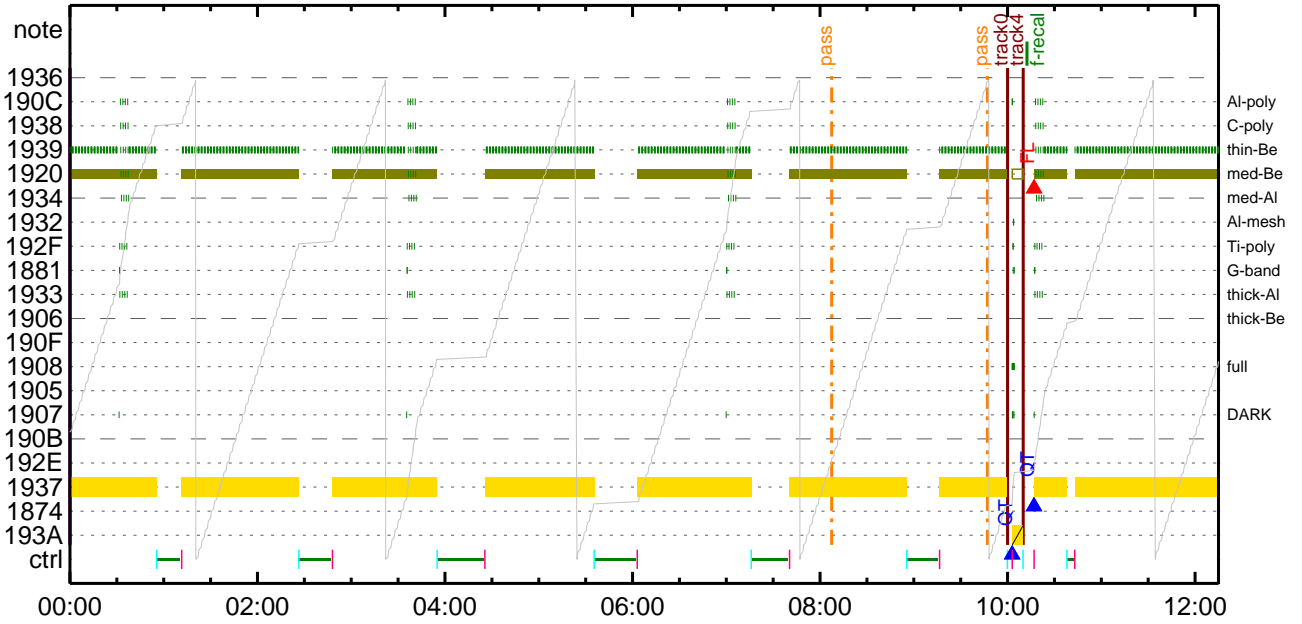
CMDI #0974 2012/10/14



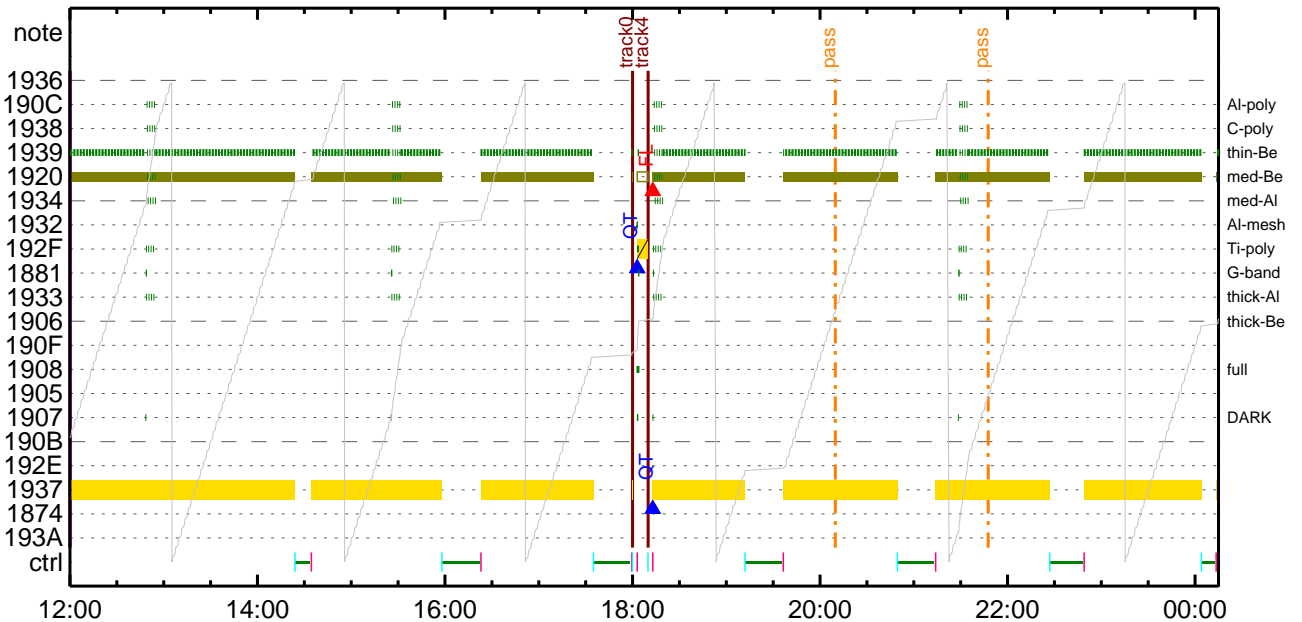
CMDI #0974 2012/10/14



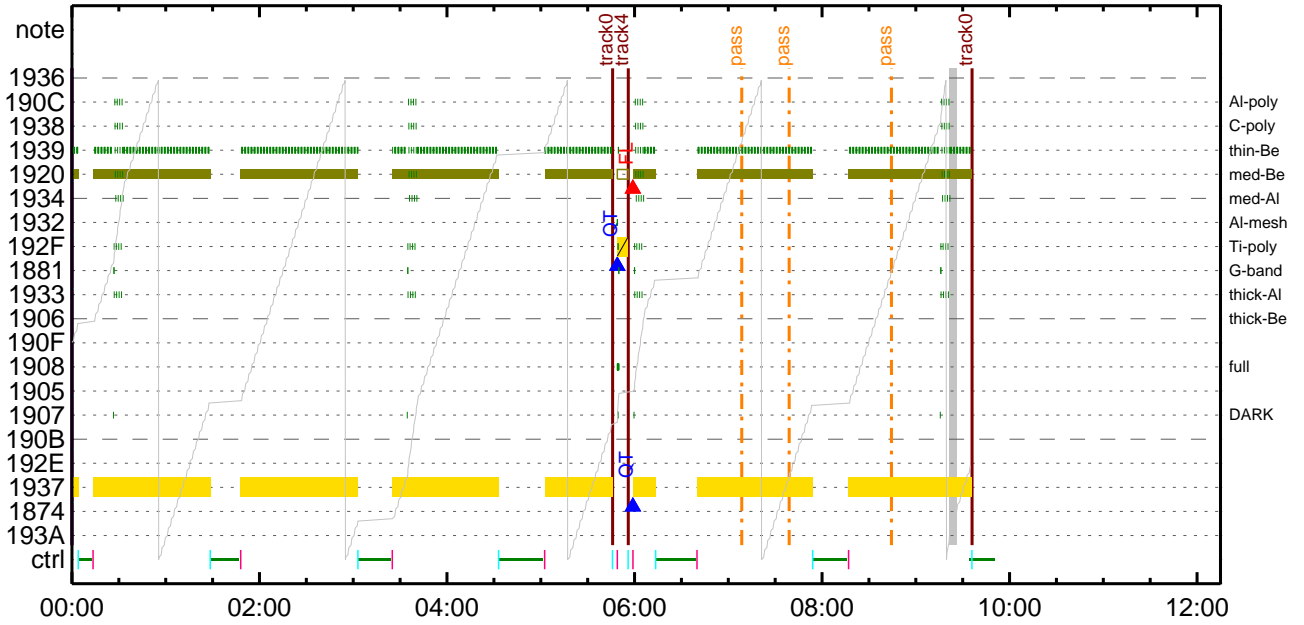
CMDI #0974 2012/10/15



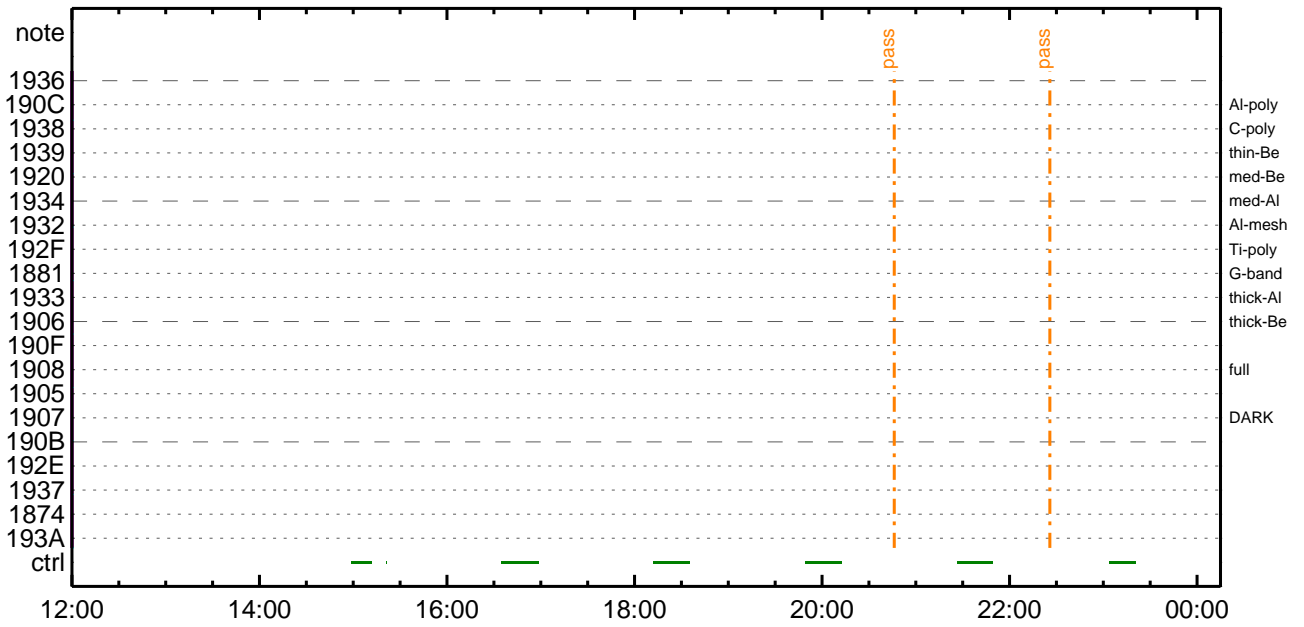
CMDI #0974 2012/10/15



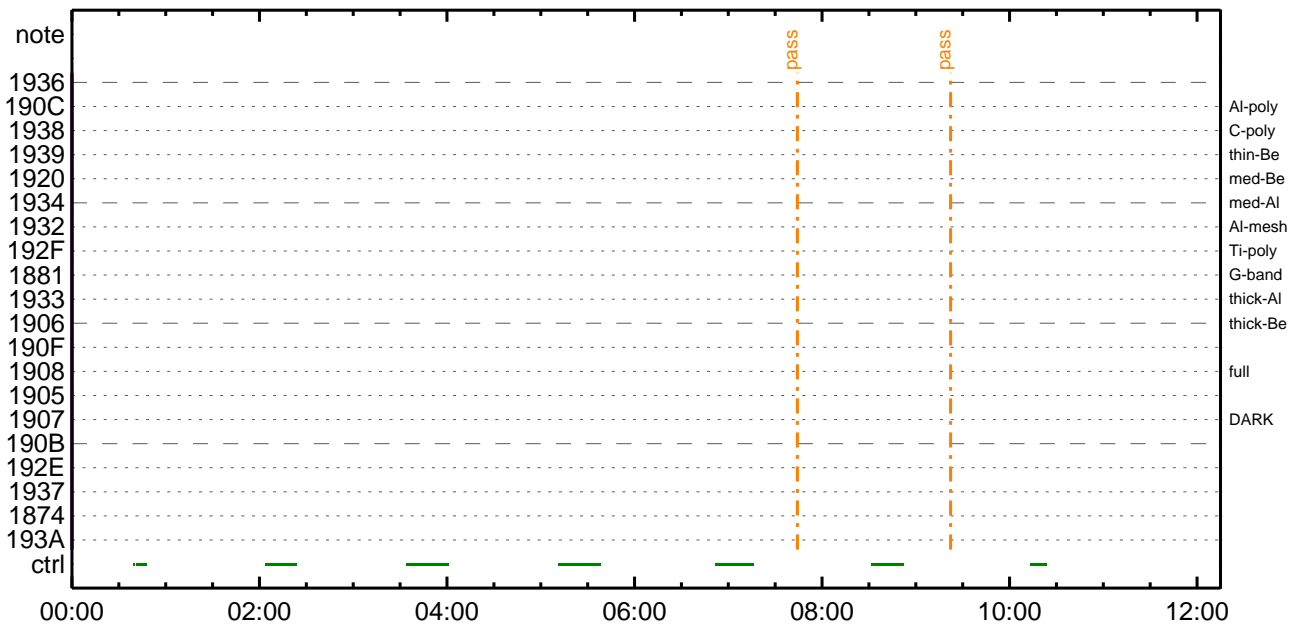
CMDI #0974 2012/10/16



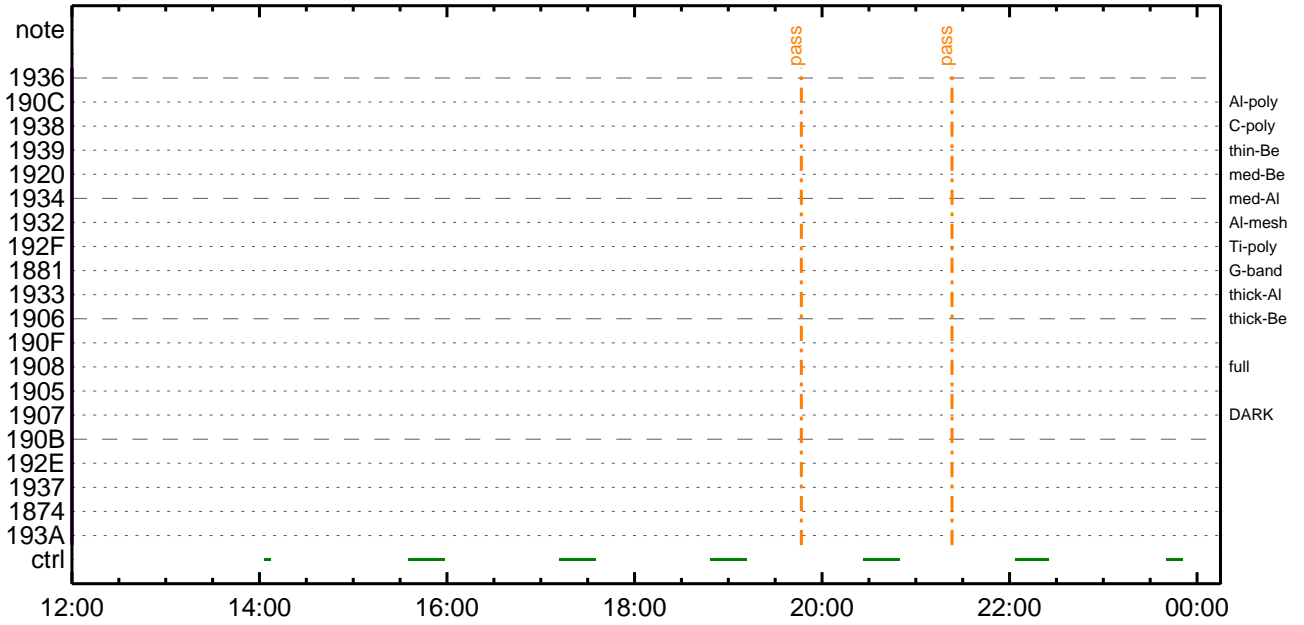
CMDI #0974 2012/10/16



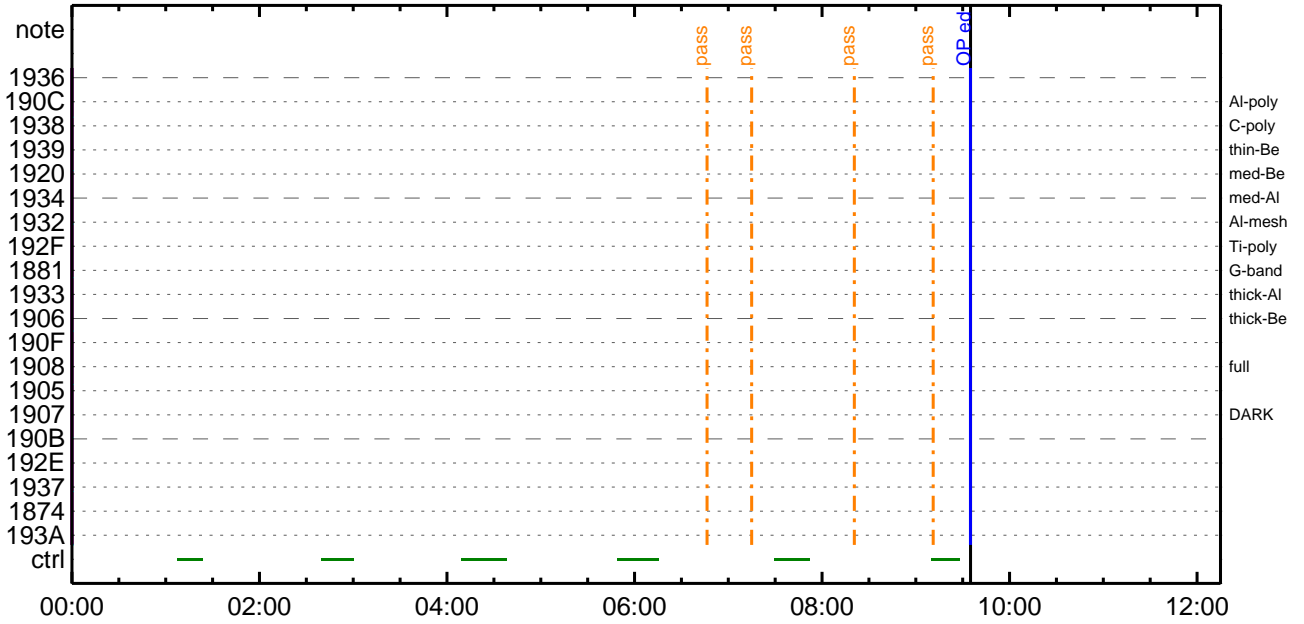
CMDI #0974 2012/10/17



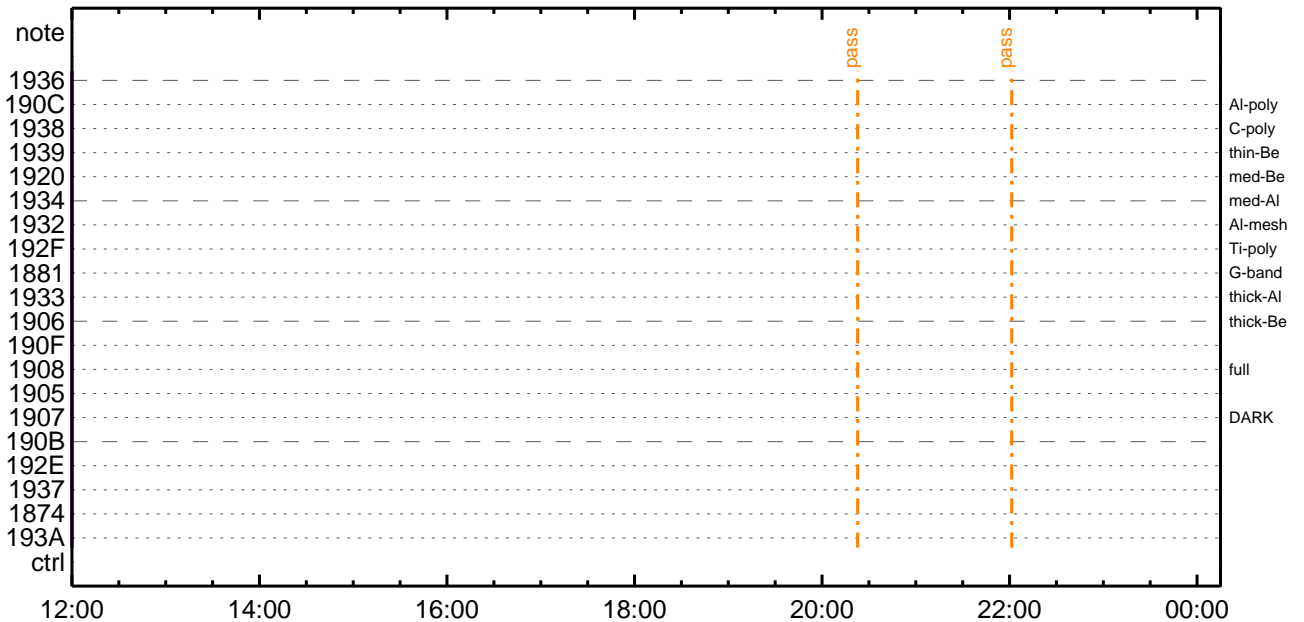
CMDI #0974 2012/10/17



CMDI #0974 2012/10/18



CMDI #0974 2012/10/18




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0194 C.
0195 +. TI 2012-10-13 10:16:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ðð[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼œİÄè%îíñœîŷÄŷŷÄŷ-¹àîŰ
0200 C.          ðð[HK1_TI_CMD_ENA/DIS]                  EQ      ENA
0201 C.          ðð[HK1_TI_CMD_NUM]                      EQ      4
0202 C.          ðð[HK1_NEXT_EXEC_PIM]                   EQ      DHU
0203 C.          ðð[HK1_NEXT_EXEC_DC]                     EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŰ°èŷÄŷÖŷx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ðð[HK1_DMP_TOP_ADRS_1]                  EQ      07
0213 C.          ðð[HK1_DMP_TOP_ADRS_0]                  EQ      2B
0214 C.          ðð[HK1_DMP_BLOCK_NUM]                   EQ      3
0215 C.          ðð[HK1_DMP_REPEAT_NUM]                  EQ      0
0216 C.          ðð[HK1_DMA_DMP_PIM]                     EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ðð[HK1_PKT_FORM_NO]                      EQ      7
0220 C.          ðð[HK1_PKT_GEN_TIME]                     EQ      0.25 s
0221 C.          ðð[HK1_S_TLM_BIT_RATE]                  EQ      32k
0222 C.          ðð[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.          ðð[HK1_DMP_CHK_FLG]                      EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷx½ªî»ð³İÇ§
0226 C.          ðð[HK1_DMP_CHK_FLG]                      EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLœîŰ°è¹ç•è²İOKœð³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ½. ŷî;¼ŷÈ;Ëœðİäœ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ðð[HK1_PKT_FORM_NO]                      EQ      2
0234 C.          ðð[HK1_PKT_GEN_TIME]                     EQ      0.5S
0235 C.          ðð[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.          ðð[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-10-13 10:16:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2012-10-13 10:16:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]                     EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2012-10-13 10:16:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-10-13 10:16:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                     EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ŷâîœî»ð¼ŷœÈÄœœ¹œÈDCBC•x²è *****
0276 C. (¼ª°İŷÖŷÄŷÈŷŷŷÄŷçŷèœ¼œœ¼Ä»Űœ¹œè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.İ Daily±çİñœÈ´œœ¹œÈDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷŷÄŷ-¼Ä»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-205 2012-10-13 12:58:53 128 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÀ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 08 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 85 83 06 06)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_DIS
0057 BC (d9)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0059 BC (c9)
0060 + DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 + DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 01)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C.
0071 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0072 C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074 BC (c2)
0075 +. TI 2012-10-13 10:16:02.0
0076 DC 07-F0 MDP_XRT_MODE_OBSV
0077 BC (c2)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C. ***** XRT END *****
0081 . C. *****
0082 C. SOT table upload
0083 C. *****
0084 . C. < Stop FG table >
0085 +. DC 07-F0 MDP_FG_CTRL_MANU
0086 BC (51)
0087 . C. -----
0088 C. MDP_FG_CTRL_MODE = MANU [ ]
0089 C. -----
0090 C.
0091 . C. <Upload FG Observation Table>
0092 . S. RAM ram-264:MDP_OBS_F
0093 ( )
0094 C.
0095 . C. < Dump RAMID=MDP_OBS_F >
```

```
0096 +. DC 07-F0 MDP_DUMP_FGTBL
0097 BC (82 07 00 00 00 38 b8)
0098 C. -----
0099 C. MDP_OBS_F verify = OK/NG [ ]
0100 C. -----
0101 C.
0102 C. *****
0103 C. SOT TI command set
0104 C. *****
0105 C. Execute, after the success of TBL upload.
0106 +. TI 2012-10-13 10:16:18.0
0107 DC 07-F0 MDP_SOT_MODE_OBSV
0108 BC (40)
0109 . C. -----
0110 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0111 C. -----
0112 C.
0113 C.
0114 . C. ***** MDP `úÃîâî»ô¼ŸαĒĀĐα¹αēDCBC•x²è *****
0115 C. (¼á°îŸÓŸĀŸĒŸĬŸĒŸĀŸçŸēαĒ¼α¼Ā»Ūα¹αē)
0116 . S. DC-BC dcbc-402:DCBC
0117 (MDP_known_event)
0118 C.
0119 C.
0120 . C. ***** ŸĐŸ¹•Ī Daily±¿ĪŃαĒŸøα¹αēDCBC•x²è *****
0121 . S. DC-BC dcbc-153:DCBC
0122 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123 C.
0124 C.
0125 . C. ¡ãLOSŸĀŸŸŸĀŸ-¼Ā»Ū¡ã
0126 C.
0127 . C. ***** LOS *****
0128 C.
```

*** OP Sequence for XRT ***

```

2012/10/13 10:26:54.0 XRT_CTRL_MANU_447_OG [0x1bf]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/13 10:27:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 04 00 00 00 00
2012/10/13 10:29:26.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/10/13 10:29:46.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/10/13 10:29:48.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/10/13 10:29:50.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/10/13 10:29:52.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/10/13 10:29:54.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/13 10:29:56.0 XRT_QT_PROG_SET_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 03
2012/10/13 10:29:58.0 XRT_FL_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2012/10/13 10:30:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/13 13:59:54.0 XRT_CTRL_MANU_447_OG [0x1bf]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/13 14:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 36 97 bf cf
2012/10/13 14:02:26.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/10/13 14:02:46.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/10/13 14:02:48.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/10/13 14:02:50.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/10/13 14:02:52.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/10/13 14:02:54.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/13 14:02:56.0 XRT_QT_PROG_SET_411_OG [0x19b]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2012/10/13 14:02:58.0 XRT_FL_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2012/10/13 14:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/13 14:46:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/13 14:46:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/13 14:46:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/10/13 14:49:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/10/13 14:58:30.0 XRT_Custom_434_OG [0x1b2]
2012/10/13 14:59:30.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/13 16:21:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/13 16:21:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/13 16:21:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/10/13 16:24:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/10/13 16:45:30.0 XRT_Custom_434_OG [0x1b2]
2012/10/13 16:46:30.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/13 17:58:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/13 17:58:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/13 17:58:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/10/13 18:01:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/10/13 18:21:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/13 18:21:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/10/13 18:22:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/10/13 18:22:16.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/10/13 18:22:18.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/10/13 18:22:20.0 XRT_ARS_DIS_406_OG [0x196]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/10/13 18:24:58.0 XRT_QT_PROG_SET_419_OG [0x1a3]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0d
2012/10/13 18:25:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/13 18:31:54.0 XRT_CTRL_MANU_447_OG [0x1bf]

```

2012/10/13	18:32:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	04 00 00 00 00
2012/10/13	18:34:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/10/13	18:34:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/10/13	18:34:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/10/13	18:34:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/10/13	18:34:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/13	18:34:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/13	18:34:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/10/13	18:34:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/10/13	18:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/13	19:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/13	19:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/13	19:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/13	19:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/13	19:59:00.5	XRT_Custom_434_OG [0x1b2]				
2012/10/13	20:00:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/13	21:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/13	21:13:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/13	21:13:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/13	21:16:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/13	21:36:00.0	XRT_Custom_434_OG [0x1b2]				
2012/10/13	21:37:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/13	22:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/13	22:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/13	22:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/13	22:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/13	23:10:30.0	XRT_Custom_434_OG [0x1b2]				
2012/10/13	23:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/14	00:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/14	00:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/14	00:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/14	00:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/14	00:35:00.0	XRT_Custom_434_OG [0x1b2]				
2012/10/14	00:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/14	01:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/14	01:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/14	01:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/14	01:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/14	02:10:30.0	XRT_Custom_434_OG [0x1b2]				
2012/10/14	02:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/14	03:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/14	03:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/14	03:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/14	03:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/14	03:47:30.5	XRT_Custom_434_OG [0x1b2]				
2012/10/14	03:48:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/14	04:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/14	04:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/14	04:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

2012/10/14	05:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/14	05:25:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/14	05:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/14	06:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/14	06:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/14	06:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/14	06:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/14	07:02:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/14	07:03:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/14	08:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/14	08:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/14	08:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/14	08:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/14	08:39:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/14	08:40:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/14	09:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/14	09:46:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/14	09:47:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/14	09:47:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/14	09:47:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/14	09:47:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/14	09:49:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/10/14	09:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/14	09:56:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/14	09:57:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/10/14	09:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/14	09:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/14	09:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/14	09:59:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/14	09:59:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/14	09:59:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/14	09:59:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/14	09:59:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/14	09:59:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/14	09:59:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/10/14	09:59:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/14	10:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/14	10:12:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/14	10:13:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/14	13:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/14	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 36 97 bf cf				
2012/10/14	14:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/14	14:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/14	14:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/14	14:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/14	14:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/14	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/14	14:02:56.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/10/14	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							

2012/10/14	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	15:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	15:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/14	15:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/14	15:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/14	15:36:00.0	XRT_Custom_434_OG [0x1b2]					
2012/10/14	15:37:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	15:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	15:37:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/14	15:37:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/14	15:40:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/14	15:45:00.0	XRT_Custom_434_OG [0x1b2]					
2012/10/14	15:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	16:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	16:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/14	16:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/14	17:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/14	17:22:00.0	XRT_Custom_434_OG [0x1b2]					
2012/10/14	17:23:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/10/14	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/10/14	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/10/14	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/10/14	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/14	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/10/14	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2012/10/14	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/10/14	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/10/14	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/10/14	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/10/14	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/14	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/14	18:12:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2012/10/14	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/10/14	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	18:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	18:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/14	18:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/14	18:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/14	18:58:30.0	XRT_Custom_434_OG [0x1b2]					
2012/10/14	18:59:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/14	20:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/14	20:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/14	20:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/14	20:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2012/10/14	20:36:00.0	XRT_Custom_434_OG [0x1b2]		
2012/10/14	20:37:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/14	21:50:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/14	21:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/14	21:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/14	21:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/14	22:12:00.0	XRT_Custom_434_OG [0x1b2]		
2012/10/14	22:13:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/14	23:27:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/14	23:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/14	23:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/14	23:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/14	23:39:30.0	XRT_Custom_434_OG [0x1b2]		
2012/10/14	23:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	00:55:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/15	00:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/15	00:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/15	00:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/15	01:10:30.0	XRT_Custom_434_OG [0x1b2]		
2012/10/15	01:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	02:26:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/15	02:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/15	02:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/15	02:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/15	02:47:00.0	XRT_Custom_434_OG [0x1b2]		
2012/10/15	02:48:00.5	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	03:55:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/15	03:55:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/15	03:55:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/15	03:58:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/15	04:24:30.0	XRT_Custom_434_OG [0x1b2]		
2012/10/15	04:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	05:35:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/15	05:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/15	05:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/15	05:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/15	06:02:00.5	XRT_Custom_434_OG [0x1b2]		
2012/10/15	06:03:00.5	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	07:16:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/15	07:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/15	07:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/15	07:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/15	07:39:30.0	XRT_Custom_434_OG [0x1b2]		
2012/10/15	07:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	08:55:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/10/15	08:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/10/15	08:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/10/15	08:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/10/15	09:15:30.0	XRT_Custom_434_OG [0x1b2]		
2012/10/15	09:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/10/15	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1

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2012/10/15	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/10/15	10:00:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2012/10/15	10:00:16.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2012/10/15	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/10/15	10:00:20.0	XRT_ARS_DIS_406_OG [0x196] MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/15	10:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/10/15	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	10:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	10:10:00.0	AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	04 00 00 00 00
2012/10/15	10:12:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8] XRT_FOCUS_RECAL	2	07-F8	78 00
2012/10/15	10:16:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/10/15	10:16:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2012/10/15	10:16:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/10/15	10:16:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2012/10/15	10:16:52.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/15	10:16:54.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	10:16:56.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/10/15	10:16:58.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/10/15	10:17:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	10:38:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	10:38:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	10:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	10:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	10:42:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/15	10:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	14:24:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	14:24:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	14:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	14:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	14:33:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/15	14:34:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	15:58:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	15:58:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	15:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	16:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	16:22:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/15	16:23:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	17:35:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	17:35:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	17:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	17:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	17:58:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/15	17:59:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/10/15	18:00:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2012/10/15	18:00:16.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2012/10/15	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/10/15	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]			

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2012/10/15	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/15	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/10/15	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	18:10:00.5	AOCS_OrE-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	04 00 00 00 00
2012/10/15	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/10/15	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/10/15	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/10/15	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/10/15	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/15	18:12:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/10/15	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/10/15	19:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	19:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	19:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	19:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	19:35:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	19:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/15	20:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	20:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	20:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	20:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	21:13:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	21:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/15	22:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/15	22:27:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/15	22:27:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/15	22:30:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/15	22:48:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/15	22:49:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/16	00:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/16	00:04:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/16	00:04:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/16	00:07:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/16	00:12:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/16	00:13:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/16	01:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/16	01:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/16	01:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/16	01:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/16	01:47:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/16	01:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/16	03:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/16	03:03:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/16	03:03:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/16	03:06:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/16	03:24:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/16	03:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2012/10/16	04:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/16	04:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/16	04:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/16	04:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/16	05:01:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/16	05:02:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/16	05:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/16	05:45:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/16	05:46:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/16	05:46:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/16	05:46:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/16	05:46:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/16	05:48:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/10/16	05:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/16	05:55:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/16	05:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/10/16	05:58:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/16	05:58:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/16	05:58:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/16	05:58:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/16	05:58:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/16	05:58:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/16	05:58:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/10/16	05:58:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/10/16	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/16	06:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/16	06:13:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/16	06:13:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/16	06:16:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/16	06:39:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/16	06:40:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/16	07:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/16	07:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/16	07:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/16	07:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/16	08:16:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/16	08:17:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/16	09:35:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/16	09:36:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				