

XRT Timeline to be uploaded on 2012/10/26

Period: 2012/10/26 09:38:00 - 2012/11/01 09:31:00

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Normal mode

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XOB #193A: HOP 186 (short exp) Al/mesh (64/2048ms) + Synoptic Q95 2x2 - Al/poly (64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512)

Term	Pointing (x, y)	Comment
10/26 09:51:00 - 10/26 09:57:54	Fixed (0.0, 0.0)	# OP start + 10min, synoptic, shifted manually, with HOP 186.
10/27 10:08:00 - 10/27 10:14:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
10/28 10:03:00 - 10/28 10:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
10/29 10:03:00 - 10/29 10:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
10/30 09:58:00 - 10/30 10:04:54	Fixed (0.0, 0.0)	synoptic, shifted manually, with HOP 186.
PROG= 01 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 84 1-time(s) 4.0sec		
└─ Al-poly/Open Al-poly/thick-Al close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Seqn= 70 1-time(s) 4.0sec		
└─ Open/Ti-poly Open/Ti-poly close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 69 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 2 1-time(s) 6.0sec		
└─ Seqn= 66 1-time(s) 2.0sec		
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Dark 2.00s Obs 1x1 2048x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Subr= 3 1-time(s) 2.0sec		
└─ Seqn= 68 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024)	DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
10/26 10:01:00 - 10/26 17:51:00	Track (-221.0, -299.4) ^{Ⓜ 10/26 09:58:00}	* Track AR 11598, with HOP 203, 14:15 - 18:15 UT.
10/27 10:18:00 - 10/27 17:59:54	Track (-2.8, -300.2) ^{Ⓜ 10/27 10:15:00}	# Cont., with HOP 203, 14:15 - 18:15 UT.
10/28 10:13:00 - 10/28 17:52:54	Track (212.5, -296.7) ^{Ⓜ 10/28 10:10:00}	# Cont., with HOP 203, 14:15 - 18:15 UT.
10/29 10:17:00 - 10/29 17:40:54	Track (417.2, -289.3) ^{Ⓜ 10/29 10:10:00}	# Cont., with HOP 203, 14:15 - 18:15 UT.
PROG= 03 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 18 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Open/G-band Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band close	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
└─ Seqn= 65 4-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Open/thick-Al Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Al-poly/Open Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ C-poly/Open C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ med-Be/Open med-Be/Open close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ med-Al/Open med-Al/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 73 70-time(s) 120.0sec		
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 12.5sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 12.5sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 12.5sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048)	Q=95 3 3 12.5sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)

Term	Pointing (x, y)	Comment
10/26 18:17:30 - 10/26 18:24:24	Fixed (0.0, 0.0)	synoptic, shifted 14.5 min
10/27 18:03:00 - 10/27 18:09:54	Fixed (0.0, 0.0)	synoptic
10/28 17:56:00 - 10/28 18:02:54	Fixed (0.0, 0.0)	synoptic, shifted -7.0 min
10/29 17:44:00 - 10/29 17:50:54	Fixed (0.0, 0.0)	synoptic, shifted -19.0 min
PROG= 13 1-time(s)		
└─ Subr= 1 1-time(s) 14.0sec		
└─ Seqn= 64 1-time(s) 4.0sec		
└─ Open/Al-mesh Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec

Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 70 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 68 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #193D: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
10/26 18:27:30 - 10/27 09:51:00	Track (-145.9, -300.2) @ 10/26 18:24:30	# Cont.
10/27 18:13:00 - 10/28 09:59:54	Track (69.0, -299.5) @ 10/27 18:10:00	# Cont.
10/28 18:06:00 - 10/29 09:59:54	Track (281.5, -294.7) @ 10/28 18:03:00	# Cont.
10/29 17:54:00 - 10/30 09:54:54	Track (478.5, -286.2) @ 10/29 17:51:00	# Cont.

PROG= 19 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 19 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 93 30-time(s) 120.0sec												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #193E: Flare Standard Obs. with eruptions mode-A (FW1=Open) without thinner filters

Term	Pointing (x, y)	Comment
10/26 10:01:00 - 10/26 17:51:00	Track (-221.0, -299.4) @ 10/26 09:58:00	* Track AR 11598, with HOP 203, 14:15 - 18:15 UT.
10/26 18:27:30 - 10/27 09:51:00	Track (-145.9, -300.2) @ 10/26 18:24:30	# Cont.
10/27 10:18:00 - 10/27 17:59:54	Track (-2.8, -300.2) @ 10/27 10:15:00	# Cont., with HOP 203, 14:15 - 18:15 UT.
10/27 18:13:00 - 10/28 09:59:54	Track (69.0, -299.5) @ 10/27 18:10:00	# Cont.
10/28 10:13:00 - 10/28 17:52:54	Track (212.5, -296.7) @ 10/28 10:10:00	# Cont., with HOP 203, 14:15 - 18:15 UT.
10/28 18:06:00 - 10/29 09:59:54	Track (281.5, -294.7) @ 10/28 18:03:00	# Cont.
10/29 10:17:00 - 10/29 17:40:54	Track (417.2, -289.3) @ 10/29 10:10:00	# Cont., with HOP 203, 14:15 - 18:15 UT.
10/29 17:54:00 - 10/30 09:54:54	Track (478.5, -286.2) @ 10/29 17:51:00	# Cont.

PROG= 20 1-time(s)

Subr= 1 30-time(s) 20.0sec												
Seqn= 94 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 35 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 95 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 94 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 35 1-time(s) 2.0sec												

thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2			1-time(s)		2.0sec							
Seqn= 95			1-time(s)		2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3			30-time(s)		60.0sec							
Seqn= 94			1-time(s)		2.0sec							
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 35			1-time(s)		2.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2			1-time(s)		2.0sec							
Seqn= 95			1-time(s)		2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3			30-time(s)		60.0sec							
Seqn= 94			1-time(s)		2.0sec							
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 35			1-time(s)		2.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

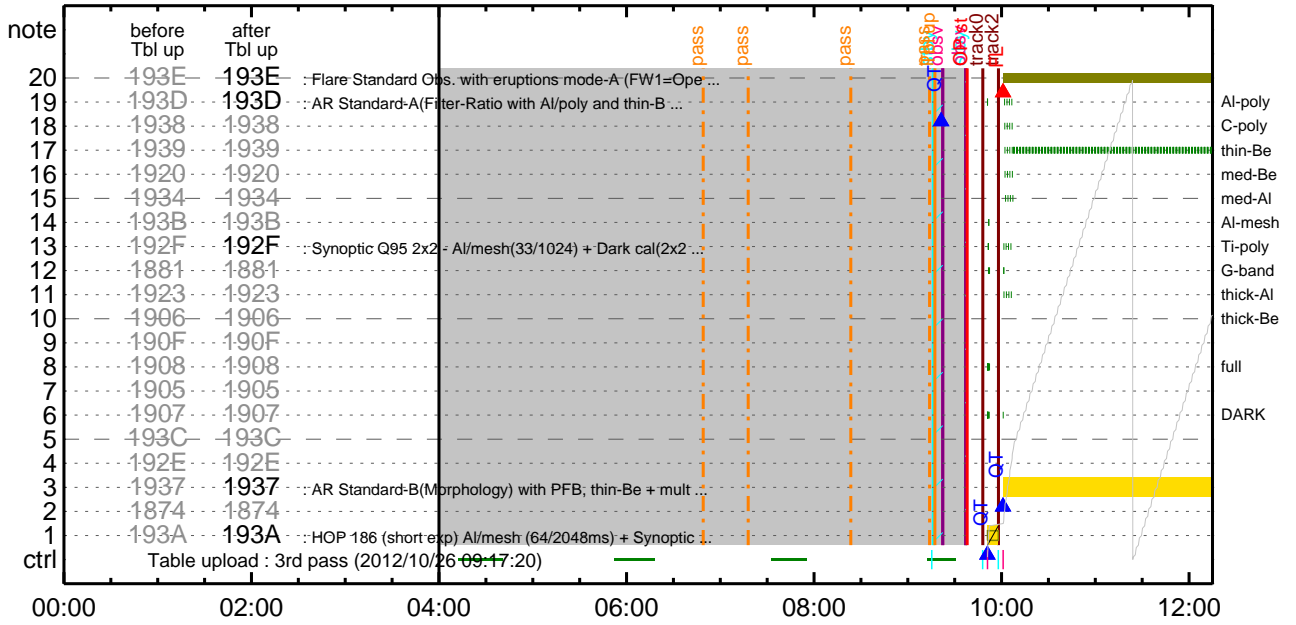
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Flare Detection

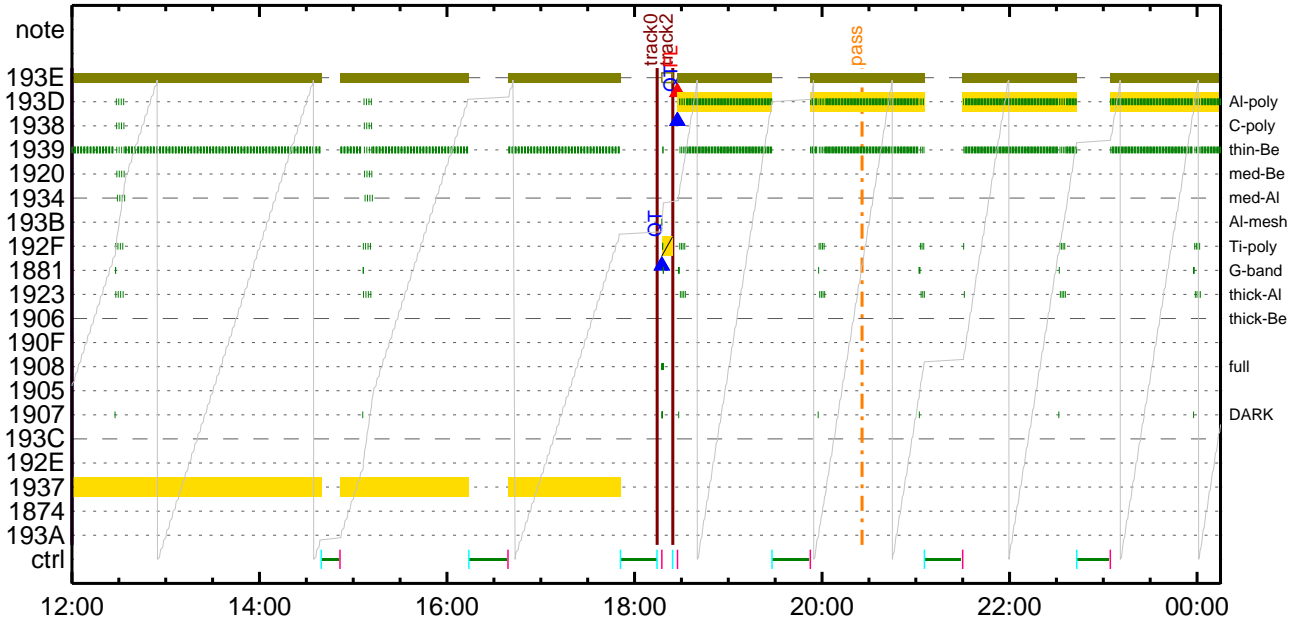
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FLD Patrol												
Term	Pointing (x, y)							Comment				
10/26 10:00:46 - 10/26 18:14:46	Track (-221.0, -299.4) @ 10/26 09:58:00							* Track AR 11598, with HOP 203, 14:15 - 18:15 UT.				
10/26 18:27:16 - 10/27 10:05:16	Track (-145.9, -300.2) @ 10/26 18:24:30							# Cont.				
10/27 10:17:46 - 10/27 18:00:16	Track (-2.8, -300.2) @ 10/27 10:15:00							# Cont., with HOP 203, 14:15 - 18:15 UT.				
10/27 18:12:46 - 10/28 10:00:16	Track (69.0, -299.5) @ 10/27 18:10:00							# Cont.				
10/28 10:12:46 - 10/28 17:53:16	Track (212.5, -296.7) @ 10/28 10:10:00							# Cont., with HOP 203, 14:15 - 18:15 UT.				
10/28 18:05:46 - 10/29 10:00:16	Track (281.5, -294.7) @ 10/28 18:03:00							# Cont.				
10/29 10:16:46 - 10/29 17:41:16	Track (417.2, -289.3) @ 10/29 10:10:00							# Cont., with HOP 203, 14:15 - 18:15 UT.				
10/29 17:53:46 - 10/30 09:55:16	Track (478.5, -286.2) @ 10/29 17:51:00							# Cont.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

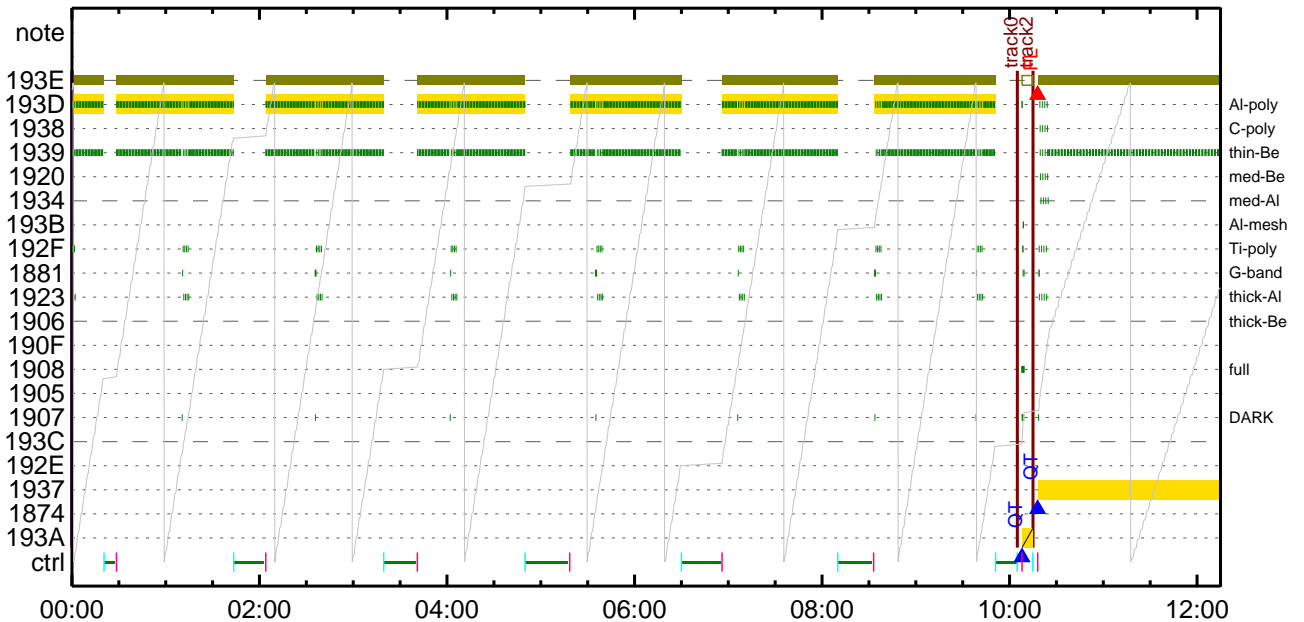
CMDI #0008 2012/10/26



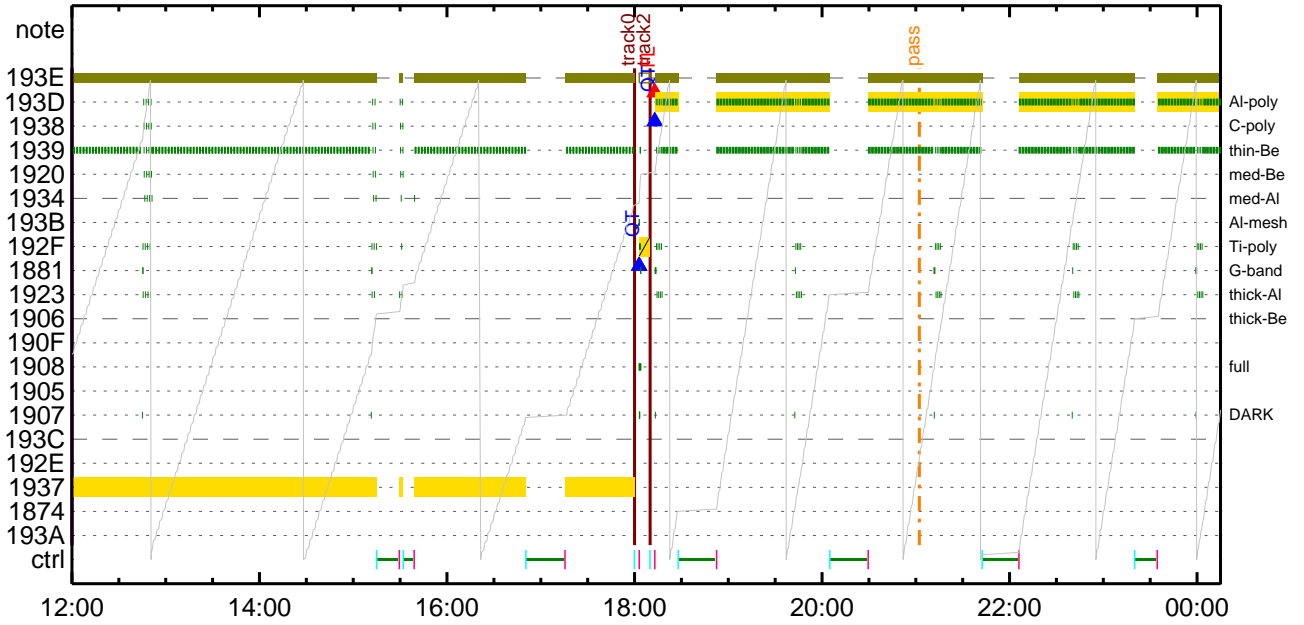
CMDI #0008 2012/10/26



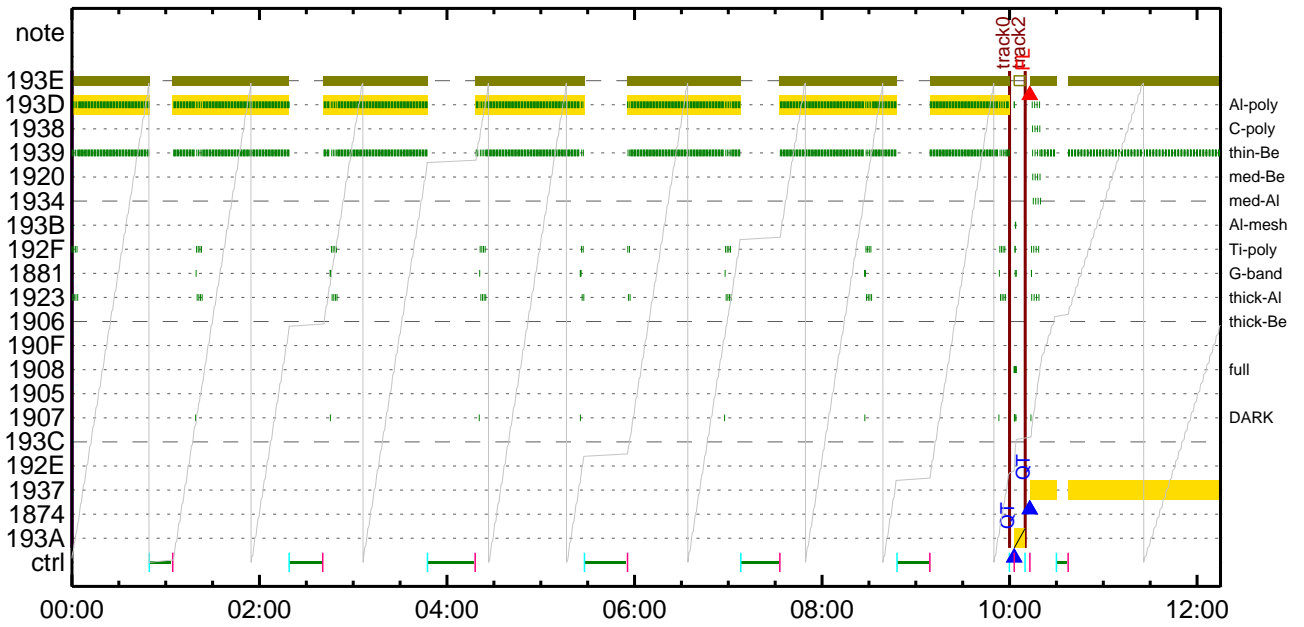
CMDI #0008 2012/10/27



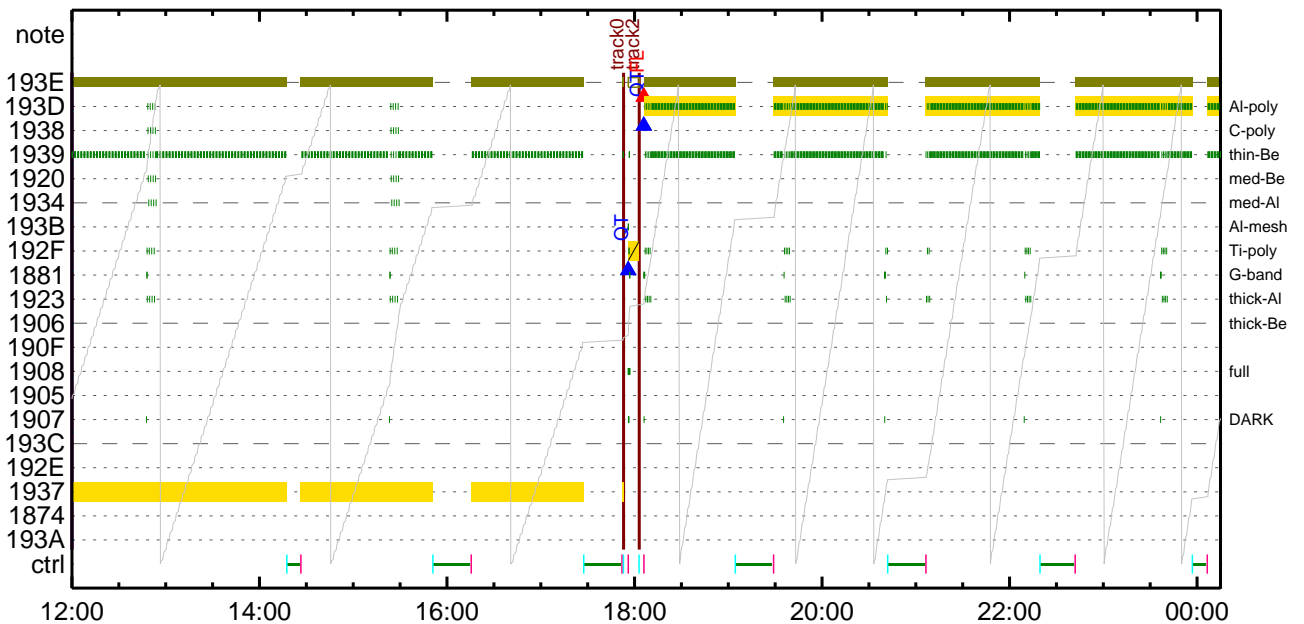
CMDI #0008 2012/10/27



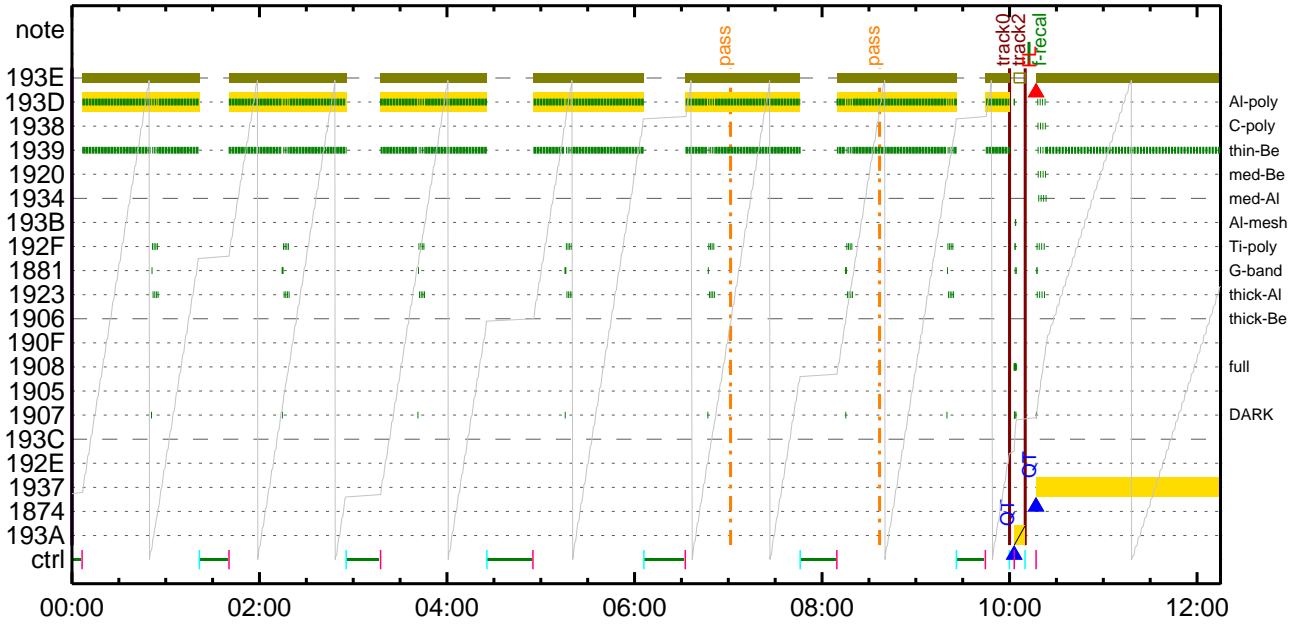
CMDI #0008 2012/10/28



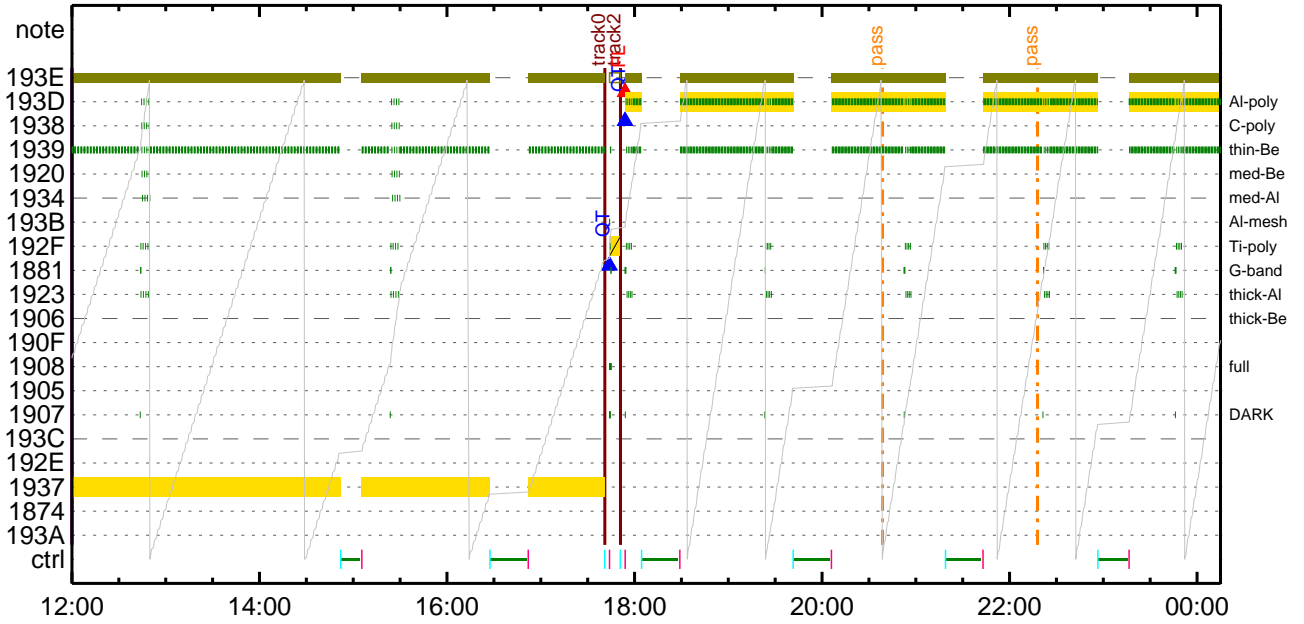
CMDI #0008 2012/10/28



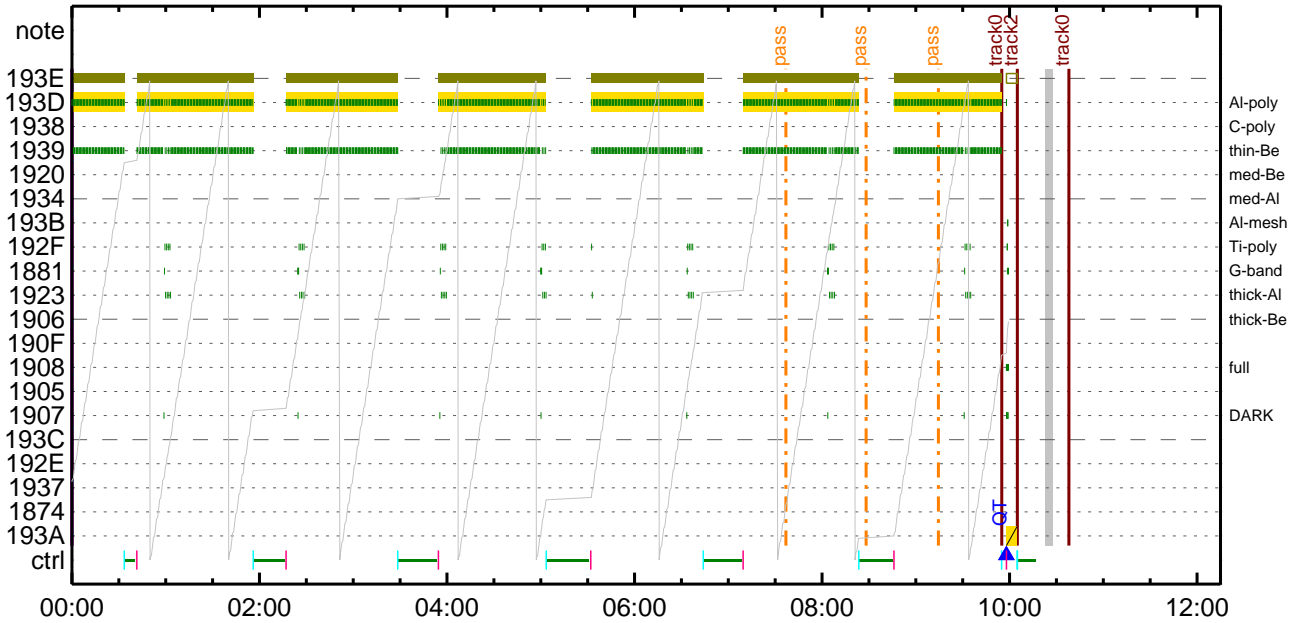
CMDI #0008 2012/10/29



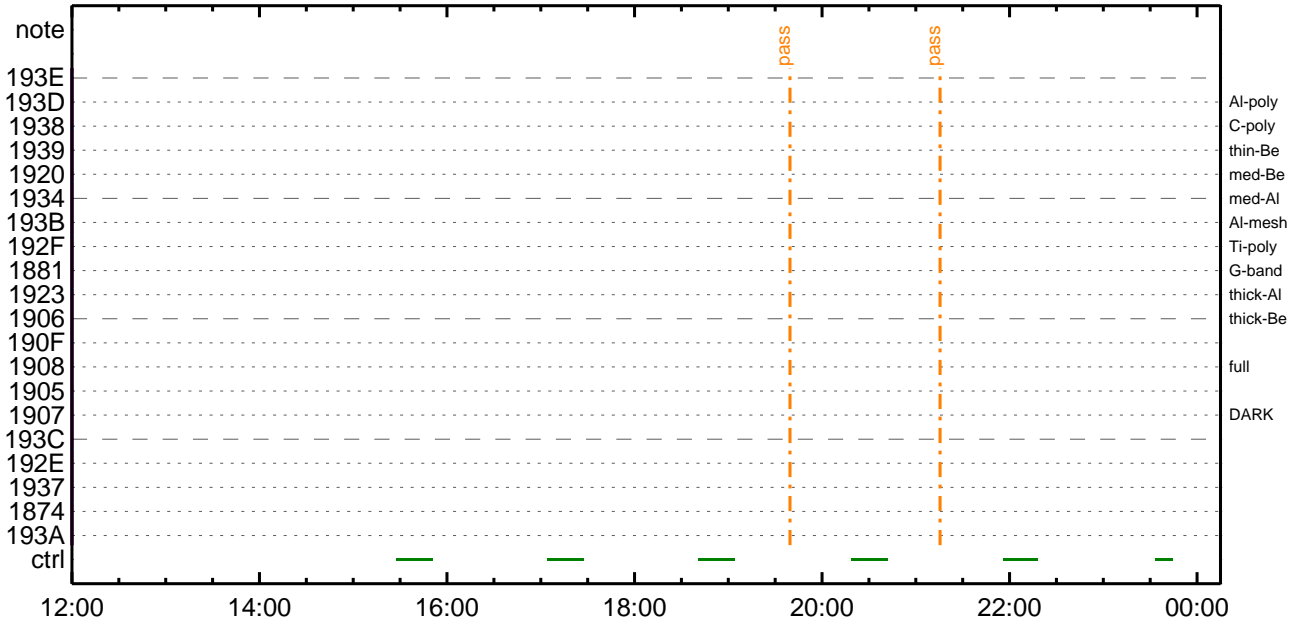
CMDI #0008 2012/10/29



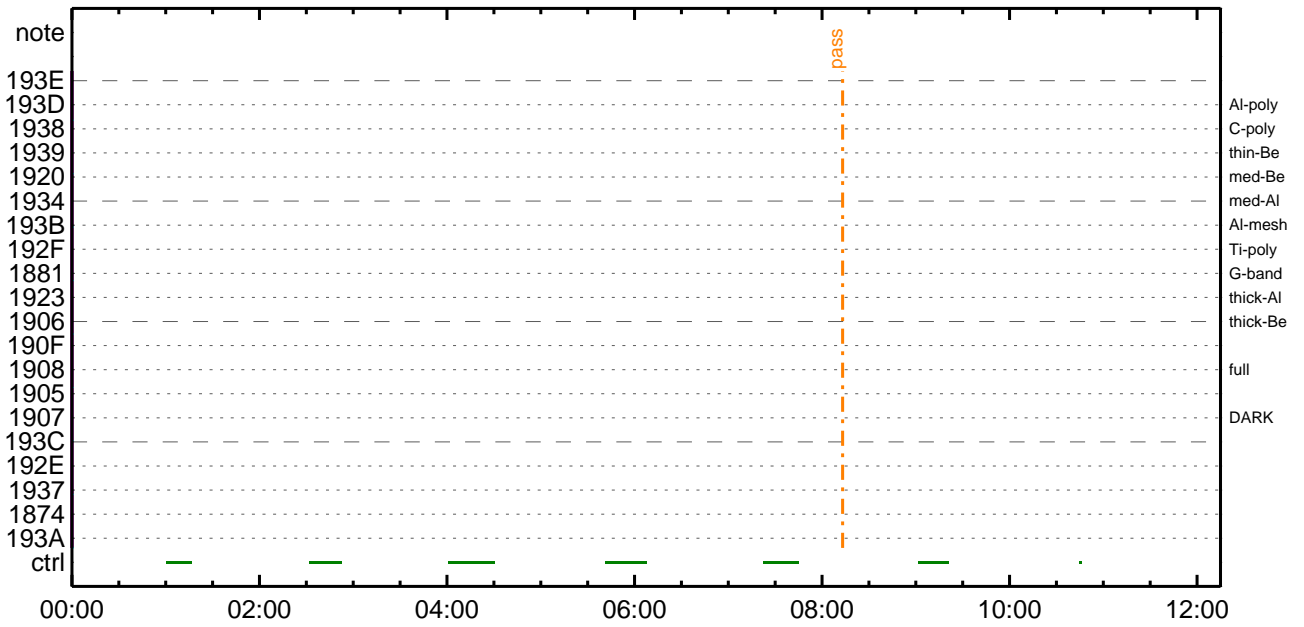
CMDI #0008 2012/10/30



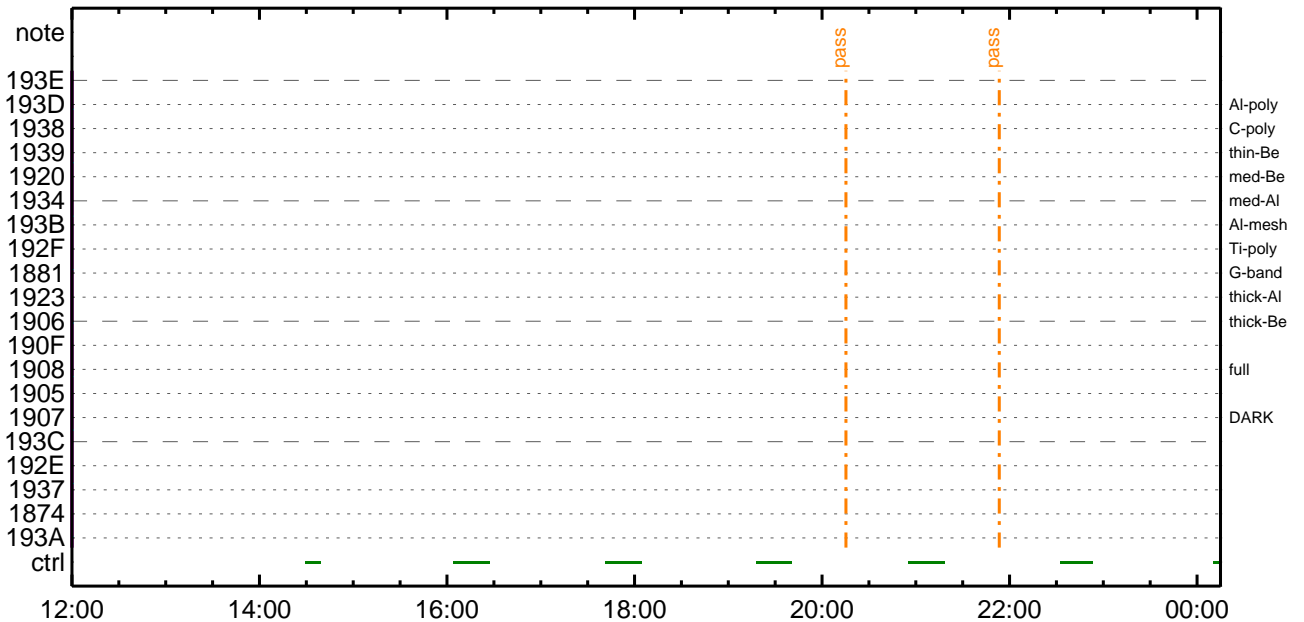
CMDI #0008 2012/10/30



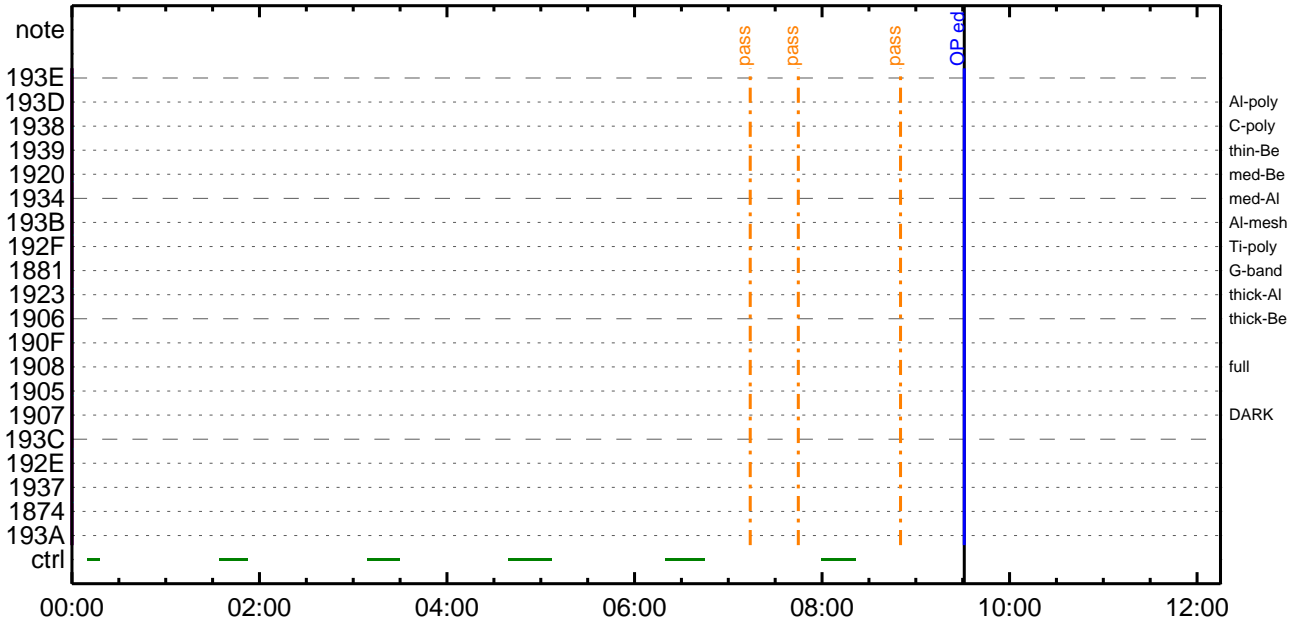
CMDI #0008 2012/10/31



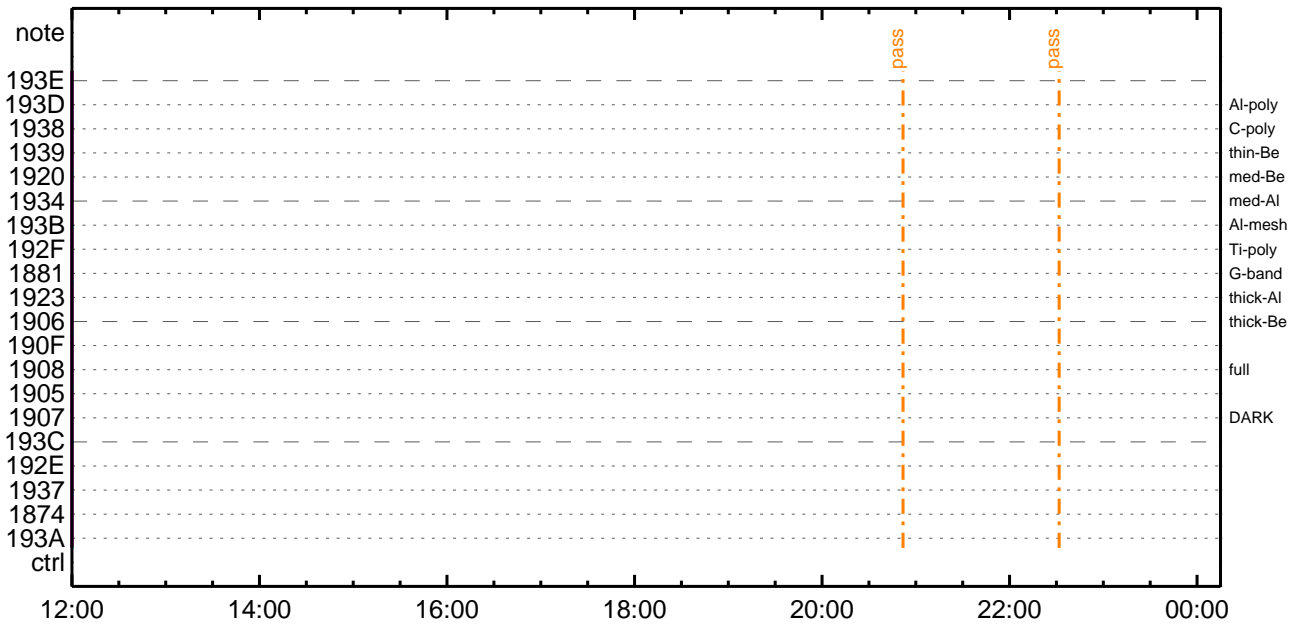
CMDI #0008 2012/10/31



CMDI #0008 2012/11/01



CMDI #0008 2012/11/01




```

0096 C.                0p0z;çSET0EDUMP0İÆ±°iŸÑŸ¹0Ç¹00|0³0E;f
0097 C.
0098 . C. TIŸ³ŸPŸ6ŸE00d0Dİç(UT)
0099 +. TI 2012-10-26 09:33:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2012-10-26 09:33:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2012-10-26 09:33:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2012-10-26 09:37:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.  °E²¼0İÄè%îÍÑ0İŸÁŸŸŸÄŸ-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èŸÄŸ6Ÿx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. ŸÄŸ6Ÿx¼ªİ»0d³İÇŸ
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İŸE¹ç•è²İOK0d³İÇŸ
0145 C.
0146 . C. DHUŸâ;¼ŸÉ;È¼Ÿ¼,Ÿi;¼ŸÈ;È0dİâ0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2012-10-26 09:37:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2012-10-26 09:37:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2012-10-26 09:37:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄİ0İ»ö¼Ÿ0ÈÄ00¹0èDCBC•x²è *****
0181 C. (¼ª°İŸÖŸÄŸEŸPŸEŸŸŸÄŸçŸè0È¼00¼Ä»Ü0¹0è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** ŸDŸ¹•İ Daily±çİÑ0È¹00¹0èDCBC•x²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOSŸÄŸŸŸÄŸ-¼Ä»Ü;ã
0192 C.
0193 . C. ***** LOS *****

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-233 2012-10-26 14:52:13 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÒø•ø¿¼í¹çøÍ;çÀ®, ùø¹øèøÈøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_FLD_DIS
0055 BC (d9)
0056 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0057 BC (c9)
0058 + DC 07-F0 MDP_XRT_AEC_RESET
0059 BC (d0)
0060 + DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 + DC 07-F0 MDP_XRT_FLD_RESET
0063 BC (da)
0064 + DC 07-F0 MDP_XRT_QT_PROG_SET
0065 BC (c4 13)
0066 . C. ----- Success Verify ? OK / NG ____
0067 C.
0068 C.
0069 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0070 C.
0071 +. DC 07-F0 MDP_XRT_MODE_OBSV
0072 BC (c2)
0073 +. TI 2012-10-26 09:37:02.0
0074 DC 07-F0 MDP_XRT_MODE_OBSV
0075 BC (c2)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C. ***** XRT END *****
0079 C.
0080 . C. ***** MDP `ûÁíøí»ò¼YøÈÁøø¹øÈDCBC•×²è *****
0081 C. (¼á°íYÓYÁYÈYB;YÉYáYçYèøÈ¼øø¼Á»Ûø¹øè)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YDÿ!•İ Daily±¿ÍÑøÈ´Øø¹øèDCBC•×²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0092 C.
0093 . C. ***** LOS *****
0094 C.
```

*** OP Sequence for XRT ***

```

2012/10/26 09:47:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 09:47:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/10/26 09:48:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/10/26 09:48:16.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/10/26 09:48:18.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/10/26 09:48:20.0 XRT_ARS_DIS_406_OG [0x196]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/10/26 09:50:58.0 XRT_QT_PROG_SET_417_OG [0x1a1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2012/10/26 09:51:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/26 09:57:54.0 XRT_CTRL_MANU_447_OG [0x1bf]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 09:58:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 02 00 00 00 00
2012/10/26 10:00:26.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/10/26 10:00:46.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/10/26 10:00:48.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/10/26 10:00:50.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/10/26 10:00:52.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/10/26 10:00:54.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/26 10:00:56.0 XRT_QT_PROG_SET_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 03
2012/10/26 10:00:58.0 XRT_FL_PROG_SET_401_OG [0x191]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 14
2012/10/26 10:01:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/26 14:39:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 14:39:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/26 14:39:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/10/26 14:42:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/10/26 14:50:30.0 XRT_Custom_434_OG [0x1b2]
2012/10/26 14:51:30.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/26 16:14:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 16:14:02.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/26 16:14:04.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/10/26 16:17:14.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/10/26 16:38:00.0 XRT_Custom_434_OG [0x1b2]
2012/10/26 16:39:00.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/26 17:51:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 17:51:02.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/10/26 17:51:04.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/10/26 17:54:14.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/10/26 18:14:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 18:14:26.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/10/26 18:14:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/10/26 18:14:46.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/10/26 18:14:48.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/10/26 18:14:50.0 XRT_ARS_DIS_406_OG [0x196]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/10/26 18:17:28.0 XRT_QT_PROG_SET_419_OG [0x1a3]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0d
2012/10/26 18:17:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/10/26 18:24:24.0 XRT_CTRL_MANU_447_OG [0x1bf]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/10/26 18:24:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 02 00 00 00 00
2012/10/26 18:26:56.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/10/26 18:27:16.0 XRT_FLD_ENA_428_OG [0x1ac]

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2012/10/26	18:27:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/10/26	18:27:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/10/26	18:27:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/10/26	18:27:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/10/26	18:27:26.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/26	18:27:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/10/26	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 14
2012/10/26	19:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/26	19:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/26	19:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/26	19:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/26	19:51:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/26	19:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/26	21:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/26	21:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/26	21:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/26	21:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/26	21:29:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/26	21:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2012/10/26	22:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/26	22:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/26	22:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/26	22:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/26	22:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/26	23:03:30.0	XRT_Custom_434_OG [0x1b2]	XRT_Custom_434_OG [0x1b2]			
2012/10/26	23:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/27	00:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/27	00:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/27	00:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/27	00:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/27	00:27:30.0	XRT_Custom_434_OG [0x1b2]	XRT_Custom_434_OG [0x1b2]			
2012/10/27	00:28:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/27	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/27	01:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/27	01:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/27	01:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/27	02:03:00.0	XRT_Custom_434_OG [0x1b2]	XRT_Custom_434_OG [0x1b2]			
2012/10/27	02:04:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/27	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/27	03:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/27	03:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/27	03:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/27	03:40:00.5	XRT_Custom_434_OG [0x1b2]	XRT_Custom_434_OG [0x1b2]			
2012/10/27	03:41:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/27	04:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/27	04:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/27	04:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/27	04:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/27	05:17:30.0	XRT_Custom_434_OG [0x1b2]	XRT_Custom_434_OG [0x1b2]			
2012/10/27	05:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/27	06:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2012/10/27	06:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	06:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/27	06:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/27	06:55:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/27	06:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/27	08:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	08:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/27	08:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/27	08:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/27	08:32:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/27	08:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	09:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	09:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/27	09:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/27	09:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/27	10:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	10:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/10/27	10:05:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/10/27	10:05:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/10/27	10:05:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/10/27	10:05:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/27	10:07:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2012/10/27	10:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/27	10:14:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	10:15:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00	
2012/10/27	10:17:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/10/27	10:17:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/10/27	10:17:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/10/27	10:17:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/10/27	10:17:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/10/27	10:17:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/27	10:17:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2012/10/27	10:17:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 14	
2012/10/27	10:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/27	15:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	15:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/27	15:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/27	15:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/27	15:28:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/27	15:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	15:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	15:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/10/27	15:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/10/27	15:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/10/27	15:38:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/10/27	15:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	16:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/10/27	16:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2012/10/27	16:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/27	16:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/27	17:14:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/27	17:15:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/27	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/27	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/27	18:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/27	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/27	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/27	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/27	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/10/27	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/27	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/27	18:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/10/27	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/27	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/27	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/27	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/27	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/27	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/27	18:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/10/27	18:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14				
2012/10/27	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/27	18:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/27	18:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/27	18:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/27	18:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/27	18:51:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/27	18:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/27	20:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/27	20:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/27	20:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/27	20:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/27	20:28:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/27	20:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/27	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/27	21:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/27	21:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/27	21:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/27	22:05:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/27	22:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/27	23:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/27	23:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/27	23:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/27	23:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/27	23:33:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/27	23:34:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	00:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	00:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2012/10/28	00:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/28	00:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/28	01:03:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/28	01:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	02:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	02:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/28	02:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/28	02:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/28	02:39:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/28	02:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	03:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	03:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/28	03:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/28	03:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/28	04:17:00.5	XRT_Custom_434_OG [0x1b2]							
2012/10/28	04:18:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	05:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	05:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/28	05:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/28	05:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/28	05:54:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/28	05:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	07:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	07:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/28	07:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/28	07:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/28	07:32:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/28	07:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	08:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	08:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/28	08:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/28	08:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/28	09:08:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/28	09:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/28	10:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/28	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/28	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/28	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/28	10:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/10/28	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/28	10:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/28	10:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/10/28	10:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/10/28	10:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/28	10:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/28	10:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/28	10:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/28	10:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							

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2012/10/28	10:12:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2012/10/28	10:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	14	
2012/10/28	10:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	10:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	10:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/28	10:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/28	10:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/28	10:36:30.0	XRT_Custom_434_OG [0x1b2]						
2012/10/28	10:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	14:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	14:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/28	14:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/28	14:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/28	14:25:30.0	XRT_Custom_434_OG [0x1b2]						
2012/10/28	14:26:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	15:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	15:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/28	15:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/28	15:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/28	16:14:30.0	XRT_Custom_434_OG [0x1b2]						
2012/10/28	16:15:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	17:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	17:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/28	17:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/28	17:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/28	17:51:00.0	XRT_Custom_434_OG [0x1b2]						
2012/10/28	17:52:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	17:52:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2012/10/28	17:53:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00 00 00 00	
2012/10/28	17:53:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/10/28	17:53:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/10/28	17:53:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/28	17:55:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2012/10/28	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	18:02:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	18:03:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00 00 00 00	
2012/10/28	18:05:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2012/10/28	18:05:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/10/28	18:05:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/10/28	18:05:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/10/28	18:05:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/28	18:05:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/28	18:05:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13	
2012/10/28	18:05:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	14	
2012/10/28	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/28	19:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/28	19:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		

2012/10/28	19:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/28	19:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/28	19:28:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/28	19:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/28	20:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/28	20:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/28	20:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/28	20:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/28	21:05:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/28	21:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/28	22:19:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/28	22:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/28	22:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/28	22:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/28	22:41:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/28	22:42:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/28	23:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/28	23:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/28	23:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/29	00:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/29	00:05:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/29	00:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/29	01:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/29	01:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/29	01:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/29	01:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/29	01:39:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/29	01:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/29	02:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/29	02:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/29	02:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/29	02:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/29	03:16:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/29	03:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/29	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/29	04:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/29	04:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/29	04:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/29	04:54:00.0	XRT_Custom_434_OG [0x1b2]			
2012/10/29	04:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/29	06:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/29	06:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/29	06:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/29	06:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/29	06:31:30.0	XRT_Custom_434_OG [0x1b2]			
2012/10/29	06:32:30.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/10/29	07:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/10/29	07:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/10/29	07:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/10/29	07:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/10/29	08:08:30.0	XRT_Custom_434_OG [0x1b2]			

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2012/10/29	08:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	09:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/29	09:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/29	09:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/29	09:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/29	09:43:30.0	XRT_Custom_434_OG [0x1b2]						
2012/10/29	09:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/29	09:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/10/29	10:00:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2012/10/29	10:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/10/29	10:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/10/29	10:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/29	10:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2012/10/29	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	10:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/29	10:10:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00		
2012/10/29	10:12:26.0	XRT_FOCUS_RECALIBRATE_440_OG [0x1b8]	XRT_FOCUS_RECAL	2	07-F8	78 00		
2012/10/29	10:16:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/10/29	10:16:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/10/29	10:16:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/10/29	10:16:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/10/29	10:16:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/29	10:16:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/29	10:16:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2012/10/29	10:16:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 14		
2012/10/29	10:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	14:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/29	14:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/29	14:52:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/29	14:55:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/29	15:04:30.0	XRT_Custom_434_OG [0x1b2]						
2012/10/29	15:05:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	16:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/29	16:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/10/29	16:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/10/29	16:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/10/29	16:51:00.0	XRT_Custom_434_OG [0x1b2]						
2012/10/29	16:52:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/10/29	17:40:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/10/29	17:41:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2012/10/29	17:41:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/10/29	17:41:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/10/29	17:41:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/10/29	17:43:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2012/10/29	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/10/29	17:50:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2012/10/29	17:51:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	02	00	00	00	00
2012/10/29	17:53:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/10/29	17:53:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/10/29	17:53:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/10/29	17:53:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/10/29	17:53:52.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/29	17:53:54.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/29	17:53:56.0	XRT_QT_PROG_SET_410_OG [0x19a] MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/10/29	17:53:58.0	XRT_FL_PROG_SET_401_OG [0x191] MDP_XRT_FL_PROG_SET	2	07-F0	c5	14			
2012/10/29	17:54:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/29	18:04:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/29	18:04:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/29	18:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/29	18:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/29	18:28:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/29	18:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/29	19:41:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/29	19:41:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/29	19:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/29	19:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/29	20:05:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/29	20:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/29	21:19:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/29	21:19:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/29	21:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/29	21:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/29	21:42:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/29	21:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/29	22:56:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/29	22:56:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/29	22:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/29	22:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/29	23:15:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/29	23:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	00:33:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	00:33:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	00:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	00:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/30	00:40:30.0	XRT_Custom_434_OG [0x1b2]							
2012/10/30	00:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	01:56:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	01:56:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	01:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	01:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/30	02:16:00.0	XRT_Custom_434_OG [0x1b2]							
2012/10/30	02:17:00.5	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	03:28:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	03:28:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	03:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	03:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							

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2012/10/30	03:53:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/30	03:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/10/30	05:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	05:03:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	05:03:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	05:06:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	05:31:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/30	05:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/10/30	06:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	06:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	06:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	06:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	07:08:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/30	07:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/10/30	08:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	08:23:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	08:23:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	08:26:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	08:45:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/10/30	08:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/10/30	09:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	09:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	09:55:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/10/30	09:55:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/10/30	09:55:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/10/30	09:55:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/10/30	09:57:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/10/30	09:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/10/30	10:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/10/30	10:05:00.5	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	10:05:01.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	02 00 00 00 00				
2012/10/30	10:05:03.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/10/30	10:05:05.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/10/30	10:08:15.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/10/30	10:38:00.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			AOCU_NM	5	02-76	00 00 00 00 00				