

# XRT Timeline to be uploaded on 2012/11/03

Period: 2012/11/03 09:41:00 - 2012/11/08 09:30:00

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## Normal mode

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<b>XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms</b>													
Term	Pointing (x, y)							Comment					
11/03 09:54:00 - 11/03 14:42:00	Fixed ( -14.0, 921.0)							# OP start + 10min HOP 81					
11/04 10:10:30 - 11/04 15:01:54	Fixed ( -14.0, -939.0)							HOP 81 day-2 south pole					
<b>PROG= 11 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 61 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs 1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 62 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
└─ Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 60 2-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

<b>XOB #1945: HOP203 - Ti/Poly - FOV256 - Q95 - 1min cadence - AR - 512FOV Context -AEC1</b>													
Term	Pointing (x, y)							Comment					
11/03 14:54:30 - 11/03 17:54:00	Track ( -91.4, -404.6) <sup>Ⓜ 11/03 14:45:00</sup>							HOP 203					
11/04 15:05:00 - 11/04 17:59:54	Track ( 117.7, -402.8) <sup>Ⓜ 11/04 15:02:00</sup>							HOP203					
11/05 14:18:00 - 11/05 17:55:54	Track ( 311.9, -398.0) <sup>Ⓜ 11/05 14:15:00</sup>							HOP203					
<b>PROG= 17 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 9 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs 1x1	512x512 (1064, 1048)	Q=95	1	0	2.0sec	
└─ Subr= 2 5-time(s) 2.0sec													
└─ Seqn= 55 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	2.00s	Obs 1x1	256x256 (1064, 1048)	Q=95	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	256x256 (1064, 1048)	Q=90	0	0	2.0sec	
└─ Seqn= 10 30-time(s) 60.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs 1x1	256x256 (1064, 1048)	Q=95	1	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

<b>XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)</b>													
Term	Pointing (x, y)							Comment					
11/03 18:20:30 - 11/03 18:27:24	Fixed ( 0.0, 0.0)							synoptic, shifted 17.5 min					
11/04 06:06:30 - 11/04 06:13:24	Fixed ( 0.0, 0.0)							synoptic, shifted 3.5 min					
11/04 18:03:00 - 11/04 18:09:54	Fixed ( 0.0, 0.0)							synoptic					
11/05 06:10:00 - 11/05 06:16:54	Fixed ( 0.0, 0.0)							synoptic, shifted 7.0 min					
11/05 17:59:00 - 11/05 18:05:54	Fixed ( 0.0, 0.0)							synoptic, shifted -4.0 min					
11/06 05:44:00 - 11/06 05:50:54	Fixed ( 0.0, 0.0)							synoptic, shifted -19.0 min					
<b>PROG= 13 1-time(s)</b>													
└─ Subr= 1 1-time(s) 14.0sec													
└─ Seqn= 64 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 70 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 67 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 69 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

<b>XOB #1942: HOP221 FOXSI - multi-filter FW1 / FW2 - 4x4 - in flight use</b>													
Term	Pointing (x, y)							Comment					
11/03 18:27:56 - 11/03 19:59:54	Track ( 723.4, -383.9) <sup>Ⓜ 11/03 18:27:30</sup>							High-Temperature region obs.					
<b>PROG= 09 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 300.0sec													

Seqn= 20		2-time(s)	40.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 29		2-time(s)	25.0sec												
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 41		1-time(s)	60.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Dark	16.0s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	32.0s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 13		1-time(s)	15.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	44ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	707ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 23		1-time(s)	15.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Subr= 2		2-time(s)	420.0sec												
Seqn= 5		1-time(s)	15.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	707ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 39		1-time(s)	15.0sec												
C-poly/Ti-poly	C-poly/thick-Al	close	Safe	Norm	63ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
C-poly/Ti-poly	C-poly/thick-Al	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 96		1-time(s)	15.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	24ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
C-poly/Open	C-poly/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 30		1-time(s)	10.0sec												
C-poly/Open	C-poly/Open	close	Safe	Dark	12ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
C-poly/Open	C-poly/Open	close	Safe	Dark	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 97		1-time(s)	20.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 43		1-time(s)	30.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 44		1-time(s)	30.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
med-Al/Open	med-Al/Open	close	Safe	Norm	8.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Seqn= 28		1-time(s)	10.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval		

**XOB #193C: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar**

Term	Pointing (x, y)	Comment
11/03 20:03:00 - 11/04 06:03:24	Track ( -46.2, -404.5) @ 11/03 20:00:00	AR11602 tracking
11/04 18:13:00 - 11/05 06:06:54	Track ( 144.5, -402.3) @ 11/04 18:10:00	AR 11602 tracking

**PROG= 05 Inf.-time(s)**

Subr= 1		1-time(s)	2.0sec												
Seqn= 19		2-time(s)	2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec		
Subr= 2		2-time(s)	2.0sec												
Seqn= 92		1-time(s)	2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec		
Seqn= 32		4-time(s)	2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec		
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec		
Seqn= 93		30-time(s)	60.0sec												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	15.0sec		
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	15.0sec		
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	15.0sec		
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	15.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval		

**XOB #193B: AR Standard-B(Morphology) with PFB, thin-Be + multfilter context, 384x384 at 1064 1048, 180s-cad w/ G-Band VLS Closed Test**

Term	Pointing (x, y)	Comment
11/04 06:16:30 - 11/04 09:53:30	Track ( 42.0, -403.9) @ 11/04 06:13:30	AR11602 tracking
11/05 06:24:00 - 11/05 14:14:54	Track ( 246.5, -400.0) @ 11/05 06:17:00	AR11602 tracking
11/05 18:09:00 - 11/06 05:40:54	Track ( 342.9, -397.0) @ 11/05 18:06:00	AR11602 tracking
11/06 05:54:00 - 11/06 09:29:00	Track ( 434.5, -393.3) @ 11/06 05:51:00	AR11602 tracking

**PROG= 14 Inf.-time(s)**

Subr= 1		1-time(s)	2.0sec												
Seqn= 18		1-time(s)	2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec		

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 19 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Seqn= 65 4-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 73 70-time(s) 180.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
11/03 09:54:00 - 11/03 14:42:00	Fixed ( -14.0, 921.0)	# OP start + 10min HOP 81
11/03 14:54:30 - 11/03 17:54:00	Track ( -91.4, -404.6) @ 11/03 14:45:00	HOP 203
11/03 20:03:00 - 11/04 06:03:24	Track ( -46.2, -404.5) @ 11/03 20:00:00	AR11602 tracking
11/04 06:16:30 - 11/04 09:53:30	Track ( 42.0, -403.9) @ 11/04 06:13:30	AR11602 tracking
11/04 10:10:30 - 11/04 15:01:54	Fixed ( -14.0, -939.0)	HOP 81 day-2 south pole
11/04 15:05:00 - 11/04 17:59:54	Track ( 117.7, -402.8) @ 11/04 15:02:00	HOP203
11/04 18:13:00 - 11/05 06:06:54	Track ( 144.5, -402.3) @ 11/04 18:10:00	AR 11602 tracking
11/05 06:24:00 - 11/05 14:14:54	Track ( 246.5, -400.0) @ 11/05 06:17:00	AR11602 tracking
11/05 14:18:00 - 11/05 17:55:54	Track ( 311.9, -398.0) @ 11/05 14:15:00	HOP203
11/05 18:09:00 - 11/06 05:40:54	Track ( 342.9, -397.0) @ 11/05 18:06:00	AR11602 tracking
11/06 05:54:00 - 11/06 09:29:00	Track ( 434.5, -393.3) @ 11/06 05:51:00	AR11602 tracking

#### PROG= 16 15-time(s)

<b>Subr= 1 45-time(s) 10.0sec</b>												
<b>Seqn= 35 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>												
<b>Seqn= 36 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 37 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 38 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

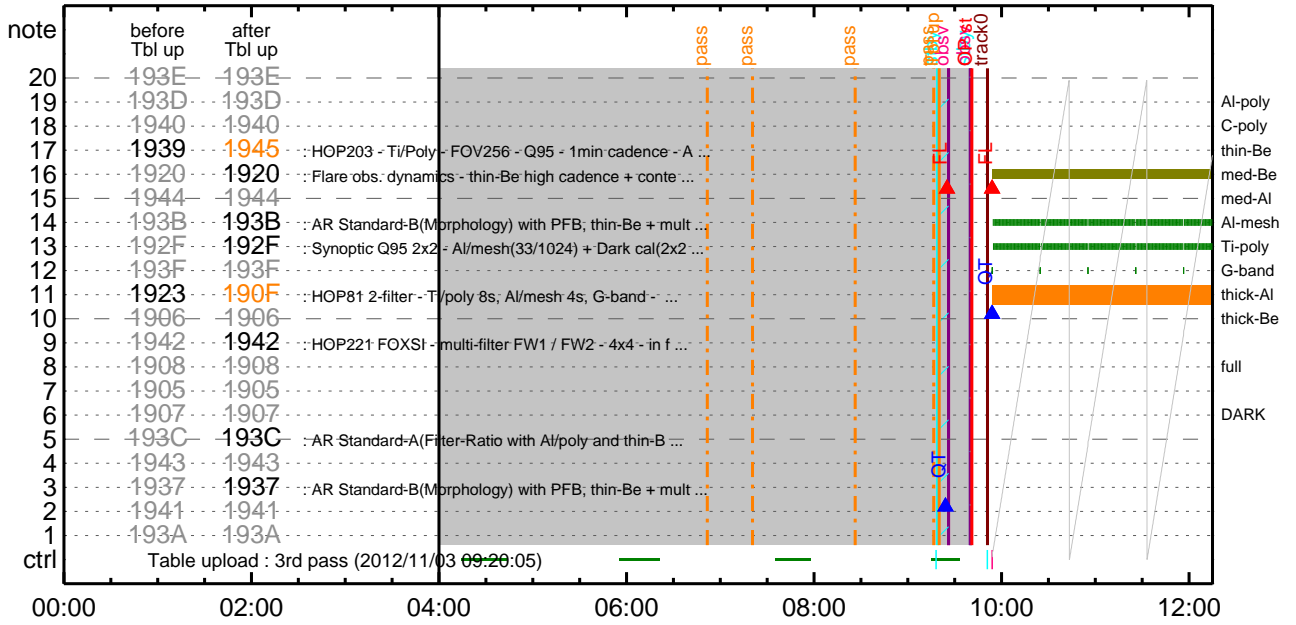
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### Flare Detection

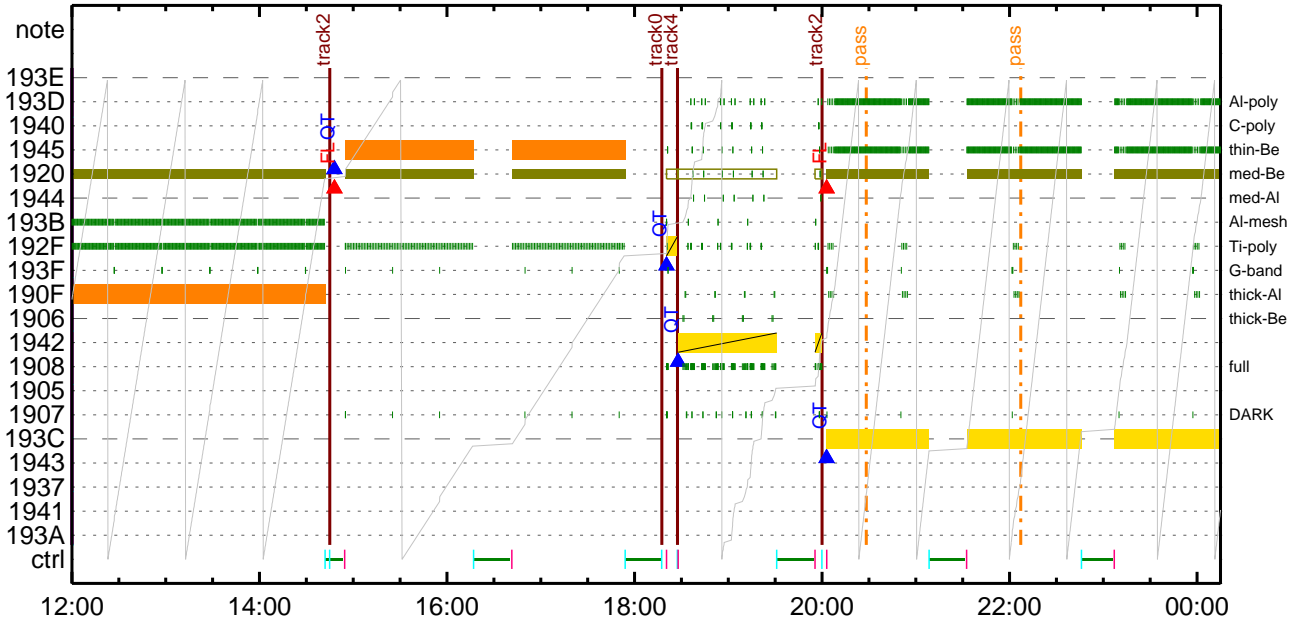
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Term	Pointing (x, y)	Comment									
11/03 09:53:46 - 11/03 18:17:46	Fixed ( -14.0, 921.0)	# OP start + 10min HOP 81									
11/03 20:02:46 - 11/04 06:03:46	Track ( -46.2, -404.5) @ 11/03 20:00:00	AR11602 tracking									
11/04 06:16:16 - 11/04 18:00:16	Track ( 42.0, -403.9) @ 11/04 06:13:30	AR11602 tracking									
11/04 18:12:46 - 11/05 06:07:16	Track ( 144.5, -402.3) @ 11/04 18:10:00	AR 11602 tracking									
11/05 06:23:46 - 11/05 17:56:16	Track ( 246.5, -400.0) @ 11/05 06:17:00	AR11602 tracking									
11/05 18:08:46 - 11/06 05:41:16	Track ( 342.9, -397.0) @ 11/05 18:06:00	AR11602 tracking									
11/06 05:53:46 - 11/08 09:30:00	Track ( 434.5, -393.3) @ 11/06 05:51:00	AR11602 tracking									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

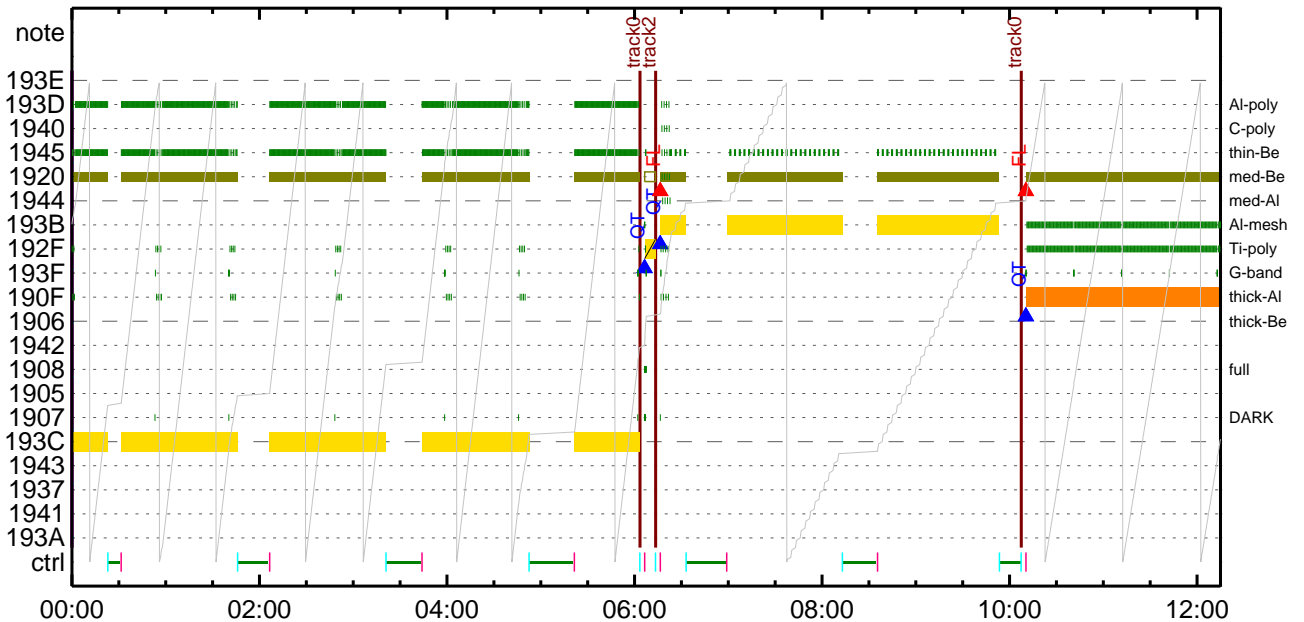
### CMDI #0029 2012/11/03



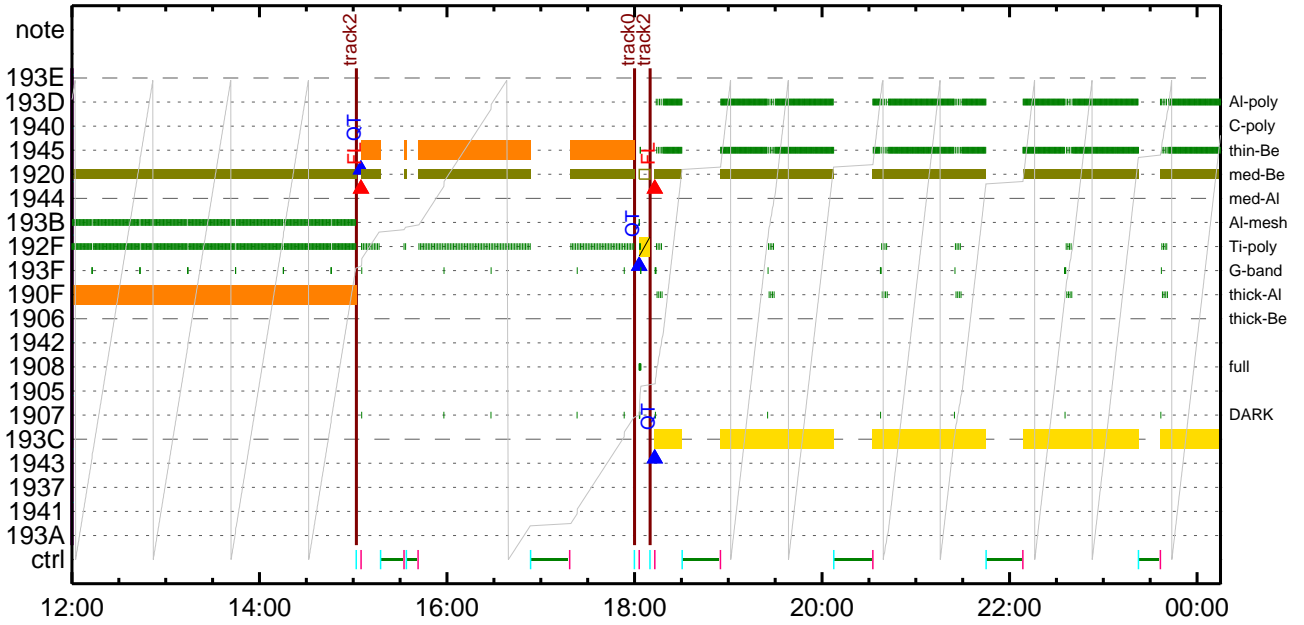
### CMDI #0029 2012/11/03



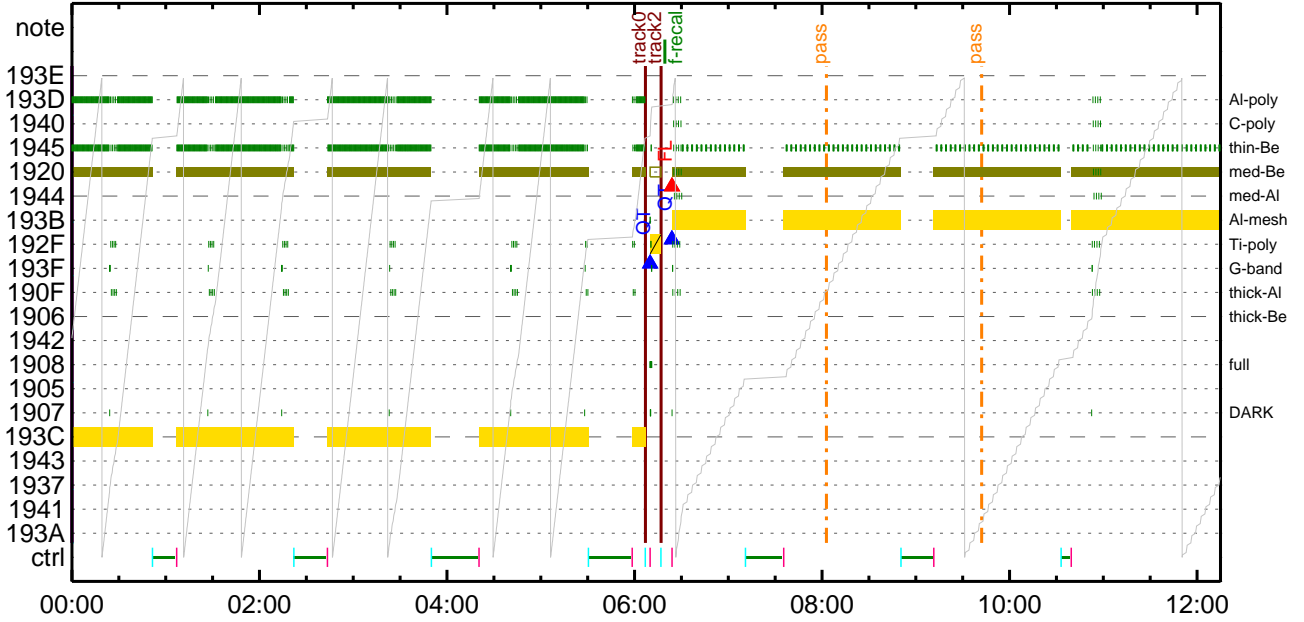
### CMDI #0029 2012/11/04



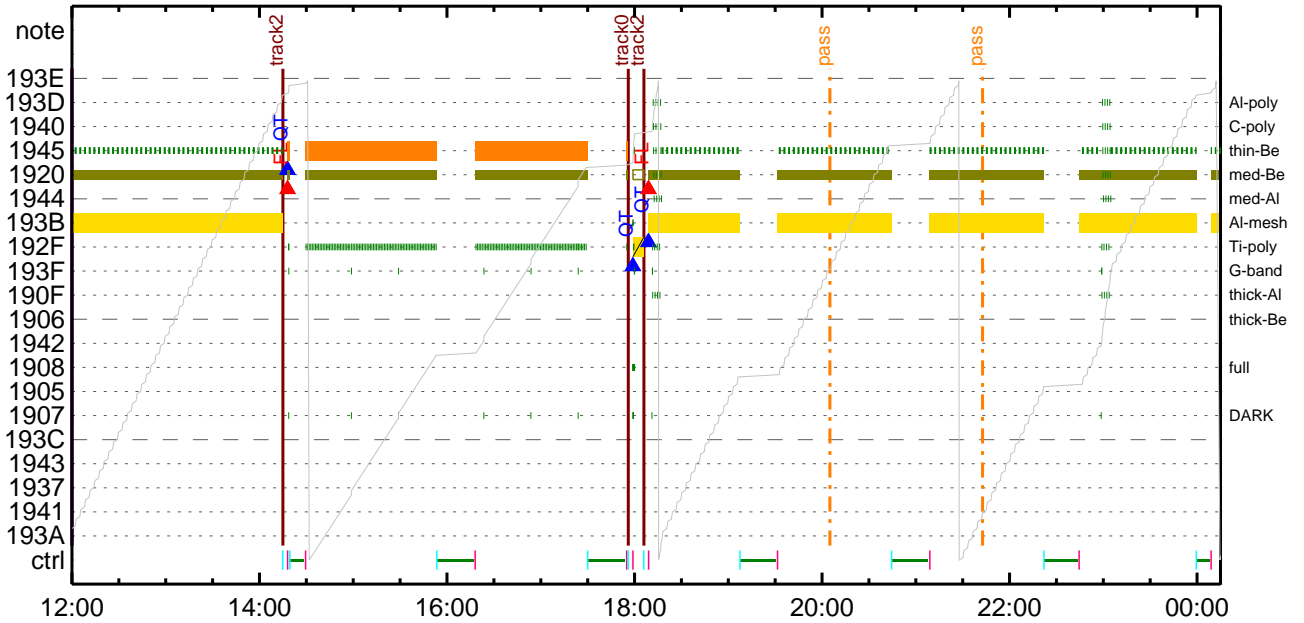
CMDI #0029 2012/11/04



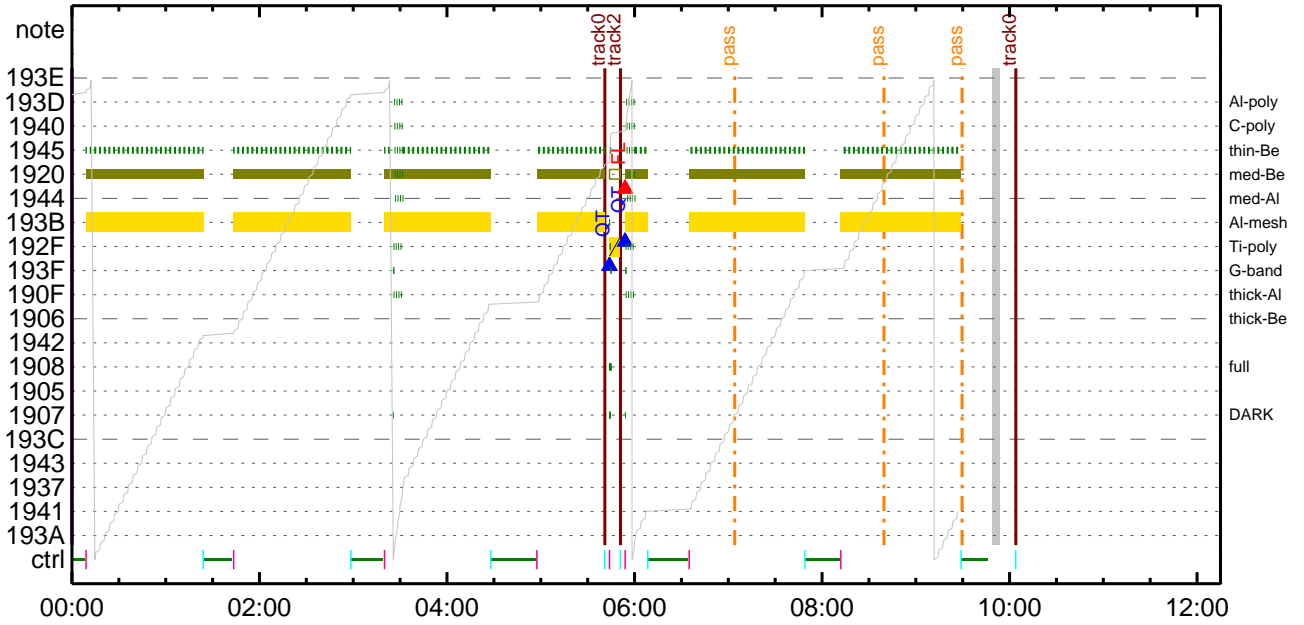
CMDI #0029 2012/11/05



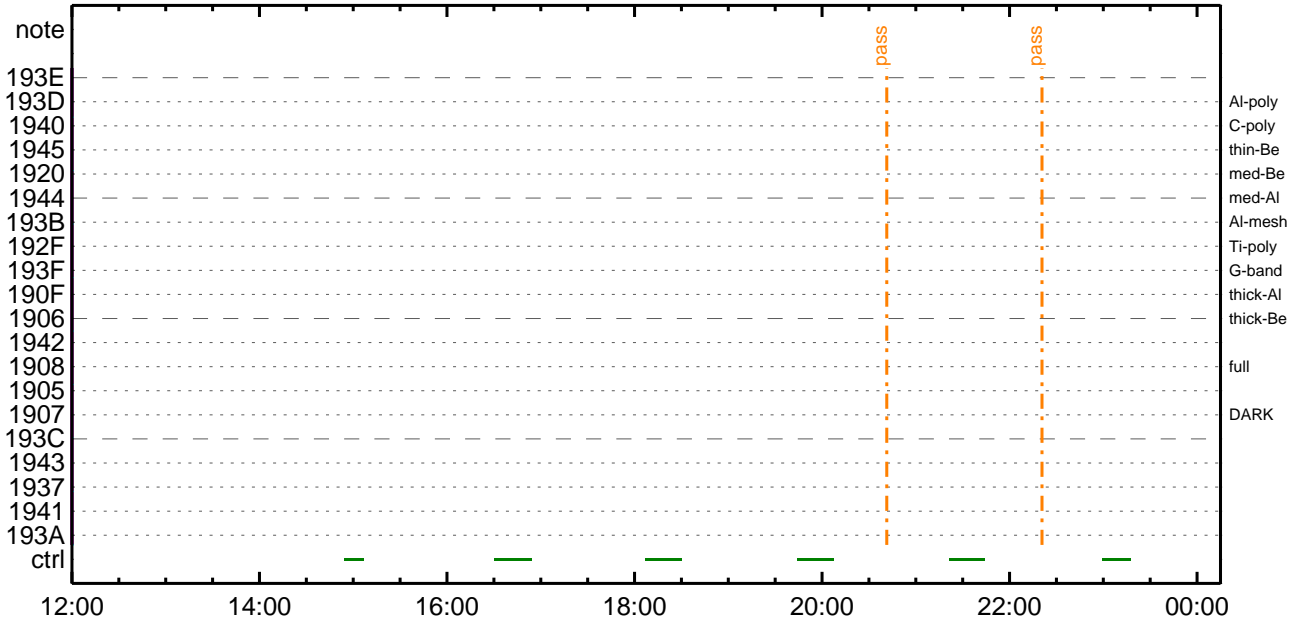
CMDI #0029 2012/11/05



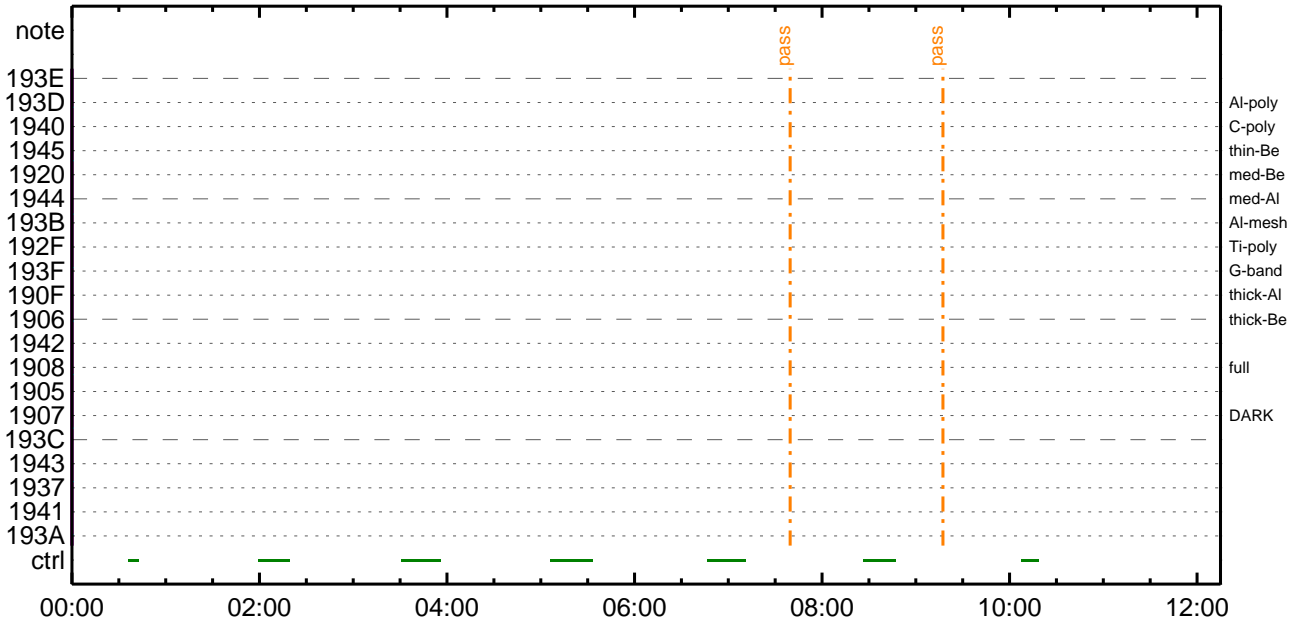
CMDI #0029 2012/11/06



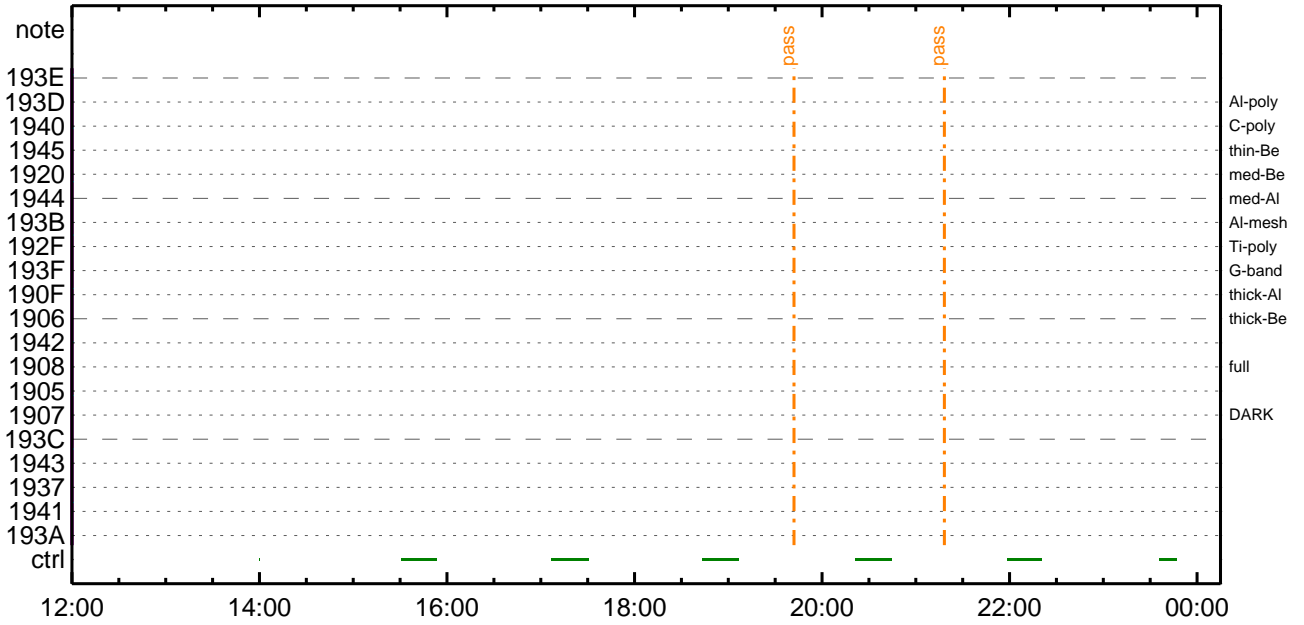
CMDI #0029 2012/11/06



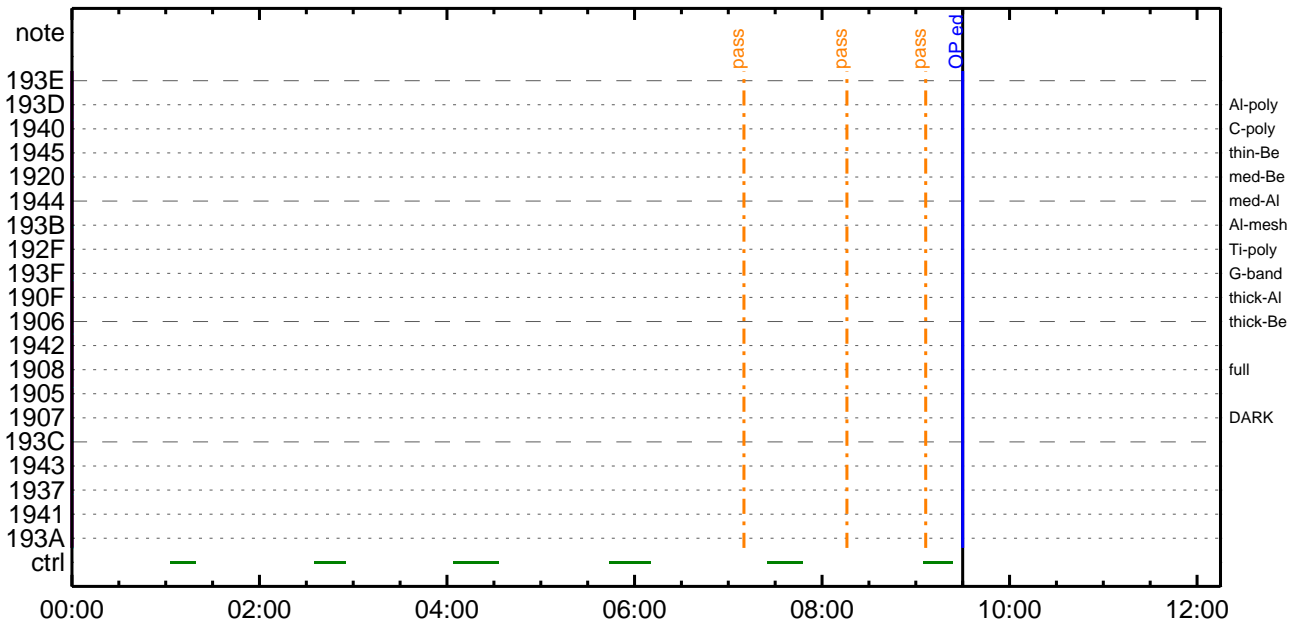
CMDI #0029 2012/11/07



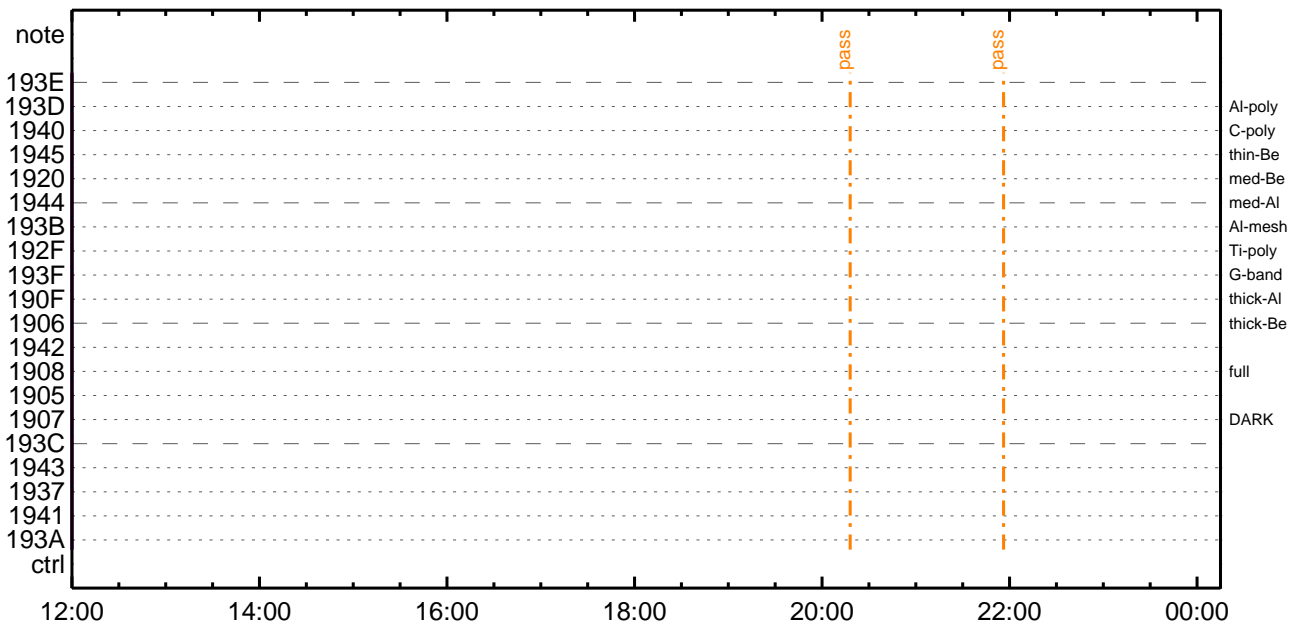
CMDI #0029 2012/11/07



CMDI #0029 2012/11/08



CMDI #0029 2012/11/08



(a) Spacecraft Operation Procedure (real-commands)

```
main-246 2012-11-03 12:37:51 205 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOS¥Ä¥$¥Ä¥-¼Ä»Û;ä
0005 . C.
0006 . C. ¥Ä¥$;¼¥³¥Ä¥Ö¥ÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 . C. Äí;ÈççðÄç•µ°®»Í×ÁÇçðÍ¥ç¥Ä¥×¥í;¼¥É;ÈÈèµ•ííÉ;ÈèÈ¼°ÇÓç•ç¼í¹ççÍ;çÄ®,ùç¹çèçðçÄ+ç®ç•çÈççç³çÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. *****
0015 . C. OP/OG¥í;¼¥É;|¥Ä¥Ö¥×
0016 . C. *****
0017 . C.
0018 . C. ;ãOP/OG¥í;¼¥É;ä
0019 . S. OP op-246:OP
0020 . C.
0021 . S. OG og-246:OG
0022 . C.
0023 . C.
0024 . C. ;ãNMOG&OPí°è¥Ä¥Ö¥×;ä
0025 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 . C. çç[HK1_PKT_FORM_NO] EQ 7
0036 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥Ä¥Ö¥×¼¥³Ä»ðð³ÍÇ§
0041 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGç¼çÈ¹ç•è²IOKçð³ÍÇ§
0043 . C.
0044 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 . C. çç[HK1_PKT_FORM_NO] EQ 7
0055 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥Ä¥Ö¥×¼¥³Ä»ðð³ÍÇ§
0060 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGç¼çÈ¹ç•è²IOKçð³ÍÇ§
0062 . C.
0063 . C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 . C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 . C. çç[HK1_PKT_FORM_NO] EQ 7
0074 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥Ä¥Ö¥×¼¥³Ä»ðð³ÍÇ§
0079 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPç¼çÈ¹ç•è²IOKçð³ÍÇ§
0081 . C.
0082 . C. ***** çÈ²¼çðÍ¼Ä´¼¼°çÈÈ-ççÄ+ç® (¼Äµ-¥Ä¥Ö¥×¼¥³ççðÄÓÄçç¼³çç¹çç¼í¹ççççâ) *****
0083 . C. DHU¥ä;¼¥É;È¥Ý¼;¥í;¼¥É;Èçðíáç¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 . C. çç[HK1_PKT_FORM_NO] EQ 2
0087 . C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 . C.
0091 . C. *****
0092 . C. TI-CMD SET (OPOG STOP/COPY/START)
0093 . C. *****
0094 . C.
0095 . C. NOTICE ;§ OPOG UPLOADççÄ+ç®ççç¼ç¹¹ç;ç°È²¼çðíTI-CMDÄ+ç®ç¼Ä¹ÔçççÈççç³çÈ;ç
```



```

0096 C.      01-03 SET 01-03 DUMP 01-03 01-03 01-03 01-03
0097 C.
0098 . C.      TI 2012-11-03 09:36:00.0
0099 +. TI 2012-11-03 09:36:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      EQ      1COUNTUP
0102 C.
0103 +. TI 2012-11-03 09:36:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      EQ      1COUNTUP
0106 C.
0107 +. TI 2012-11-03 09:36:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      EQ      1COUNTUP
0110 C.
0111 +. TI 2012-11-03 09:40:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      EQ      1COUNTUP
0114 C.
0115 C.      ENA
0116 C.      4
0117 C.      DHU
0118 C.      0xB3
0119 C.
0120 C.
0121 . C.      *****
0122 C.      TI 01-23 DHU_DMA_DMP_PRM_SET
0123 C.      *****
0124 C.
0125 C.      TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      EQ      07
0129 C.      EQ      2B
0130 C.      EQ      3
0131 C.      EQ      0
0132 C.      EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      EQ      7
0136 C.      EQ      0.25 s
0137 C.      EQ      32k
0138 C.      EQ      4M
0139 C.      EQ      EXEC
0140 C.
0141 . C.      *****
0142 C.      EQ      NON
0143 C.
0144 . C.      RAM ID=TI_TBL 01-22 DHU_MODE_CHNG
0145 C.
0146 . C.      DHU 01-22 DHU_MODE_CHNG
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      EQ      2
0150 C.      EQ      0.5S
0151 C.      EQ      32K
0152 C.      EQ      4M
0153 C.
0154 C.      *****
0155 C.      SOT TI command set
0156 C.      *****
0157 C.      Execute, after the success of OP upload.
0158 +. TI 2012-11-03 09:40:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 . C.      -----
0162 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0163 C.      -----
0164 C.      ***** SOT END *****
0165 . C.      Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C.      ***** Start EIS operation (TI set) *****
0169 C.      Execute, after the success of OP upload.
0170 C.      Set EIS TI-commands
0171 +. TI 2012-11-03 09:40:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC      (21 02)
0174 +. TI 2012-11-03 09:40:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC      (22)
0177 . C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0178 C.      ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C.      ***** XRT START *****
0183 C.      Execute, after the success of OP upload.
0184 +. TI 2012-11-03 09:40:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC      (c3)
0187 . C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0188 C.
0189 C.      ***** XRT END *****
0190 C.
0191 . C.      ***** MDP 07-FC DCBC 07-FC *****
0192 C.      (07-FC DCBC 07-FC)
0193 . S.      DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-248 2012-11-03 12:37:51 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èð¿ðÁð•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÀ®, ùð¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 85 83 04 04)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 20 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_DIS
0059 BC (d9)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0061 BC (c9)
0062 + DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 + DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 03)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 10)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2012-11-03 09:40:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 . C. *****
0086 C. SOT table upload
0087 C. *****
0088 . C. < Stop FG table >
0089 +. DC 07-F0 MDP_FG_CTRL_MANU
0090 BC (51)
0091 . C. -----
0092 C. MDP_FG_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 . C. <Upload FG Observation Table>
```

```

0096 . S. RAM ram-261:MDP_OBS_F
0097 ( )
0098 C.
0099 . C. < Dump RAMID=MDP_OBS_F >
0100 +. DC 07-F0 MDP_DUMP_FGTBL
0101 BC (82 07 00 00 00 38 b8)
0102 C. -----
0103 C. MDP_OBS_F verify = OK/NG [ ]
0104 C. -----
0105 C.
0106 C. *****
0107 C. SOT TI command set
0108 C. *****
0109 C. Execute, after the success of TBL upload.
0110 +. TI 2012-11-03 09:40:18.0
0111 DC 07-F0 MDP_SOT_MODE_OBSV
0112 BC (40)
0113 C. -----
0114 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0115 C. -----
0116 C.
0117 C.
0118 . C. ***** MDP `ûÃîñï»ö%ÿôÊÂð¹æDCBC•x²è *****
0119 C. (%â°îÿÓ¥ÄÿÈÿþÿÈÿä¥çÿëæ%¼µ¶¸»Ü¹æè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ¥D¥¹•Ï Daily±¿îÑæÉ´Ø¹æðDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOS¥Á¥S¥Ã¥-¼Ã»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

```

Nov 03, 12 12:38

XRT\_OGLIST\_0029.chk

\*\*\* OP Sequence for XRT \*\*\*

2012/11/03	09:50:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	09:51:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 ae 25 01 3f				
2012/11/03	09:53:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/03	09:53:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/03	09:53:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/03	09:53:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/03	09:53:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/03	09:53:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/03	09:53:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/11/03	09:53:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/03	09:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/03	14:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	14:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/03	14:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/03	14:44:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	14:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/03	14:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/03	14:47:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/03	14:47:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/03	14:47:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/03	14:47:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/03	14:47:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/03	14:47:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/03	14:47:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/11/03	14:47:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/03	14:53:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/03	14:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/03	16:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	16:17:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/03	16:17:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/03	16:20:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/03	16:40:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/03	16:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/03	17:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	17:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/03	17:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/03	17:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/03	18:17:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	18:17:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/03	18:17:30.5	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/03	18:17:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/03	18:17:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/03	18:17:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/03	18:20:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/11/03	18:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/03	18:27:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/03	18:27:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							

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2012/11/03	18:27:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	04	00	00	00
2012/11/03	18:27:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/11/03	18:27:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/11/03	18:27:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/11/03	18:27:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/11/03	18:27:54.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2012/11/03	18:27:56.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/03	19:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/03	19:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/03	19:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/03	19:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/03	19:54:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/03	19:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/03	19:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/03	20:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00
2012/11/03	20:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/11/03	20:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/11/03	20:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/11/03	20:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/11/03	20:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/11/03	20:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/03	20:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2012/11/03	20:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/11/03	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/03	21:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/03	21:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/03	21:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/03	21:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/03	21:31:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/03	21:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/03	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/03	22:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/03	22:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/03	22:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/03	23:06:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/03	23:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	00:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	00:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	00:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/04	00:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/04	00:30:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/04	00:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	01:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	01:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	01:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/04	01:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/04	02:05:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/04	02:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	03:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							



2012/11/04	03:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	03:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/04	03:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/04	03:43:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/04	03:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/04	04:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	04:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/04	04:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/04	04:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/04	05:20:31.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/04	05:21:31.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/11/04	06:03:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCES_OrE-point_Start_3_OG [0x099]	5	02-76	00 00 00 00 00	
2012/11/04	06:03:30.0	AOCES_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/11/04	06:03:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/11/04	06:03:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/11/04	06:03:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/04	06:06:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/11/04	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/04	06:13:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	06:13:30.0	AOCES_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00	
2012/11/04	06:15:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/11/04	06:16:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/04	06:16:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/04	06:16:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/04	06:16:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/04	06:16:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/04	06:16:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2012/11/04	06:16:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/11/04	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/04	06:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	06:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/04	06:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/04	06:33:04.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/04	06:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/04	06:58:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/04	06:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	08:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	08:13:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/04	08:13:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/04	08:16:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/04	08:34:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/04	08:35:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	09:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	09:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/04	09:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/04	09:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/04	10:07:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/04	10:07:30.0	AOCES_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 53 74 01 3f	

2012/11/04	10:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/11/04	10:10:16.0	XRT_FLD_ENA_418_OG [0x1a2]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/11/04	10:10:18.0	XRT_FLRCTRL_ENA_401_OG [0x191]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/11/04	10:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/11/04	10:10:22.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/11/04	10:10:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	10:10:26.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2012/11/04	10:10:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/11/04	10:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	15:01:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	15:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00
2012/11/04	15:04:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/11/04	15:04:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/11/04	15:04:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/11/04	15:04:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/11/04	15:04:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/11/04	15:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	15:04:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2012/11/04	15:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/11/04	15:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	15:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	15:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	15:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/04	15:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/04	15:31:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/04	15:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	15:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	15:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	15:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/04	15:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/04	15:40:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/04	15:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	16:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	16:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/04	16:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/04	16:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/04	17:17:31.0	XRT_Custom_434_OG [0x1b2]							
2012/11/04	17:18:31.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/11/04	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00
2012/11/04	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/11/04	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/11/04	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/11/04	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2012/11/04	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/04	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/04	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00

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2012/11/04	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/04	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/04	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/04	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/04	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/04	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/04	18:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/11/04	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/04	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/04	18:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/04	18:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/04	18:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/04	18:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/04	18:54:00.0	XRT_Custom_434_OG [0x1b2]			
2012/11/04	18:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/04	20:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/04	20:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/04	20:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/04	20:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/04	20:31:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/04	20:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/04	21:45:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/04	21:45:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/04	21:45:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/04	21:48:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/04	22:07:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/04	22:08:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/04	23:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/04	23:22:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/04	23:22:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/04	23:25:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/04	23:35:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/04	23:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/05	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/05	00:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/05	00:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/05	00:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/05	01:06:00.0	XRT_Custom_434_OG [0x1b2]			
2012/11/05	01:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/05	02:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/05	02:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/05	02:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/05	02:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/05	02:42:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/05	02:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/05	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/05	03:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/05	03:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/05	03:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/05	04:19:30.0	XRT_Custom_434_OG [0x1b2]			

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2012/11/05	04:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	05:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	05:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/05	05:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/05	05:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/05	05:57:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/05	05:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	06:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	06:06:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/11/05	06:07:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2012/11/05	06:07:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/11/05	06:07:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/11/05	06:07:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/11/05	06:09:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2012/11/05	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	06:16:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	06:17:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00		
2012/11/05	06:19:26.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]	XRT_FOCUS_RECAL	2	07-F8	78 00		
2012/11/05	06:23:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/11/05	06:23:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/11/05	06:23:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/11/05	06:23:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/11/05	06:23:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/11/05	06:23:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/05	06:23:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2012/11/05	06:23:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/11/05	06:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	07:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/05	07:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/05	07:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/05	07:34:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/05	07:35:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	08:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	08:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/05	08:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/05	08:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/05	09:10:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/05	09:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	10:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	10:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/05	10:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/05	10:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/05	10:38:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/05	10:39:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/05	14:14:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/05	14:15:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00		
2012/11/05	14:17:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/11/05	14:17:46.0	XRT_FLD_ENA_428_OG [0x1ac]						

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2012/11/05	14:17:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/05	14:17:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/05	14:17:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/05	14:17:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/05	14:17:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2012/11/05	14:17:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/05	14:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/05	14:19:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/05	14:19:32.5	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/05	14:19:34.5	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/05	14:22:44.5	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/05	14:28:30.0	XRT_Custom_434_OG [0x1b2]					
2012/11/05	14:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/05	15:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/05	15:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/05	15:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/05	15:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/05	16:17:01.0	XRT_Custom_434_OG [0x1b2]					
2012/11/05	16:18:01.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/05	17:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/05	17:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/05	17:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/05	17:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/05	17:54:00.0	XRT_Custom_434_OG [0x1b2]					
2012/11/05	17:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/05	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/05	17:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/11/05	17:56:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2012/11/05	17:56:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/11/05	17:56:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/11/05	17:56:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/05	17:58:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2012/11/05	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/05	18:05:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/05	18:06:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00	00
2012/11/05	18:08:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/11/05	18:08:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/05	18:08:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/05	18:08:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/05	18:08:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/05	18:08:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/05	18:08:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2012/11/05	18:08:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/05	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/05	19:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/05	19:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/05	19:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/05	19:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					

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2012/11/05	19:30:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/05	19:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/05	20:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/05	20:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/05	20:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/05	20:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/05	21:08:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/05	21:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/05	22:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/05	22:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/05	22:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/05	22:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/05	22:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/05	22:43:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/05	22:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/05	23:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/05	23:59:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/05	23:59:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/05	23:59:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	00:02:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	00:02:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/06	00:08:00.0	XRT_Custom_434_OG [0x1b2]								
2012/11/06	00:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/06	01:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/06	01:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/06	01:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/06	01:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	01:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	01:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/06	01:42:30.0	XRT_Custom_434_OG [0x1b2]								
2012/11/06	01:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/06	02:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/06	02:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/06	02:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/06	02:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	02:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	03:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/06	03:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/06	03:19:00.0	XRT_Custom_434_OG [0x1b2]								
2012/11/06	03:20:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/06	04:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/06	04:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/06	04:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/06	04:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	04:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/06	04:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/06	04:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/06	04:56:30.0	XRT_Custom_434_OG [0x1b2]								
2012/11/06	04:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
2012/11/06	05:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/06	05:40:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/06	05:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/06	05:41:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/06	05:41:18.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/06	05:41:18.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/06	05:41:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/06	05:41:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/06	05:43:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]								
2012/11/06	05:43:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/11/06	05:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
2012/11/06	05:50:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/06	05:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/06	05:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/06	05:53:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/06	05:53:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/06	05:53:46.0	XRT_FLD_ENA_428_OG [0x1ac]								

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2012/11/06	05:53:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/06	05:53:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/06	05:53:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/06	05:53:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/06	05:53:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/11/06	05:53:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/06	05:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/06	06:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/06	06:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/06	06:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/06	06:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/06	06:34:00.0	XRT_Custom_434_OG [0x1b2]				
2012/11/06	06:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/06	07:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/06	07:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/06	07:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/06	07:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/06	08:11:00.0	XRT_Custom_434_OG [0x1b2]				
2012/11/06	08:12:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/06	09:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/06	09:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/06	09:29:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/06	09:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/06	10:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/06	10:04:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00