

XRT Timeline to be uploaded on 2012/11/10

Period: 2012/11/10 10:44:00 - 2012/11/15 10:33:00

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Normal mode

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XOB #193D: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band													
Term	Pointing (x, y)		Comment										
11/10 11:01:00 - 11/10 17:59:54	Track (-720.7, 153.7)	# OP start + 10min NOAA11611 tracking										
11/10 18:13:00 - 11/11 05:30:24	Track (-675.6, 151.3)	# NOAA11611 tracking										
11/11 05:43:30 - 11/11 17:56:30	Track (-597.3, 148.0)	# Cont.										
11/11 18:33:00 - 11/12 06:05:54	Track (-501.3, 145.1)	# Cont.										
11/12 06:19:00 - 11/12 17:59:54	Track (-406.4, 143.2)	# Cont.										
11/12 18:13:00 - 11/13 06:09:54	Track (-305.0, 141.9)	# Cont.										
11/13 06:23:00 - 11/13 08:53:30	Track (-197.1, 141.3)	# Cont.										
PROG= 19 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 32 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 93 30-time(s) 120.0sec													
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)													
Term	Pointing (x, y)		Comment										
11/10 18:03:00 - 11/10 18:09:54	Fixed (0.0, 0.0)	synoptic										
11/11 05:33:30 - 11/11 05:40:24	Fixed (0.0, 0.0)	synoptic, shifted -29.5 min										
11/11 18:23:00 - 11/11 18:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min										
11/12 06:09:00 - 11/12 06:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min										
11/12 18:03:00 - 11/12 18:09:54	Fixed (0.0, 0.0)	synoptic										
11/13 06:13:00 - 11/13 06:19:54	Fixed (0.0, 0.0)	synoptic, shifted 10.0 min										
PROG= 13 1-time(s)													
Subr= 1 1-time(s) 14.0sec													
└─ Seqn= 64 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 70 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 67 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 69 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops												
Term	Pointing (x, y)		Comment									
11/10 11:01:00 - 11/10 17:59:54	Track (-720.7, 153.7)	# OP start + 10min NOAA11611 tracking									
11/10 18:13:00 - 11/11 05:30:24	Track (-675.6, 151.3)	# NOAA11611 tracking									
11/11 05:43:30 - 11/11 17:56:30	Track (-597.3, 148.0)	# Cont.									

11/11 18:33:00 - 11/12 06:05:54 Track (-501.3, 145.1) @ 11/11 18:30:00 # Cont.
 11/12 06:19:00 - 11/12 17:59:54 Track (-406.4, 143.2) @ 11/12 06:16:00 # Cont.
 11/12 18:13:00 - 11/13 06:09:54 Track (-305.0, 141.9) @ 11/12 18:10:00 # Cont.
 11/13 06:23:00 - 11/13 08:53:30 Track (-197.1, 141.3) @ 11/13 06:20:00 # Cont.

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec		Seqn= 35 1-time(s) 2.0sec		thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec		Seqn= 36 1-time(s) 2.0sec		med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Seqn= 37 1-time(s) 2.0sec		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Seqn= 38 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
				Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

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Active Region Search

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NOT USED

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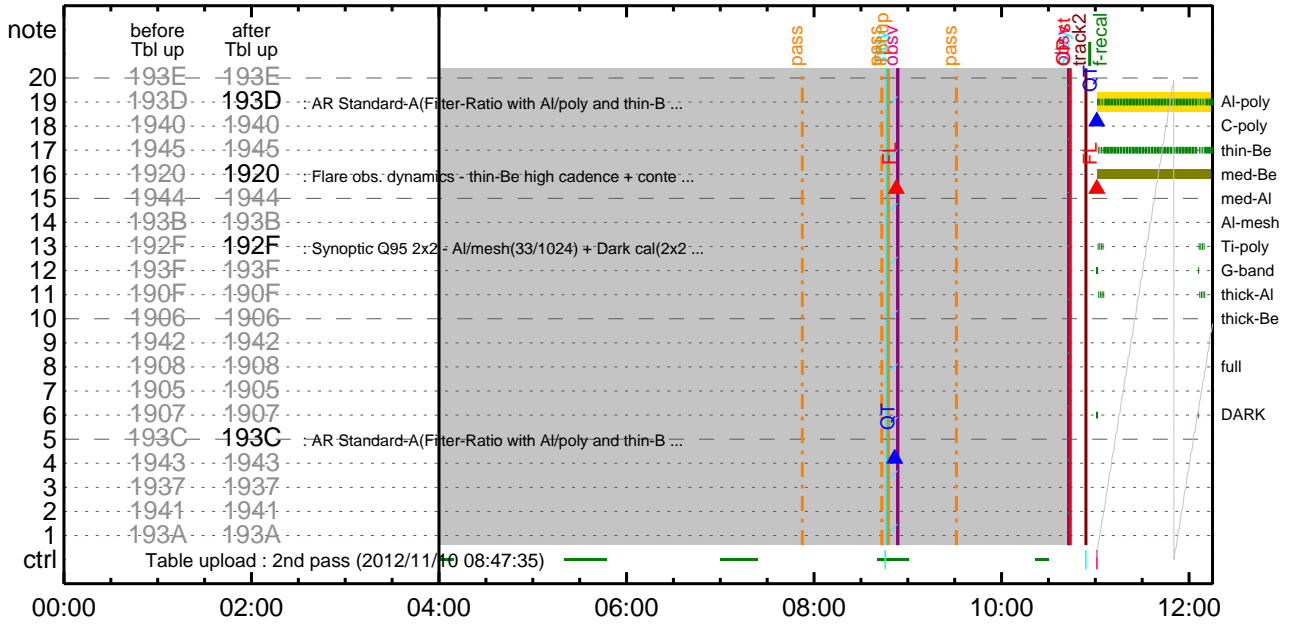
Flare Detection

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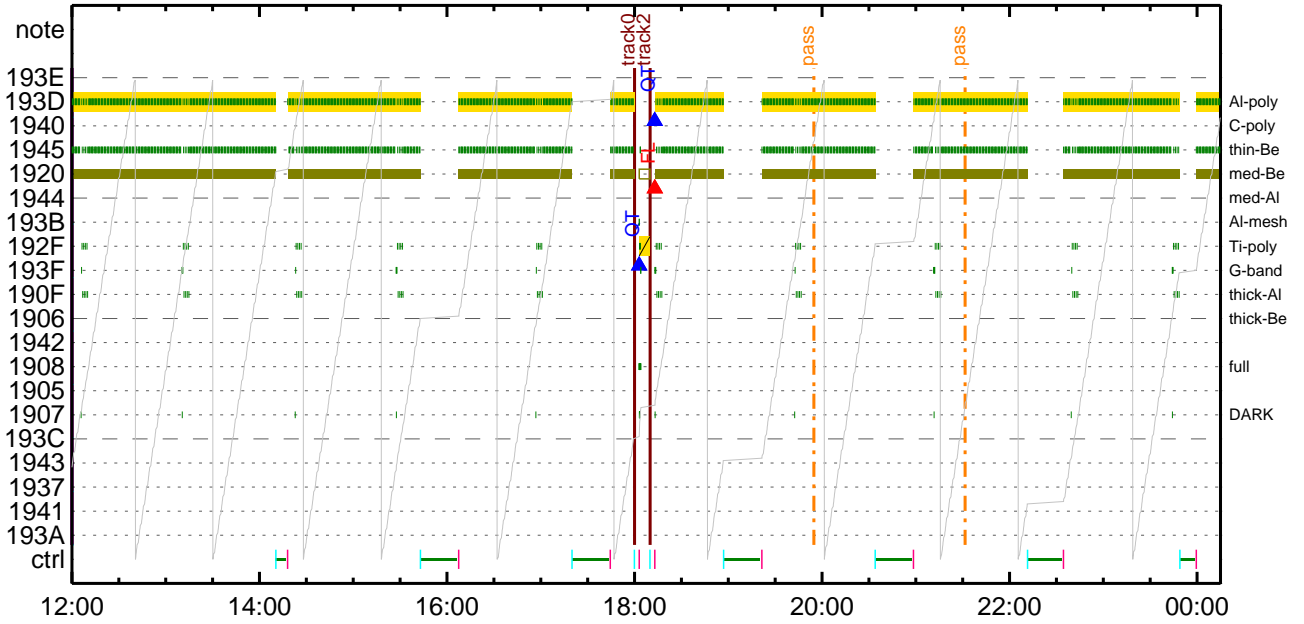
FLD Patrol

Term	Pointing (x, y)	Comment
11/10 11:00:46 - 11/10 18:00:16	Track (-720.7, 153.7) @ 11/10 10:54:00	# OP start + 10min NOAA11611 tracking
11/10 18:12:46 - 11/11 05:30:46	Track (-675.6, 151.3) @ 11/10 18:10:00	# NOAA11611 tracking
11/11 05:43:16 - 11/11 18:20:16	Track (-597.3, 148.0) @ 11/11 05:40:30	# Cont.
11/11 18:32:46 - 11/12 06:06:16	Track (-501.3, 145.1) @ 11/11 18:30:00	# Cont.
11/12 06:18:46 - 11/12 18:00:16	Track (-406.4, 143.2) @ 11/12 06:16:00	# Cont.
11/12 18:12:46 - 11/13 06:10:16	Track (-305.0, 141.9) @ 11/12 18:10:00	# Cont.
11/13 06:22:46 - 11/15 10:33:00	Track (-197.1, 141.3) @ 11/13 06:20:00	# Cont.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

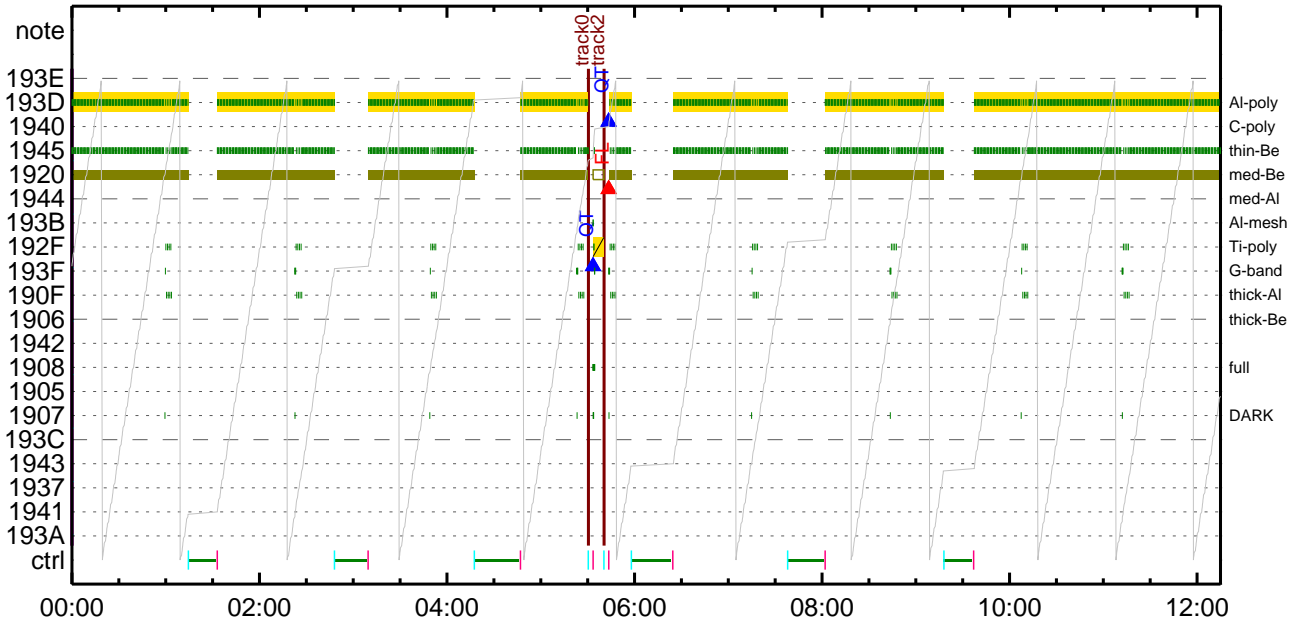
CMDI #0046 2012/11/10



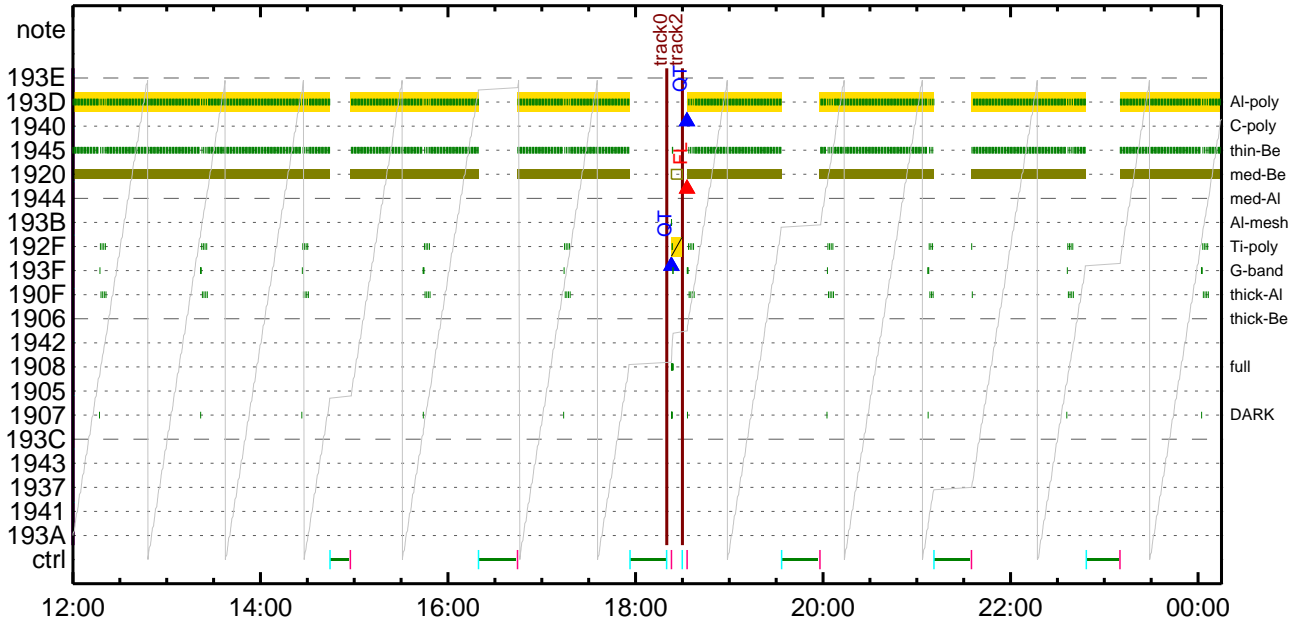
CMDI #0046 2012/11/10



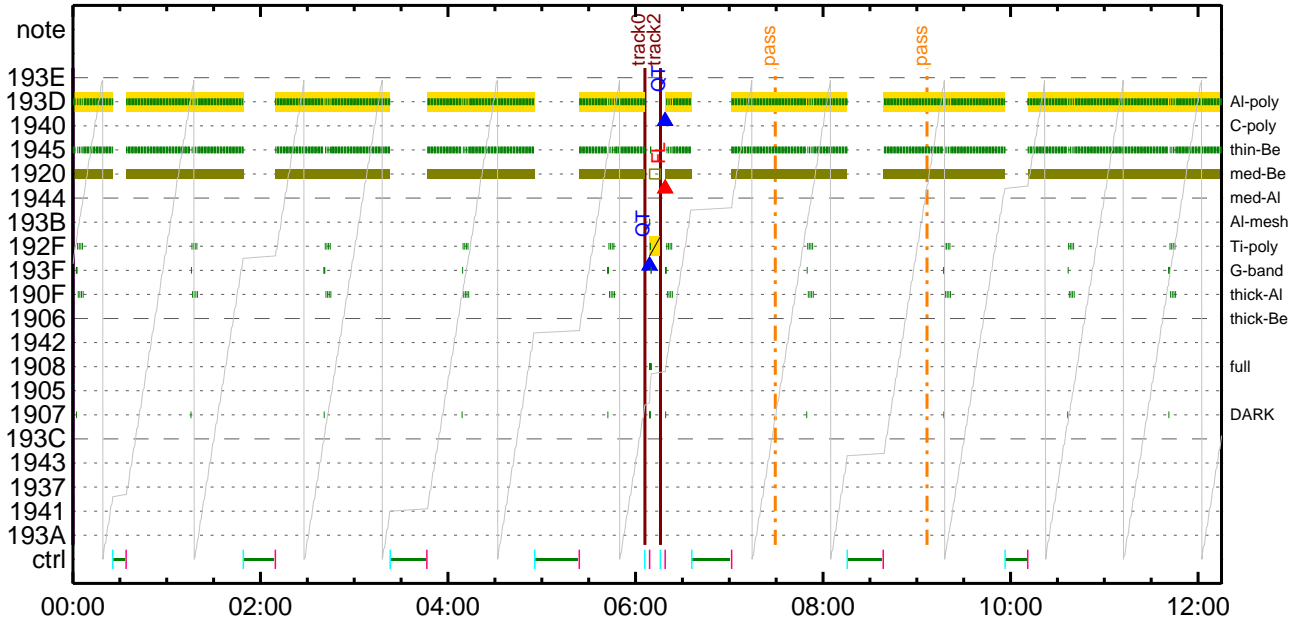
CMDI #0046 2012/11/11



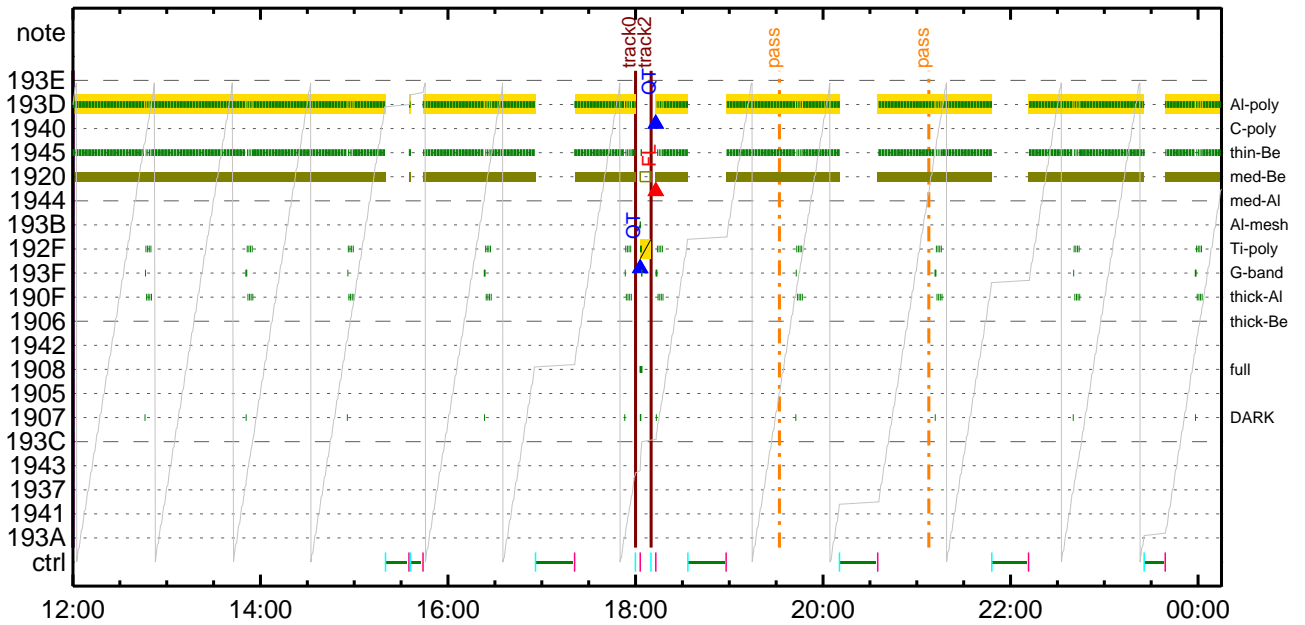
CMDI #0046 2012/11/11



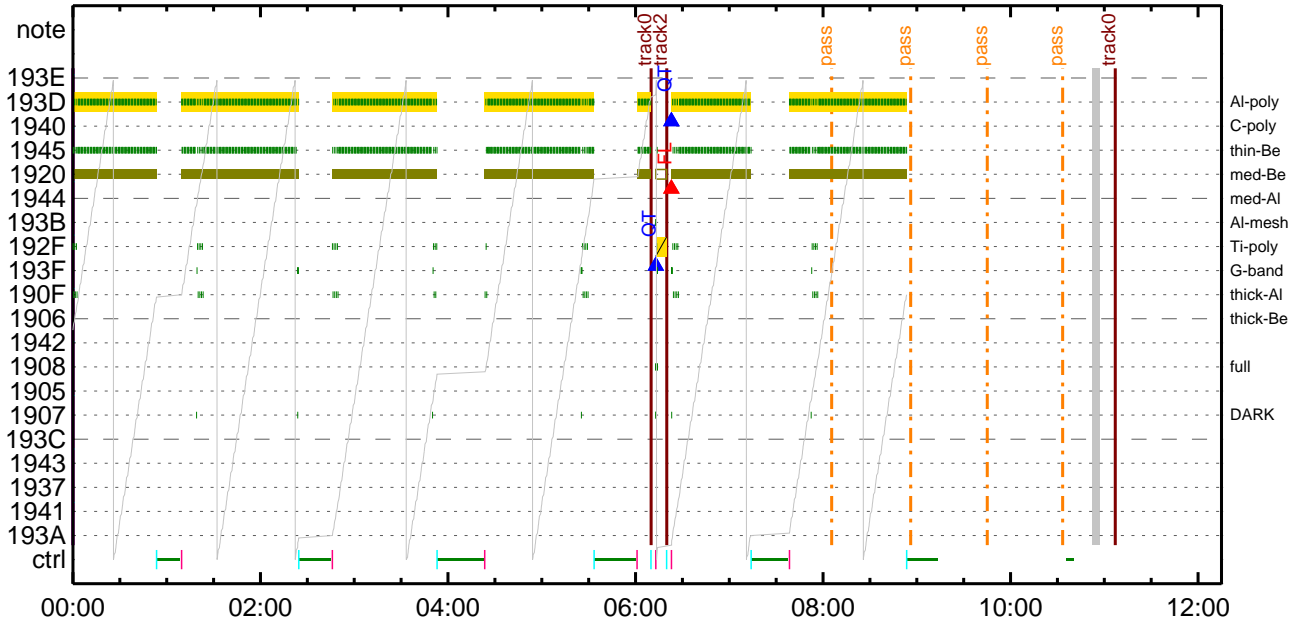
CMDI #0046 2012/11/12



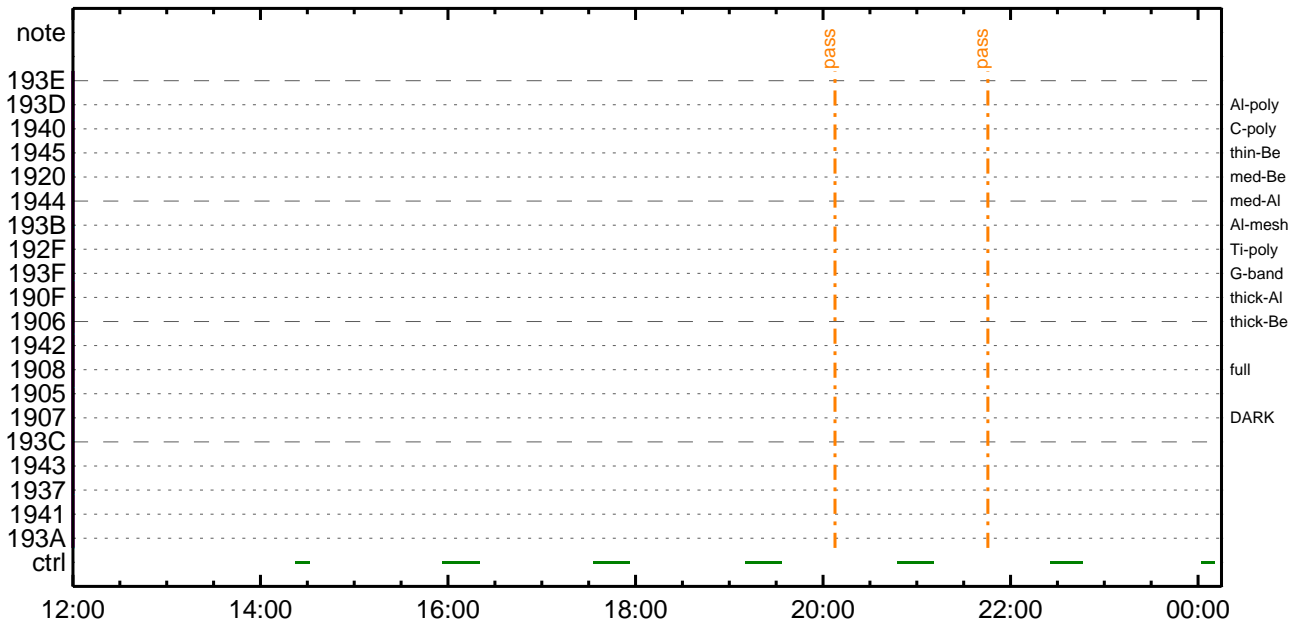
CMDI #0046 2012/11/12



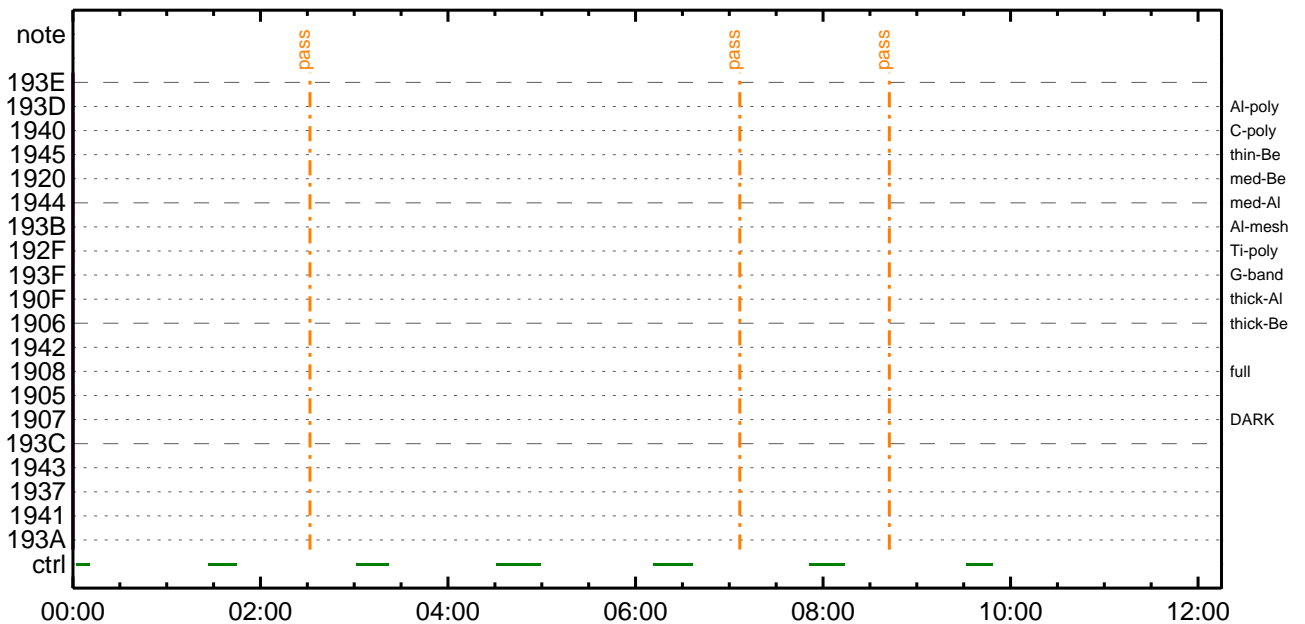
CMDI #0046 2012/11/13



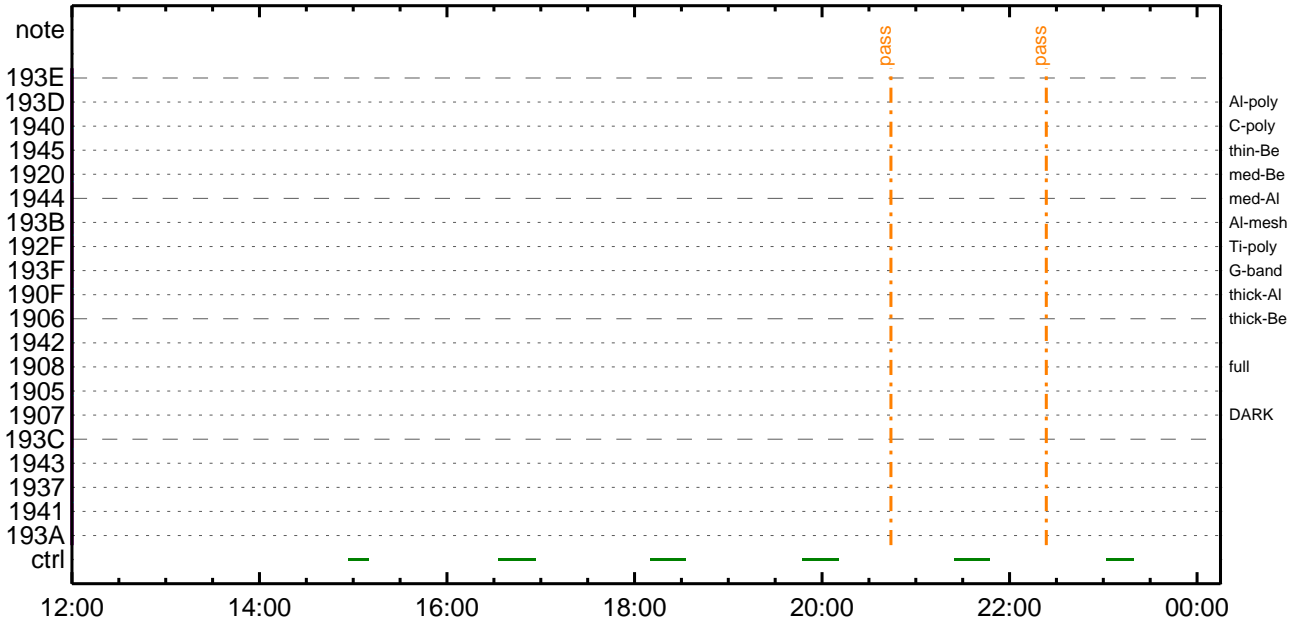
CMDI #0046 2012/11/13



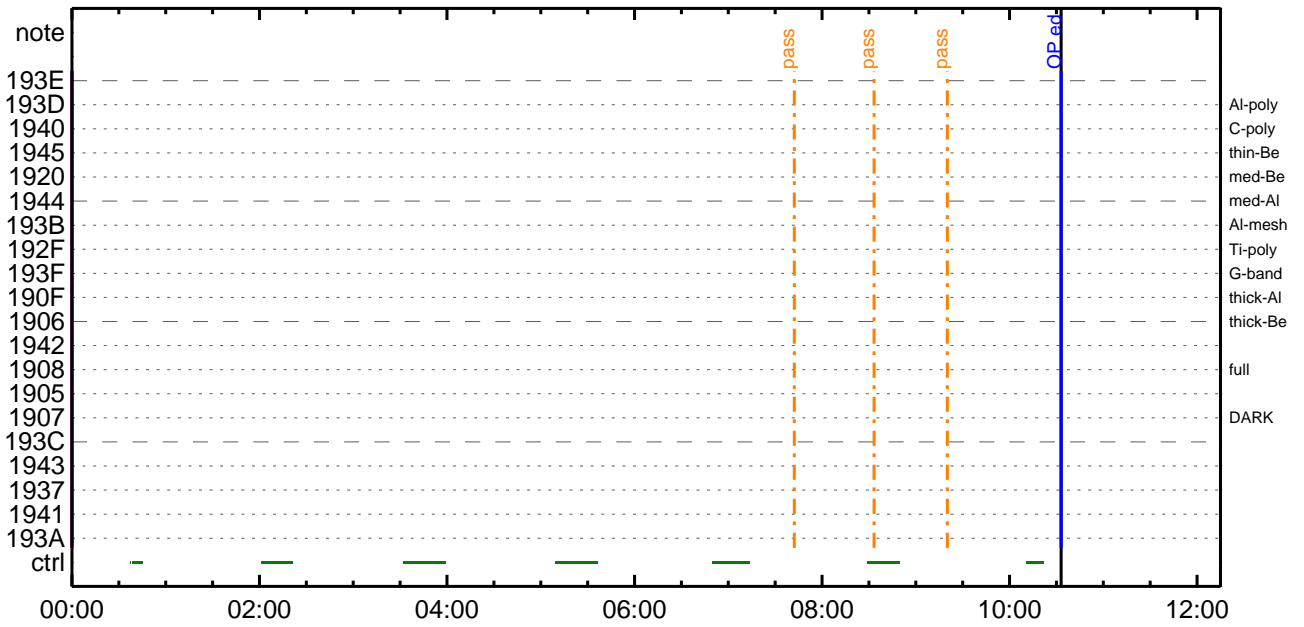
CMDI #0046 2012/11/14



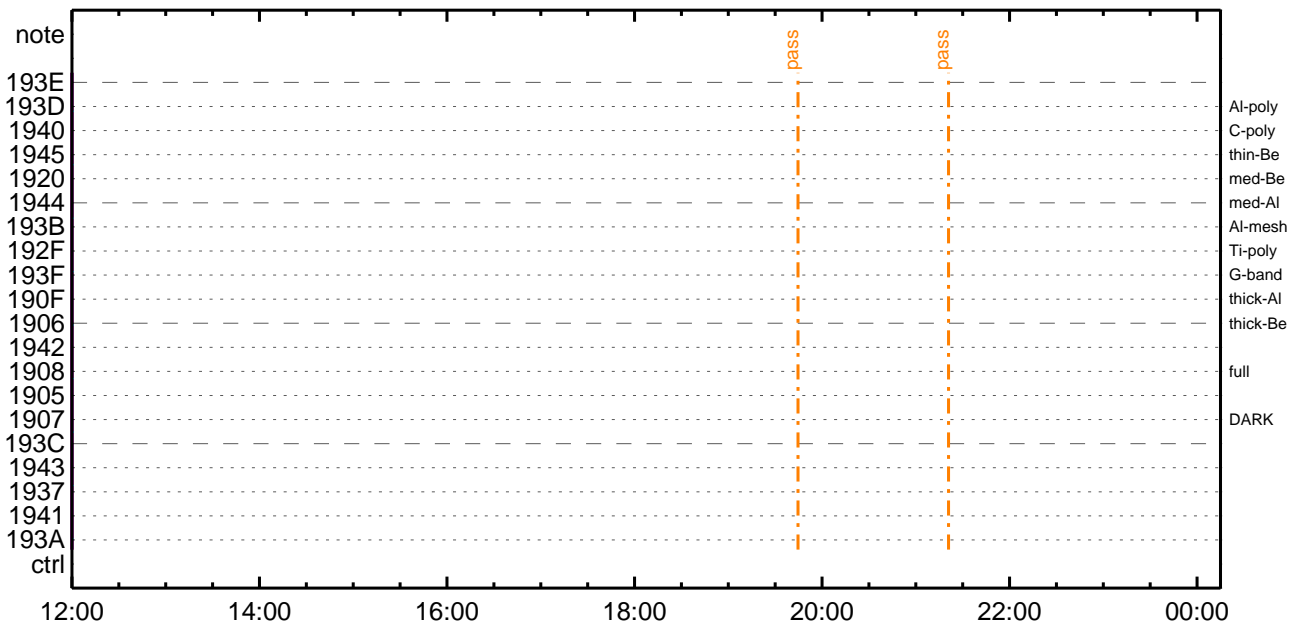
CMDI #0046 2012/11/14



CMDI #0046 2012/11/15



CMDI #0046 2012/11/15




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä
0100 C. *****
0101 C.
0102 C. ;äOP/OGY1;4YE;ä
0103 S. OP op-262:OP
0104 (
0105 S. OG og-262:OG
0106 (
0107 C.
0108 C. ;äNMOG&OPfî°èYÄYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÄYóYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGðî½Ë¹ç•ë²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÄYóYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGðî½Ë¹ç•ë²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÄYóYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPðî½Ë¹ç•ë²îOKò³îÇ§
0165 C.
0166 C. ***** òË²¼òî½Ë¹ç•ë²îOKò³îÇ§ *****
0167 C. DHUYâ;4YE;Ë½Y½;Yî;4YE;Ëòîäª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADð-Á÷ç@NGUî½Ë¹ç;ç°Ë²¼òî½Ë¹ç@ðî½Ë¹çòª•òËòò³òË;f
0180 C. ðªòç;çSETòEDUMPðî½Ë¹ç°Ë²¼òî½Ë¹ç³òË;f
0181 C.
0182 C. TIY³Y½YóYËòðÄîç(UT)
0183 +. TI 2012-11-10 10:39:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-11-10 10:39:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-11-10 10:39:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2012-11-10 10:43:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.             꺆꺆[HK1_TI_CMD_NUM]             EQ             1COUNTUP
0198 C.
0199 C. °ê²¼¸îÄè%îíñ¸îYÄY$YÄY~¹âiÜ
0200 C.             꺆꺆[HK1_TI_CMD_ENA/DIS]         EQ             ENA
0201 C.             꺆꺆[HK1_TI_CMD_NUM]             EQ             4
0202 C.             꺆꺆[HK1_NEXT_EXEC_PIM]          EQ             DHU
0203 C.             꺆꺆[HK1_NEXT_EXEC_DC]           EQ             0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èYÄYÖYx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC           (03 ab 03 01 02)
0212 C.             꺆꺆[HK1_DMP_TOP_ADRS_1]         EQ             07
0213 C.             꺆꺆[HK1_DMP_TOP_ADRS_0]         EQ             2B
0214 C.             꺆꺆[HK1_DMP_BLOCK_NUM]          EQ             3
0215 C.             꺆꺆[HK1_DMP_REPEAT_NUM]         EQ             0
0216 C.             꺆꺆[HK1_DMA_DMP_PIM]           EQ             DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC           (07 0b f8)
0219 C.             꺆꺆[HK1_PKT_FORM_NO]            EQ             7
0220 C.             꺆꺆[HK1_PKT_GEN_TIME]            EQ             0.25 s
0221 C.             꺆꺆[HK1_S_TLM_BIT_RATE]         EQ             32k
0222 C.             꺆꺆[HK1_X_TLM_BIT_RATE]         EQ             4M
0223 C.             꺆꺆[HK1_DMP_CHK_FLG]            EQ             EXEC
0224 C.
0225 C. YÄYÖYx½ªî»¸³îÇS
0226 C.             꺆꺆[HK1_DMP_CHK_FLG]            EQ             NON
0227 C.
0228 C. RAM ID=TI_TBL¸î¼È¹ç.ê²îOK¸¸³îÇS
0229 C.
0230 C. DHUYâ;¼YÉ;È¼Y¼. Yî;¼YÉ;È¸¸îâ¸¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC           (02 0a f8)
0233 C.             꺆꺆[HK1_PKT_FORM_NO]            EQ             2
0234 C.             꺆꺆[HK1_PKT_GEN_TIME]            EQ             0.5S
0235 C.             꺆꺆[HK1_S_TLM_BIT_RATE]         EQ             32K
0236 C.             꺆꺆[HK1_X_TLM_BIT_RATE]         EQ             4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-11-10 10:43:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC           (21 02)
0247 +. TI 2012-11-10 10:43:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC           (22)
0250 C.             [ ] [HK1_TI_CMD_NUM]           EQ             2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2012-11-10 10:43:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC           (c3)
0260 C.             [ ] [HK1_TI_CMD_NUM]           EQ             1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `¸îÄî¸î»¸¼Y¸¸ÈÄ¸¸¹¸¸ÈDCBC¸x²è *****
0265 C. (¼ª°îYÖYÄYÈYpYÈYfâYçYè¸È%¼¸¸¼Ä»Ü¸¹¸è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** YD Y¹.Î Daily±;îñ¸E`Ø¸¹¸¸ÈDCBC¸x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;ãLOS YÄY$YÄY~¼Ä»Ü;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

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0096 BC (da)
0097 + DC 07-F0 MDP_XRT_QT_PROG_SET
0098 BC (c4 05)
0099 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0100 BC (c5 10)
0101 . C. ----- Success Verify ? OK / NG ____
0102 C.
0103 C.
0104 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0105 C.
0106 +. DC 07-F0 MDP_XRT_MODE_OBSV
0107 BC (c2)
0108 +. TI 2012-11-10 10:43:02.0
0109 DC 07-F0 MDP_XRT_MODE_OBSV
0110 BC (c2)
0111 . C. ----- Success Verify ? OK / NG ____
0112 C.
0113 C. ***** XRT END *****
0114 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0115 +. DC 07-FC EIS_MODE_MANU
0116 BC (21 02)
0117 . C. Verify EIS in MANUAL mode
0118 . C. Estimated OBSTBL upload time is 30s
0119 C. *****
0120 C. EIS START OBSTBL LOAD
0121 C. *****
0122 . S. RAM ram-820:EIS_OBSTBL
0123 ( )
0124 +. DC 07-FC EIS_DUMP_OBSTBL
0125 BC (07 07 07 00 00 70 00)
0126 C.
0127 C. Execute, after the success of OBSTBL upload.
0128 C. Set EIS TI-commands
0129 +. TI 2012-11-10 10:43:50.0
0130 DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0133 C. *****
0134 C. EIS END OBSTBL LOAD
0135 C. *****
0136 C.
0137 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂÐð¹ñèDCBC•x²è *****
0138 C. (%á°îÿÓÿÄÿËÿPÿËÿáÿçÿèñ¼¼¼¼»Ûñ¹ñè)
0139 . S. DC-BC dcbc-402:DCBC
0140 (MDP_known_event)
0141 C.
0142 C.
0143 . C. ***** ¥Ð¥¹•ï Daily±¿îÑñË'Øñ¹ñèDCBC•x²è *****
0144 . S. DC-BC dcbc-153:DCBC
0145 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0146 C.
0147 C.
0148 . C. ;ãLOS¥Á¥$¥Ä¥¹¼Ä»Û;ä
0149 C.
0150 . C. ***** LOS *****
0151 C.

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0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP `uãîñî»ò¼ÿñĒĀĐñ¹ñĒDCBC•x²ĕ *****
0100 C. (¼á°îÿÓÿĀÿĒÿĤÿĒÿáÿçÿĕñĒ¼ñ¼Ā»Ûñ¹ñĒĕ)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ¼Đÿ¹•Ī Daily±;ĪññĒ´Øñ¹ñĒDCBC•x²ĕ *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ĵãLOSÿÁÿŞÿĀÿˆ¼Ā»Û;ã
0111 C.
0112 . C. ***** LOS *****
0113 C.
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*** OP Sequence for XRT ***

2012/11/10	10:53:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/10	10:54:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/10	10:56:26.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/11/10	11:00:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/10	11:00:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/10	11:00:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/10	11:00:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/10	11:00:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/10	11:00:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/10	11:00:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/11/10	11:00:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/10	11:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/10	14:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/10	14:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/10	14:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/10	14:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/10	14:17:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/10	14:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/10	15:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/10	15:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/10	15:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/10	15:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/10	16:06:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/10	16:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/10	17:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/10	17:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/10	17:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/10	17:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/10	17:43:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/10	17:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/10	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/10	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/10	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/10	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/10	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/10	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/10	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/11/10	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/10	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/10	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/10	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/10	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/10	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/10	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/10	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/10	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/10	18:12:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				

2012/11/10	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/11/10	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/10	18:57:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/10	18:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/10	18:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/10	19:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/10	19:20:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/10	19:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/10	20:34:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/10	20:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/10	20:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/10	20:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/10	20:57:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/10	20:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/10	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/10	22:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/10	22:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/10	22:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/10	22:33:30.5	XRT_Custom_434_OG [0x1b2]						
2012/11/10	22:34:30.5	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/10	23:49:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/10	23:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/10	23:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/10	23:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/10	23:58:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/10	23:59:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/11	01:14:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/11	01:14:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/11	01:14:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/11	01:17:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/11	01:32:00.0	XRT_Custom_434_OG [0x1b2]						
2012/11/11	01:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/11	02:48:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/11	02:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/11	02:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/11	02:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/11	03:08:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/11	03:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/11	04:17:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/11	04:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/11/11	04:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/11/11	04:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/11/11	04:46:00.0	XRT_Custom_434_OG [0x1b2]						
2012/11/11	04:47:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/11/11	05:30:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/11	05:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/11/11	05:30:30.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2012/11/11	05:30:46.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/11/11	05:30:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/11/11	05:30:50.0	XRT_ARS_DIS_406_OG [0x196]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			

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2012/11/11	05:33:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2012/11/11	05:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	05:40:24.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	05:40:30.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02	00 00 00 00	
2012/11/11	05:42:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2012/11/11	05:43:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/11/11	05:43:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/11/11	05:43:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/11/11	05:43:22.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/11/11	05:43:24.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	05:43:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13	
2012/11/11	05:43:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2012/11/11	05:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	05:58:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	05:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	05:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/11	06:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/11	06:23:30.0	XRT_Custom_434_OG [0x1b2]					
2012/11/11	06:24:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	07:38:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	07:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	07:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/11	07:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/11	08:01:00.0	XRT_Custom_434_OG [0x1b2]					
2012/11/11	08:02:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	09:18:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	09:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	09:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/11	09:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/11	09:36:00.0	XRT_Custom_434_OG [0x1b2]					
2012/11/11	09:37:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	14:44:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	14:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	14:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/11	14:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/11	14:56:30.0	XRT_Custom_434_OG [0x1b2]					
2012/11/11	14:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	16:19:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	16:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	16:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/11	16:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/11	16:43:30.0	XRT_Custom_434_OG [0x1b2]					
2012/11/11	16:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/11	17:56:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	17:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/11	17:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/11	17:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/11	18:19:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/11	18:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	

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2012/11/11	18:20:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2012/11/11	18:20:16.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/11	18:20:18.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/11	18:20:20.0	XRT_ARS_DIS_406_OG [0x196] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/11	18:22:58.0	XRT_QT_PROG_SET_419_OG [0x1a3] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2012/11/11	18:23:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/11	18:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/11	18:30:00.0	AOCS_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02	00	00	00	00
2012/11/11	18:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/11/11	18:32:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/11	18:32:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/11	18:32:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/11	18:32:52.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/11	18:32:54.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/11	18:32:56.0	XRT_QT_PROG_SET_439_OG [0x1b7] MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/11/11	18:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/11/11	18:33:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/11	19:33:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/11	19:33:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/11	19:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/11	19:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/11	19:57:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/11	19:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/11	21:11:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/11	21:11:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/11	21:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/11	21:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/11	21:34:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/11	21:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/11	22:48:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/11	22:48:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/11	22:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/11	22:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/11	23:09:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/11	23:10:00.5	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	00:25:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	00:25:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	00:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	00:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	00:33:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	00:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	01:49:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	01:49:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	01:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	01:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	02:08:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	02:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	03:23:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	03:23:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/11/12	03:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	03:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	03:45:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	03:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	04:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	04:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	04:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	04:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	05:23:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	05:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	06:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/12	06:06:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/12	06:06:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/12	06:06:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/12	06:06:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/12	06:08:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/11/12	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	06:15:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	06:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/12	06:18:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/12	06:18:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/12	06:18:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/12	06:18:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/12	06:18:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/12	06:18:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	06:18:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/11/12	06:18:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/12	06:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	06:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	06:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	06:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	06:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	07:00:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	07:01:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	08:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	08:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	08:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	08:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	08:37:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	08:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	09:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	09:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	09:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	09:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	10:10:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	10:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	15:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	15:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/11/12	15:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	15:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	15:34:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	15:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	15:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	15:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	15:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	15:43:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	15:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	16:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	16:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	16:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	16:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	17:20:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	17:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/12	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/12	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/12	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/12	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/12	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/11/12	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/12	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/12	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/12	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/12	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/12	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/12	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	18:12:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/11/12	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/12	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	18:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	18:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	18:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	18:57:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	18:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	20:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	20:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	20:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	20:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	20:34:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	20:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	21:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	21:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/11/12	21:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	21:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	22:10:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	22:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/12	23:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/12	23:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/12	23:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/12	23:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/12	23:38:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/12	23:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/13	00:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/13	00:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/13	00:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/13	00:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/13	01:08:30.0	XRT_Custom_434_OG [0x1b2]							
2012/11/13	01:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/13	02:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/13	02:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/13	02:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/13	02:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/13	02:45:00.5	XRT_Custom_434_OG [0x1b2]							
2012/11/13	02:46:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/13	03:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/13	03:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/13	03:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/13	03:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/13	04:22:30.5	XRT_Custom_434_OG [0x1b2]							
2012/11/13	04:23:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/13	05:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/13	05:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/13	05:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/13	05:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/13	06:00:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/13	06:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/13	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/13	06:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/13	06:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/13	06:10:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/11/13	06:10:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/11/13	06:10:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/13	06:12:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/11/13	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/13	06:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/13	06:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/11/13	06:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/13	06:22:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/13	06:22:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/13	06:22:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/13	06:22:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/13	06:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]							

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2012/11/13	06:22:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	13	
2012/11/13	06:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2012/11/13	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/13	07:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/13	07:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/13	07:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/13	07:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/13	07:37:30.0	XRT_Custom_434_OG [0x1b2]						
2012/11/13	07:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/11/13	08:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/11/13	08:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/11/13	08:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/11/13	08:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/11/13	11:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00
						00	00	00