XRT Timeline to be uploaded on 2012/11/22

Period: 2012/11/22 09:59:00 - 2012/11/27 09:49:00

* * * * * **Normal mode** * * * * *

XOB #194E: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-short - w leak image

Term	Pointing (x, y)	Comment
11/22 17:02:00 - 11/22 17:04:54	Fixed (-528.0, -528.0)	XRT post-bakeout four-quadrant pointing 1/4

PROG= 15 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 1 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 51 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #194F: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -short-w leak image

Term	Pointing (x, y)	Comment
11/22 17:07:00 - 11/22 17:09:54	Fixed (528.0, -528.0)	2/4

PROG= 05 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 2 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 51 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1950: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-short-w leak image

Term	Pointing (x, y)	Comment
11/22 17:12:00 - 11/22 17:14:54	Fixed (528.0, 528.0)	3/4

PROG= 04 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 3 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 51 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1951: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-short-w leak image

Term	Pointing (x, y)	Comment
11/22 17:17:00 - 11/22 17:19:54	Fixed (-528.0, 528.0)	4/4

PROG= 17 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 4 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2	**1-time(s)**	**2.0sec**										
Seqn= 51	**2-time(s)**	**2.0sec**										
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	**1-time(s)**	**2.0sec**										
Seqn= 15	**1-time(s)**	**2.0sec**										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8	**1-time(s)**	**2.0sec**										
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #194D: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2

Term	Pointing (x, y)	Comment
11/22 17:23:00 - 11/22 17:29:54	Fixed (0.0, 0.0)	synoptic, shifted -20.0 min,
11/23 18:10:00 - 11/23 18:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min

PROG= 09	**1-time(s)**											
Subr= 1	**1-time(s)**	**12.0sec**										
Seqn= 56	**1-time(s)**	**4.0sec**										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6	**1-time(s)**	**2.0sec**										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 46	**1-time(s)**	**4.0sec**										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	**1-time(s)**	**2.0sec**										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69	**1-time(s)**	**2.0sec**										
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	**1-time(s)**	**2.0sec**										
Seqn= 68	**1-time(s)**	**2.0sec**										
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #193D: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
11/22 17:33:00 - 11/23 18:06:54	Track (145.1, 80.4) @ 11/22 17:30:00	# AR11618 obs MMFW

PROG= 19	**Inf.-time(s)**											
Subr= 1	**1-time(s)**	**2.0sec**										
Seqn= 19	**2-time(s)**	**2.0sec**										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	**2-time(s)**	**2.0sec**										
Seqn= 92	**1-time(s)**	**2.0sec**										
Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32	**4-time(s)**	**2.0sec**										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 93	**30-time(s)**	**120.0sec**										
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
11/23 18:20:00 - 11/24 09:10:00	Track (368.7, 84.4) @ 11/23 18:17:00	cont.

PROG= 03	**Inf.-time(s)**											
Subr= 1	**1-time(s)**	**2.0sec**										
Seqn= 18	**1-time(s)**	**2.0sec**										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 19	**1-time(s)**	**2.0sec**										

Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 65 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 73 70-time(s) 120.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * Flare mode * * * * *

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
11/22 17:33:00 - 11/23 18:06:54	Track (145.1, 80.4) @ 11/22 17:30:00	# AR11618 obs MMFW
11/23 18:20:00 - 11/24 09:10:00	Track (368.7, 84.4) @ 11/23 18:17:00	cont.

PROG= 16 15-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 35 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 36 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * Active Region Search * * * * *

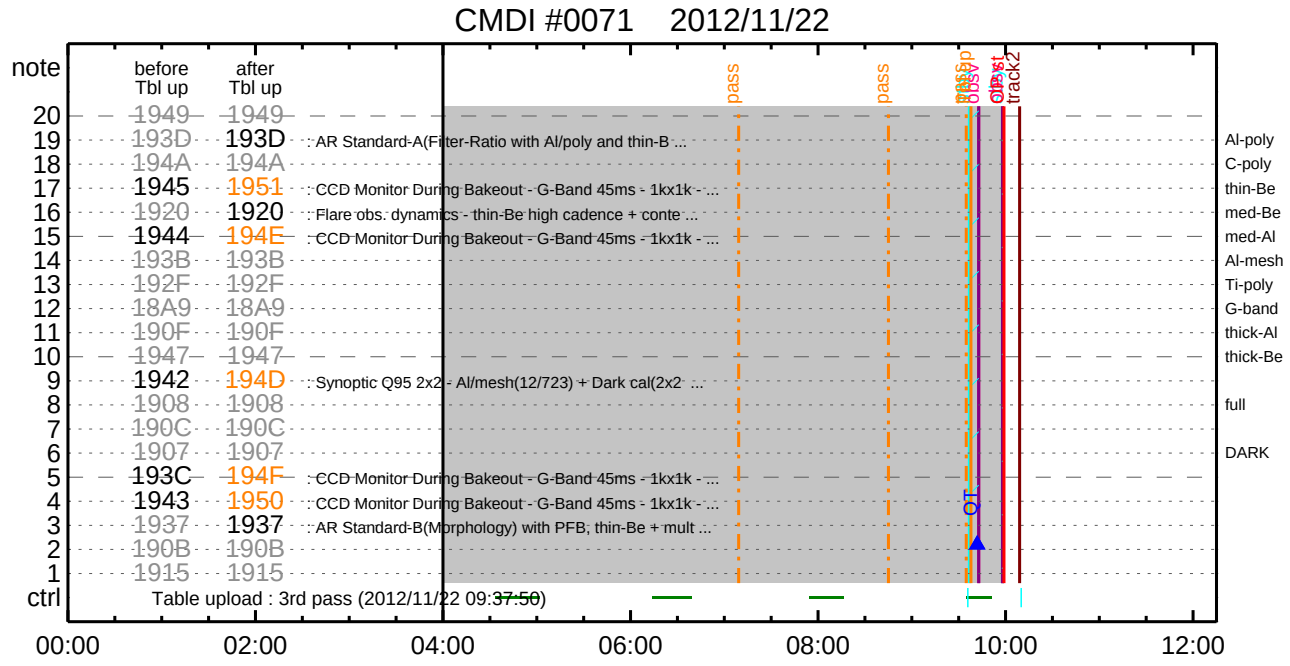
NOT USED

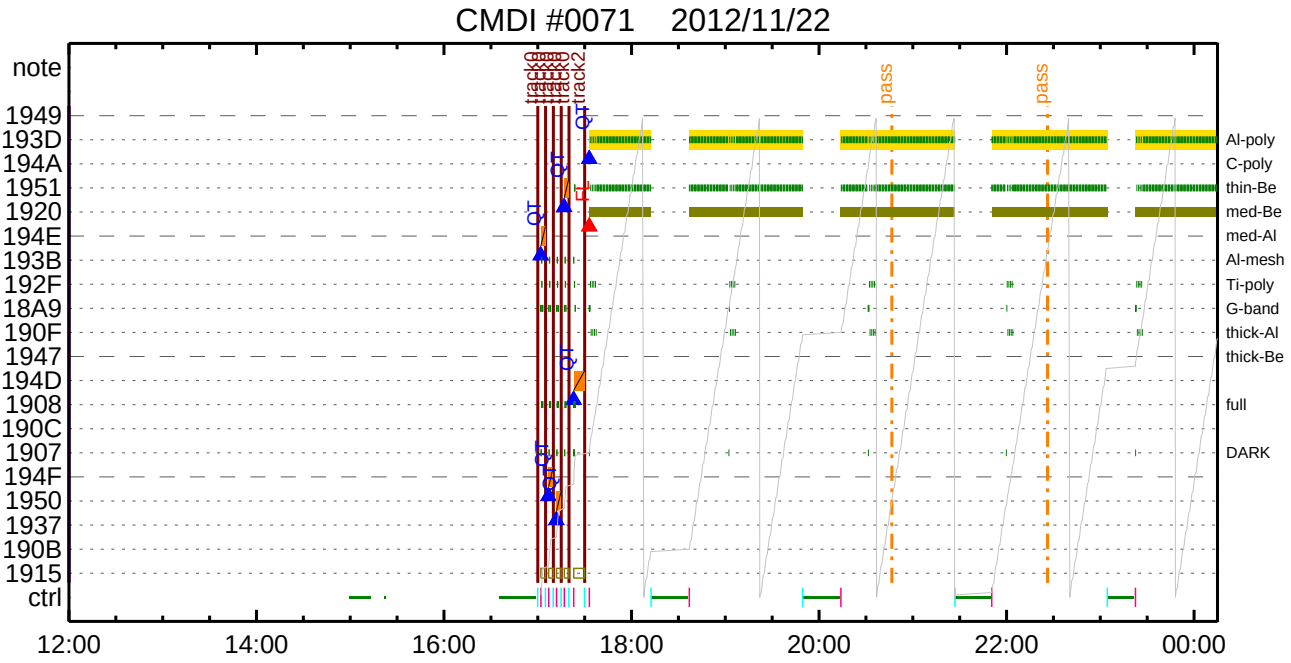
* * * * * Flare Detection * * * * *

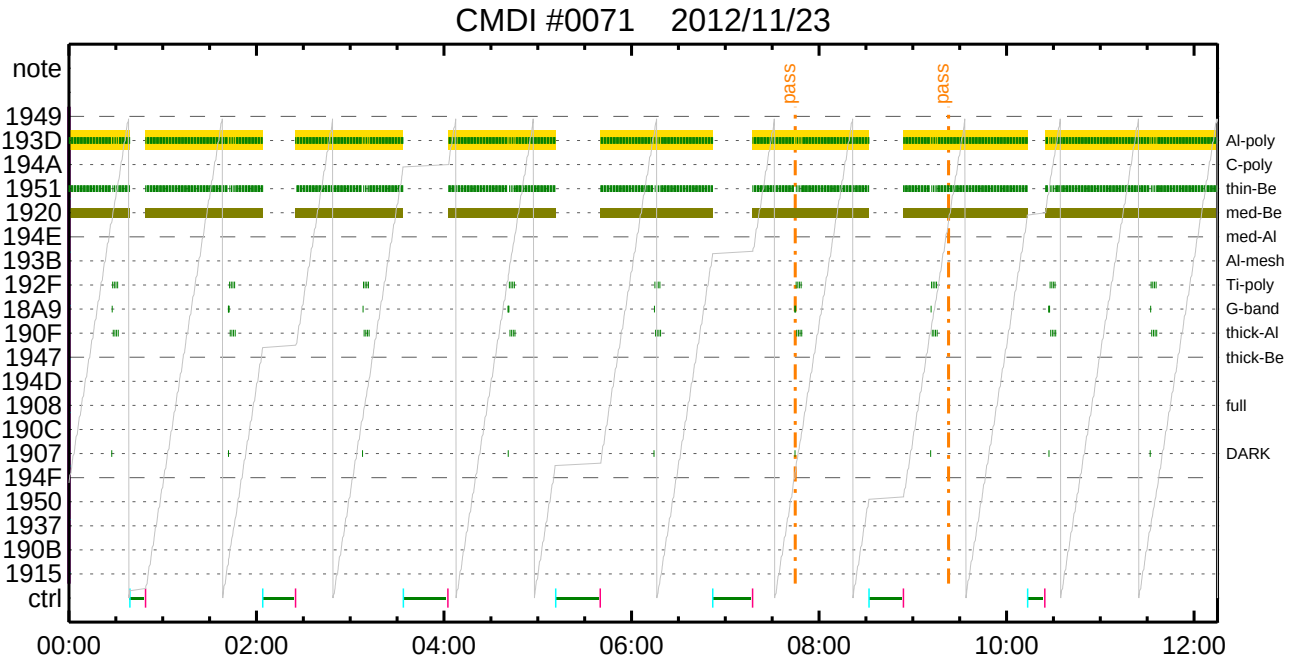
FLD Patrol

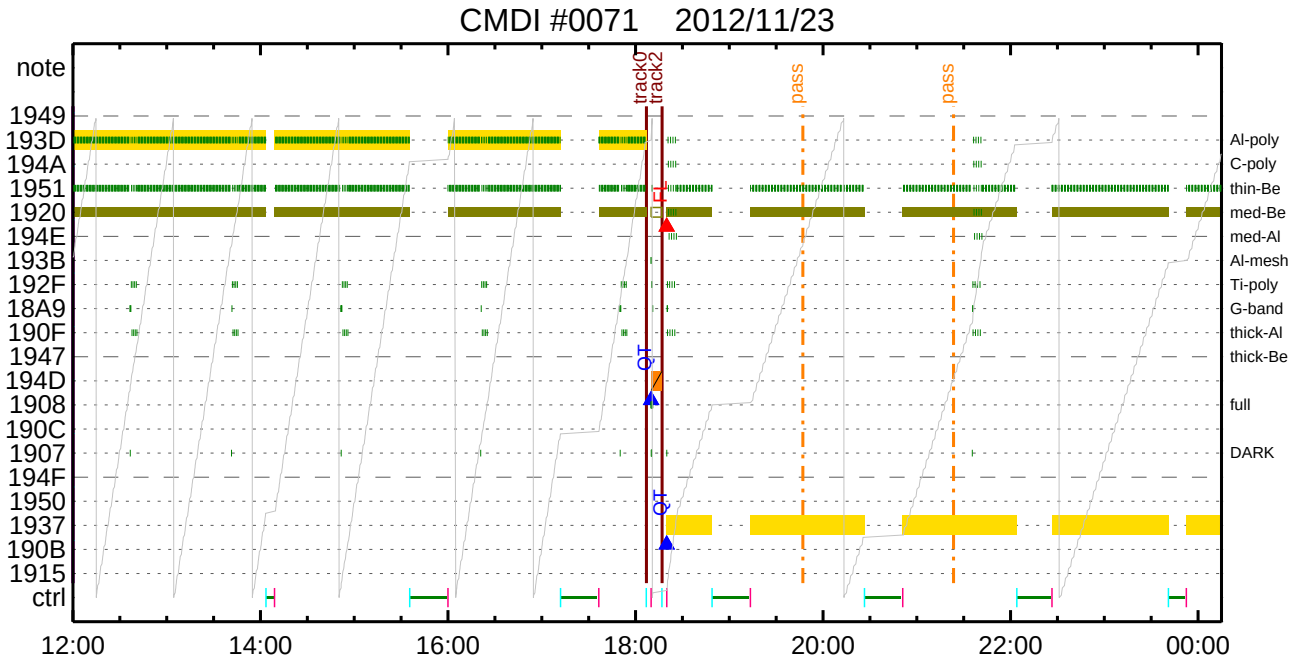
Term	Pointing (x, y)	Comment
11/22 17:32:46 - 11/23 18:07:16	Track (145.1, 80.4) @ 11/22 17:30:00	# AR11618 obs MMFW
11/23 18:19:46 - 11/27 09:49:00	Track (368.7, 84.4) @ 11/23 18:17:00	cont.

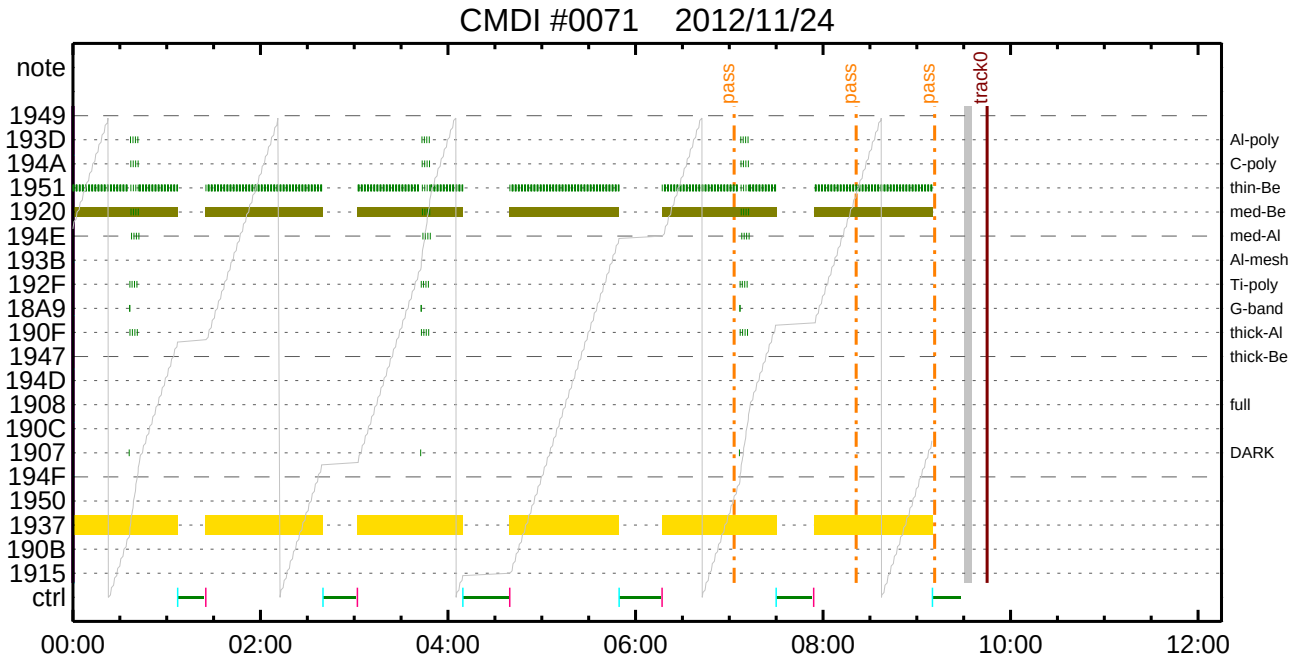
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

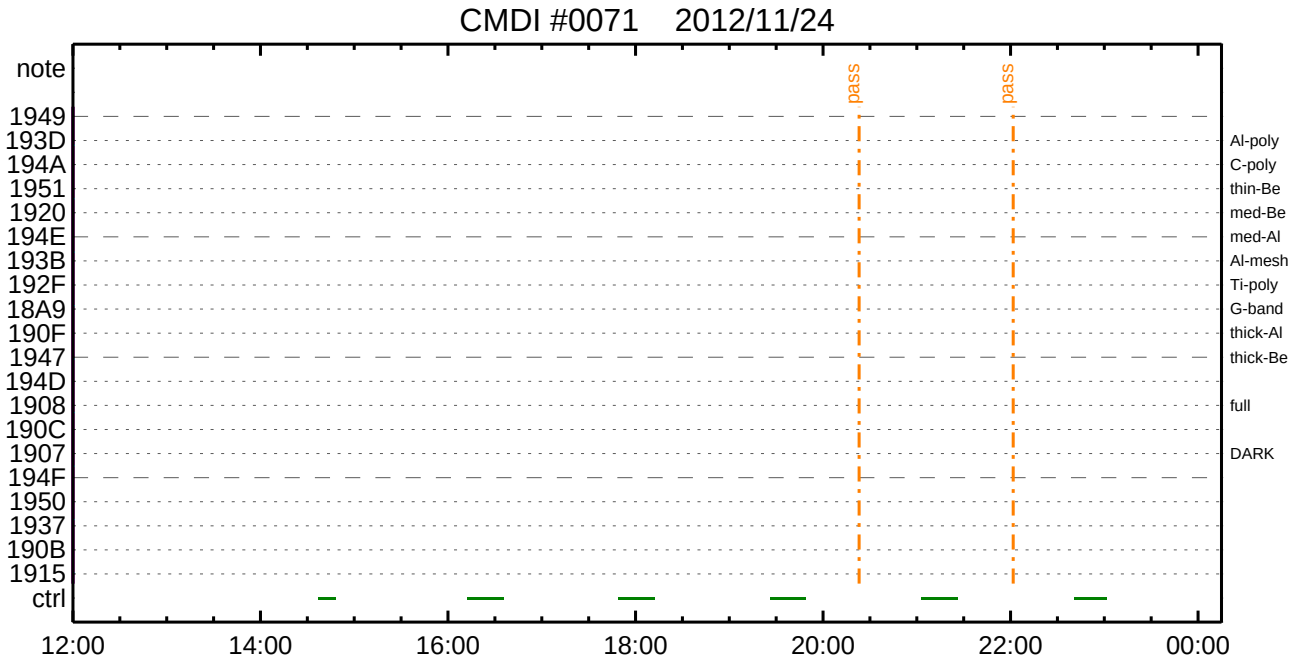

CMDI #0071 2012/11/22
note
before Tbl up
after Tbl up
20 1949 1949
19 193D 193D : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
18 194A 194A
17 1945 1951 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
16 1920 1920 : Flare obs. dynamics - thin-Be high cadence + conte ...
15 1944 194E : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
14 193B 193B
13 192F 192F
12 18A9 18A9
11 190F 190F
10 1947 1947
9 1942 194D : Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 ...
8 1908 1908
7 190C 190C
6 1907 1907
5 193C 194F : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
4 1943 1950 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
3 1937 1937 : AR Standard-B(Morphology) with PFB; thin-Be + mult ...
2 190B 190B
1 1915 1915
ctrl Table upload : 3rd pass (2012/11/22 09:37:50)
pass
pass
track2
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

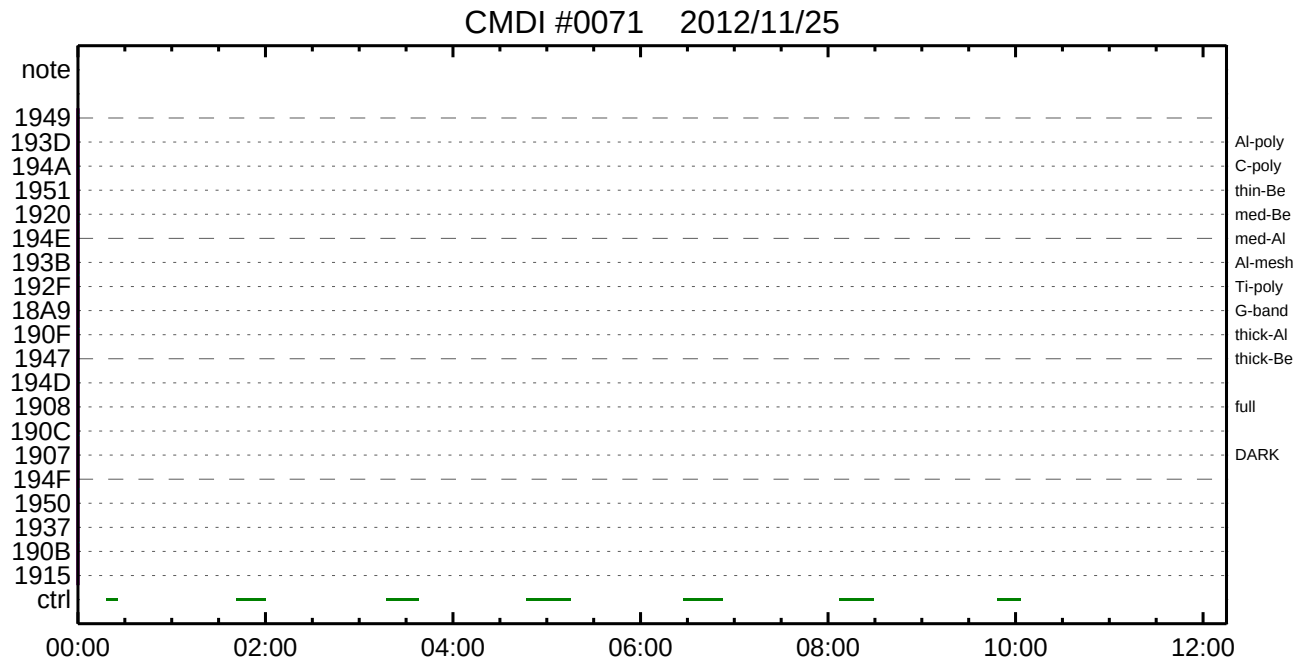

CMDI #0071 2012/11/22
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
track2
QT
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

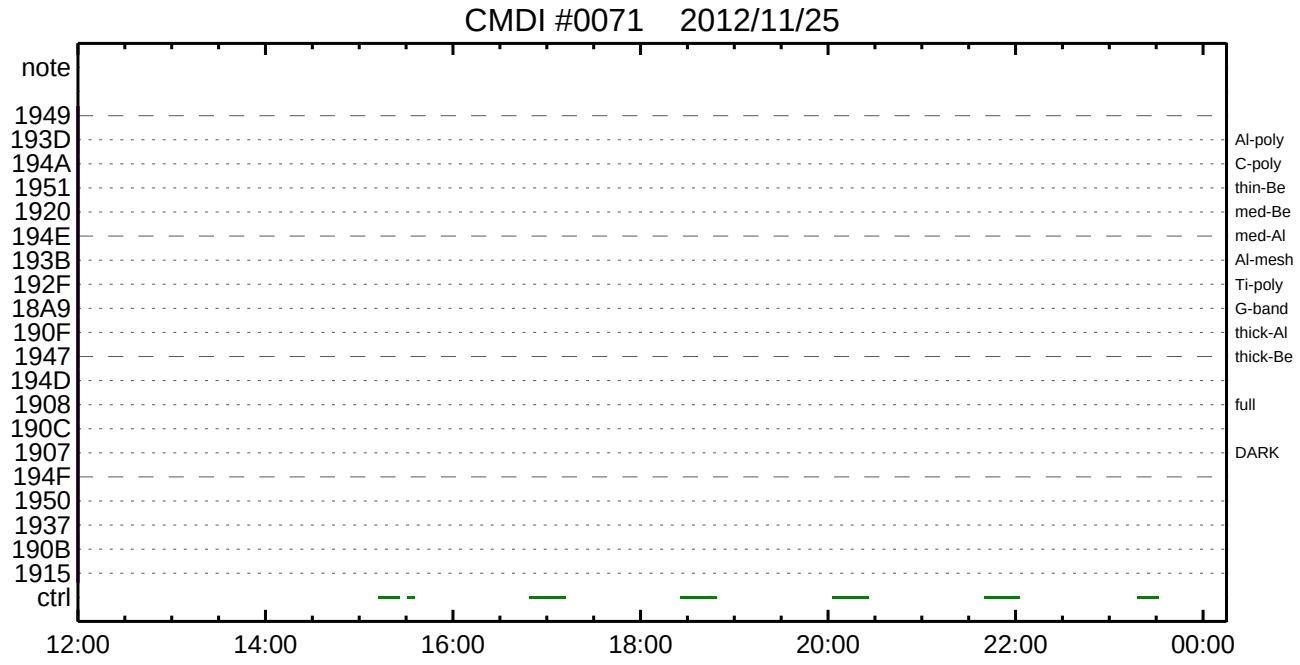

CMDI #0071 2012/11/23
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

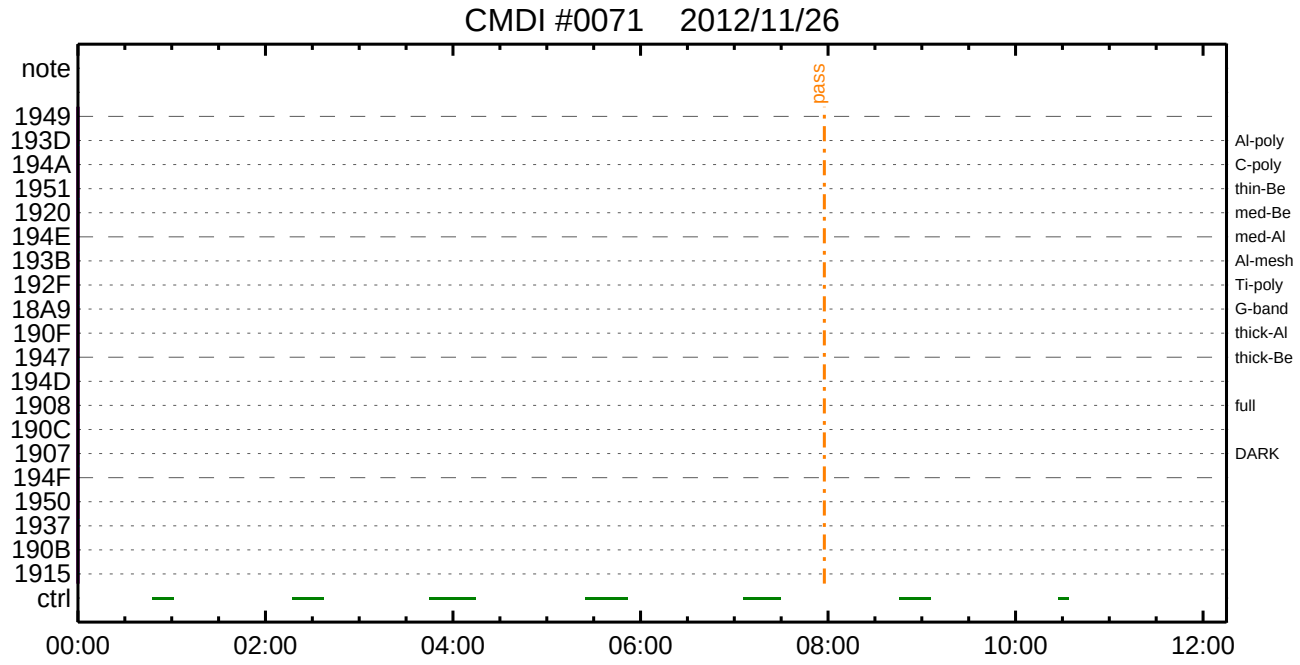

CMDI #0071 2012/11/23
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
track0
track2
pass
pass
OUT
IN
OUT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

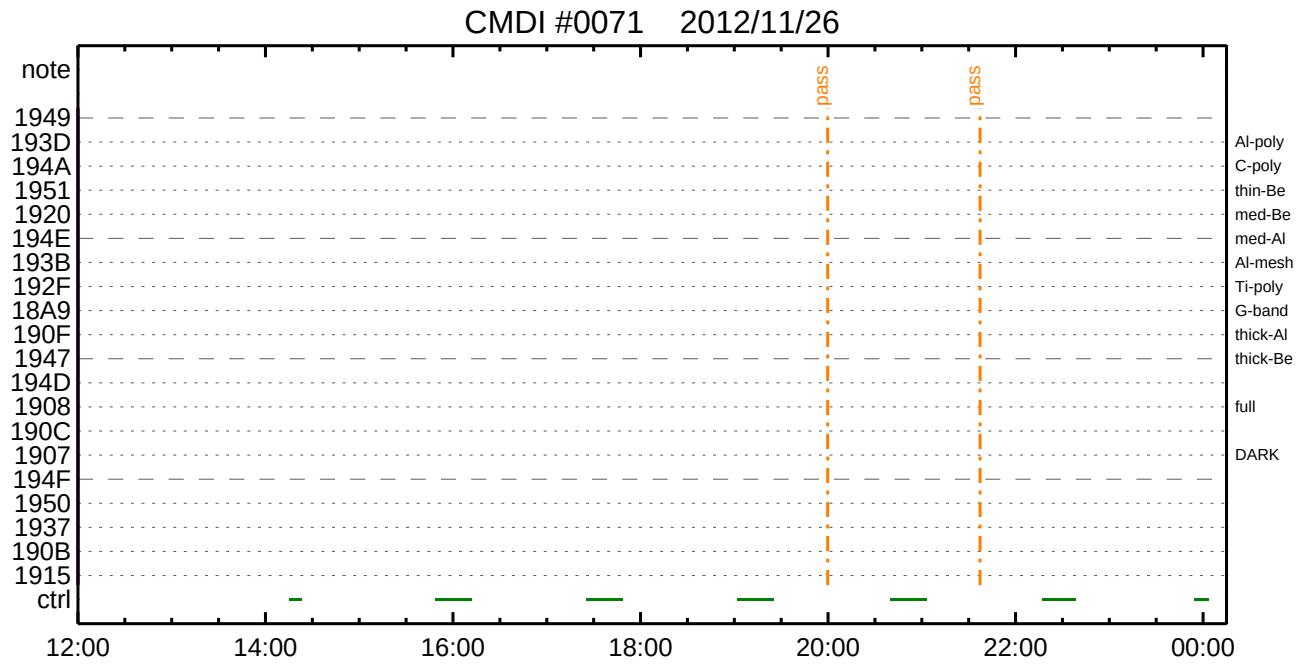

CMDI #0071 2012/11/24
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
pass
pass
pass
track0
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

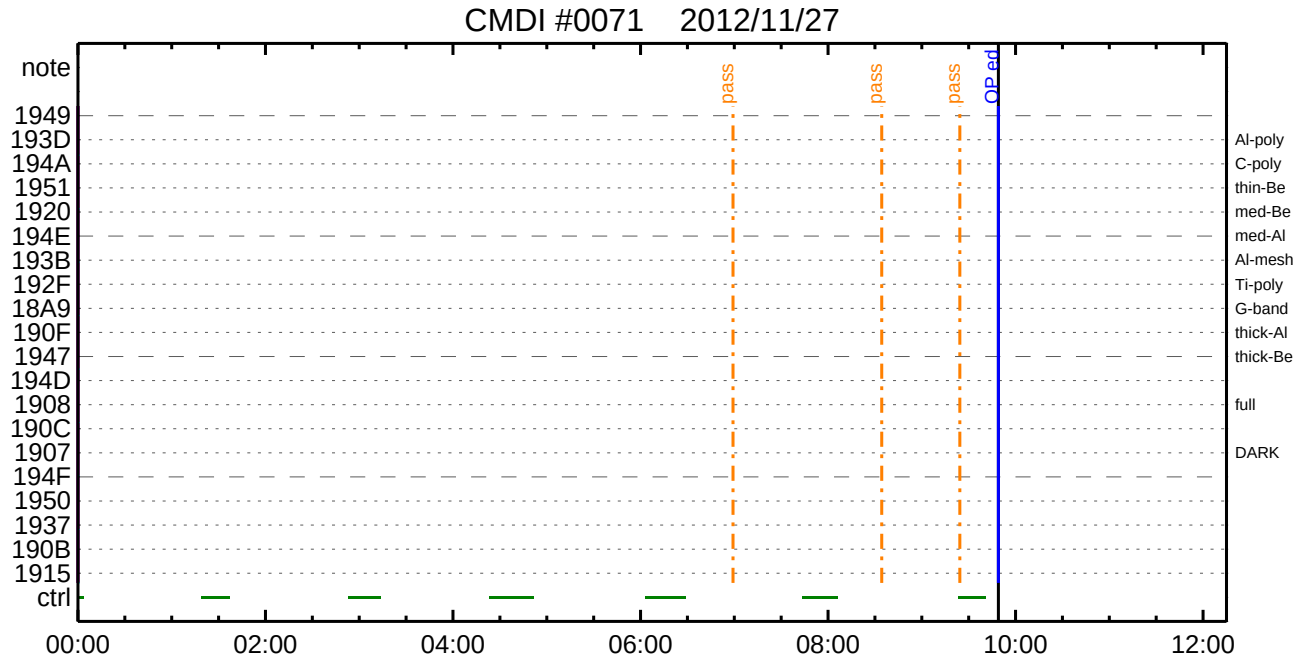

CMDI #0071 2012/11/24
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

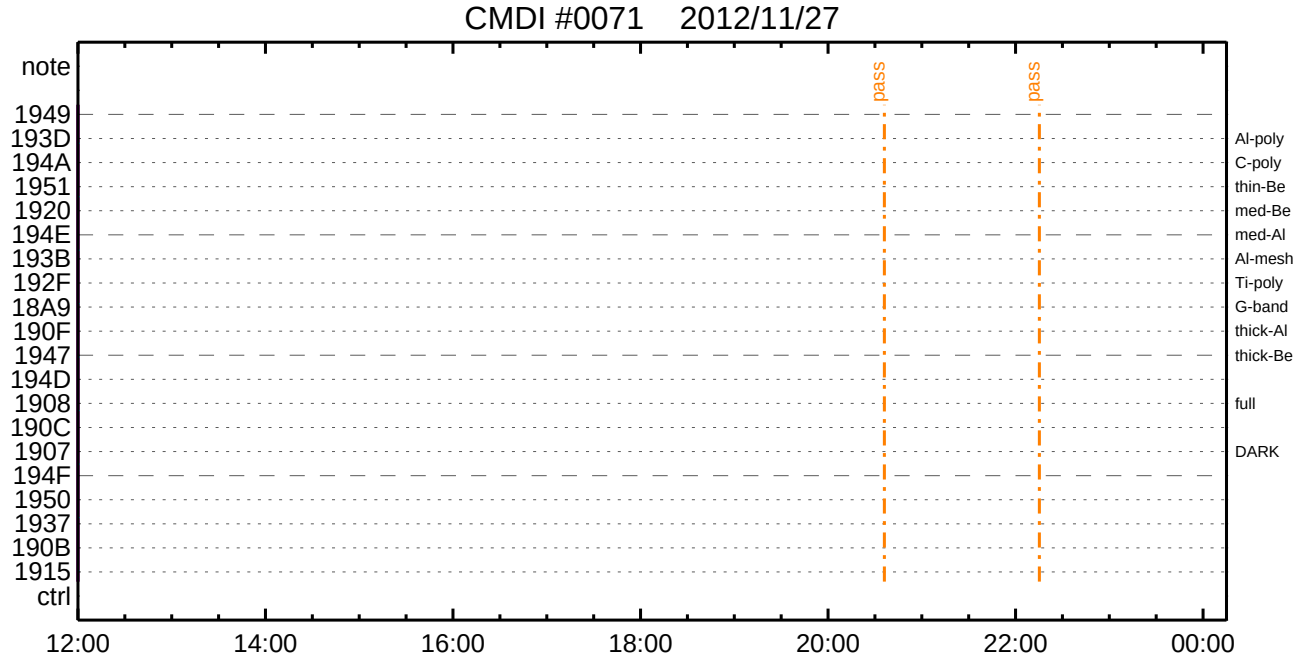

CMDI #0071 2012/11/25
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0071 2012/11/25
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0071 2012/11/26
note
pass
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0071 2012/11/26
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0071 2012/11/27
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0071 2012/11/27
note
1949
193D
194A
1951
1920
194E
193B
192F
18A9
190F
1947
194D
1908
190C
1907
194F
1950
1937
190B
1915
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-285 2012-11-22 12:29:02 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-285:OP
0104     ()
0105   . S. OG    og-285:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2012-11-22 09:54:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2012-11-22 09:54:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2012-11-22 09:54:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2012-11-22 09:58:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2012-11-22 09:58:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2012-11-22 09:58:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2012-11-22 09:58:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ************
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2012-11-22 09:58:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-286 2012-11-22 12:29:02 166 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_MANU
0131     BC       (21 02)
0132    . C. Verify EIS in MANUAL mode
0133    . C. Estimated OBSTBL upload time is 10s
0134     C. ********************************
0135     C. EIS START OBSTBL LOAD
0136     C. ********************************
0137    . S. RAM   ram-820:EIS_OBSTBL
0138     ()
0139   +. DC 07-FC EIS_DUMP_OBSTBL
0140     BC       (07 07 07 00 00 70 00)
0141     C.
0142     C. Execute, after the success of OBSTBL upload.
0143     C. Set EIS TI-commands
0144   +. TI 2012-11-22 09:58:50.0
0145     DC 07-FC EIS_MODE_CHG_ENA
0146     BC       (20)
0147    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0148     C. ********************************
0149     C. EIS END OBSTBL LOAD
0150     C. ********************************
0151     C.
0152    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0153     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0154    . S. DC-BC dcbc-402:DCBC
0155     (MDP_known_event)
0156     C.
0157     C.
0158    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0159    . S. DC-BC dcbc-153:DCBC
0160     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161     C.
0162     C.
0163    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0164     C.
0165    . C. ********** LOS **********
0166     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-287 2012-11-22 12:29:02 151 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¾É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017    . C. < Stop FG table >
0018   +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020    . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024    . C. <Upload FG Observation Table>
0025    . S. RAM   ram-267:MDP_OBS_F
0026     ()
0027     C.
0028    . C. < Dump RAMID=MDP_OBS_F >
0029   +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035    . C. < Resume FG table (auto mode) >
0036   +. DC 07-F0 MDP_FG_CTRL_AUTO
0037     BC       (50)
0038    . C. ------------------------------------
0039     C.  MDP_FG_CTRL_MODE      = AUTO    [ ]
0040     C. ------------------------------------
0041     C.
0042     C. ********************************
0043     C.  SOT TI command set
0044     C. ********************************
0045     C. Execute, after the success of TBL upload.
0046   +. TI 2012-11-22 09:58:18.0
0047     DC 07-F0 MDP_SOT_MODE_OBSV
0048     BC       (40)
0049    . C. ------------------------------------
0050     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0051     C. ------------------------------------
0052     C.
0053     C. Only when FG_CTRL_AUTO is used in RT.
0054   +. TI 2012-11-22 09:58:20.0
0055     DC 07-F0 MDP_FG_CTRL_AUTO
0056     BC       (50)
0057    . C. ------------------------------------
0058     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0059     C. ------------------------------------
0060     C. ************ SOT END ***********
0061     C.
0062     C. ************ XRT START ************
0063     C.
0064   +. DC 07-F0 MDP_XRT_CTRL_MANU
0065     BC       (c1)
0066   +  DC 07-F0 MDP_XRT_MODE_STBY
0067     BC       (c3)
0068    . C. ------------ Success Verify ?              OK / NG____
0069     C.
0070     C. XRT Obs. Table Upload
0071    . S. RAM   ram-291:MDP_OBS_X
0072     ()
0073     C.
0074   +. DC 07-F0 MDP_DUMP_XRTTBL
0075     BC       (84 07 00 00 00 3a d4)
0076    . C. ------------ Comparison Check ?                        OK / ERR ____
0077     C.
0078     C.
0079   +. DC 07-F0 MDP_XRT_ROI_SET
0080     BC       (cd 01 b1 b1 04 04)
0081   +  DC 07-F0 MDP_XRT_ROI_SET
0082     BC       (cd 02 b1 b1 08 08)
0083   +  DC 07-F0 MDP_XRT_ROI_SET
0084     BC       (cd 03 b1 b1 08 08)
0085   +  DC 07-F0 MDP_XRT_ROI_SET
0086     BC       (cd 04 b1 b1 06 06)
0087   +  DC 07-F0 MDP_XRT_ROI_SET
0088     BC       (cd 05 85 83 06 06)
0089   +  DC 07-F0 MDP_XRT_ROI_SET
0090     BC       (cd 06 85 83 06 06)
0091   +  DC 07-F0 MDP_XRT_ROI_SET
0092     BC       (cd 07 c0 c0 10 10)
0093   +  DC 07-F0 MDP_XRT_ROI_SET
0094     BC       (cd 08 80 80 20 20)
0095   +  DC 07-F0 MDP_XRT_ROI_SET
```

```
0096     BC       (cd 09 40 c0 10 10)
0097   +  DC 07-F0 MDP_XRT_ROI_SET
0098     BC       (cd 0a 40 40 10 10)
0099   +  DC 07-F0 MDP_XRT_ROI_SET
0100     BC       (cd 0b c0 40 10 10)
0101   +  DC 07-F0 MDP_XRT_ROI_SET
0102     BC       (cd 0c 80 80 20 08)
0103   +  DC 07-F0 MDP_XRT_ROI_SET
0104     BC       (cd 0d 80 80 08 20)
0105   +  DC 07-F0 MDP_XRT_ROI_SET
0106     BC       (cd 0e 85 83 08 08)
0107   +  DC 07-F0 MDP_XRT_ROI_SET
0108     BC       (cd 0f 80 80 06 06)
0109   +  DC 07-F0 MDP_XRT_ROI_SET
0110     BC       (cd 10 80 80 08 08)
0111   +  DC 07-F0 MDP_XRT_FLD_DIS
0112     BC       (d9)
0113   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0114     BC       (c9)
0115   +  DC 07-F0 MDP_XRT_AEC_RESET
0116     BC       (d0)
0117   +  DC 07-F0 MDP_XRT_ARS_DIS
0118     BC       (d5)
0119   +  DC 07-F0 MDP_XRT_FLD_RESET
0120     BC       (da)
0121   +  DC 07-F0 MDP_XRT_QT_PROG_SET
0122     BC       (c4 03)
0123    . C. ---------- Success Verify ?            OK / NG ____
0124     C.
0125     C.
0126    . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0127     C.
0128   +. DC 07-F0 MDP_XRT_MODE_OBSV
0129     BC       (c2)
0130   +. TI 2012-11-22 09:58:02.0
0131     DC 07-F0 MDP_XRT_MODE_OBSV
0132     BC       (c2)
0133    . C. ---------- Success Verify ?            OK / NG ____
0134     C.
0135     C. ************ XRT END ***********
0136     C.
0137    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0138     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¾Â»Ü¤¹¤ë)
0139    . S. DC-BC dcbc-402:DCBC
0140     (MDP_known_event)
0141     C.
0142     C.
0143    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0144    . S. DC-BC dcbc-153:DCBC
0145     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0146     C.
0147     C.
0148    . C. ¡ãLOS¥Á¥§¥Ã¥¯¾Â»Ü¡ä
0149     C.
0150    . C. *********** LOS ***********
0151     C.
```

```
*** OP Sequence for XRT ***

2012/11/22  10:09:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/11/22  10:10:01.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  10:10:03.0  XRT_TCIB_XRT_S_HTR_A_DIS_431_OG [0x1af]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2012/11/22  16:59:54.0  XRT_CTRL_MANU_437_OG [0x1b5]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  17:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 2e f1 2e f1
2012/11/22  17:01:32.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/11/22  17:01:52.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2012/11/22  17:01:54.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/11/22  17:01:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/11/22  17:01:58.0  XRT_ARS_DIS_412_OG [0x19c]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/22  17:02:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  17:04:54.0  XRT_CTRL_MANU_437_OG [0x1b5]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  17:05:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 2e f1 d1 0f
2012/11/22  17:06:32.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/11/22  17:06:52.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/11/22  17:06:54.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/11/22  17:06:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/11/22  17:06:58.0  XRT_ARS_DIS_412_OG [0x19c]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/22  17:07:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  17:09:54.0  XRT_CTRL_MANU_437_OG [0x1b5]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  17:10:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 d1 0f d1 0f
2012/11/22  17:11:32.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/11/22  17:11:52.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2012/11/22  17:11:54.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/11/22  17:11:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/11/22  17:11:58.0  XRT_ARS_DIS_412_OG [0x19c]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/22  17:12:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  17:14:54.0  XRT_CTRL_MANU_437_OG [0x1b5]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  17:15:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 d1 0f 2e f1
2012/11/22  17:16:32.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/11/22  17:16:52.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/11/22  17:16:54.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/11/22  17:16:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/11/22  17:16:58.0  XRT_ARS_DIS_412_OG [0x19c]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/22  17:17:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  17:19:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  17:19:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/11/22  17:20:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/11/22  17:20:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/11/22  17:20:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/11/22  17:20:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/22  17:22:58.0  XRT_QT_PROG_SET_445_OG [0x1bd]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2012/11/22  17:23:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  17:29:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  17:30:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/11/22  17:32:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
```

```
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/11/22  17:32:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/11/22  17:32:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/11/22  17:32:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/11/22  17:32:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/22  17:32:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/22  17:32:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2012/11/22  17:32:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/11/22  17:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  18:12:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  18:12:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/22  18:12:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/22  18:15:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/22  18:36:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/22  18:37:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  19:49:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  19:49:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/22  19:49:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/22  19:52:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/22  20:13:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/22  20:14:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  21:27:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  21:27:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/22  21:27:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/22  21:30:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/22  21:49:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/22  21:50:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/22  23:04:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/22  23:04:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/22  23:04:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/22  23:07:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/22  23:21:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/22  23:22:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  00:39:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  00:39:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  00:39:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  00:42:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  00:48:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  00:49:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  02:04:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  02:04:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  02:04:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  02:07:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  02:24:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  02:25:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  03:34:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  03:34:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  03:34:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  03:37:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  04:01:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  04:02:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
```

```
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  05:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  05:11:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  05:11:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  05:14:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  05:39:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  05:40:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  06:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  06:52:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  06:52:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  06:55:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  07:16:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  07:17:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  08:32:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  08:32:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  08:32:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  08:35:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  08:53:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  08:54:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  10:13:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  10:13:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  10:13:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  10:16:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  10:23:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  10:24:30.5  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  14:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  14:03:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  14:03:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  14:06:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  14:08:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  14:09:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  15:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  15:35:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  15:35:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  15:38:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  15:59:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  16:00:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  17:12:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  17:12:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  17:12:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  17:15:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  17:35:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  17:36:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  18:06:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  18:06:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/11/23  18:07:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2012/11/23  18:07:16.0  XRT_FLD_DIS_404_OG [0x194]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2012/11/23  18:07:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/11/23  18:07:20.0  XRT_ARS_DIS_406_OG [0x196]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/23  18:09:58.0  XRT_QT_PROG_SET_445_OG [0x1bd]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2012/11/23  18:10:00.0  XRT_CTRL_AUTO_408_OG [0x198]
```

```
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  18:16:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  18:17:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2012/11/23  18:19:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/11/23  18:19:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/11/23  18:19:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/11/23  18:19:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/11/23  18:19:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/11/23  18:19:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  18:19:56.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2012/11/23  18:19:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/11/23  18:20:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  18:49:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  18:49:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  18:49:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  18:52:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  19:12:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  19:13:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  20:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  20:26:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  20:26:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  20:29:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  20:50:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  20:51:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  22:04:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  22:04:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  22:04:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  22:07:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  22:25:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  22:26:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/23  23:41:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/23  23:41:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/23  23:41:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/23  23:44:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/23  23:51:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/23  23:52:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/24  01:07:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/24  01:07:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/24  01:07:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/24  01:10:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/24  01:24:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/24  01:25:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/24  02:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/24  02:40:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/24  02:40:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/24  02:43:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/24  03:01:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/24  03:02:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/24  04:09:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/24  04:09:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
```

```
2012/11/24  04:09:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/24  04:12:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/24  04:38:30.0  XRT_Custom_434_OG [0x1b2]
2012/11/24  04:39:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/24  05:49:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/24  05:49:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/24  05:49:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/24  05:52:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/24  06:16:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/24  06:17:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/24  07:30:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/24  07:30:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/24  07:30:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/24  07:33:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/24  07:53:00.0  XRT_Custom_434_OG [0x1b2]
2012/11/24  07:54:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/11/24  09:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/11/24  09:10:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/11/24  09:10:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/11/24  09:13:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/11/24  09:45:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 00 00 00 00
```