

Period: 2012/11/27 09:49:00 - 2012/12/01 09:01:00

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XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context. 384x384 at 1064 1048. 120s-cad w/ G-Band VLS Closed Test

XOB #194B: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33)

Term	Pointing (x, y)	Comment
11/27 17:41:30 - 11/27 18:02:00	Fixed (0.0, 0.0)	synoptic, shifted -21.5 min , extend synoptic for SOT
PROG= 01 2-time(s) └─ Subr= 1 1-time(s) 85.0sec └─ Seqn= 82 1-time(s) 7.0sec └─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec └─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec └─ Seqn= 6 1-time(s) 22.0sec		

A	1	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	2	Seqn= 76	1-time(s)	8.0sec											
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	3	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Seqn= 77	1-time(s)	12.0sec											
	4	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	5	Seqn= 79	1-time(s)	10.0sec											
		C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	6	C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Seqn= 75	1-time(s)	10.0sec											
	7	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
		Seqn= 78	1-time(s)	15.0sec											
	8	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
9	Subr= 2	1-time(s)	150.0sec												
	Seqn= 34	1-time(s)	29.0sec												
	med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
	Seqn= 83	1-time(s)	84.0sec												
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
	Seqn= 69	2-time(s)	13.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
10	Seqn= 68	1-time(s)	10.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec		
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

PROG= 11 Inf.-time(s)												
Subr= 1		1-time(s)	2.0sec									
Seqn= 61		2-time(s)	2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0 2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 62		1-time(s)	30.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0 2.0sec
Subr= 3		30-time(s)	2.0sec									
Seqn= 60		2-time(s)	30.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
11/27 10:02:00 - 11/27 10:11:54	Fixed (-919.5, -19.0)	# OP start + 10min HOP 79 EWscans 1/21
11/27 10:15:00 - 11/27 10:21:54	Fixed (-720.5, -19.0)	2/21
11/27 10:25:00 - 11/27 10:31:54	Fixed (-521.4, -19.0)	3/21
11/27 10:35:00 - 11/27 10:41:54	Fixed (-322.3, -19.0)	4/21
11/27 10:45:00 - 11/27 10:51:54	Fixed (-123.3, -19.0)	5/21
11/27 10:55:00 - 11/27 11:01:54	Fixed (75.8, -19.0)	6/21
11/27 11:05:00 - 11/27 11:11:54	Fixed (274.9, -19.0)	7/21
11/27 11:15:00 - 11/27 11:21:54	Fixed (473.9, -19.0)	8/21
11/27 11:25:00 - 11/27 11:31:54	Fixed (673.0, -19.0)	9/21
11/27 11:35:00 - 11/27 11:41:54	Fixed (872.0, -19.0)	10/21
11/27 11:45:00 - 11/27 11:51:54	Fixed (-913.8, -226.5)	11/21
11/27 11:55:00 - 11/27 12:01:54	Fixed (-742.7, -285.3)	12/21
11/27 12:05:00 - 11/27 12:11:54	Fixed (-559.7, -289.6)	13/21
11/27 12:15:00 - 11/27 12:21:54	Fixed (-372.9, -292.1)	14/21
11/27 12:25:00 - 11/27 12:31:54	Fixed (-182.1, -293.5)	15/21
11/27 12:35:00 - 11/27 12:41:54	Fixed (13.0, -293.9)	16/21
11/27 12:45:00 - 11/27 12:51:54	Fixed (154.1, -293.5)	17/21
11/27 12:55:00 - 11/27 13:01:54	Fixed (344.9, -292.1)	18/21
11/27 13:05:00 - 11/27 13:11:54	Fixed (531.7, -289.6)	19/21
11/27 13:15:00 - 11/27 13:21:54	Fixed (714.7, -285.3)	20/21
11/27 13:25:00 - 11/27 13:31:54	Fixed (899.8, -245.5)	21/21
11/27 13:35:00 - 11/27 17:38:24	Fixed (924.0, 105.3)	AR 11618
11/27 18:26:30 - 11/27 20:56:54	Fixed (924.0, 105.3)	AR 11618, including SOT prominence observations over 21:00-21:30 UT.
11/27 21:00:00 - 11/27 21:16:30	Fixed (924.0, 105.3)	AR 11618, including SOT prominence observations over 21:00-21:30 UT.
11/27 21:43:30 - 11/28 05:56:54	Fixed (924.0, 105.3)	AR 11618, including SOT prominence observations over 21:00-21:30 UT.
11/28 06:10:00 - 11/28 10:19:54	Fixed (924.0, 105.3)	AR 11618
11/28 10:23:00 - 11/28 15:19:54	Fixed (-14.0, 881.0)	HOP 81 at the North pole
11/28 15:23:00 - 11/28 17:59:54	Fixed (924.0, 105.3)	AR 11618
11/28 18:13:00 - 11/28 18:38:30	Fixed (924.0, 105.3)	AR 11618
11/28 19:03:00 - 11/28 23:59:54	Track (229.0, -0.5) @ 11/28 19:00:00	ToO HOP 182 West 15deg
11/29 00:03:00 - 11/29 06:14:54	Fixed (924.0, 105.3)	AR 11618
11/29 06:28:00 - 11/29 08:59:00	Fixed (924.0, 105.3)	AR 11618

PROG= 16 15-time(s)												
Subr= 1		45-time(s)	10.0sec									
Seqn= 35		1-time(s)	2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Subr= 2		1-time(s)	10.0sec									
Seqn= 36		1-time(s)	2.0sec									
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Seqn= 37		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0 2.0sec
Seqn= 38		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

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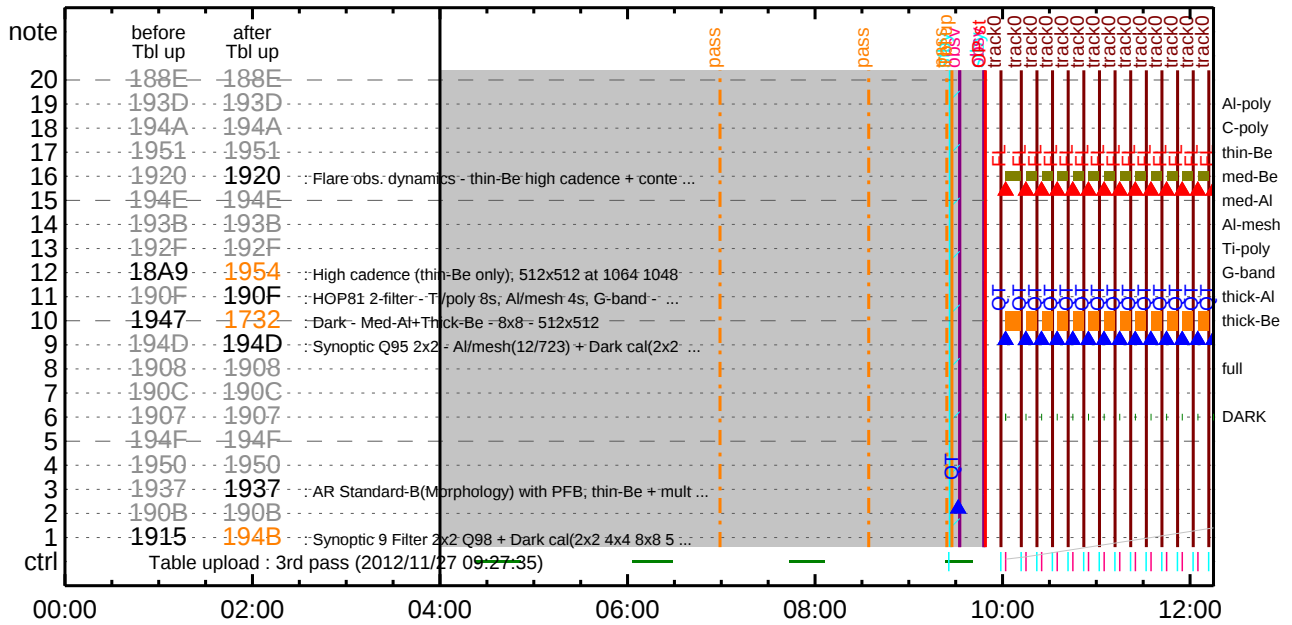
Flare Detection

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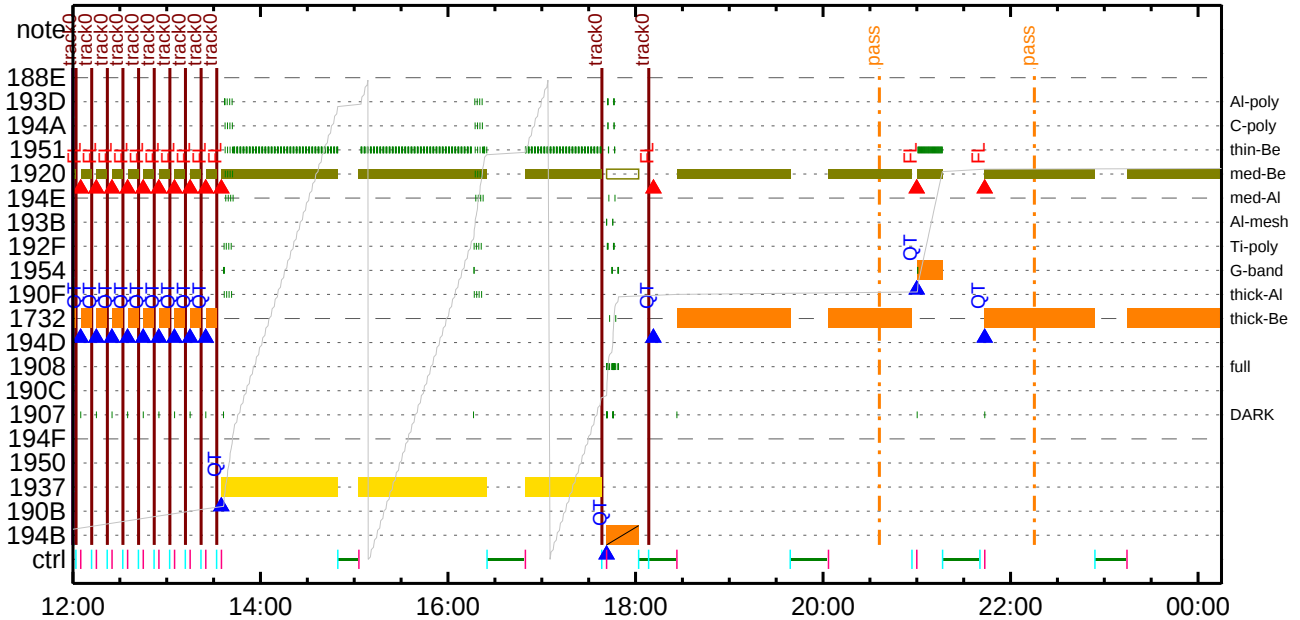
FLD Patrol		
Term	Pointing (x, y)	Comment
11/27 10:01:46 - 11/27 17:38:46	Fixed (-919.5, -19.0)	# OP start + 10min HOP 79 EWscans 1/21
11/27 18:11:16 - 11/28 05:57:16	Fixed (924.0, 105.3)	AR 11618, including SOT prominence observations over 21:00-21:30 UT.
11/28 06:09:46 - 11/28 18:00:16	Fixed (924.0, 105.3)	AR 11618
11/28 18:12:46 - 11/29 06:15:16	Fixed (924.0, 105.3)	AR 11618
11/29 06:27:46 - 12/01 09:01:00	Fixed (924.0, 105.3)	AR 11618

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

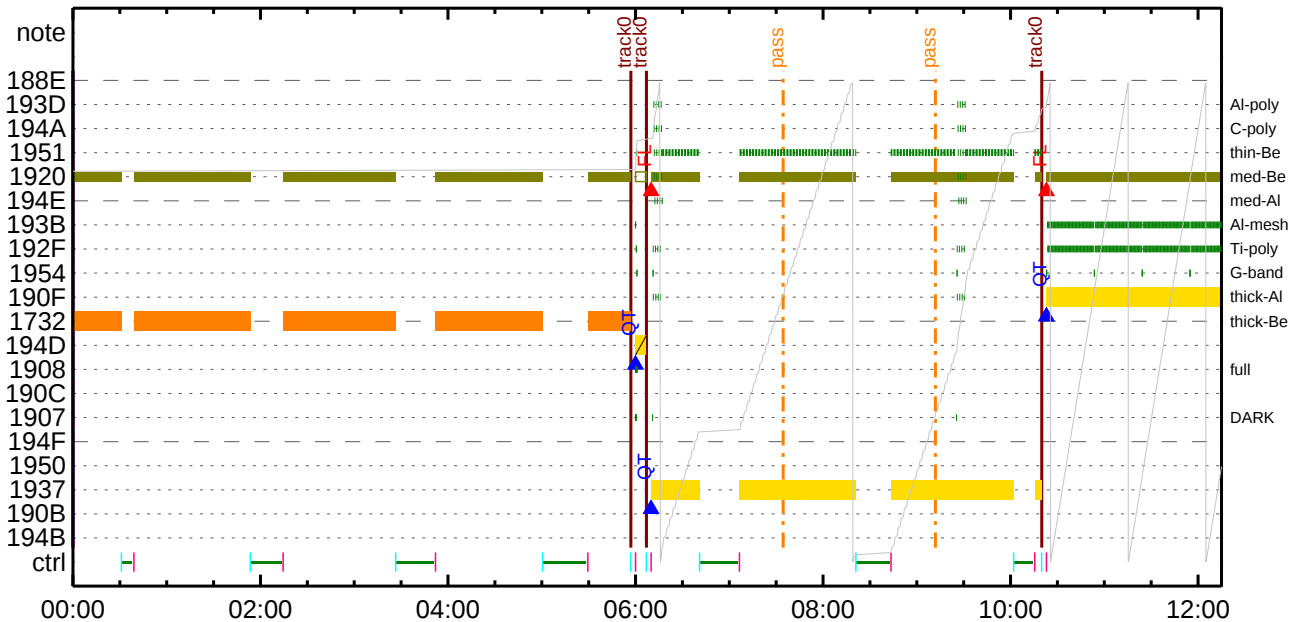
CMDI #0081 2012/11/27



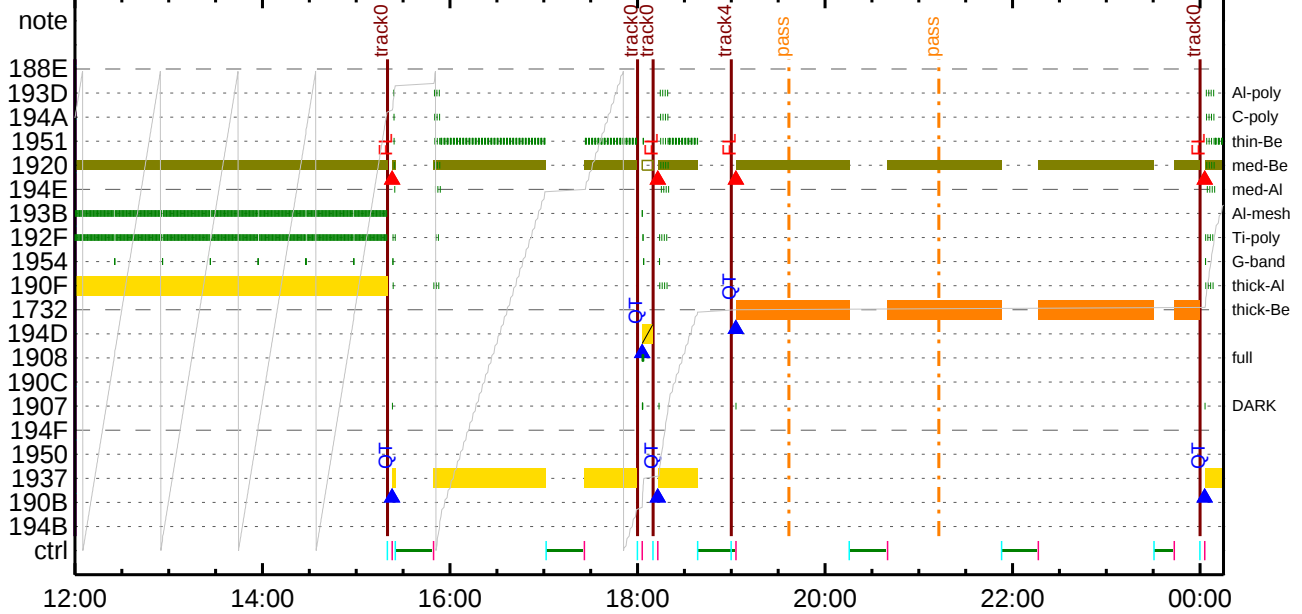
CMDI #0081 2012/11/27



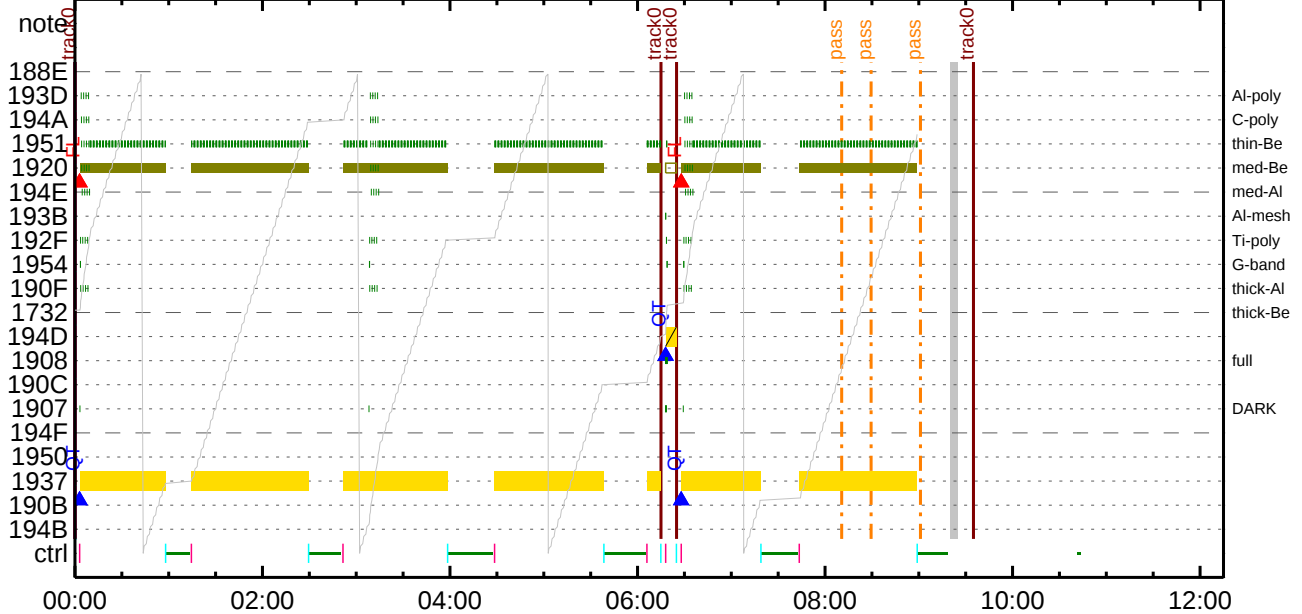
CMDI #0081 2012/11/28



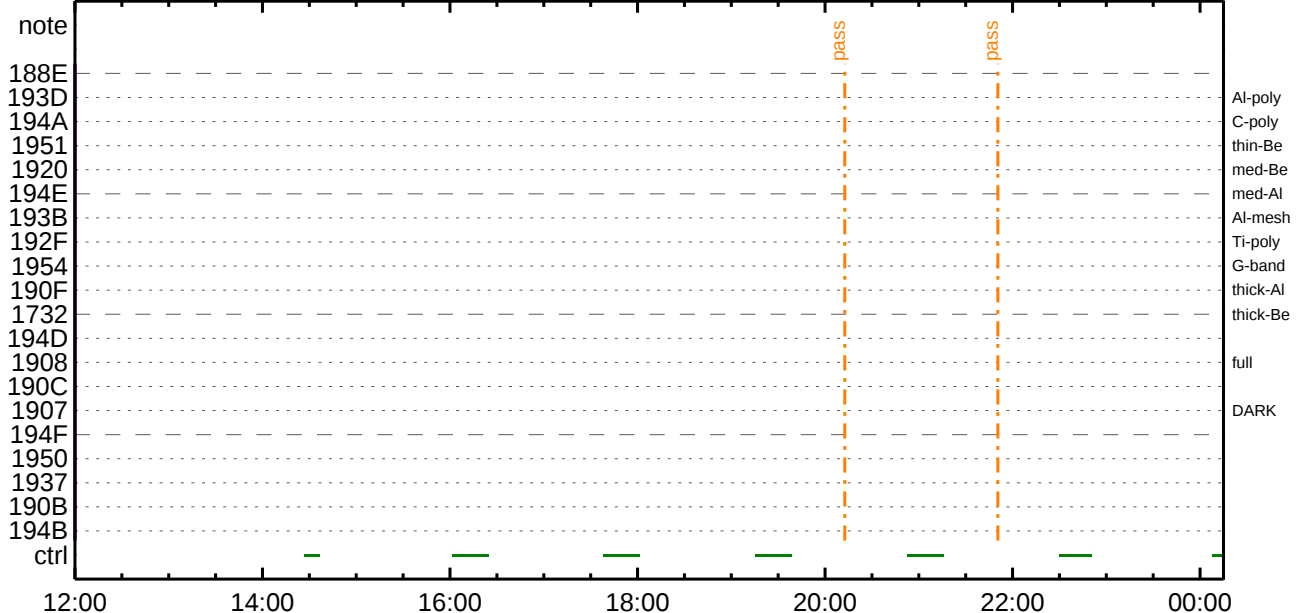
CMDI #0081 2012/11/28



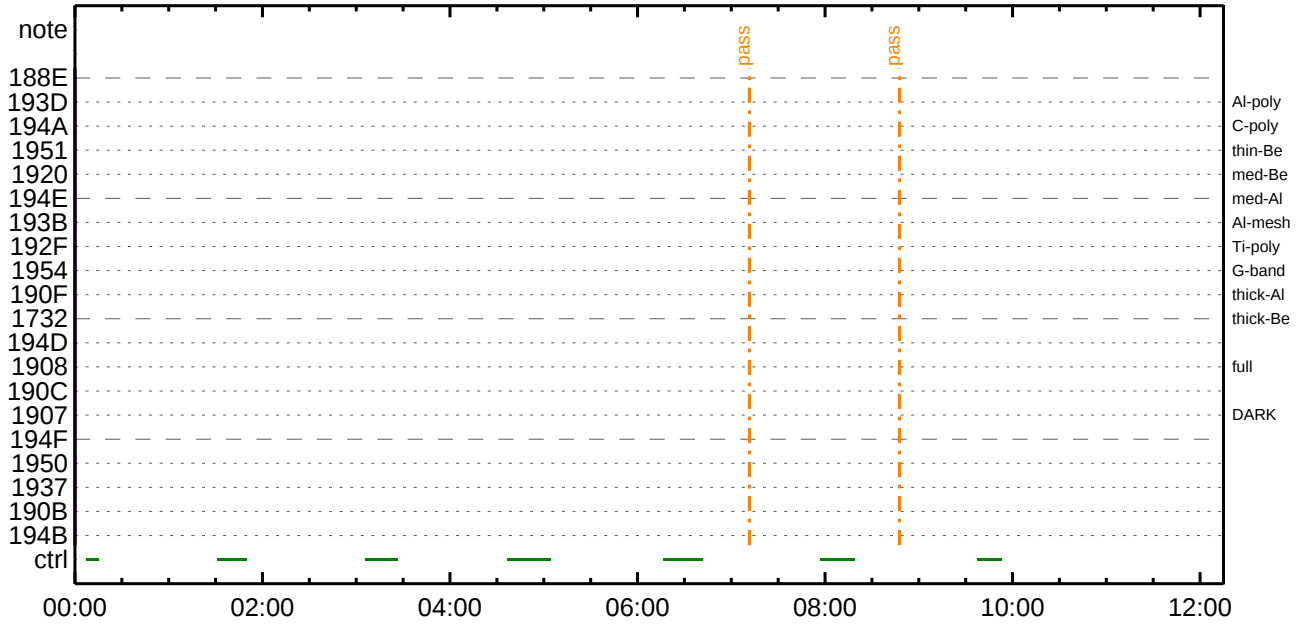
CMDI #0081 2012/11/29



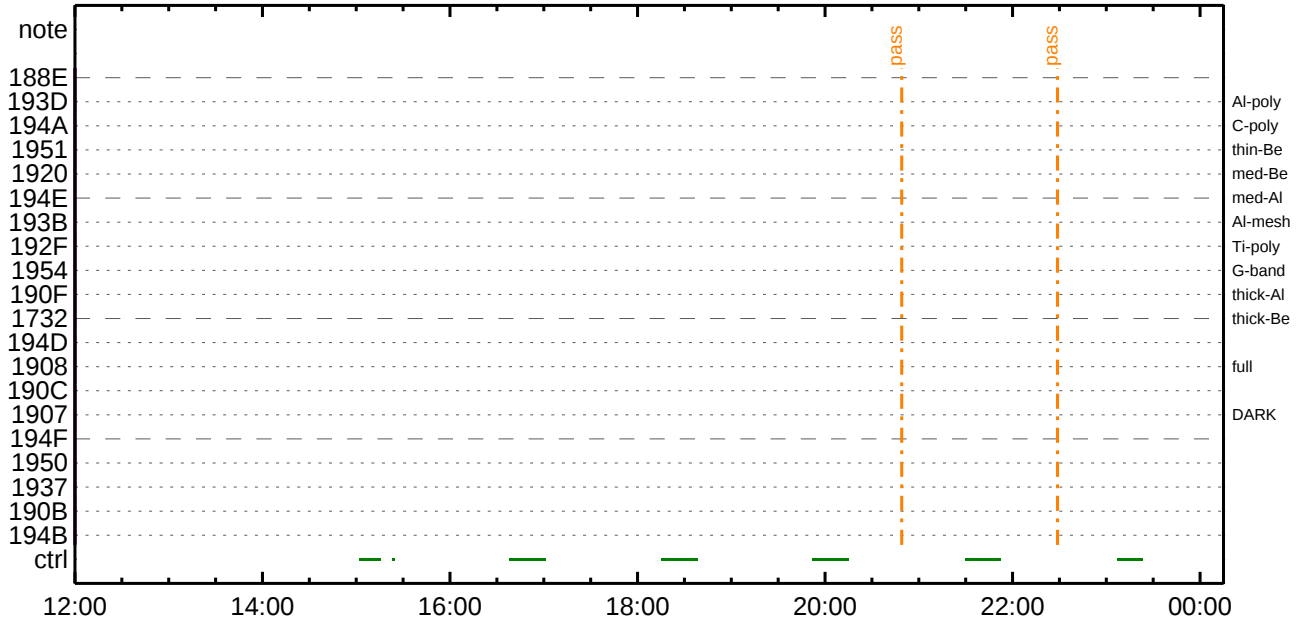
CMDI #0081 2012/11/29



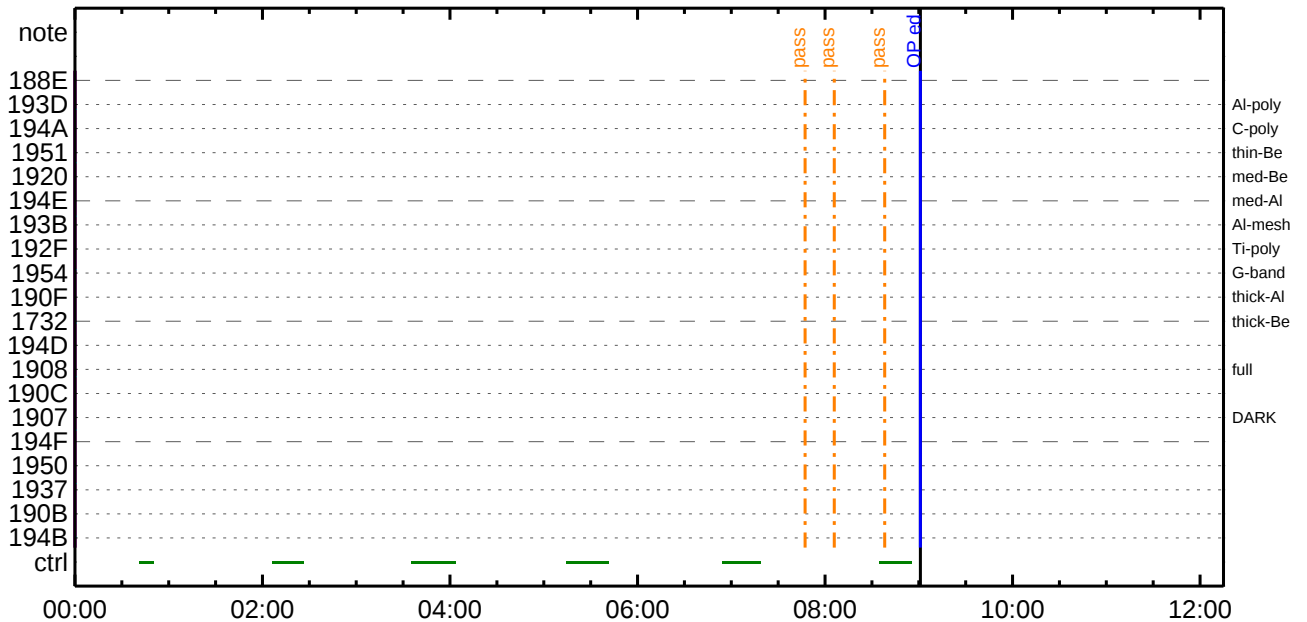
CMDI #0081 2012/11/30



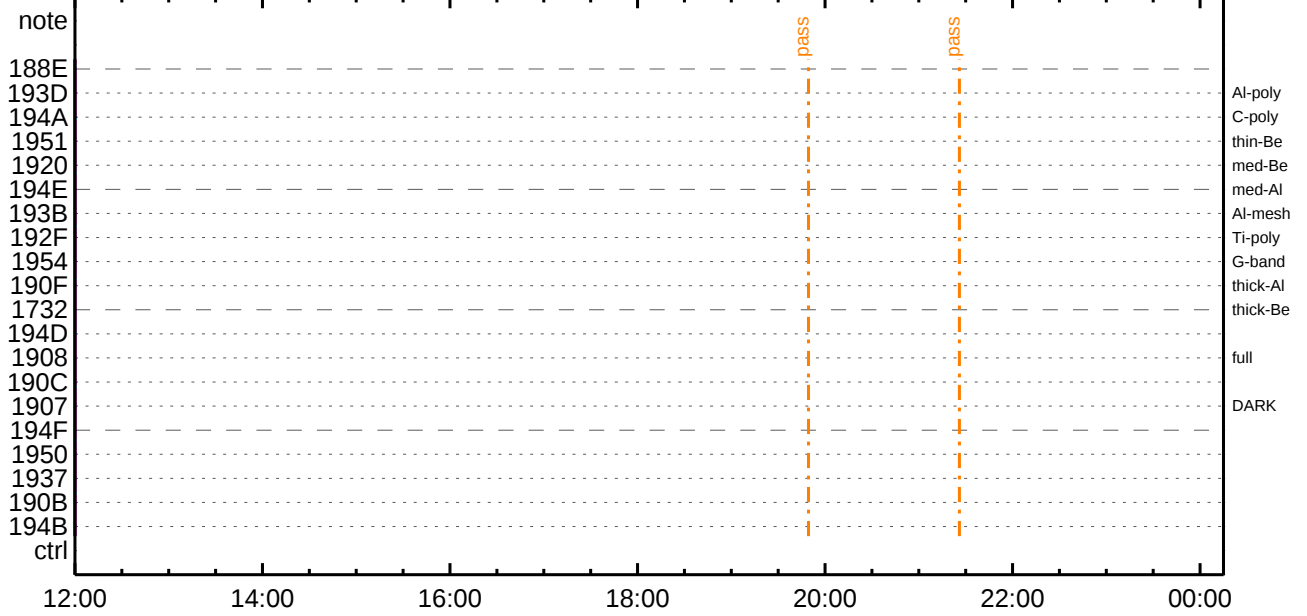
CMDI #0081 2012/11/30



CMDI #0081 2012/12/01



CMDI #0081 2012/12/01



(a) Spacecraft Operation Procedure (real-commands)

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main-294 2012-11-27 14:42:42 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¿•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ¿µ•İĒĒjĒ¿Ē°Ç¿¿•¿¿İ¹Ç¿İjÇÀ®, Ü¿¹¿Ē¿¿¿¿ÇÁ÷¿®¿•¿Ē¿¿¿¿¿Ē¿jĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ĒĒ, İ×ĒY¿¿ĒLOS¿¿¿¿İ»p´Ŏ¿Ŏ¹İİ, ¿•jÇĒŎİ×¿ĒXĒŎON¿İ¹Ŏ¿Ēİ¿Ē¿¿¿¿¿Ē¿jĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ŏ¥Ē¥İ¥Á¥¼ŎĀŎ¿-°ĒĒ¿¿•¿¿¿¿jÇ°Ē²¼¿İ°ĒĒ, ¼ĒĒ¿Ç¿¿¿¿Ē¹Ŏ¿¹¿ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ĒĒ
0033 C. *****
0034 C. ¨ RESTARTjĒPT1jĒ¿•¿¿¿¿¿İ¹Ç¿İjÇ°Ē²¼¿İ¼Ē¹Ŏ¿¿¿¿jÇDCBC-150¿Ŏ¿Ē¿¿¿jĒ
0035 C.
0036 . C. jãPT1°ĒĒ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Ŏ, j¼Ŏ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0045 C.
0046 . C. jã¥Ç¥Ŏ¥Ē¥ĒĒĒĒ¿jĒĒ•Ē°²ŎĒ¿jĒ, Ē¿İ°ĒĒ, °Ē³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Ŏ, j¼Ŏ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0052 C.
0053 C.
0054 . C. PT1°ĒĒ, ¿-¼«Ē°ĒĒ»¿¿¿¿¿, ĒjÇ°Ē²¼¿¿¿¿Ē¹Ŏ¿¹¿¿jĒ
0055 C. ¥Ç¥Ŏ¥Ē¥ĒĒĒĒ¿ĒĒ•Ē°²ŎĒ¿¿-ŎĒ¿¿¿İ¹Ç¿İ°İ°İ»¿¹¿Ē¿¿¿¿ĒĒ¿jĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ĒĒ
0059 C. *****
0060 C. ¨ RESTARTjĒPT2jĒ¿•¿¿¿¿¿İ¹Ç¿İjÇ°Ē²¼¿İ¼Ē¹Ŏ¿¿¿¿jÇDCBC-151¿Ŏ¿Ē¿¿¿jĒ
0061 C.
0062 . C. jãPT2°ĒĒ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Ŏ, j¼Ŏ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0071 C.
0072 . C. jã¥Ç¥Ŏ¥Ē¥ĒĒĒĒ¿jĒĒ•Ē°²ŎĒ¿jĒ, Ē¿İ°ĒĒ, °Ē³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Ŏ, j¼Ŏ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0078 C.
0079 . C. *****
0080 C. DR°ĒĒ, ĒĒ»¿jÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ĒĒ, ĒĒ»¿jã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 . C.  
0097 . C.  
0098 . C. *****  
0099 . C. OP/OG¶î¼¥É¡|¥À¥ó×  
0100 . C. *****  
0101 . C.  
0102 . C. ¡ãOP/OG¶î¼¥É¡ä  
0103 . S. OP op-294:OP  
0104 . (  
0105 . S. OG og-294:OG  
0106 . )  
0107 . C.  
0108 . C. ¡ãNMOG&OPÎÏ°è¥À¥ó×¡ä  
0109 . C. NMOG(0x200000-0x207FFF;S 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0111 BC (20 00 7f 01 02)  
0112 . C. ¶¢[HK1_DMP_TOP_ADRS_1] EQ 40  
0113 . C. ¶¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0114 . C. ¶¢[HK1_DMP_BLOCK_NUM] EQ 127  
0115 . C. ¶¢[HK1_DMP_REPEAT_NUM] EQ 0  
0116 . C. ¶¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0117 BC (07 0b f8)  
0119 . C. ¶¢[HK1_PKT_FORM_NO] EQ 7  
0120 . C. ¶¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0121 . C. ¶¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0122 . C. ¶¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0123 . C. ¶¢[HK1_DMP_CHK_FLG] EQ EXEC  
0124 . C. ¥À¥ó×½ªÎ»ð³İÇŞ  
0125 . C. ¶¢[HK1_DMP_CHK_FLG] EQ NON  
0126 . C. RAM ID=NMOG□Î%Ê¹Ç•ë²İOKðð³İÇŞ  
0127 . C.  
0128 . C. NMOG(0x208000-0x20FFFF;S 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0130 BC (20 80 7f 01 02)  
0131 . C. ¶¢[HK1_DMP_TOP_ADRS_1] EQ 41  
0132 . C. ¶¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0133 . C. ¶¢[HK1_DMP_BLOCK_NUM] EQ 127  
0134 . C. ¶¢[HK1_DMP_REPEAT_NUM] EQ 0  
0135 . C. ¶¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0137 BC (07 0b f8)  
0138 . C. ¶¢[HK1_PKT_FORM_NO] EQ 7  
0139 . C. ¶¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0140 . C. ¶¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0141 . C. ¶¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0142 . C. ¶¢[HK1_DMP_CHK_FLG] EQ EXEC  
0143 . C. ¥À¥ó×½ªÎ»ðð³İÇŞ  
0144 . C. ¶¢[HK1_DMP_CHK_FLG] EQ NON  
0145 . C. RAM ID=NMOG□Î%Ê¹Ç•ë²İOKðð³İÇŞ  
0146 . C.  
0147 . C. NMOG(0x210000-0x2100FF;S 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0149 BC (21 00 41 01 02)  
0150 . C. ¶¢[HK1_DMP_TOP_ADRS_1] EQ 42  
0151 . C. ¶¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0152 . C. ¶¢[HK1_DMP_BLOCK_NUM] EQ 65  
0153 . C. ¶¢[HK1_DMP_REPEAT_NUM] EQ 0  
0154 . C. ¶¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0155 BC (07 0b f8)  
0157 . C. ¶¢[HK1_PKT_FORM_NO] EQ 7  
0158 . C. ¶¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0159 . C. ¶¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0160 . C. ¶¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0161 . C. ¶¢[HK1_DMP_CHK_FLG] EQ EXEC  
0162 . C. ¥À¥ó×½ªÎ»ðð³İÇŞ  
0163 . C. ¶¢[HK1_DMP_CHK_FLG] EQ NON  
0164 . C. RAM ID=NMOG,RAM ID=OP□Î%Ê¹Ç•ë²İOKðð³İÇŞ  
0165 . C.  
0166 . C. **** °Ê²¼□İ%Ã´ŒÁ°¤ĖĖ-□Ó÷Ł® (%âµ-¥À¥ó×½ē¼ÇððÄÖÅæ□Ç¾ª°¨ ¢Ė%ı¹Ç□Çðâ) ****  
0167 . C. DHU¥â¡¼¥É¡Ė¼Y½, ¥ı¼¥É¡Ėðİāđ¹  
+ DC 01-22 DHU_MODE_CHNG  
0169 BC (02 0a f8)  
0170 . C. ¶¢[HK1_PKT_FORM_NO] EQ 2  
0171 . C. ¶¢[HK1_PKT_GEN_TIME] EQ 0.5S  
0172 . C. ¶¢[HK1_S_TLM_BIT_RATE] EQ 32K  
0173 . C. ¶¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0174 . C.  
0175 . C. *****  
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 . C. *****  
0178 . C.  
0179 . C. NOTICE ¡Ş OPOG UPLOAD¬-Á÷ŁNG□İ%ı¹Ç¡¢°Ê²¼□İTI-CMDÁ÷Ł®İ¼Ã¹Ôª•¤Ê¤¤³¤Ė¡£  
0180 . C. ¤ª¤Ł¡¢SET¤ĖDUMP¤İÆ±°ıŒN¥¹Ç¿¹Ôª!³¤Ė¡£  
0181 . C.  
0182 . C. Tİ¥³¥ª¤¥ÉððßİŁ(UT)  
+ TI 2012-11-27 09:44:00.0  
0184 DC 01-B3 DHU_OP_STOP  
0185 . C. ¶¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0186 . C.  
+ TI 2012-11-27 09:44:01.0  
0188 DC 01-B4 DHU_OP_COPY  
0189 . C. ¶¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0190 . C.  
0191 + TI 2012-11-27 09:44:01.0  
0192 DC 01-B5 DHU_OPOG_COPY  
0193 . C. ¶¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

0194	C.			
0195	+. TI	2012-11-27 09:48:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	☐☐[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê²¼☐İÄê%ıİŇ☐İ¥Á¥\$¥Ä¥¹äİÜ		
0200	C.	☐☐[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	☐☐[HK1_TI_CMD_NUM]	EQ	4
0202	C.	☐☐[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	☐☐[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİ°è¥Ä¥0¥×		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;S 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	☐☐[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	☐☐[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	☐☐[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	☐☐[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	☐☐[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	☐☐[HK1_PKT_FORM_NO]	EQ	7
0220	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Ä¥0¥×½ªİ»☐ò³İÇ\$		
0226	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL☐İ¥Ê¹Ç•ê²İOK☐ò³İÇ\$		
0229	C.			
0230	C.	DHU¥äı½¥ÉıÊ¼¥½, ¥İı½¥ÊıÊ☐òİä☐¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	☐☐[HK1_PKT_FORM_NO]	EQ	2
0234	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2012-11-27 09:48:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2012-11-27 09:48:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2012-11-27 09:48:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2012-11-27 09:48:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP ´ÜÄİ☐İ»ò¼¥☐ÊÄĐ☐¹☐êDCBC•×²è *****		
0276	C.	(%â°İ¥0¥Ä¥Ê¥½¥Ê¥â¥Ç¥ê☐Ê½☐☐¥Ä»Ü☐¹☐ê)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥Đ¥¹•İ Daily±çİŇ☐Ê´Đ☐¹☐êDCBC•×²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	ıäLOS¥Ä¥\$¥Ä¥½¥Ä»Üıä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			

(a) Spacecraft Operation Procedure (real-commands)

```
main-295 2012-11-27 14:42:42 175 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¿•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ¿µ•İĒĒjĒ¿Ē°Ç¿¿•¿¿İ¹Ç¿İjÇÀ®, Ü¿¹¿Ē¿¿¿¿ÇÁ÷¿®¿•¿Ē¿¿¿¿¿Ē¿jĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¿¿¿LOS¿¿¿Ç¿İ»p´Œ¿¿¹İĒ, ¿•jÇĒŒİ×¿ĒXĀŒŒON¿İ¹Œ¿Ēİ¿¿¿¿¿¿¿Ē¿jĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Œ¥É¥İ¥Ã¥¼ŒĀŒ¿-°ĀĀĒ¿•¿¿¿¿jÇ°Ē²¼¿İ°ÆĀ, ¼Ē½Ç¿¿¿¿Ā¹Œ¿¹¿Ē¿jĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ RESTARTjĒPT1jĒ¿•¿¿¿¿¿İ¹Ç¿İjÇ°Ē²¼¿İ¼Ā¹Œ¿¿¿¿¿jÇDCBC-150¿Œ¿Ē¿¿¿jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Œ¿, j¼Ū)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ¿, j¼Ū)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ¿, j¼Ū)
0045 C.
0046 . C. jã¥Ç¥Œ¥Æ¥ĒĀŪĀ¿jĒĀ•Ā°²ŒĒ¿jĒ, Ā¿İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Œ¿, j¼Ū)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ¿, j¼Ū)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ¿, j¼Ū)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¿-¼«Æ°ĀĀ»İ¿•¿¿, ĀjÇ°Ē²¼¿¿¿¿Ā¹Œ¿¹¿Ē¿jĒ
0055 C. ¥Ç¥Œ¥Æ¥ĒĀŪĀ¿¿ĀĀ•Ā°²ŒĒ¿¿¿¿¿¿¿¿İ¹Ç¿İ°İ»¿¹¿Ē¿¿¿¿¿ĀŒ¿jĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ RESTARTjĒPT2jĒ¿•¿¿¿¿¿İ¹Ç¿İjÇ°Ē²¼¿İ¼Ā¹Œ¿¿¿¿¿jÇDCBC-151¿Œ¿Ē¿¿¿jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Œ¿, j¼Ū)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ¿, j¼Ū)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ¿, j¼Ū)
0071 C.
0072 . C. jã¥Ç¥Œ¥Æ¥ĒĀŪĀ¿jĒĀ•Ā°²ŒĒ¿jĒ, Ā¿İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Œ¿, j¼Ū)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ¿, j¼Ū)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ¿, j¼Ū)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Āã»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Āã»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 1737667.7 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_MANU
0140 BC (21 02)
0141 . C. Verify EIS in MANUAL mode
0142 . C. Estimated OBSTBL upload time is 1m22s
0143 C. *****
0144 C. EIS START OBSTBL LOAD
0145 C. *****
0146 . S. RAM ram-820:EIS_OBSTBL
0147 ( )
0148 +. DC 07-FC EIS_DUMP_OBSTBL
0149 BC (07 07 07 00 00 70 00)
0150 C.
0151 C. Execute, after the success of OBSTBL upload.
0152 C. Set EIS TI-commands
0153 +. TI 2012-11-27 09:48:50.0
0154 DC 07-FC EIS_MODE_CHG_ENA
0155 BC (20)
0156 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0157 C. *****
0158 C. EIS END OBSTBL LOAD
0159 C. *****
0160 C.
0161 . C. ***** MDP '0ÃÎï»0%ÝðÊÃÐ¹²ëDCBC•x²è *****
0162 C. (%ã°ï¥0¥Ã¥Ê¥ð¥Ê¥ã¥¢¥ê¥ë½¼¼¼Ã»Û¹²ë)
0163 . S. DC-BC dcbc-402:DCBC
0164 (MDP_known_event)
0165 C.
0166 C.
0167 . C. ***** ¥Ð¥¹•ï Daily±¿ÎÑ²Ë´0¹²ëDCBC•x²è *****
0168 . S. DC-BC dcbc-153:DCBC
0169 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170 C.
0171 C.
0172 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Û¡ã
0173 C.
0174 . C. ***** LOS *****
0175 C.

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```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 85 83 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0f 80 80 06 06)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 10 80 80 08 08)
0114 + DC 07-F0 MDP_XRT_FLD_DIS
0115 BC (d9)
0116 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0117 BC (c9)
0118 + DC 07-F0 MDP_XRT_AEC_RESET
0119 BC (d0)
0120 + DC 07-F0 MDP_XRT_ARS_DIS
0121 BC (d5)
0122 + DC 07-F0 MDP_XRT_FLD_RESET
0123 BC (da)
0124 + DC 07-F0 MDP_XRT_QT_PROG_SET
0125 BC (c4 03)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C.
0129 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0130 C.
0131 +. DC 07-F0 MDP_XRT_MODE_OBSV
0132 BC (c2)
0133 +. TI 2012-11-27 09:48:02.0
0134 DC 07-F0 MDP_XRT_MODE_OBSV
0135 BC (c2)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C. ***** XRT END *****
0139 C.
0140 . C. ***** MDP `ûÃÎ±Î»ö¼Ý±ĒÂð±±ēDCBC•x²è *****
0141 C. (%ã°Î¥0¥Ã¥Ē¥ð¥Ē¥ã¥Ĉ¥ê±Ē¼±¼Ã»Ů±±ē)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ¥Ð¥¹•İ Daily±¿İŇ±Ē´0±±ēDCBC•x²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ¡ãLOS¥Ã¥$¥Ã¥¬¼Ã»Ů¡ã
0152 C.
0153 . C. ***** LOS *****
0154 C.
```

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XRT_OGLIST_0081.chk

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*** OP Sequence for XRT ***

2012/11/27	09:58:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	09:59:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 01 b2 51 ba
2012/11/27	10:01:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	10:01:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	10:01:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	10:01:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	10:01:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	10:01:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	10:01:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	10:01:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	10:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	10:11:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	10:12:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 01 b2 40 08
2012/11/27	10:14:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	10:14:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	10:14:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	10:14:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	10:14:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	10:14:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	10:14:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	10:14:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	10:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	10:21:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	10:22:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 01 b2 2e 56
2012/11/27	10:24:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	10:24:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	10:24:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	10:24:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	10:24:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	10:24:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	10:24:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	10:24:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	10:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	10:31:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	10:32:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 01 b2 1c a3
2012/11/27	10:34:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	10:34:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	10:34:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	10:34:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	10:34:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	10:34:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	10:34:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	10:34:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	10:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	10:41:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	10:42:00.0	AOCS_ORe-point_Start_5_OG [0x09b]			

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2012/11/27	10:44:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	5	02-76 00 01 b2 0a f1
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/27	10:44:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/27	10:44:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/27	10:44:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/27	10:44:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/27	10:44:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/27	10:44:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2012/11/27	10:44:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/11/27	10:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/27	10:51:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/27	10:52:00.0	AOCS_ORe-point_Start_6_OG [0x09c]		
		AOCU_NM	5	02-76 00 01 b2 f9 40
2012/11/27	10:54:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/27	10:54:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/27	10:54:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/27	10:54:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/27	10:54:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/27	10:54:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/27	10:54:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2012/11/27	10:54:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/11/27	10:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/27	11:01:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/27	11:02:00.0	AOCS_ORe-point_Start_7_OG [0x09d]		
		AOCU_NM	5	02-76 00 01 b2 e7 96
2012/11/27	11:04:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/27	11:04:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/27	11:04:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/27	11:04:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/27	11:04:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/27	11:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/27	11:04:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2012/11/27	11:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/11/27	11:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/27	11:11:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/27	11:12:00.0	AOCS_ORe-point_Start_8_OG [0x09e]		
		AOCU_NM	5	02-76 00 01 b2 d5 e4
2012/11/27	11:14:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/27	11:14:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/27	11:14:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/27	11:14:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/27	11:14:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/27	11:14:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/27	11:14:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2012/11/27	11:14:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/11/27	11:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/27	11:21:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/27	11:22:00.0	AOCS_ORe-point_Start_9_OG [0x09f]		
		AOCU_NM	5	02-76 00 01 b2 c4 32
2012/11/27	11:24:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/27	11:24:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8

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2012/11/27	11:24:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	11:24:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	11:24:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	11:24:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	11:24:56.0	XRT_QT_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	11:24:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	11:25:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	11:31:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	11:32:00.0	AOCS_ORe-point_Start_10_0G [0x0a0]	AOCU_NM	5	02-76	00 01 b2 b2 7f
2012/11/27	11:34:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	11:34:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	11:34:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	11:34:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	11:34:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	11:34:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	11:34:56.0	XRT_QT_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	11:34:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	11:35:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	11:41:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	11:42:00.0	AOCS_ORe-point_Start_11_0G [0x0a1]	AOCU_NM	5	02-76	00 14 20 51 37
2012/11/27	11:44:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	11:44:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	11:44:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	11:44:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	11:44:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	11:44:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	11:44:56.0	XRT_QT_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	11:44:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	11:45:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	11:51:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	11:52:00.0	AOCS_ORe-point_Start_12_0G [0x0a2]	AOCU_NM	5	02-76	00 19 58 42 04
2012/11/27	11:54:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	11:54:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	11:54:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	11:54:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	11:54:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	11:54:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	11:54:56.0	XRT_QT_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	11:54:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	11:55:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	12:01:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	12:02:00.0	AOCS_ORe-point_Start_13_0G [0x0a3]	AOCU_NM	5	02-76	00 19 ba 31 c2
2012/11/27	12:04:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	12:04:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	12:04:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	12:04:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	12:04:52.0	XRT_ARS_DIS_438_0G [0x1b6]				

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2012/11/27	12:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/27	12:04:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/27	12:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/27	12:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/27	12:11:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/27	12:12:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/27	12:14:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00	19 fb 21 26
2012/11/27	12:14:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/27	12:14:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/27	12:14:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/27	12:14:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/27	12:14:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/27	12:14:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/27	12:14:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/27	12:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/27	12:21:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/27	12:22:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/27	12:24:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00	1a 14 10 31
2012/11/27	12:24:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/27	12:24:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/27	12:24:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/27	12:24:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/27	12:24:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/27	12:24:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/27	12:24:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/27	12:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/27	12:31:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/27	12:32:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/27	12:34:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00	1a 1c fe da
2012/11/27	12:34:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/27	12:34:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/27	12:34:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/27	12:34:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/27	12:34:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/27	12:34:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/27	12:34:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/27	12:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/27	12:41:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/27	12:42:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/27	12:44:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00	1a 14 f2 4e
2012/11/27	12:44:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/27	12:44:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/27	12:44:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/27	12:44:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/27	12:44:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/27	12:44:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a

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2012/11/27	12:44:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/11/27	12:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/27	12:51:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/27	12:52:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	19	fb	e1	59
2012/11/27	12:54:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/11/27	12:54:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/27	12:54:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/27	12:54:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/27	12:54:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/27	12:54:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/27	12:54:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/11/27	12:54:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/11/27	12:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/27	13:01:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/27	13:02:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	19	ba	d0	bd
2012/11/27	13:04:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/11/27	13:04:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/27	13:04:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/27	13:04:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/27	13:04:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/27	13:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/27	13:04:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/11/27	13:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/11/27	13:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/27	13:11:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/27	13:12:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	19	58	c0	7b
2012/11/27	13:14:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/11/27	13:14:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/27	13:14:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/27	13:14:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/27	13:14:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/27	13:14:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/27	13:14:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/11/27	13:14:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/11/27	13:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/27	13:21:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/27	13:22:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	15	d2	b0	09
2012/11/27	13:24:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/11/27	13:24:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/27	13:24:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/27	13:24:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/27	13:24:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/27	13:24:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/27	13:24:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/11/27	13:24:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/11/27	13:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/27	13:31:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							

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2012/11/27	13:32:00.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCs_ORe-point_Start_22_OG [0x0ac]			
		AOCU_NM	5	02-76	00 f6 a0 ad dc
2012/11/27	13:34:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	13:34:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	13:34:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	13:34:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	13:34:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	13:34:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	13:34:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/11/27	13:34:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	13:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	14:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	14:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	14:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/27	14:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	15:02:00.0	XRT_Custom_434_OG [0x1b2]			
2012/11/27	15:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	16:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	16:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	16:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/27	16:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	16:48:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/27	16:49:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	17:38:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	17:38:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/11/27	17:38:30.0	AOCs_ORe-point_Start_23_OG [0x0ad]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/11/27	17:38:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/11/27	17:38:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/11/27	17:38:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	17:41:28.0	XRT_QT_PROG_SET_409_OG [0x199]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/11/27	17:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	18:02:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	18:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	18:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/27	18:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	18:08:24.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	18:08:30.0	AOCs_ORe-point_Start_22_OG [0x0ac]			
		AOCU_NM	5	02-76	00 f6 a0 ad dc
2012/11/27	18:10:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	18:11:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	18:11:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	18:11:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	18:11:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	18:11:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	18:11:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	18:11:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	18:25:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/27	18:26:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	19:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	19:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]			

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2012/11/27	19:39:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	19:42:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/27	20:02:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	20:03:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	20:56:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	20:59:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	20:59:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	20:59:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	20:59:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	20:59:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	20:59:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	20:59:56.0	XRT_QT_PROG_SET_407_0G [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/11/27	20:59:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	21:00:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	21:16:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	21:16:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	21:16:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/27	21:19:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	21:40:24.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	21:42:56.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/27	21:43:16.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/27	21:43:18.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/27	21:43:20.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/27	21:43:22.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/27	21:43:24.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	21:43:26.0	XRT_QT_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/11/27	21:43:28.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/27	21:43:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/27	22:54:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/27	22:54:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/27	22:54:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/27	22:57:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	23:13:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/27	23:14:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	00:31:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	00:31:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	00:31:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	00:34:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	00:38:00.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	00:39:00.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	01:53:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	01:53:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	01:53:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	01:56:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	02:13:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	02:14:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	03:26:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	03:26:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da

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2012/11/28	03:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	03:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	03:51:00.0	XRT_Custom_434_OG [0x1b2]			
2012/11/28	03:52:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	05:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	05:00:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	05:00:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	05:03:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	05:28:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/28	05:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	05:56:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/11/28	05:57:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/11/28	05:57:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/11/28	05:57:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/11/28	05:57:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/28	05:59:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2012/11/28	06:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	06:06:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	06:07:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]			
		AOCU_NM	5	02-76	00 f6 a0 ad dc
2012/11/28	06:09:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/11/28	06:09:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/11/28	06:09:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/11/28	06:09:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/11/28	06:09:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/11/28	06:09:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	06:09:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/11/28	06:09:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/11/28	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	06:41:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	06:41:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	06:41:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	06:44:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	07:05:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/28	07:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	08:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	08:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	08:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	08:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	08:42:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/28	08:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	10:02:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	10:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/28	10:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/28	10:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/11/28	10:14:30.0	XRT_Custom_434_OG [0x1b2]			
2012/11/28	10:15:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/11/28	10:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/28	10:20:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]			
		AOCU_NM	5	02-76	00 b1 b3 01 3f

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2012/11/28	10:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/28	10:22:46.0	XRT_FLD_ENA_418_OG [0x1a2]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/28	10:22:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/28	10:22:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/28	10:22:52.0	XRT_ARS_DIS_412_OG [0x19c]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/28	10:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/28	10:22:56.0	XRT_QT_PROG_SET_410_OG [0x19a]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2012/11/28	10:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/11/28	10:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/28	15:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/28	15:20:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]		
		AOCU_NM	5	02-76 00 f6 a0 ad dc
2012/11/28	15:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/28	15:22:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/28	15:22:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/28	15:22:50.5	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/28	15:22:52.5	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/28	15:22:54.5	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/28	15:22:56.5	XRT_QT_PROG_SET_427_OG [0x1ab]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2012/11/28	15:22:58.5	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/11/28	15:23:00.5	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/28	15:25:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/28	15:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/28	15:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/11/28	15:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/11/28	15:48:30.0	XRT_Custom_434_OG [0x1b2]		
2012/11/28	15:49:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/28	17:01:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/28	17:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/11/28	17:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/11/28	17:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/11/28	17:25:00.0	XRT_Custom_434_OG [0x1b2]		
2012/11/28	17:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/28	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/28	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2012/11/28	18:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]		
		AOCU_NM	5	02-76 00 00 00 00 00
2012/11/28	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2012/11/28	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2012/11/28	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/28	18:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 09
2012/11/28	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/11/28	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/11/28	18:10:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]		
		AOCU_NM	5	02-76 00 f6 a0 ad dc
2012/11/28	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/11/28	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/11/28	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/11/28	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/11/28	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/11/28	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]		

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2012/11/28	18:12:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/28	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2012/11/28	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/28	18:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/28	18:38:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/28	18:38:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/28	18:41:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/28	18:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/28	19:00:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/28	19:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	04	00 00 00 00
2012/11/28	19:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/28	19:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/28	19:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/28	19:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/28	19:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/28	19:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/28	19:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/28	19:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/28	20:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/28	20:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/28	20:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/28	20:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/28	20:39:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/28	20:40:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434	1	07-F0	c0	
2012/11/28	21:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/28	21:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/28	21:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/28	21:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/28	22:15:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/28	22:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434	1	07-F0	c0	
2012/11/28	23:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/28	23:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/28	23:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/28	23:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/28	23:42:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/28	23:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434	1	07-F0	c0	
2012/11/28	23:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	00:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	00:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00	f6 a0 ad dc
2012/11/29	00:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/29	00:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/29	00:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/29	00:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/29	00:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/29	00:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	00:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2012/11/29	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/29	00:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2012/11/29	00:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	00:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	01:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/29	01:13:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/29	01:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	02:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	02:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	02:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/29	02:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/29	02:50:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	02:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	03:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	03:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/29	03:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/29	04:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	04:27:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	04:28:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	05:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/29	05:38:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/29	05:38:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	05:41:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	06:05:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	06:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/29	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/29	06:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	06:15:00.0	AOCs_Or-point_Start_23_OG [0x0ad]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/11/29	06:15:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/11/29	06:15:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/11/29	06:15:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/11/29	06:17:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/29	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2012/11/29	06:24:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	06:25:00.0	AOCs_Or-point_Start_22_OG [0x0ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	06:27:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 f6 a0 ad dc	
2012/11/29	06:27:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/11/29	06:27:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/29	06:27:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/29	06:27:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/29	06:27:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/29	06:27:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	06:27:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2012/11/29	06:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/11/29	07:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	07:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/29	07:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/29	07:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/29	07:42:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/29	07:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/29	08:59:00.0	XRT_CTRL_MANU_400_OG [0x190]					

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2012/11/29	08:59:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/11/29	08:59:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/11/29	09:02:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/11/29	09:35:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00