

XRT Timeline to be uploaded on 2012/11/29

Period: 2012/11/29 09:25:00 - 2012/12/04 10:51:00

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Normal mode

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XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms													
Term	Pointing (x, y)						Comment						
11/29 09:38:00 - 11/29 15:59:54	Fixed (-14.0, -939.0)						# OP start + 10min, HOP 81, polar monitoring, S pole.						
PROG= 11 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 61 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 62 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 60 2-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1732: Dark - Med-Al+Thick-Be - 8x8 - 512x512													
Term	Pointing (x, y)						Comment						
11/29 16:28:00 - 11/29 17:38:00	Track (838.1, -252.4) @ 11/29 16:00:00						* Track AR 11620.						
11/30 03:03:00 - 11/30 07:57:00	Track (-183.1, 214.7) @ 11/30 03:00:00						* Track AR 11621.						
11/30 08:33:00 - 11/30 09:59:54	Track (895.3, -249.4) @ 11/30 08:30:00						* Track AR 11620						
11/30 10:03:00 - 11/30 14:59:54	Track (-69.1, 679.2) @ 11/30 10:00:00						* HOP 182, horizontal field study, first pointing.						
12/01 00:03:00 - 12/01 04:59:54	Track (-10.0, -852.1) @ 12/01 00:00:00						* ToO HOP 182, second pointing.						
12/01 06:13:00 - 12/01 08:34:30	Fixed (914.0, -260.0)						# Cont.						
PROG= 10 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 81 1-time(s) 2.0sec													
	med-Al/thick-Be	med-Al/thick-Be	close	Safe	Dark	500ms	Obs	8x8	512x512 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #194D: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/24)													
Term	Pointing (x, y)						Comment						
11/29 18:04:30 - 11/29 18:11:24	Fixed (0.0, 0.0)						synoptic, shifted 1.5 min						
11/30 08:23:00 - 11/30 08:29:54	Fixed (0.0, 0.0)						synoptic, shifted manually.						
11/30 17:54:30 - 11/30 18:01:24	Fixed (0.0, 0.0)						synoptic, shifted -8.5 min						
12/01 06:03:00 - 12/01 06:09:54	Fixed (0.0, 0.0)						synoptic						
PROG= 09 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 56 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 46 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 54 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 69 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #193D: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band													
Term	Pointing (x, y)						Comment						
11/29 18:14:30 - 11/30 02:59:54	Track (847.0, -252.0) @ 11/29 18:11:30						# Cont.						
11/30 15:18:30 - 11/30 17:51:24	Fixed (914.0, -260.0)						* AR 11620, fixed pointing near W limb.						
11/30 18:04:30 - 11/30 23:59:54	Fixed (914.0, -260.0)						# Cont.						
PROG= 19 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													

Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32		4-time(s)	2.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 93		30-time(s)	120.0sec									
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
12/01 05:03:00 - 12/01 05:59:54	Fixed (914.0, -260.0)	* AR 11620 at W limb.
PROG= 03 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 18 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 65 4-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 73 70-time(s) 120.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
11/29 09:38:00 - 11/29 15:59:54	Fixed (-14.0, -939.0)	# OP start + 10min, HOP 81, polar monitoring, S pole.
11/29 16:28:00 - 11/29 17:38:00	Track (838.1, -252.4) @ 11/29 16:00:00	* Track AR 11620.
11/29 18:14:30 - 11/30 02:59:54	Track (847.0, -252.0) @ 11/29 18:11:30	# Cont.
11/30 03:03:00 - 11/30 07:57:00	Track (-183.1, 214.7) @ 11/30 03:00:00	* Track AR 11621.
11/30 08:33:00 - 11/30 09:59:54	Track (895.3, -249.4) @ 11/30 08:30:00	* Track AR 11620
11/30 10:03:00 - 11/30 14:59:54	Track (-69.1, 679.2) @ 11/30 10:00:00	* HOP 182, horizontal field study, first pointing.
11/30 15:18:30 - 11/30 17:51:24	Fixed (914.0, -260.0)	* AR 11620, fixed pointing near W limb.
11/30 18:04:30 - 11/30 23:59:54	Fixed (914.0, -260.0)	# Cont.
12/01 00:03:00 - 12/01 04:59:54	Track (-10.0, -852.1) @ 12/01 00:00:00	* ToO HOP 182, second pointing.
12/01 05:03:00 - 12/01 05:59:54	Fixed (914.0, -260.0)	* AR 11620 at W limb.
12/01 06:13:00 - 12/01 08:34:30	Fixed (914.0, -260.0)	# Cont.
PROG= 16 15-time(s)		
Subr= 1 45-time(s) 10.0sec		
Seqn= 35 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
Seqn= 36 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 37 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 38 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

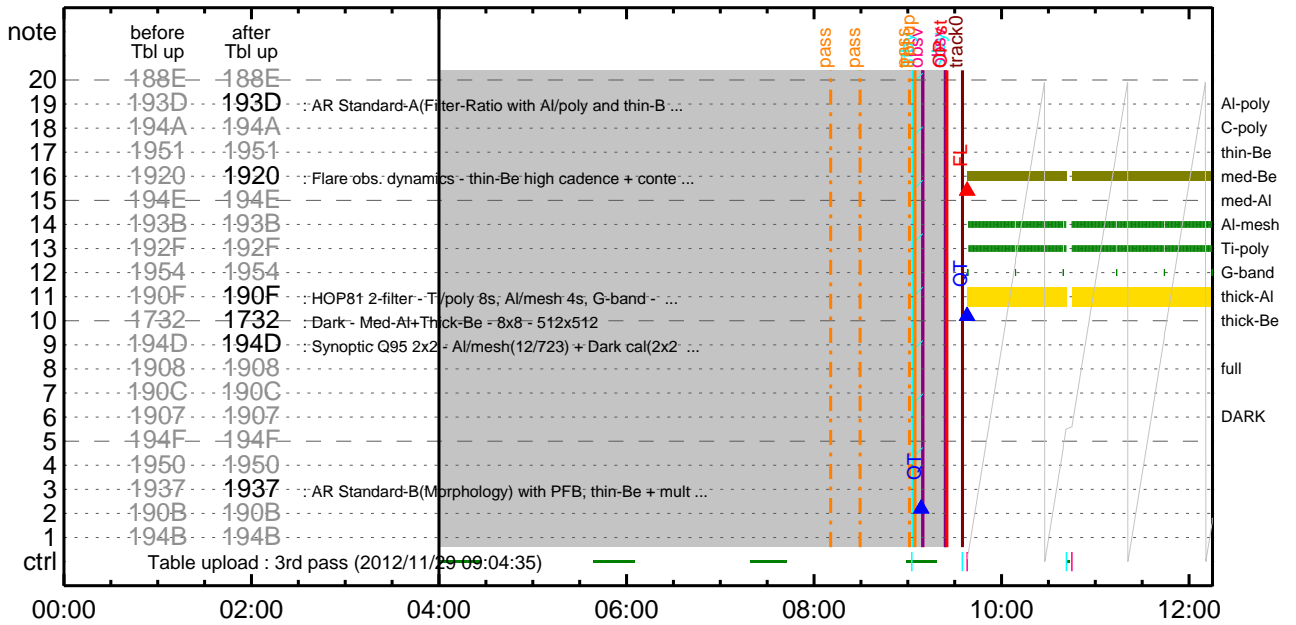
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Flare Detection

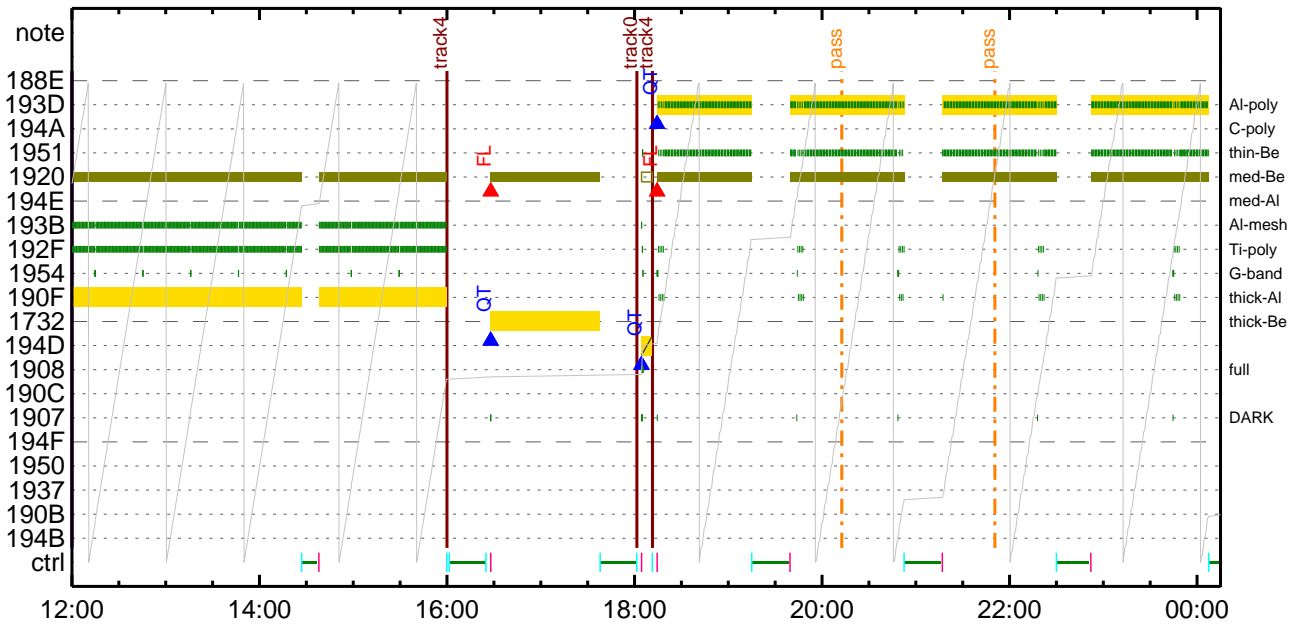
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FLD Patrol												
Term		Pointing (x, y)						Comment				
11/29 09:37:46 - 11/29 18:01:46		Fixed (-14.0, -939.0)						# OP start + 10min, HOP 81, polar monitoring, S pole.				
11/29 18:14:16 - 11/30 08:20:16		Track (847.0, -252.0) @ 11/29 18:11:30						# Cont.				
11/30 08:32:46 - 11/30 17:51:46		Track (895.3, -249.4) @ 11/30 08:30:00						* Track AR 11620				
11/30 18:04:16 - 12/01 06:00:16		Fixed (914.0, -260.0)						# Cont.				
12/01 06:12:46 - 12/04 10:51:00		Fixed (914.0, -260.0)						# Cont.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

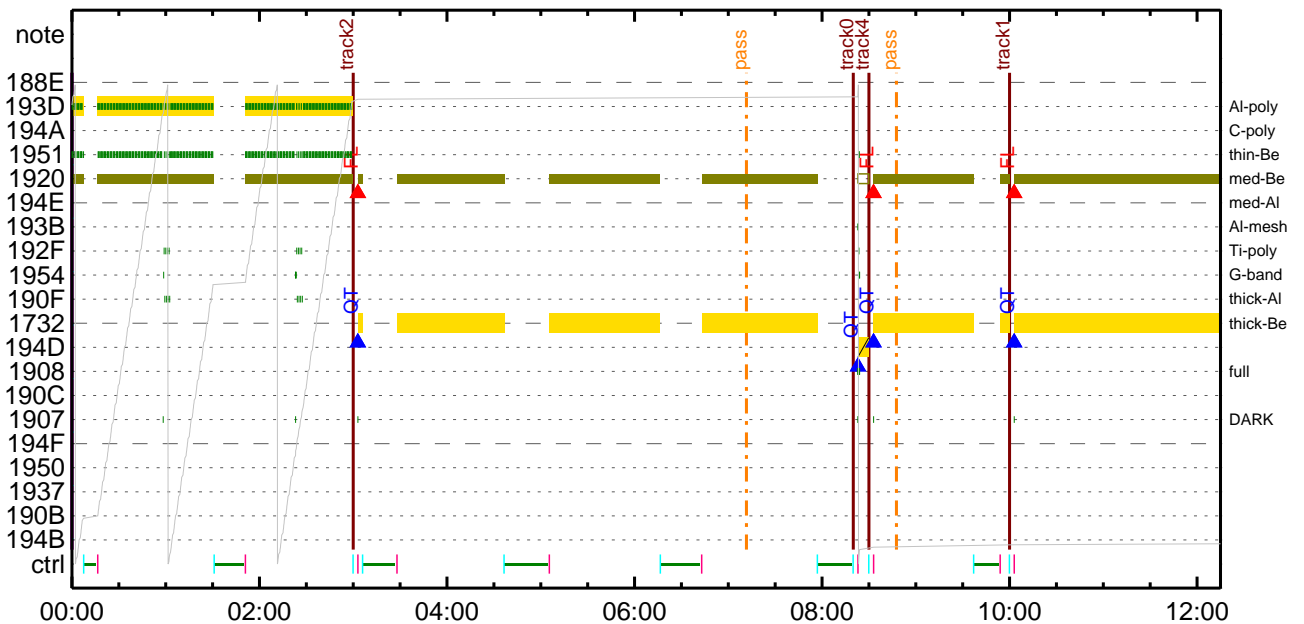
CMDI #0088 2012/11/29



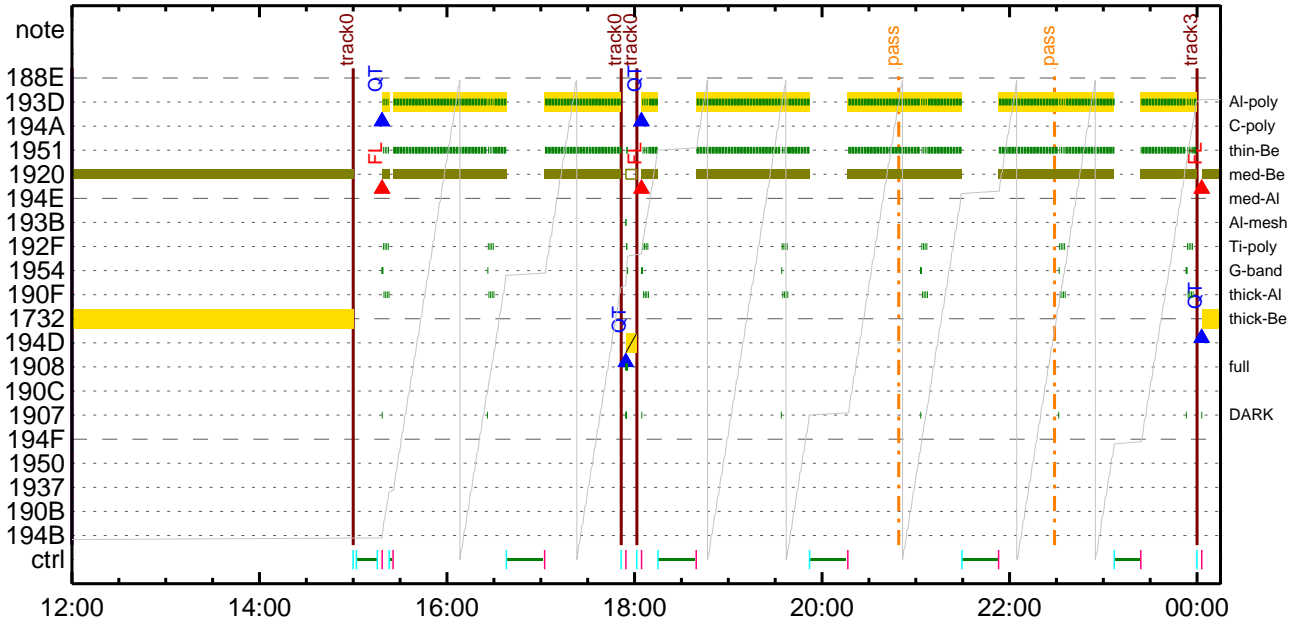
CMDI #0088 2012/11/29



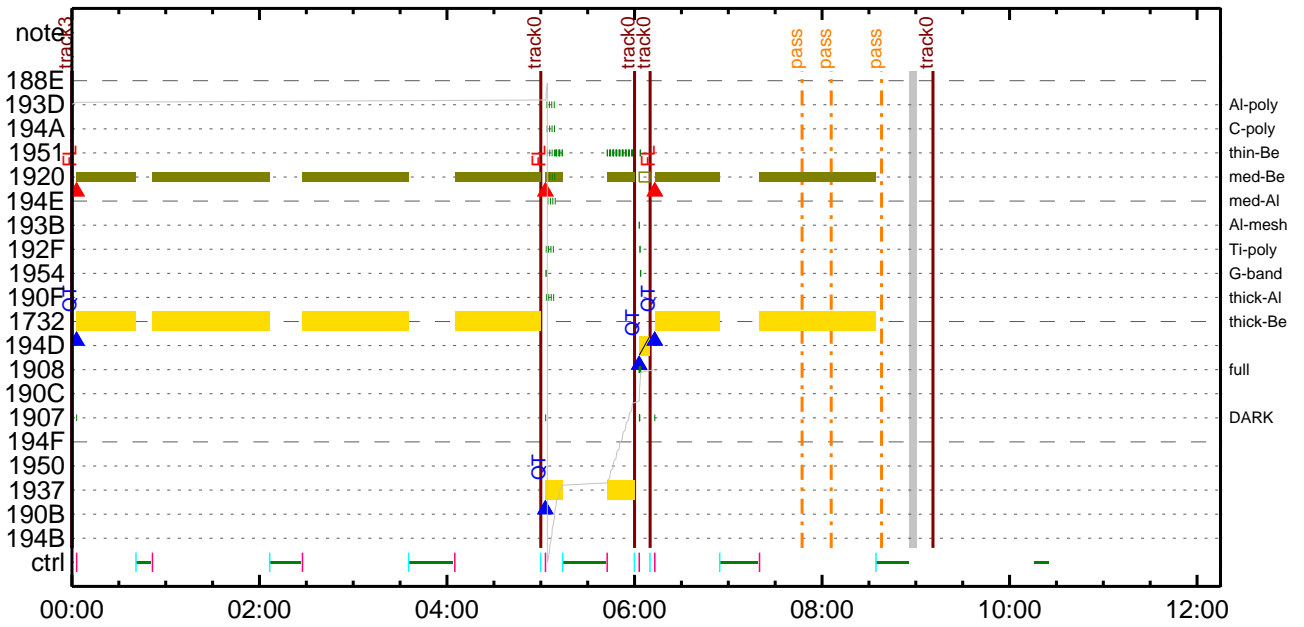
CMDI #0088 2012/11/30



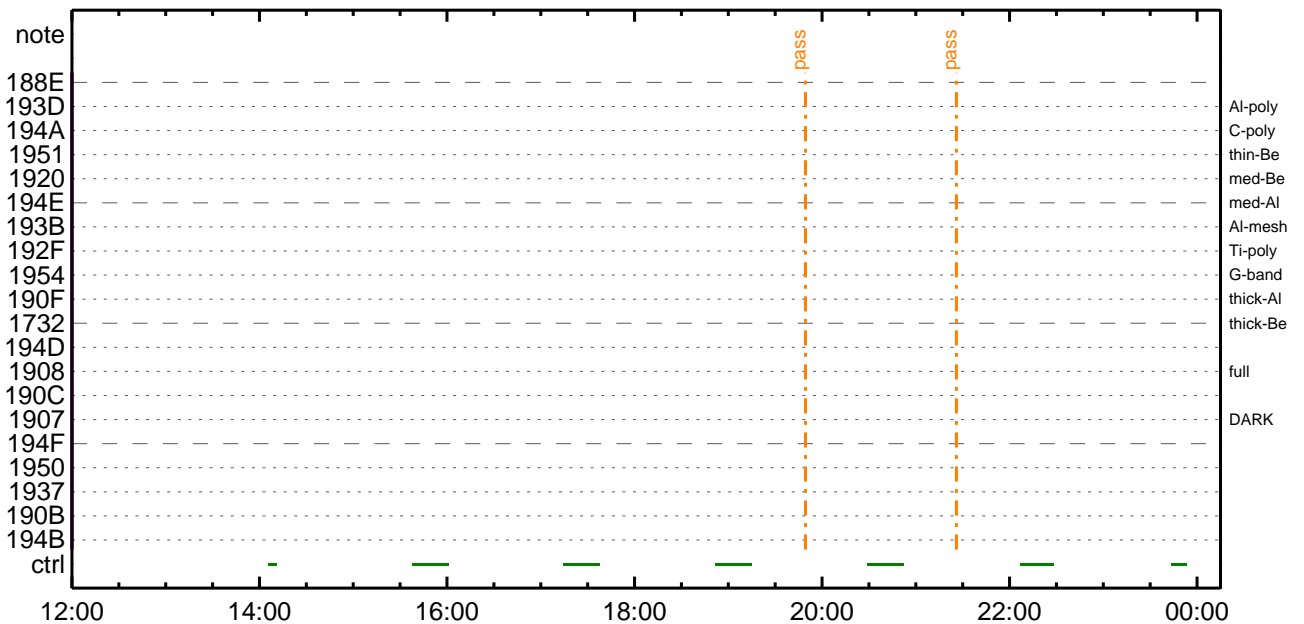
CMDI #0088 2012/11/30



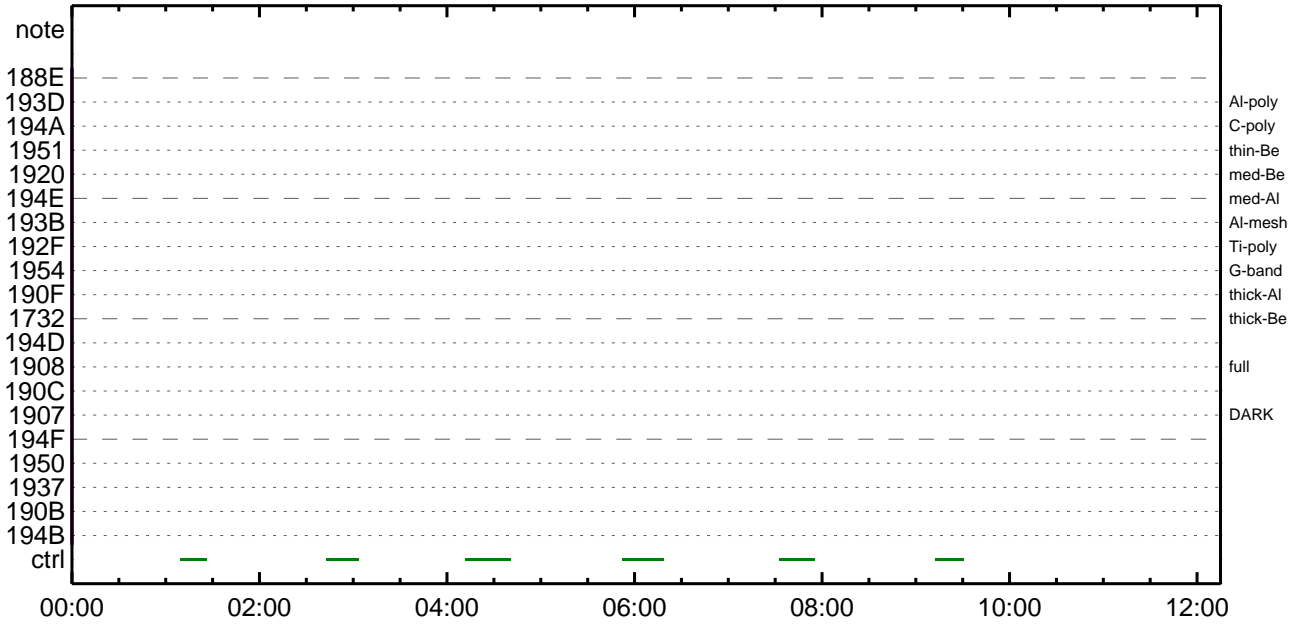
CMDI #0088 2012/12/01



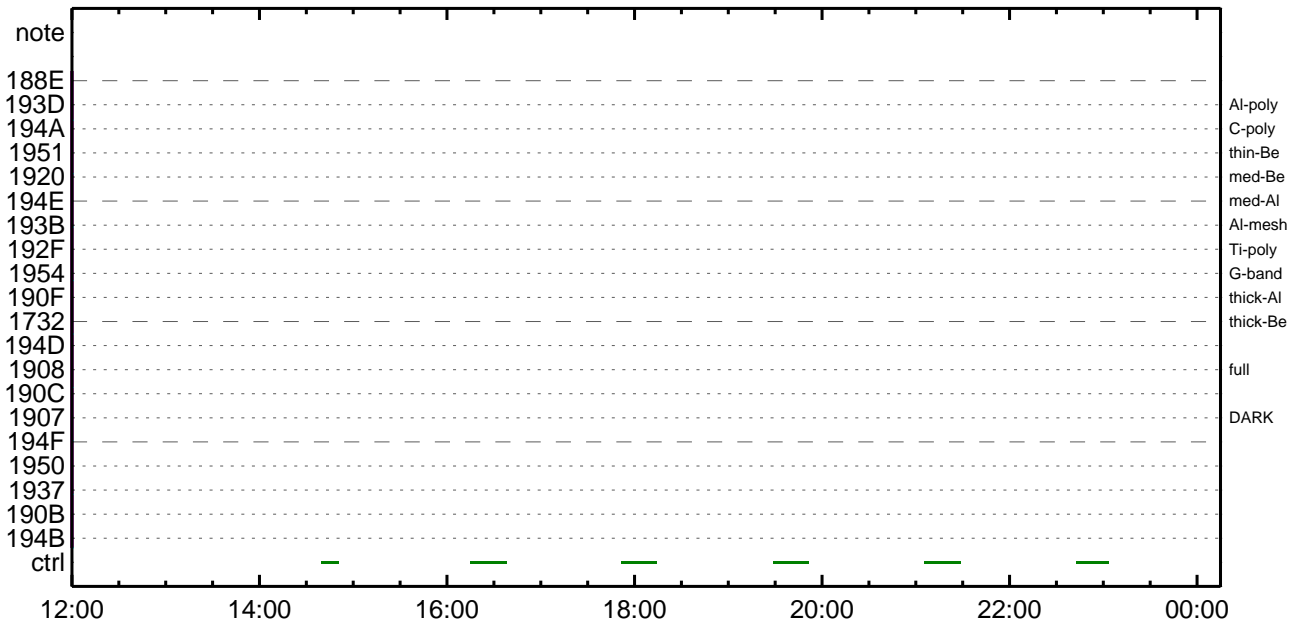
CMDI #0088 2012/12/01



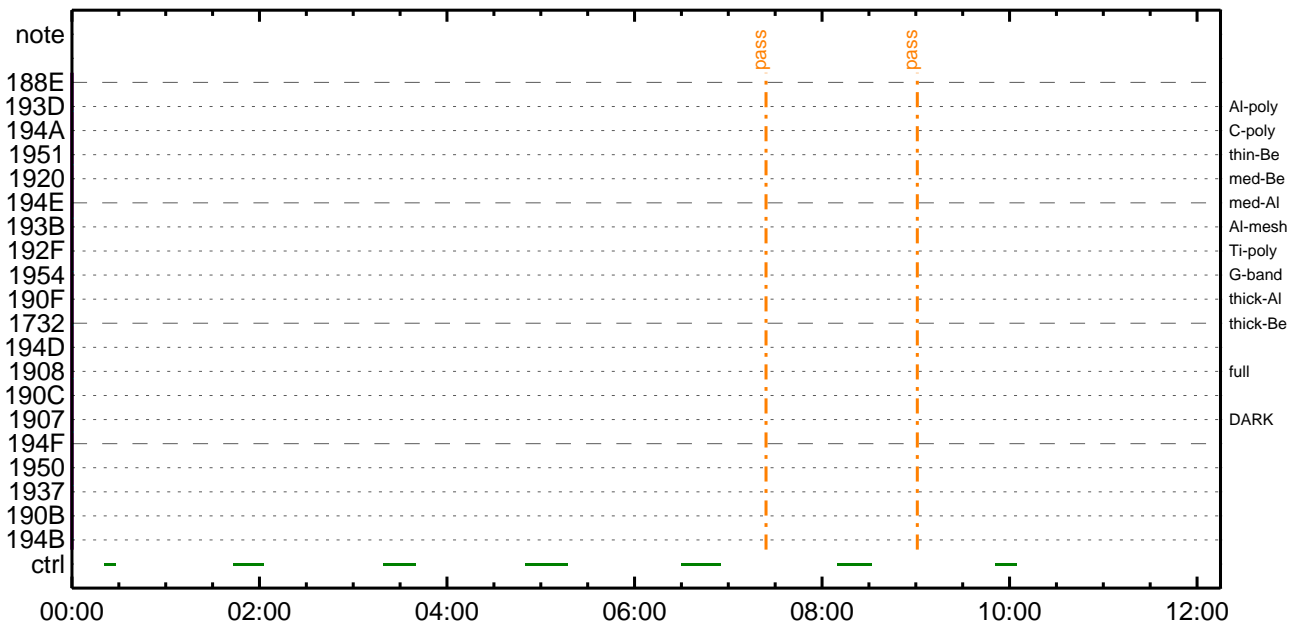
CMDI #0088 2012/12/02



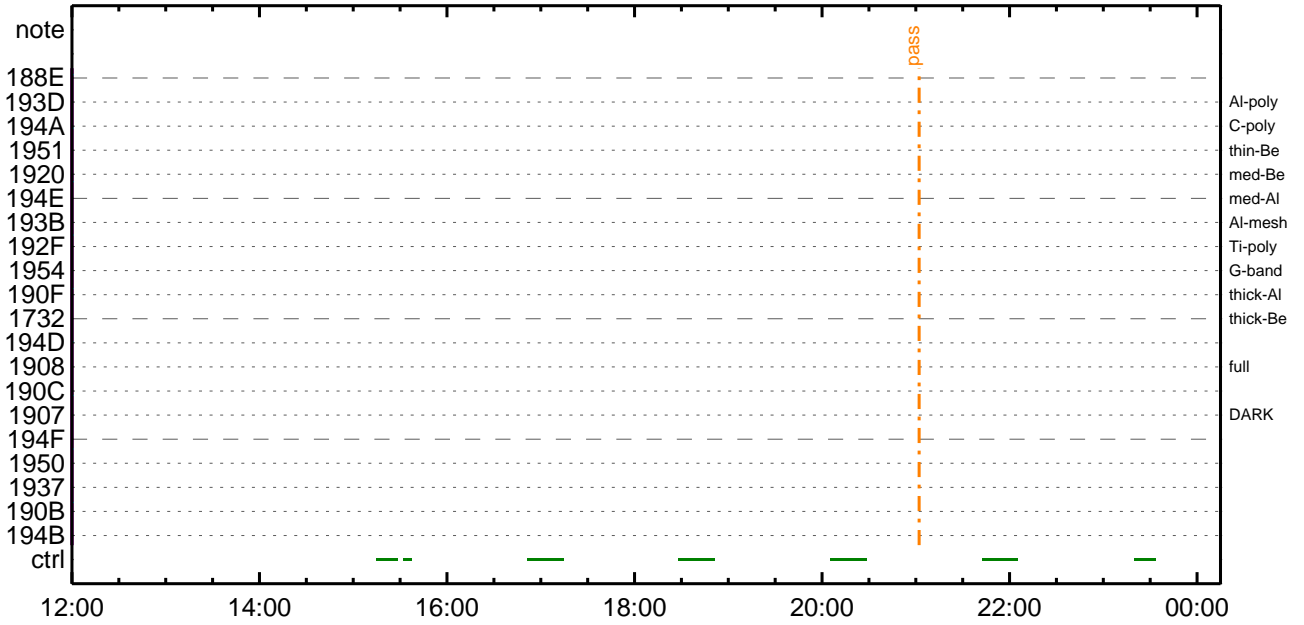
CMDI #0088 2012/12/02



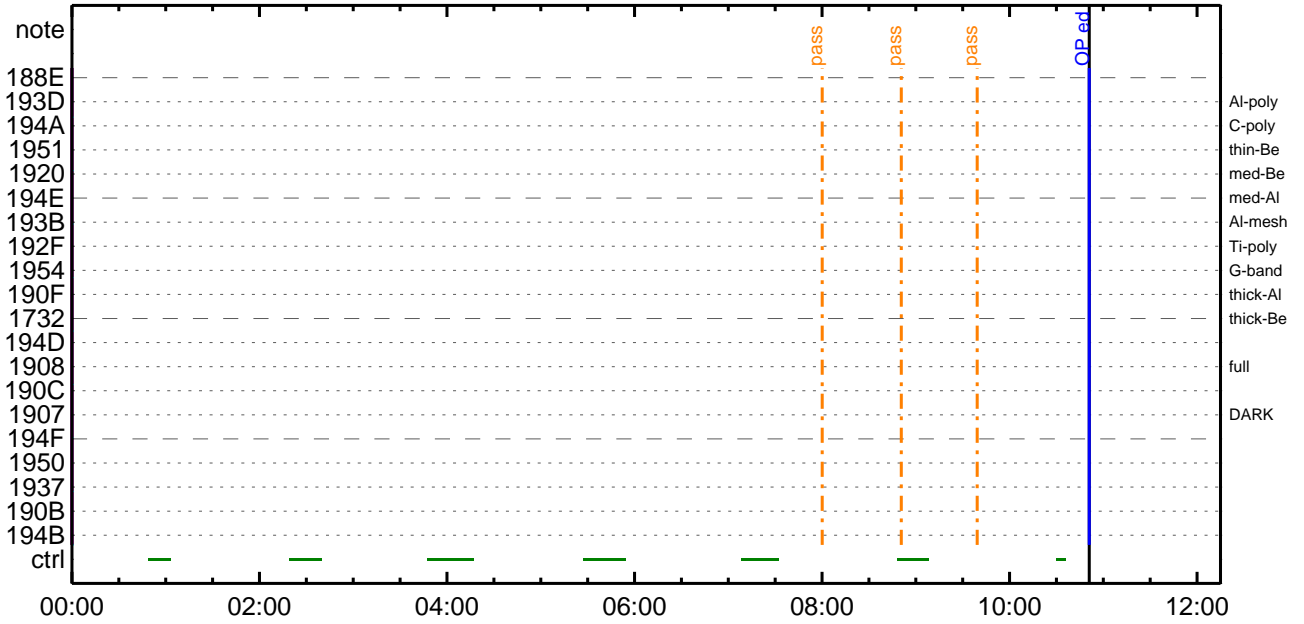
CMDI #0088 2012/12/03



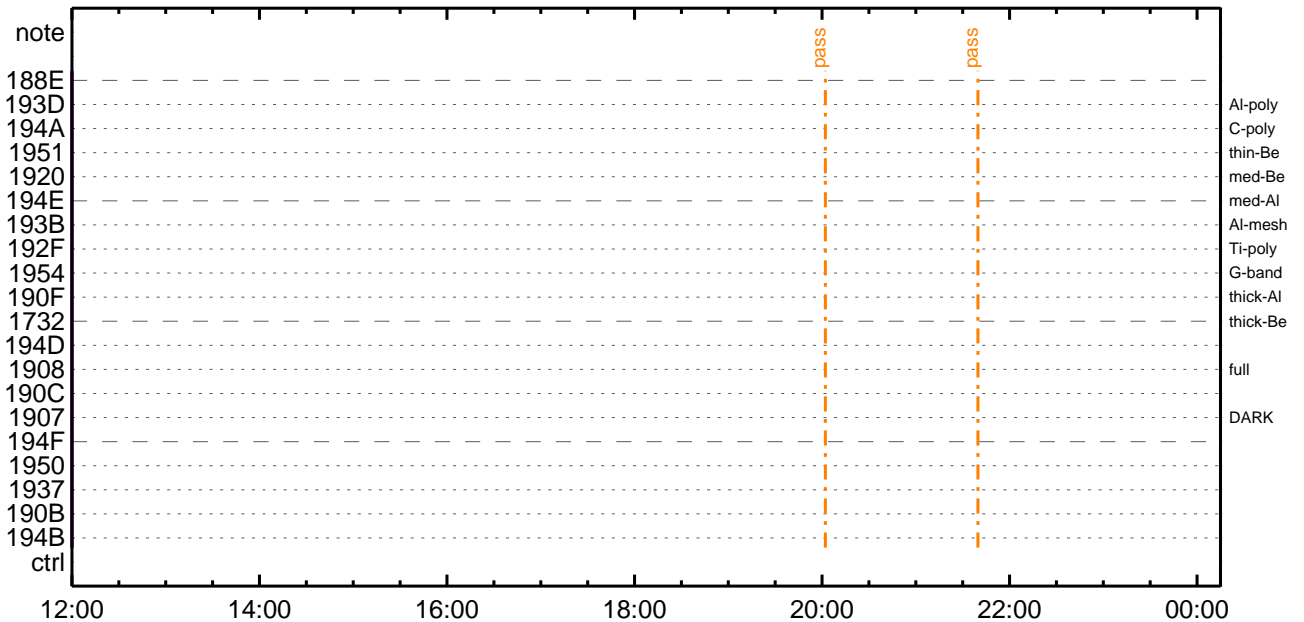
CMDI #0088 2012/12/03



CMDI #0088 2012/12/04



CMDI #0088 2012/12/04




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0096 C.
0097 C.
0098 C.
0099 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-299:OP
0104 ( )
0105 S. OG og-299:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°eãY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. Y6Yx½ªî»ð³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¾Ë¹ç•ë²îOKð³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. Y6Yx½ªî»ð³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¾Ë¹ç•ë²îOKð³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. Y6Yx½ªî»ð³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¾Ë¹ç•ë²îOKð³îç§
0165 C.
0166 C. ***** °Ë²¼çî¾Ë¹ç¶°ËË-ç°À÷¿@ (¾µ-Y6Yx½ªî»ð³îç¶°ËËç¾ª°²¼çççâ) *****
0167 C. DHUªâ;4YE;Ë½Y;Y;½YE;Ëðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-À÷¿@NG²î¾Ë¹ç;ç°Ë²¼çî¾Ë¹ç¶°ËËç¾ª°²¼çççâ³ççç;ç
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C.
0182 C. TIY³Y6Yx½ªî»ð³îç¶°ËËç¾ª°²¼çççâ (UT)
0183 +. TI 2012-11-29 09:20:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-11-29 09:20:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-11-29 09:20:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

0194 C.
0195 +. TI 2012-11-29 09:24:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °È²¼αîÄë%îíÑòîŷÄŷ\$ŷÄŷ-¹àîÜ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. TÍîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªî»αò³îÇ§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLαîŷÈ¹çªè²îOKαò³îÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÈ;Èŷ½. ŷî;¼ŷÈ;Èαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-11-29 09:24:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2012-11-29 09:24:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2012-11-29 09:24:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 . C. ***** MDP `ûÄîöŷ¼ŷÈÄð¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷÈŷâŷçŷèè²¼α¼Ä»Üα¹αè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C. ***** ŷDŷ¹.î Daily±;îÑòÈ¹Øα¹αèDCBC•×²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C. ¡ãLOSŷÄŷ\$ŷÄŷ-¼Ä»Ü;ã
0276 C.
0277 . C. ***** LOS *****
0278 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-301 2012-11-29 13:15:20 98 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□í;çÁ®, ù□¹□è□È□çÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 08 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 20 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_DIS
0059 BC (d9)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0061 BC (c9)
0062 + DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 + DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 03)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2012-11-29 09:24:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 C.
0084 . C. ***** MDP `úÁí□í»ò¼Y□ÈÁ□□¹□èDCBC•x²è *****
0085 C. (¼ã°íYÓYÁYÈY¥YÈYÁYçYÈÈ¼°¼Á»Û¹□è)
0086 . S. DC-BC dcbc-402:DCBC
0087 (MDP_known_event)
0088 C.
0089 C.
0090 . C. ***** YD¥¹•í Daily±¿íÑ□È´Ø□¹□èDCBC•x²è *****
0091 . S. DC-BC dcbc-153:DCBC
0092 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0093 C.
0094 C.
0095 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
```

0096 C.
0097 . C. ***** LOS *****
0098 C.

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*** OP Sequence for XRT ***

2012/11/29	09:34:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	09:35:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 53 74 01 3f				
2012/11/29	09:37:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/29	09:37:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/29	09:37:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/29	09:37:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/29	09:37:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/29	09:37:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/29	09:37:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/11/29	09:37:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/29	09:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/29	10:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	10:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/29	10:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/29	10:44:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/29	10:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/29	10:45:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/29	14:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	14:27:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/29	14:27:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/29	14:30:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/29	14:37:00.0	XRT_Custom_434_OG [0x1b2]							
2012/11/29	14:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/29	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	16:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/11/29	16:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	16:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/29	16:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/29	16:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/29	16:24:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	16:27:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/29	16:27:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/29	16:27:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/29	16:27:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/29	16:27:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/29	16:27:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/29	16:27:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2012/11/29	16:27:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/29	16:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/29	17:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	17:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/29	17:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/29	17:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/29	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/29	18:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/29	18:01:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/11/29	18:01:46.0	XRT_FLD_DIS_404_OG [0x194]							

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2012/11/29	18:01:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/11/29	18:01:50.0	XRT_ARS_DIS_406_OG [0x196]									
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/11/29	18:04:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]									
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	09				
2012/11/29	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/29	18:11:24.0	XRT_CTRL_MANU_447_OG [0x1bf]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/29	18:11:30.0	AOCS_ORe-point_Start_2_OG [0x098]									
			AOCU_NM	5	02-76	04	00	00	00	00	00
2012/11/29	18:13:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2012/11/29	18:14:16.0	XRT_FLD_ENA_428_OG [0x1ac]									
			MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/11/29	18:14:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]									
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/11/29	18:14:20.0	XRT_AEC_RESET_423_OG [0x1a7]									
			MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/11/29	18:14:22.0	XRT_ARS_DIS_438_OG [0x1b6]									
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/11/29	18:14:24.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2012/11/29	18:14:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]									
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	13				
2012/11/29	18:14:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]									
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10				
2012/11/29	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/29	19:15:00.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/29	19:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2012/11/29	19:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/11/29	19:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/11/29	19:38:30.0	XRT_Custom_434_OG [0x1b2]									
2012/11/29	19:39:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/29	20:52:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/29	20:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2012/11/29	20:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/11/29	20:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/11/29	21:16:00.0	XRT_Custom_434_OG [0x1b2]									
2012/11/29	21:17:00.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/29	22:30:00.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/29	22:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2012/11/29	22:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/11/29	22:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/11/29	22:51:00.0	XRT_Custom_434_OG [0x1b2]									
2012/11/29	22:52:00.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/30	00:07:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/30	00:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2012/11/30	00:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/11/30	00:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/11/30	00:15:30.0	XRT_Custom_434_OG [0x1b2]									
2012/11/30	00:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/30	01:31:00.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/30	01:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2012/11/30	01:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/11/30	01:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/11/30	01:50:00.0	XRT_Custom_434_OG [0x1b2]									
2012/11/30	01:51:00.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/11/30	02:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/11/30	03:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]									
			AOCU_NM	5	02-76	02	00	00	00	00	00
2012/11/30	03:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2012/11/30	03:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]									

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2012/11/30	03:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/30	03:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/30	03:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/30	03:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/30	03:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/30	03:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/30	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/30	03:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/30	03:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/30	03:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/30	03:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/30	03:27:00.0	XRT_Custom_434_OG [0x1b2]					
2012/11/30	03:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/30	04:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/30	04:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/30	04:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/30	04:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/30	05:04:30.0	XRT_Custom_434_OG [0x1b2]					
2012/11/30	05:05:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/30	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/30	06:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/30	06:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/30	06:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/30	06:42:00.0	XRT_Custom_434_OG [0x1b2]					
2012/11/30	06:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/30	07:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/30	07:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/30	07:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/11/30	08:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/11/30	08:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/30	08:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2012/11/30	08:20:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00
2012/11/30	08:20:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/11/30	08:20:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/11/30	08:20:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/30	08:22:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2012/11/30	08:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/11/30	08:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/11/30	08:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04	00 00 00 00
2012/11/30	08:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/11/30	08:32:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/11/30	08:32:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/11/30	08:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/11/30	08:32:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/11/30	08:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/11/30	08:32:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/11/30	08:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/11/30	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

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2012/11/30	09:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	09:37:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/30	09:37:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/30	09:40:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/30	09:53:00.0	XRT_Custom_434_OG [0x1b2]								
2012/11/30	09:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/30	09:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	10:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01 00 00 00 00				
2012/11/30	10:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/30	10:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/30	10:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/30	10:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/30	10:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/30	10:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/30	10:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2012/11/30	10:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/30	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/30	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	15:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 17 1a ae c1				
2012/11/30	15:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	15:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/30	15:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/30	15:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/30	15:15:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	15:17:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/11/30	15:18:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/11/30	15:18:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/11/30	15:18:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/11/30	15:18:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/11/30	15:18:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/30	15:18:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/11/30	15:18:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/11/30	15:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/30	15:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	15:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/30	15:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/30	15:24:30.0	XRT_Custom_434_OG [0x1b2]								
2012/11/30	15:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/30	15:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/30	16:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	16:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/11/30	16:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/11/30	16:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/11/30	17:01:30.0	XRT_Custom_434_OG [0x1b2]								
2012/11/30	17:02:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/11/30	17:51:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/11/30	17:51:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/11/30	17:51:30.0	AOCS_ORe-point_Start_3_OG [0x099]								

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2012/11/30	17:51:46.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2012/11/30	17:51:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2012/11/30	17:51:50.0	XRT_ARS_DIS_406_OG [0x196]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/11/30	17:54:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]		2	07-F0	c4	09			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	09			
2012/11/30	17:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/11/30	18:01:24.0	XRT_CTRL_MANU_447_OG [0x1bf]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/11/30	18:01:30.0	AOCS_ORe-point_Start_6_OG [0x09c]		5	02-76	00	17	1a	ae	c1
		AOCU_NM		5	02-76	00	17	1a	ae	c1
2012/11/30	18:03:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2012/11/30	18:04:16.0	XRT_FLD_ENA_428_OG [0x1ac]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2012/11/30	18:04:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2012/11/30	18:04:20.0	XRT_AEC_RESET_423_OG [0x1a7]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2012/11/30	18:04:22.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/11/30	18:04:24.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/11/30	18:04:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]		2	07-F0	c4	13			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	13			
2012/11/30	18:04:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]		2	07-F0	c5	10			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	10			
2012/11/30	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/11/30	18:15:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/11/30	18:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/11/30	18:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/11/30	18:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/11/30	18:38:30.0	XRT_Custom_434_OG [0x1b2]		1	07-F0	e8				
2012/11/30	18:39:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/11/30	19:52:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/11/30	19:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/11/30	19:52:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/11/30	19:55:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/11/30	20:15:30.0	XRT_Custom_434_OG [0x1b2]		1	07-F0	e8				
2012/11/30	20:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/11/30	21:29:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/11/30	21:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/11/30	21:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/11/30	21:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/11/30	21:52:00.0	XRT_Custom_434_OG [0x1b2]		1	07-F0	e8				
2012/11/30	21:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/11/30	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/11/30	23:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/11/30	23:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/11/30	23:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/11/30	23:23:00.0	XRT_Custom_434_OG [0x1b2]		1	07-F0	e8				
2012/11/30	23:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/11/30	23:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/12/01	00:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2012/12/01	00:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2012/12/01	00:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2012/12/01	00:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2012/12/01	00:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2012/12/01	00:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/12/01	00:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				

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2012/12/01	00:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/12/01	00:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/12/01	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	00:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	00:41:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/01	00:41:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/01	00:44:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/01	00:50:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/01	00:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	02:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	02:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/01	02:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/01	02:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/01	02:26:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/01	02:27:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	03:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	03:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/01	03:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/01	03:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/01	04:04:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/01	04:05:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	04:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	05:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	17	1a	ae	c1
2012/12/01	05:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/12/01	05:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/01	05:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/01	05:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/01	05:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/01	05:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/01	05:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/12/01	05:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/12/01	05:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	05:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	05:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/01	05:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/01	05:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/01	05:41:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/01	05:42:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/12/01	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/12/01	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/01	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/01	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/01	06:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2012/12/01	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/01	06:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/01	06:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	17	1a	ae	c1
2012/12/01	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							

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2012/12/01	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/12/01	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/12/01	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/12/01	06:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/12/01	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/01	06:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2012/12/01	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/12/01	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/01	06:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/01	06:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/01	06:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/01	06:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/01	07:19:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/01	07:20:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/01	08:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/01	08:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/01	08:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/01	08:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/01	09:11:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
			AOCU_NM	5	02-76	00	00	00	00