

Period: 2012/12/01 09:01:00 - 2012/12/06 10:28:00

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XOB #194D: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(2																
Term			Pointing (x, y)					Comment								
12/01 17:58:30 - 12/01 18:05:24			Fixed (0.0, 0.0)					synoptic, shifted -4.5 min								
12/02 06:31:30 - 12/02 06:38:24			Fixed (0.0, 0.0)					synoptic, shifted 28.5 min								
12/02 18:17:30 - 12/02 18:24:24			Fixed (0.0, 0.0)					synoptic, shifted 14.5 min								
12/03 06:04:54 - 12/03 06:10:24			Fixed (0.0, 0.0)					synoptic, shifted 0.5 min								
12/03 18:03:00 - 12/03 18:09:54			Fixed (0.0, 0.0)					synoptic								
12/04 06:07:30 - 12/04 06:14:24			Fixed (0.0, 0.0)					synoptic, shifted 4.5 min								
PROG= 09 1-time(s)																
Subr= 1 1-time(s) 12.0sec																
Seqn= 56 1-time(s) 4.0sec																
└ Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec																
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)			DPCM	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)			DPCM	0	0	2.0sec
Seqn= 46 1-time(s) 4.0sec																
└ Open/Ti-poly			Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Ti-poly			Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec																
└ thin-Be/Open			thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ thin-Be/Open			thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 69 1-time(s) 2.0sec																
└ Open/G-band			Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec																
Seqn= 68 1-time(s) 2.0sec																
└ Open/G-band			Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)			DPCM	0	0	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

XOB #1732: Dark - Med-Al+Thick-Be - 8x8 - 512x512																					
Term		Pointing (x, y)				Comment															
12/02 06:41:30 - 12/02 17:51:30		Fixed (914.0, -260.0)		# Cont.																	
12/02 18:27:30 - 12/03 06:00:24		Fixed (0.0, 939.0)		# N pole, for EIS streamer study.																	
12/04 06:17:30 - 12/04 08:48:00		Fixed (-947.0, 200.0)		# Returning active area, near E limb.																	
PROG= 10 1-time(s)																					
└ Subr= 1 1-time(s) 2.0sec																					
└ Seqn= 81 1-time(s) 2.0sec																					
└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 8x8 512x512 (1024, 1024) DPCM 0 0 2.0sec																					
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin		ROI: size (center)		Comp.		AEC Buffer		Interval	

XOB #193D: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly														
Term		Pointing (x, y)							Comment					
12/03 06:13:30 - 12/03 09:29:54		Track (-100.1, 190.7) @ 12/03 06:10:30							# AR 11625.					
12/03 09:33:00 - 12/03 17:59:54		Track (-69.6, 191.0) @ 12/03 09:30:00							* Pointing-update provision.					
12/03 18:13:00 - 12/04 06:04:24		Track (10.3, 191.7) @ 12/03 18:10:00							# Cont.					
PROG= 19 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 19		2-time(s)		2.0sec										
		Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2		2-time(s)		2.0sec										
Seqn= 92		1-time(s)		2.0sec										
		Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32		4-time(s)		2.0sec										

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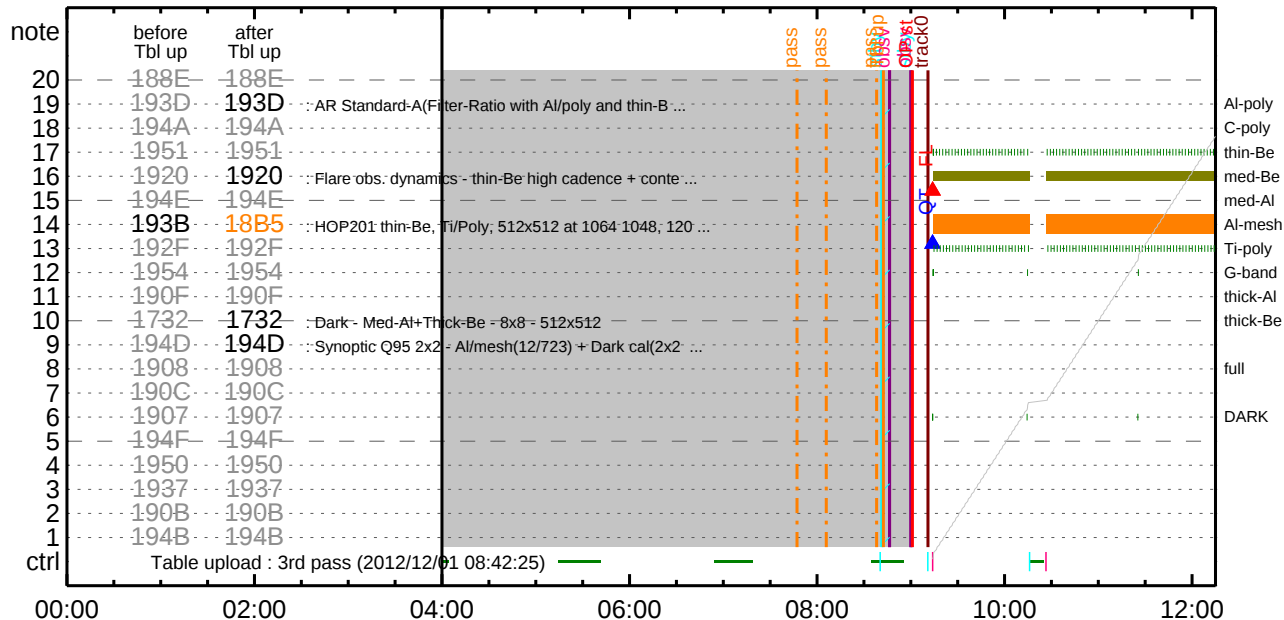
Flare mode

Active Region Search

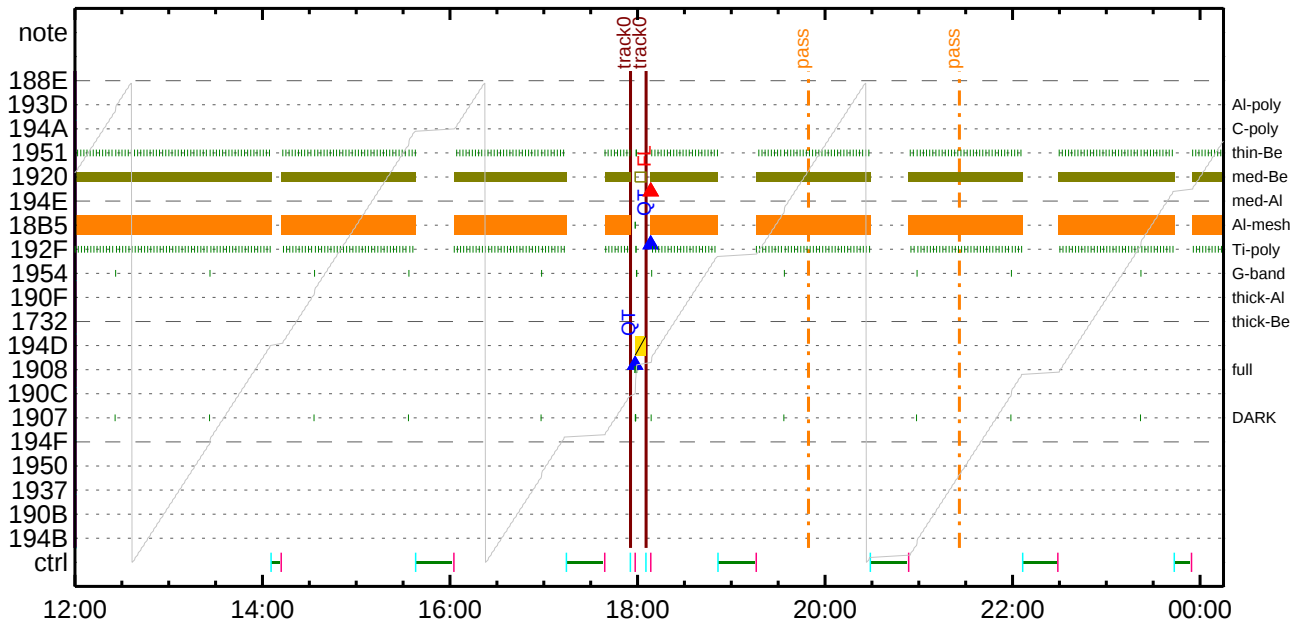
NOT USED

Flare Detection

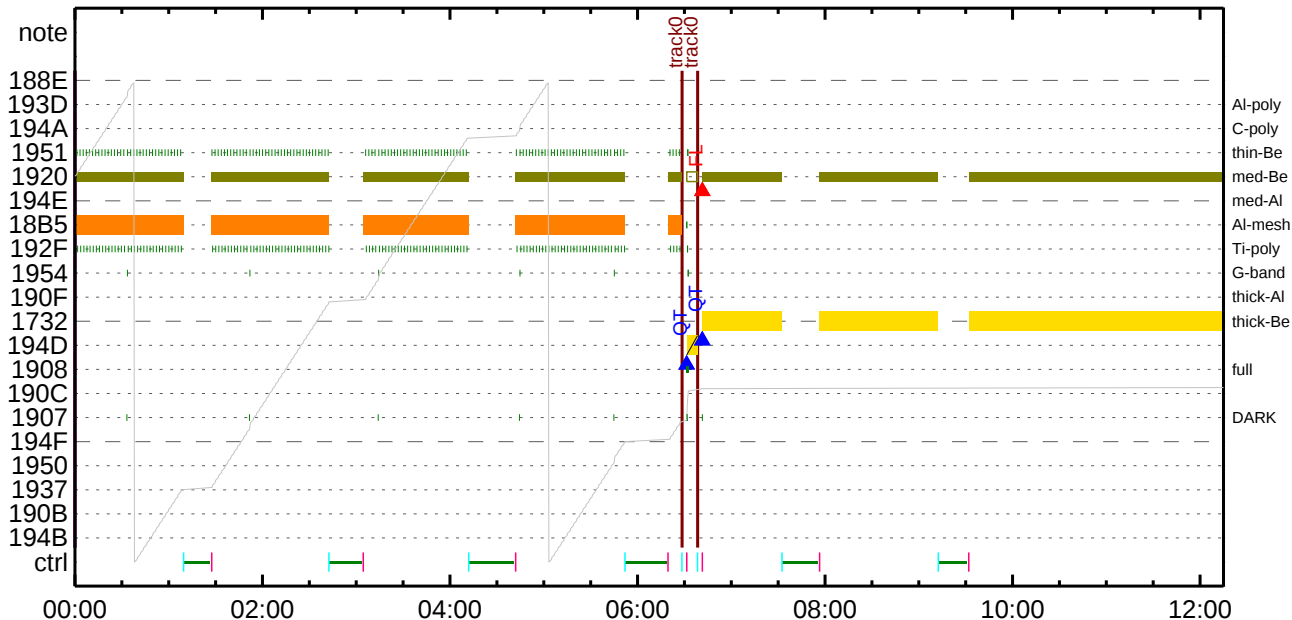
CMDI #0092 2012/12/01



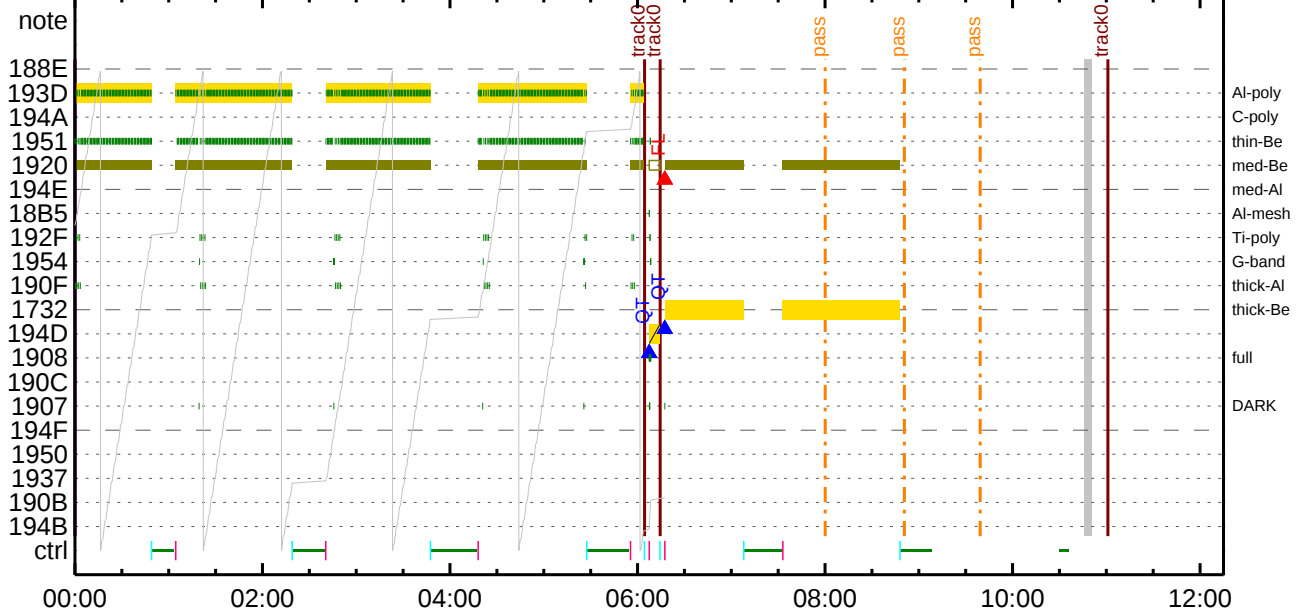
CMDI #0092 2012/12/01



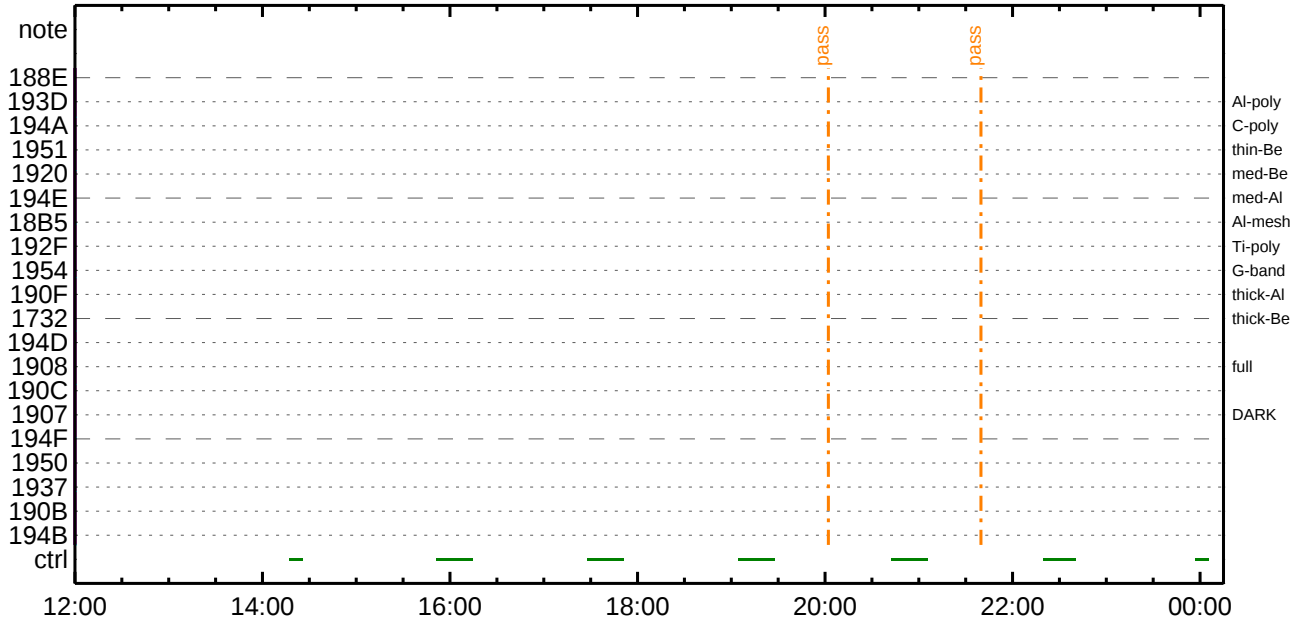
CMDI #0092 2012/12/02



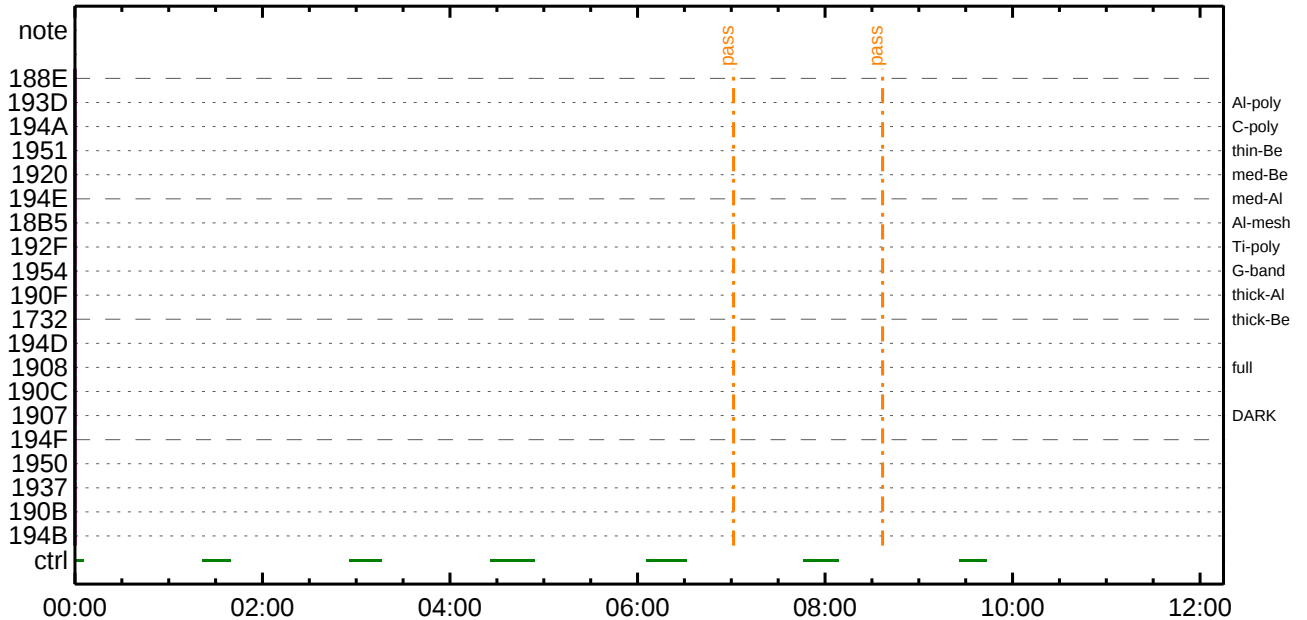
CMDI #0092 2012/12/04



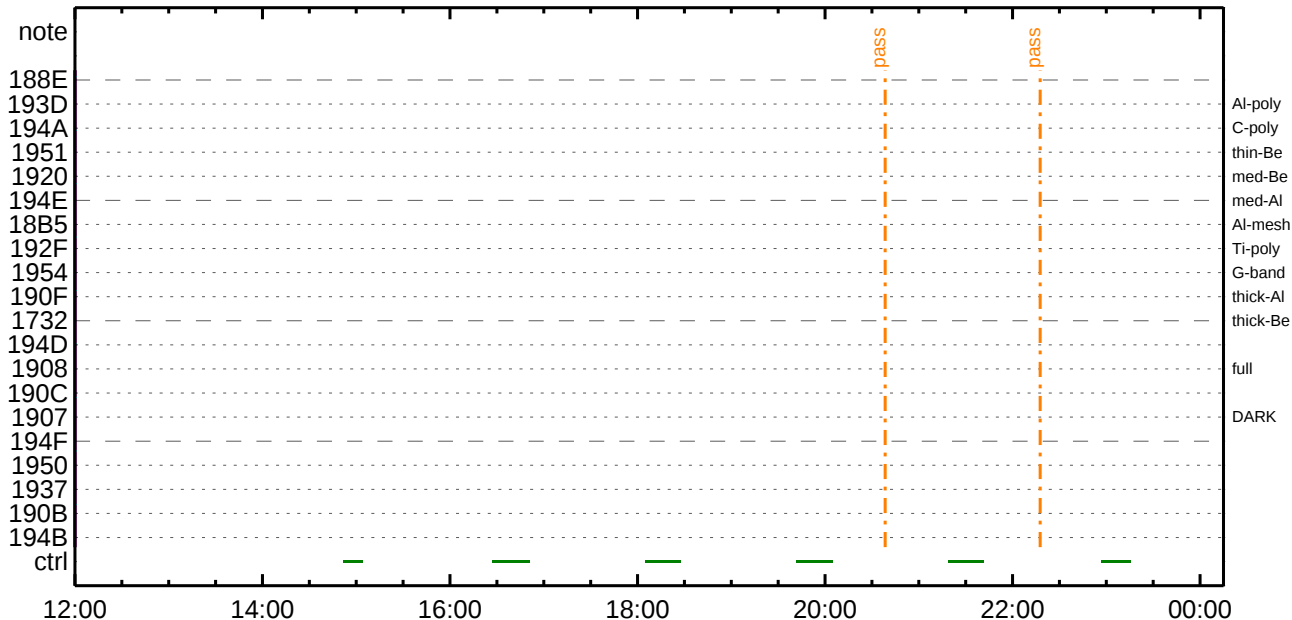
CMDI #0092 2012/12/04



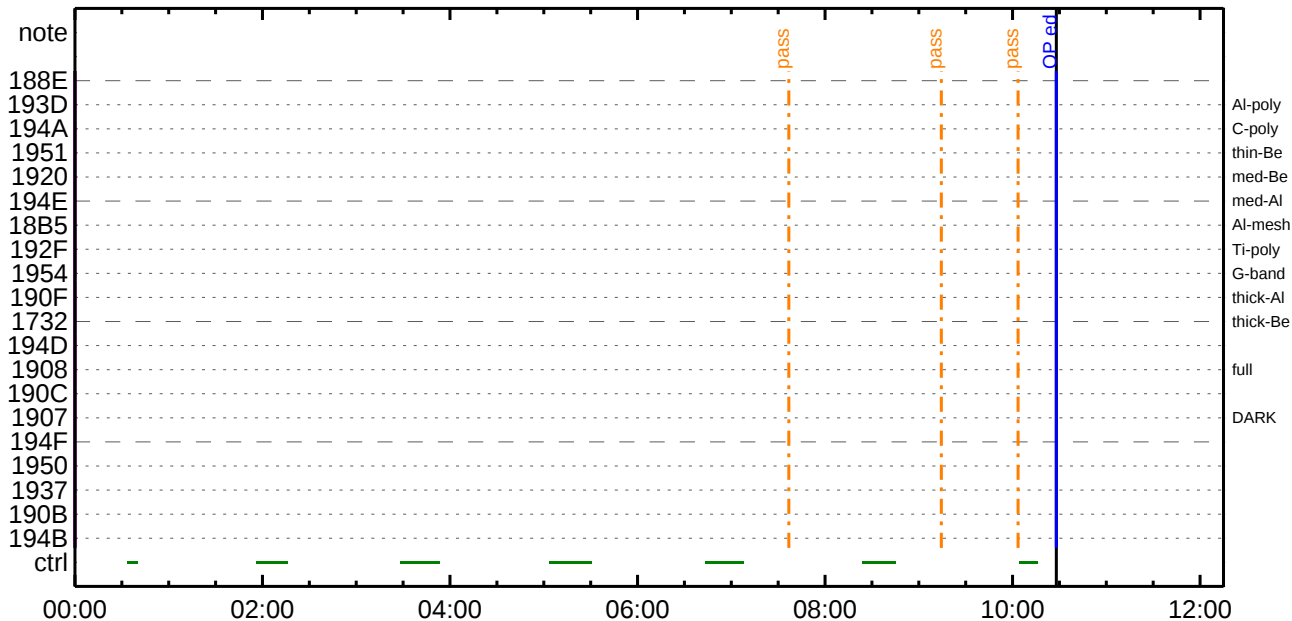
CMDI #0092 2012/12/05



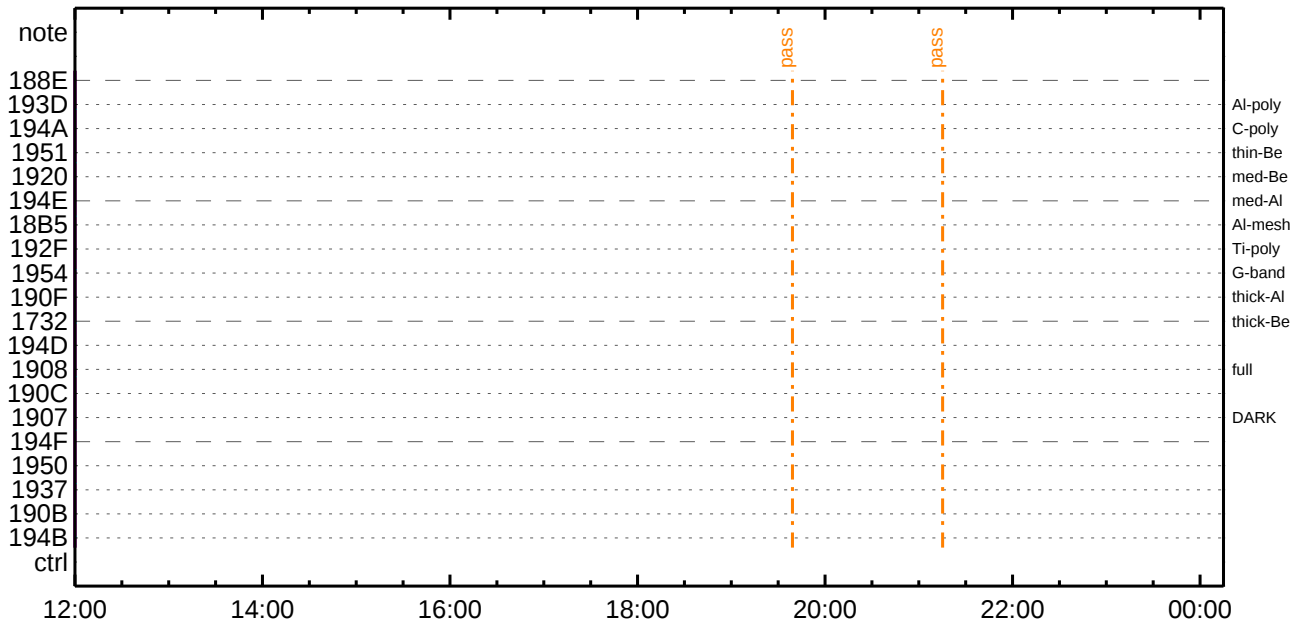
CMDI #0092 2012/12/05



CMDI #0092 2012/12/06



CMDI #0092 2012/12/06



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main-304 2012-12-01 12:25:28 278 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. jãAOS¥Á¥$¥Ã¥¬¼Ä»Üjã
0005 . C.
0006 . C. ¥Ä¥ßj¼¥³¥D¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 . C. ÄijË□¿□Ä□•µ°Æ»I×AQ□I¥¢¥Ä¥×¥i;¼¥Ê;ËËè½µ•îÍÊ;ËÈ¼°ÇÖ□•□¿½¹Ç□İjÇÀ®, ù□¹□è□▷□ÇÄ÷¿®□•□Ê□□□³□Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. *****
0015 . C. XÄ÷¿®µjON
0016 . C. *****
0017 . C. ¢´ °ÆÄ, İxÊÝ□äLOS▷▷Ç□İ»þ´ Ö□ò¹íÎ, □•j¢ÉÔİx□EXÄÓÖN□İ¹Ö□È□ı□È□□□³□Èj£
0018 . C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 . C. ¤¤[HK1_XPA_ON/OFF] EQ ON
0025 . C. ¤¤[HK1_XPA_PWR_HI/L0] EQ HI
0026 . C. ¤¤[HK1_XMOD_ON/OFF] EQ ON
0027 . C. ¤¤[HK1_XMOD_QPSK/PM] EQ QPSK
0028 . C.
0029 . C. X¥D¥Ó¥É¥İ¥Ä¥¬¼ÖÄÖ□¬¼Äê□•□¿□èj¢°Ê²¼□İ°ÆÄ, ¼ê½Ç□ò¼Ä¹Ö□¹□èj£
0030 . C.
0031 . C. *****
0032 . C. DR PT1 Äİ¼ı°ÆÄ,
0033 . C. *****
0034 . C. ¢´ RESTARTjÊPT1jË□•□¿□□½¹Ç□İj¢°Ê²¼□İ¼Ä¹Ö□»□°j¢DCBC-150□Ø¿Ê□àj£
0035 . C.
0036 . C. jãPT1°ÆÄ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 . C. ¤¤[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, j¼Ú)
0043 . C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¼Ú)
0044 . C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¼Ú)
0045 . C.
0046 . C. jã¥¢¥ó¥Æ¥ÊÄÜÄØjÊÄ•Äº²öÈðjË, ä□İ°ÆÄ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 . C. ¤¤[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, j¼Ú)
0050 . C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¼Ú)
0051 . C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¼Ú)
0052 . C.
0053 . C.
0054 . C. PT1°ÆÄ, □¬¼«Æ°Ää»ß□•□¿, äj¢°Ê²¼□ò¼Ä¹Ö□¹□èj£
0055 . C. ¥¢¥ó¥Æ¥ÊÄÜÄØ□äÄ•Äº²öÈð¬¼á□□½¹Ç□İ' °İ»□¹□è□▷□ÇÂÖ□Äj£
0056 . C.
0057 . C. *****
0058 . C. DR PT2 Äİ¼ı°ÆÄ,
0059 . C. *****
0060 . C. ¢´ RESTARTjÊPT2jË□•□¿□□½¹Ç□İj¢°Ê²¼□İ¼Ä¹Ö□»□°j¢DCBC-151□Ø¿Ê□àj£
0061 . C.
0062 . C. jãPT2°ÆÄ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 . C. ¤¤[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, j¼Ú)
0069 . C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¼Ú)
0070 . C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¼Ú)
0071 . C.
0072 . C. jã¥¢¥ó¥Æ¥ÊÄÜÄØjÊÄ•Äº²öÈðjË, ä□İ°ÆÄ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 . C. ¤¤[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, j¼Ú)
0076 . C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¼Ú)
0077 . C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¼Ú)
0078 . C.
0079 . C. *****
0080 . C. DR°ÆÄ, Ää»ßj¢XÄ÷¿®µjOFF
0081 . C. *****
0082 . C.
0083 . C. jãDR°ÆÄ, Ää»ßjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 . C. ¤¤[HK1_REP_STA/STP] EQ STOP
0087 . C. ¤¤[HK1_S_VC4_ON/OFF] EQ OFF
0088 . C. ¤¤[HK1_X_VC4_ON/OFF] EQ OFF
0089 . C.
0090 . C. jãXÄ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 . C. ¤¤[HK1_XMOD_ON/OFF] EQ OFF
0095 . C. ¤¤[HK1_XPA_ON/OFF] EQ OFF
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[illegible]

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0194 C.
0195 +. TI 2012-12-01 09:00:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.
0198 C.
0199 C. °Ê²¼□İÄê¾İİŇ□İ¥Á¥$¥Ä¥¯¹àİÜ
0200 C.
0201 C.
0202 C.
0203 C.
0204 C.
0205 C.
0206 C.
0207 C.
0208 C.
0209 C.
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.
0213 C.
0214 C.
0215 C.
0216 C.
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.
0220 C.
0221 C.
0222 C.
0223 C.
0224 C.
0225 C.
0226 C.
0227 C.
0228 C.
0229 C.
0230 C.
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.
0234 C.
0235 C.
0236 C.
0237 C.
0238 C.
0239 C.
0240 C.
0241 C.
0242 C.
0243 C.
0244 +. TI 2012-12-01 09:00:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2012-12-01 09:00:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C.
0251 C.
0252 C.
0253 C.
0254 C.
0255 C.
0256 C.
0257 +. TI 2012-12-01 09:00:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 C.
0261 C.
0262 C.
0263 C.
0264 C.
0265 C.
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C.
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C.
0276 C.
0277 C.
0278 C.

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main-305 2012-12-01 12:45:28 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥$¥Ã¥¬¼Á»Üjã
0005 C.
0006 C. ¥Ã¥ßj¼¥³¥p¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀījĒ□¿Á□•μ°Æ»Ī×ÁÇ□Ī¥Ç¥Ã¥×¥īj¼¥ĒjĒĒ½μ•īĒjĒ□Ē¼°Ç□□•□¿¼ī¹Ç□ījÇÀ®, ū□¹□Ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 C. Verify EIS in MANUAL mode
0049 C. Estimated OBSTBL upload time is 56s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 C.
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-12-01 09:00:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ŪĀĪ□Ī»Ō¼Ÿ□ĒĀ□¹□ĒDCBC•×²Ē *****
0069 C. (¼Ā°ĪŸŌ¥Ā¥Ē¥p¥Ē¥Ā¥Ç¥ĒĒ½□□¼Ā»Ū□¹□Ē)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** ¥Đ¥¹•Ī Daily±¿ĪŃ□Ē´Ō□¹□ĒDCBC•×²Ē *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. jãLOS¥Á¥$¥Ã¥¬¼Á»Üjã
0080 C.
0081 . C. ***** LOS *****
0082 C.

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main-306      2012-12-01 12:45:28 96 33 SOLAR-B MAIN //
0001          C.
0002          . C. ***** AOS *****
0003          C.
0004          . C. jãAOS¥Á¥$¥Ã¥¬¼Å»Üjã
0005          C.
0006          C. ¥Ä¥ß;¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008          C.
0009          . C. ***** AOCS : Reload orbital element (send every contact) *****
0010          C. Äí;Ë²¿À²•µªÆ»Î×ÀÇ²Í¥¢¥Ä¥×¥ì;¼¥Ê;ËËè½µ•îË;È²É¾°ÇÒ²•²¿%¹¹Ç²İ;ÇÀ®, ù²¹²æ²²²ÇÄ÷¿®²•²Ê²²²³²È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012          C.
0013          C.
0014          C.
0015          C. ***** XRT START *****
0016          C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018          BC         (c1)
0019 +. DC 07-F0 MDP_XRT_MODE_STBY
0020          BC         (c3)
0021          . C. ----- Success Verify ?                      OK / NG____
0022          C.
0023          C. XRT Obs. Table Upload
0024          . S. RAM    ram-291:MDP_OBS_X
0025          ()
0026          C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028          BC         (84 07 00 00 00 3a d4)
0029          . C. ----- Comparison Check ?                  OK / ERR ____
0030          C.
0031          C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033          BC         (cd 01 b1 b1 04 04)
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035          BC         (cd 02 b1 b1 08 08)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037          BC         (cd 03 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039          BC         (cd 04 b1 b1 06 06)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041          BC         (cd 05 85 83 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043          BC         (cd 06 85 83 08 08)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045          BC         (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047          BC         (cd 08 80 80 20 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049          BC         (cd 09 80 80 08 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051          BC         (cd 0a 80 80 08 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053          BC         (cd 0b 85 83 06 06)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055          BC         (cd 0f 80 80 06 06)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057          BC         (cd 10 80 80 08 08)
0058 +. DC 07-F0 MDP_XRT_FLD_DIS
0059          BC         (d9)
0060 +. DC 07-F0 MDP_XRT_FLRCTRL_DIS
0061          BC         (c9)
0062 +. DC 07-F0 MDP_XRT_AEC_RESET
0063          BC         (d0)
0064 +. DC 07-F0 MDP_XRT_ARS_DIS
0065          BC         (d5)
0066 +. DC 07-F0 MDP_XRT_FLD_RESET
0067          BC         (da)
0068          . C. ----- Success Verify ?                      OK / NG ____
0069          C.
0070          C.
0071          . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0072          C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074          BC         (c2)
0075 +. TI 2012-12-01 09:00:02.0
0076          DC 07-F0 MDP_XRT_MODE_OBSV
0077          BC         (c2)
0078          . C. ----- Success Verify ?                      OK / NG ____
0079          C.
0080          C. ***** XRT END *****
0081          C.
0082          . C. ***** MDP ´ÔÄÎ²Í»Ö¼Ý²ÊÂÐ²¹²èDCBC•×²è *****
0083          C. (%äºî¥Ö¥Ä¥Ê¥Ð¥É¥â¥¢¥ë¥è¥½%²²%Ä»Ü²¹²è)
0084          . S. DC-BC dcbc-402:DCBC
0085          (MDP_known_event)
0086          C.
0087          C.
0088          . C. ***** ¥Ð¥¹•İ Daily±¿İÑ²Ê´Ø¹²èDCBC•×²è *****
0089          . S. DC-BC dcbc-153:DCBC
0090          (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0091          C.
0092          C.
0093          . C. jãLOS¥Á¥$¥Ã¥¬¼Å»Üjã
0094          C.
0095          . C. ***** LOS *****
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*** OP Sequence for XRT ***

2012/12/01	09:10:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	09:11:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 00 17 1a ae c1
2012/12/01	09:13:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/12/01	09:13:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/12/01	09:13:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/12/01	09:13:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/12/01	09:13:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/12/01	09:13:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/01	09:13:56.0	XRT_QT_PROG_SET_415_OG [0x19f]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0e
2012/12/01	09:13:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/12/01	09:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/01	10:16:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	10:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/01	10:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/01	10:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/01	10:25:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/01	10:26:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/01	14:05:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	14:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/01	14:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/01	14:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/01	14:11:00.0	XRT_Custom_434_OG [0x1b2]		
2012/12/01	14:12:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/01	15:38:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	15:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/01	15:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/01	15:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/01	16:01:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/01	16:02:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/01	17:14:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	17:14:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/01	17:14:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/01	17:17:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/01	17:38:00.0	XRT_Custom_434_OG [0x1b2]		
2012/12/01	17:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/01	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	17:55:26.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2012/12/01	17:55:30.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2012/12/01	17:55:46.0	XRT_FLD_DIS_404_OG [0x194]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2012/12/01	17:55:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2012/12/01	17:55:50.0	XRT_ARS_DIS_406_OG [0x196]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/12/01	17:58:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 09
2012/12/01	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/01	18:05:24.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/01	18:05:30.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 00 17 1a ae c1
2012/12/01	18:07:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/12/01	18:08:16.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/12/01	18:08:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		

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2012/12/01	18:08:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/01	18:08:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/01	18:08:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/01	18:08:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/01	18:08:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/12/01	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/01	18:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/01	18:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/01	18:51:34.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/01	18:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	19:15:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	19:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/01	20:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/01	20:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/01	20:29:04.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/01	20:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	20:52:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	20:53:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/01	22:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/01	22:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/01	22:06:34.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/01	22:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	22:28:00.5	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	22:29:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/01	23:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/01	23:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/01	23:43:34.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/01	23:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	23:53:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/01	23:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/02	01:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	01:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	01:09:34.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	01:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	01:26:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	01:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/02	02:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	02:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	02:42:34.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	02:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	03:03:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	03:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/02	04:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	04:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	04:12:04.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	04:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	04:41:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	04:42:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]	1	07-F0	c0
2012/12/02	05:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	05:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2012/12/02	05:52:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	05:55:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	06:18:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	06:19:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	06:28:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	06:28:26.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/02	06:28:30.0	AOCS_0Re-point_Start_2_0G [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/02	06:28:46.0	XRT_FLD_DIS_404_0G [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/02	06:28:48.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/02	06:28:50.0	XRT_ARS_DIS_406_0G [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/02	06:31:28.0	XRT_QT_PROG_SET_445_0G [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2012/12/02	06:31:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	06:38:24.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	06:38:30.0	AOCS_0Re-point_Start_1_0G [0x097]	AOCU_NM	5	02-76	00 17 1a ae c1
2012/12/02	06:40:56.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/02	06:41:16.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/02	06:41:18.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/02	06:41:20.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/02	06:41:22.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/02	06:41:24.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	06:41:26.0	XRT_QT_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/12/02	06:41:28.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/02	06:41:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	07:32:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	07:32:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	07:32:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	07:35:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	07:55:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	07:56:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	09:12:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	09:12:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	09:12:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	09:15:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	09:31:00.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	09:32:00.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	14:39:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	14:39:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	14:39:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	14:42:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	14:50:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	14:51:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	16:14:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	16:14:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	16:14:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	16:17:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	16:38:00.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	16:39:00.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	17:51:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	17:51:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2012/12/02	17:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	17:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	18:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	18:14:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	18:14:30.0	AOCS_0Re-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/02	18:14:46.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/02	18:14:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/02	18:14:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/02	18:17:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/02	18:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2012/12/02	18:24:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	18:24:30.0	AOCS_0Re-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	18:26:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 ac 8c 00 00
2012/12/02	18:27:16.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/02	18:27:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/02	18:27:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/02	18:27:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/02	18:27:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/02	18:27:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	18:27:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/12/02	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/02	19:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	19:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	19:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	19:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	19:51:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	19:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]			
2012/12/02	21:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	21:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	21:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	21:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	21:29:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	21:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]			
2012/12/02	22:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/02	22:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/02	22:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/02	22:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/02	23:03:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/02	23:04:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]			
2012/12/03	00:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/03	00:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/03	00:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03	00:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/03	00:28:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/03	00:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CUSTOM_434_OG [0x1b2]			
2012/12/03	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/03	01:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/03	01:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2012/12/03	01:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/03	02:03:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/03	02:04:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	03:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/03	03:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/03	03:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/03	03:40:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/03	03:41:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	04:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	04:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/03	04:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/03	04:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/03	05:17:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/03	05:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	06:00:26.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/12/03	06:00:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/12/03	06:04:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/12/03	06:04:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/03	06:04:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/03	06:04:50.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/03	06:04:52.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2012/12/03	06:04:54.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	06:10:24.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	06:10:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/12/03	06:12:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/03	06:13:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/03	06:13:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/03	06:13:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/03	06:13:22.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/03	06:13:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/03	06:13:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/12/03	06:13:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/03	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	06:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	06:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/03	06:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/03	06:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/03	06:55:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/03	06:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	08:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	08:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/03	08:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/03	08:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/03	08:32:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/03	08:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/03	09:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/03	09:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				

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2012/12/03	09:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00	
2012/12/03	09:32:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2012/12/03	09:32:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2012/12/03	09:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2012/12/03	09:32:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2012/12/03	09:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2012/12/03	09:32:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 13	
2012/12/03	09:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10	
2012/12/03	09:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2012/12/03	09:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2012/12/03	09:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2012/12/03	09:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2012/12/03	09:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2012/12/03	10:05:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/03	10:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2012/12/03	15:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2012/12/03	15:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2012/12/03	15:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2012/12/03	15:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2012/12/03	15:28:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/03	15:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2012/12/03	15:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2012/12/03	15:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2012/12/03	15:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2012/12/03	15:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2012/12/03	15:37:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/03	15:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2012/12/03	16:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2012/12/03	16:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2012/12/03	16:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2012/12/03	16:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2012/12/03	17:14:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/03	17:15:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2012/12/03	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2012/12/03	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2012/12/03	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2012/12/03	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2012/12/03	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2012/12/03	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2012/12/03	18:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 09	
2012/12/03	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2012/12/03	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2012/12/03	18:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76 04 00 00 00 00	
2012/12/03	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00	
2012/12/03	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2012/12/03	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2012/12/03	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2012/12/03	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2012/12/03	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]			

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2012/12/03	18:12:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/12/03	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/03	18:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/03	18:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/03	18:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03	18:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/03	18:51:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/03	18:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/03	20:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/03	20:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/03	20:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03	20:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/03	20:28:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/03	20:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/03	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/03	21:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/03	21:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03	21:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/03	22:05:01.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/03	22:06:01.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/03	23:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/03	23:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/03	23:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/03	23:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/03	23:33:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/03	23:34:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/04	00:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/04	00:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/04	00:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/04	00:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/04	01:03:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/04	01:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/04	02:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/04	02:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/04	02:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/04	02:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/04	02:39:30.5	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/04	02:40:30.5	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/04	03:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/04	03:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/04	03:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/04	03:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/04	04:17:01.5	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/04	04:18:01.5	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/04	05:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/04	05:27:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/04	05:27:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/04	05:30:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/04	05:54:30.5	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/12/04	05:55:30.5	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/04	06:04:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/04	06:04:26.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2012/12/04	06:04:30.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2012/12/04	06:04:46.0	XRT_FLD_DIS_404_OG [0x194]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2012/12/04	06:04:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2012/12/04	06:04:50.0	XRT_ARS_DIS_406_OG [0x196]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/12/04	06:07:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 09
2012/12/04	06:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/04	06:14:24.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/04	06:14:30.0	AOCS_ORe-point_Start_5_OG [0x09b]		
		AOCU_NM	5	02-76 00 ee 36 54 31
2012/12/04	06:16:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/12/04	06:17:16.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/12/04	06:17:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/12/04	06:17:20.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/12/04	06:17:22.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/12/04	06:17:24.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/04	06:17:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2012/12/04	06:17:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/12/04	06:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/04	07:08:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/04	07:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/04	07:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/04	07:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/04	07:32:00.0	XRT_Custom_434_OG [0x1b2]		
2012/12/04	07:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/04	08:48:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/04	08:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/04	08:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/04	08:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/04	11:01:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00