

XRT Timeline to be uploaded on 2012/12/08

Period: 2012/12/08 10:04:00 - 2012/12/13 09:54:00

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Normal mode

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XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms												
Term		Pointing (x, y)					Comment					
12/08 10:17:00 - 12/08 16:13:54		Fixed (-14.0, 881.0)					# OP start + 10min, HOP 81, polar monitoring, N.					
12/09 10:03:00 - 12/09 15:40:30		Fixed (-14.0, -939.0)					* HOP 81, polar monitoring, S.					
PROG= 11 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 61 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band close		Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)		DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 62 1-time(s) 30.0sec												
└─ Open/G-band Open/G-band open		Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec												
└─ Seqn= 60 2-time(s) 30.0sec												
└─ Open/Al-mesh Open/Al-mesh close		Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
└─ Open/Ti-poly Open/Ti-poly close		Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #193D: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band												
Term		Pointing (x, y)					Comment					
12/08 16:21:00 - 12/08 17:53:54		Track (887.7, 124.7) ^{@ 12/08 16:14:00}					* Track AR 11623.					
12/09 01:07:00 - 12/09 05:59:54		Track (917.0, 124.7) ^{@ 12/09 01:04:00}					* AR 11623.					
12/09 06:13:00 - 12/09 09:59:54		Track (930.9, 124.6) ^{@ 12/09 06:10:00}					# Cont.					
12/09 16:04:30 - 12/09 17:57:54		Fixed (935.0, 124.0)					* AR 11623, fixed pointing at W limb.					
12/09 23:11:00 - 12/10 06:30:54		Fixed (935.0, 124.0)					* AR 11623, fixed pointing at W limb.					
12/10 06:44:00 - 12/10 17:54:00		Fixed (935.0, 124.0)					# Cont.					
PROG= 19 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band close		Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close		Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
└─ Open/G-band Open/G-band open		Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
└─ Seqn= 32 4-time(s) 2.0sec												
└─ Al-poly/Open thin-Be/Open close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└─ Open/Ti-poly Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└─ thin-Be/Open med-Be/Open close		Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└─ Open/thick-Al Open/thick-Al close		Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└─ Seqn= 93 30-time(s) 120.0sec												
└─ thin-Be/Open Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└─ Al-poly/Open Open/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	15.0sec
└─ thin-Be/Open Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec
└─ Al-poly/Open Open/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	15.0sec
└─ thin-Be/Open Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec
└─ Al-poly/Open Open/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	15.0sec
└─ thin-Be/Open Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec
└─ Al-poly/Open Open/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)												
Term		Pointing (x, y)					Comment					
12/08 17:57:00 - 12/08 18:03:54		Fixed (0.0, 0.0)					synoptic, shifted -6.0 min					
12/09 06:03:00 - 12/09 06:09:54		Fixed (0.0, 0.0)					synoptic					
12/09 18:01:00 - 12/09 18:07:54		Fixed (0.0, 0.0)					synoptic, shifted -2.0 min					
12/10 06:34:00 - 12/10 06:40:54		Fixed (0.0, 0.0)					synoptic, shifted 31.0 min					
12/10 18:20:00 - 12/10 18:26:54		Fixed (0.0, 0.0)					synoptic, shifted 17.0 min					
12/11 06:06:00 - 12/11 06:12:54		Fixed (0.0, 0.0)					synoptic, shifted 3.0 min					
PROG= 13 1-time(s)												
Subr= 1 1-time(s) 14.0sec												
└─ Seqn= 64 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Ti-poly close		Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Open/Al-mesh Open/Ti-poly close		Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└─ Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└─ Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└─ Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
└─ Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
└─ Seqn= 70 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close		Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Open/Ti-poly Open/Ti-poly close		Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Seqn= 67 1-time(s) 2.0sec												

thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 68		1-time(s)		2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1732: Dark - Med-Al+Thick-Be - 8x8 - 512x512

Term	Pointing (x, y)	Comment
12/08 18:07:00 - 12/09 01:03:54	Track (78.6, 206.4) @ 12/08 18:04:00	# EIS, returning old-AR observations, and spectral atlas.
12/09 18:11:00 - 12/09 23:07:54	Track (295.5, 208.3) @ 12/09 18:08:00	# EIS obs of returning old-AR.
12/10 18:30:00 - 12/10 23:26:54	Track (498.4, 209.6) @ 12/10 18:27:00	# EIS obs of returning old-AR.
PROG= 10 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 81 1-time(s) 2.0sec		
med-Al/thick-Be	med-Al/thick-Be	close Safe Dark 500ms Obs 8x8 512x512 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
12/10 23:30:00 - 12/11 06:02:54	Fixed (935.0, 124.0)	* AR 11623 at W limb.
12/11 06:16:00 - 12/11 09:50:00	Fixed (935.0, 124.0)	# Cont.
PROG= 03 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 18 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 65 4-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 73 70-time(s) 120.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
12/08 10:17:00 - 12/08 16:13:54	Fixed (-14.0, 881.0)	# OP start + 10min, HOP 81, polar monitoring, N.
12/08 16:21:00 - 12/08 17:53:54	Track (887.7, 124.7) @ 12/08 16:14:00	* Track AR 11623.
12/08 18:07:00 - 12/09 01:03:54	Track (78.6, 206.4) @ 12/08 18:04:00	# EIS, returning old-AR observations, and spectral atlas.
12/09 01:07:00 - 12/09 05:59:54	Track (917.0, 124.7) @ 12/09 01:04:00	* AR 11623.
12/09 06:13:00 - 12/09 09:59:54	Track (930.9, 124.6) @ 12/09 06:10:00	# Cont.
12/09 10:03:00 - 12/09 15:40:30	Fixed (-14.0, -939.0)	* HOP 81, polar monitoring, S.
12/09 16:04:30 - 12/09 17:57:54	Fixed (935.0, 124.0)	* AR 11623, fixed pointing at W limb.
12/09 18:11:00 - 12/09 23:07:54	Track (295.5, 208.3) @ 12/09 18:08:00	# EIS obs of returning old-AR.
12/09 23:11:00 - 12/10 06:30:54	Fixed (935.0, 124.0)	* AR 11623, fixed pointing at W limb.
12/10 06:44:00 - 12/10 17:54:00	Fixed (935.0, 124.0)	# Cont.
12/10 18:30:00 - 12/10 23:26:54	Track (498.4, 209.6) @ 12/10 18:27:00	# EIS obs of returning old-AR.
12/10 23:30:00 - 12/11 06:02:54	Fixed (935.0, 124.0)	* AR 11623 at W limb.
12/11 06:16:00 - 12/11 09:50:00	Fixed (935.0, 124.0)	# Cont.
PROG= 16 15-time(s)		
Subr= 1 45-time(s) 10.0sec		
Seqn= 35 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
Seqn= 36 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 37 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 38 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

L	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

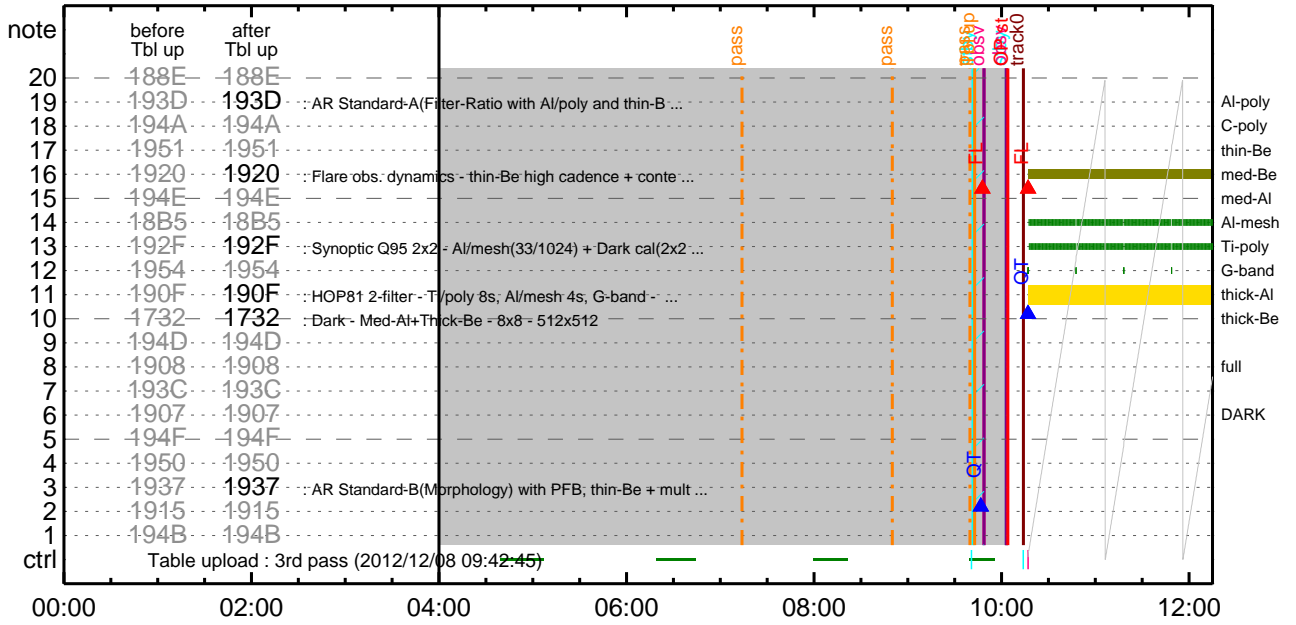
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Flare Detection

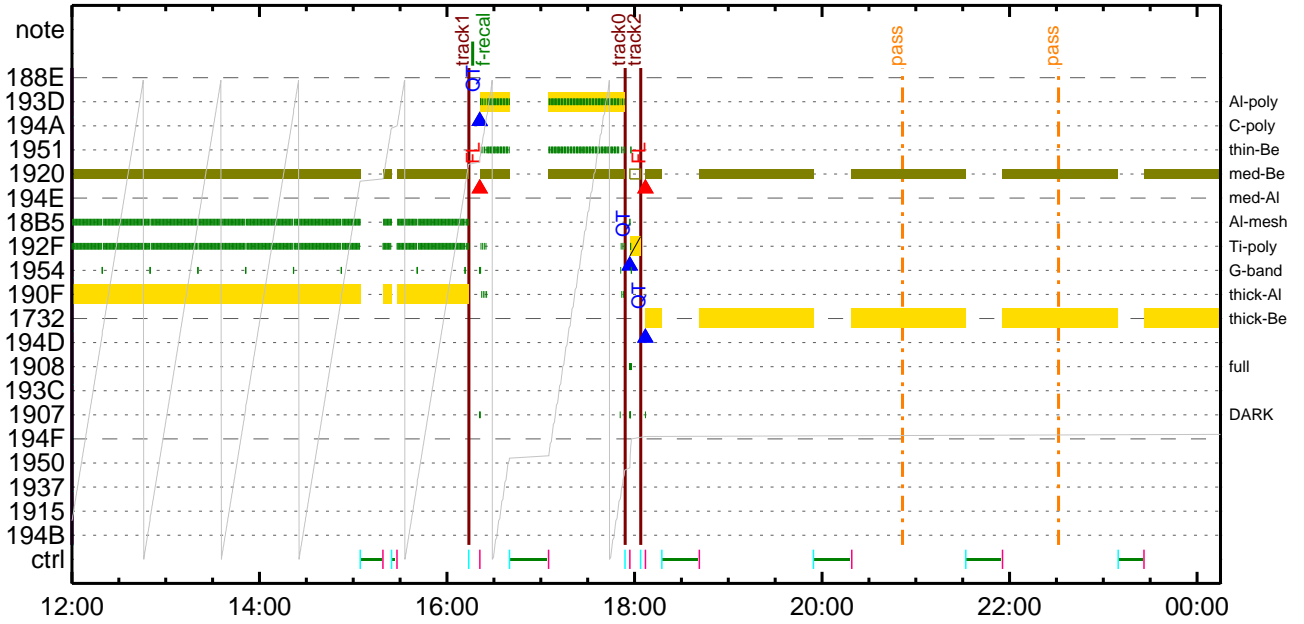
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FLD Patrol												
Term	Pointing (x, y)					Comment						
12/08 10:16:46 - 12/08 17:54:16	Fixed (-14.0, 881.0)					# OP start + 10min, HOP 81, polar monitoring, N.						
12/08 18:06:46 - 12/09 06:00:16	Track (78.6, 206.4) @ 12/08 18:04:00					# EIS, returning old-AR observations, and spectral atlas.						
12/09 06:12:46 - 12/09 17:58:16	Track (930.9, 124.6) @ 12/09 06:10:00					# Cont.						
12/09 18:10:46 - 12/10 06:31:16	Track (295.5, 208.3) @ 12/09 18:08:00					# EIS obs of returning old-AR.						
12/10 06:43:46 - 12/10 18:17:16	Fixed (935.0, 124.0)					# Cont.						
12/10 18:29:46 - 12/11 06:03:16	Track (498.4, 209.6) @ 12/10 18:27:00					# EIS obs of returning old-AR.						
12/11 06:15:46 - 12/13 09:54:00	Fixed (935.0, 124.0)					# Cont.						
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

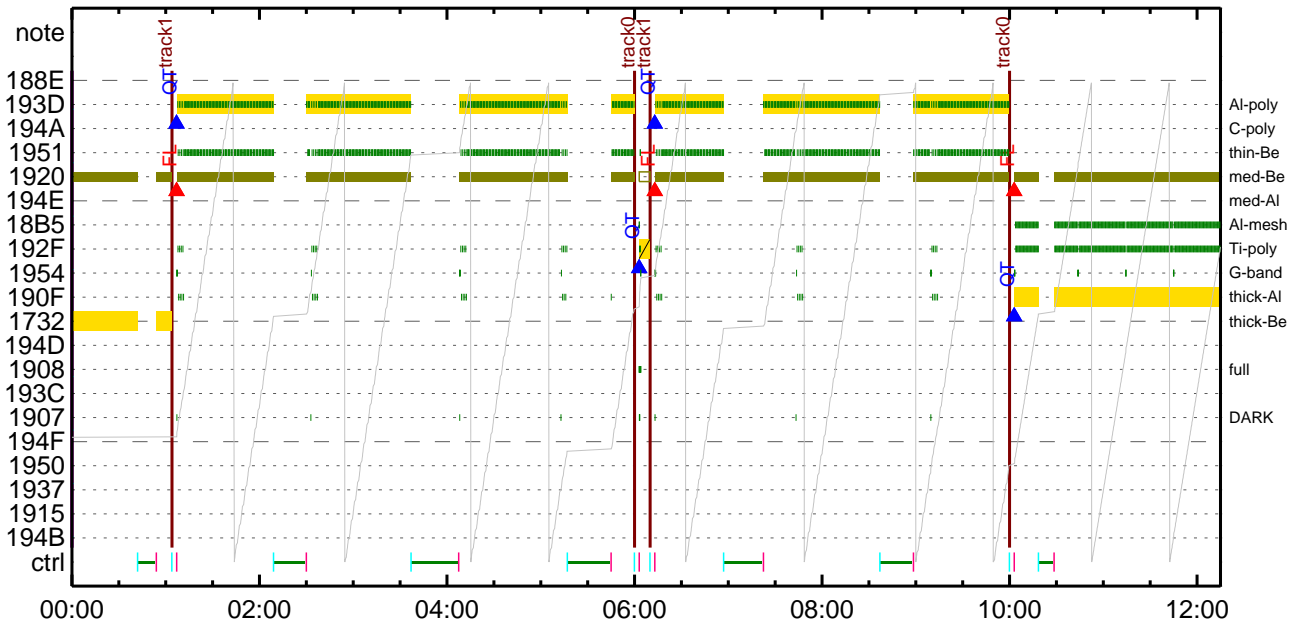
CMDI #0111 2012/12/08



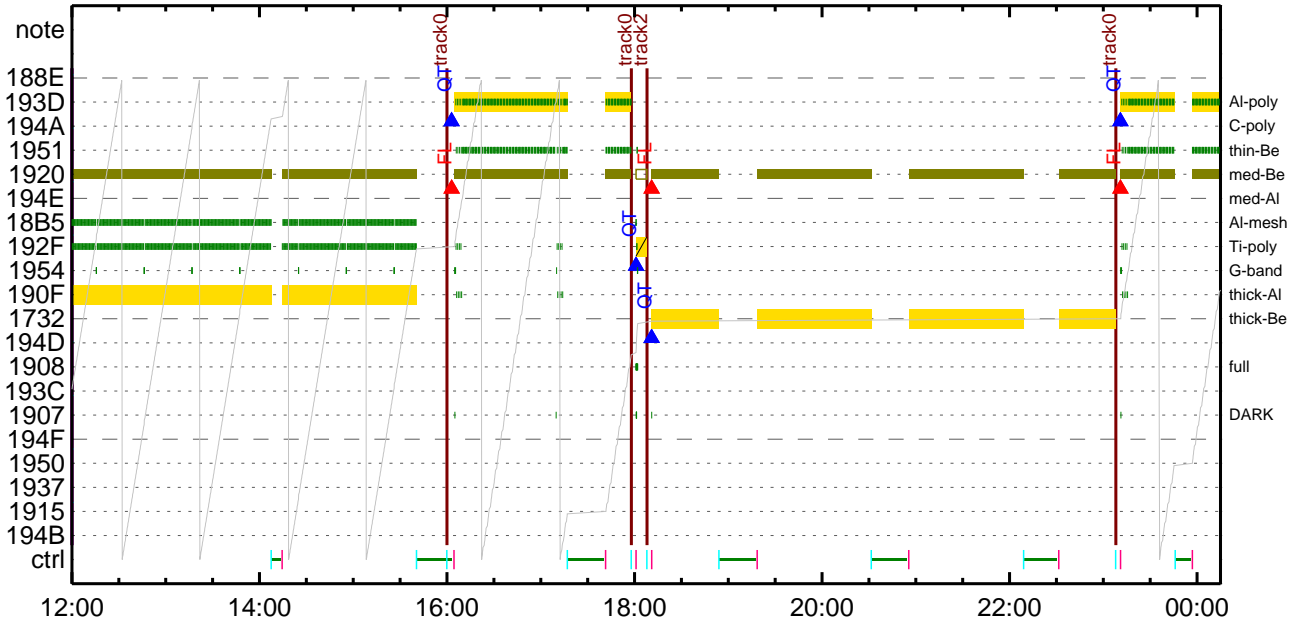
CMDI #0111 2012/12/08



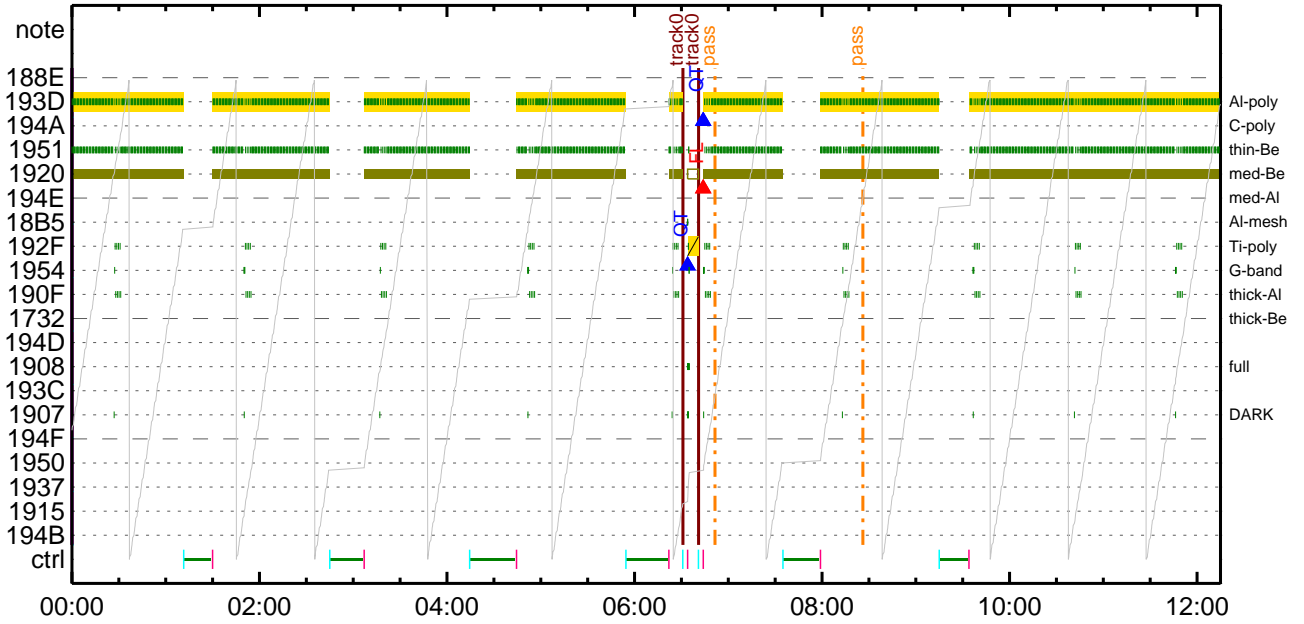
CMDI #0111 2012/12/09



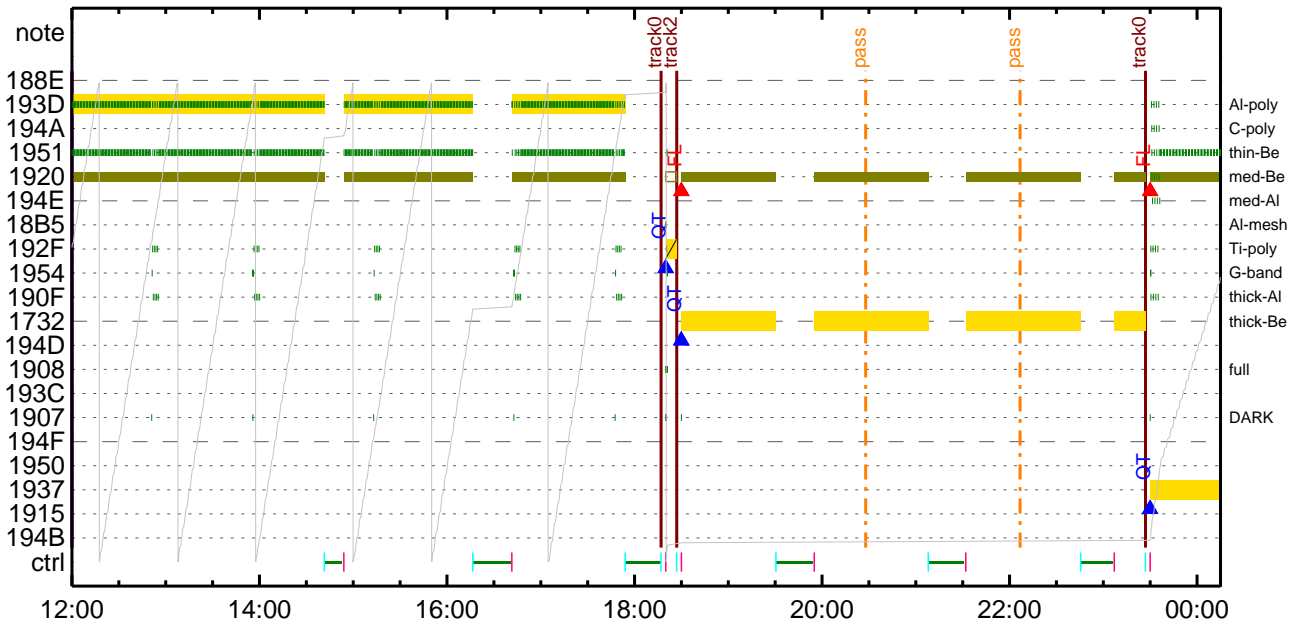
CMDI #0111 2012/12/09



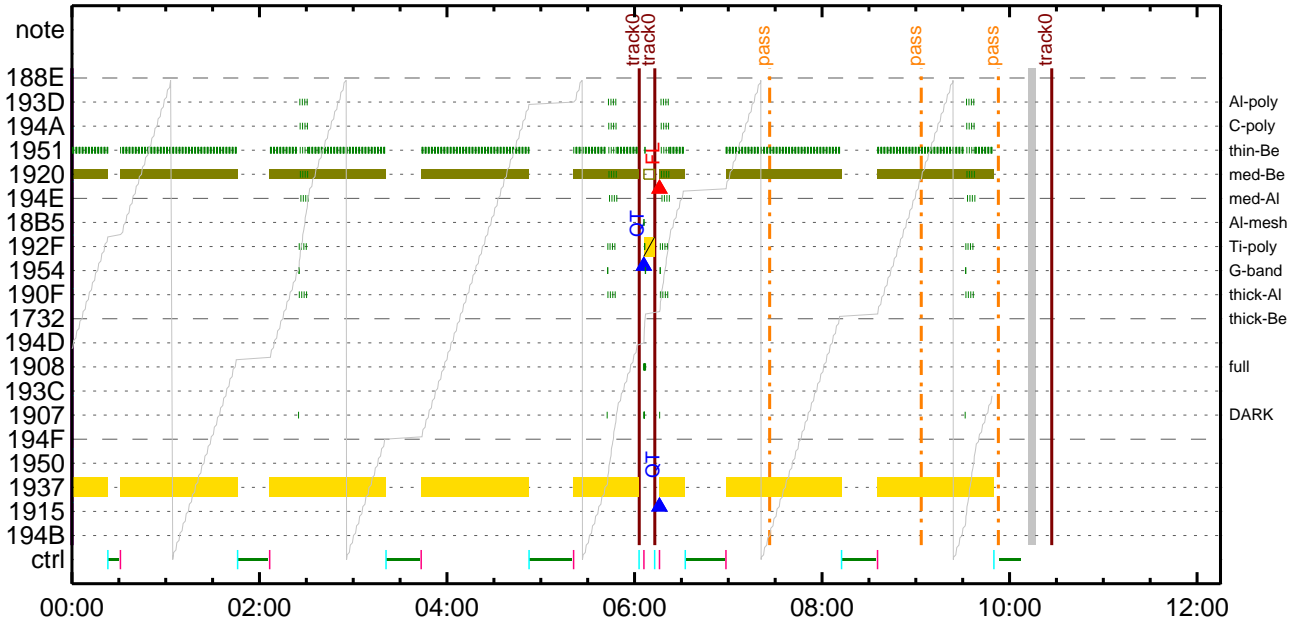
CMDI #0111 2012/12/10



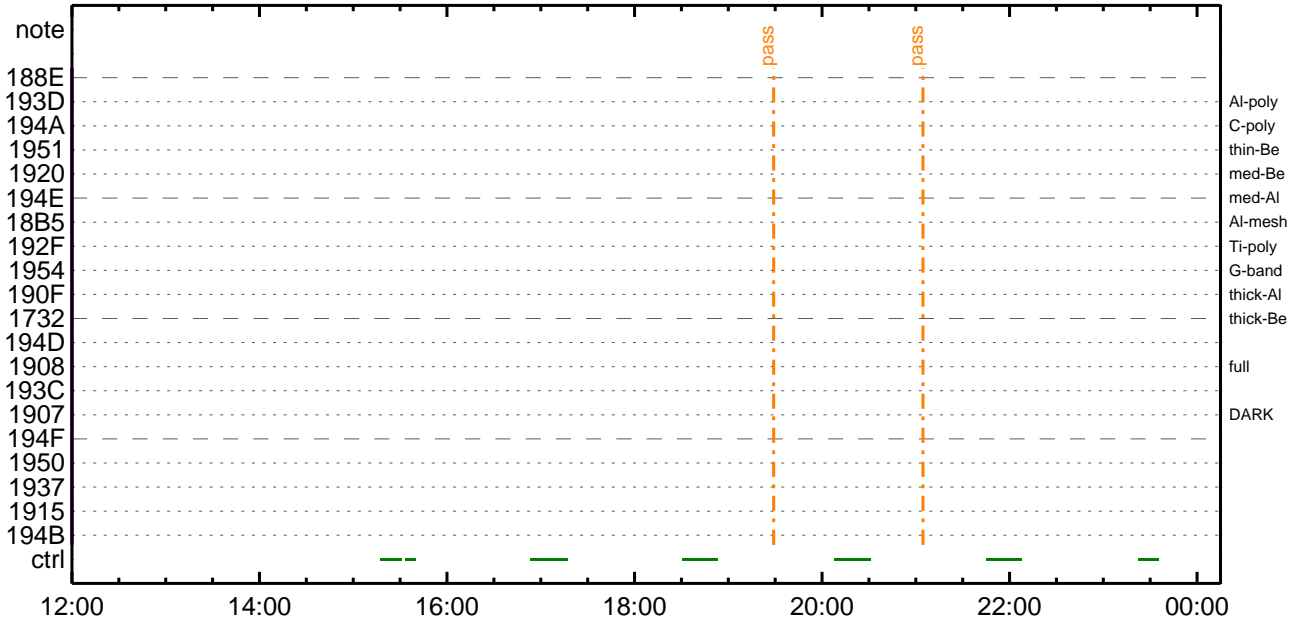
CMDI #0111 2012/12/10



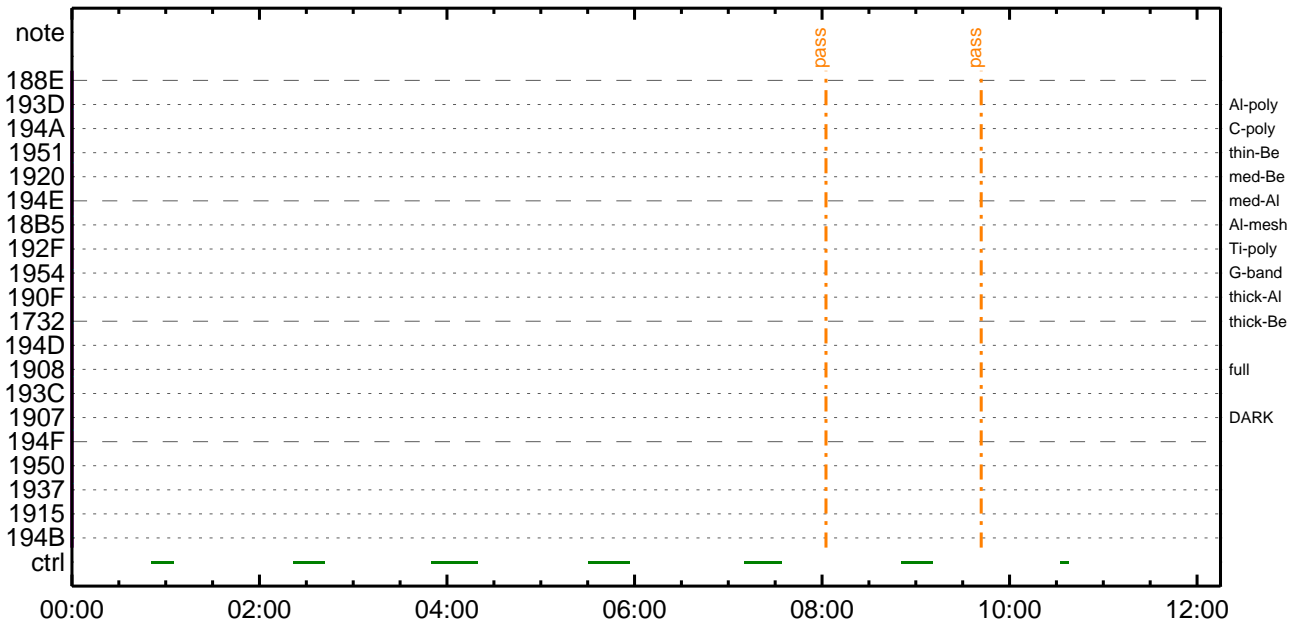
CMDI #0111 2012/12/11



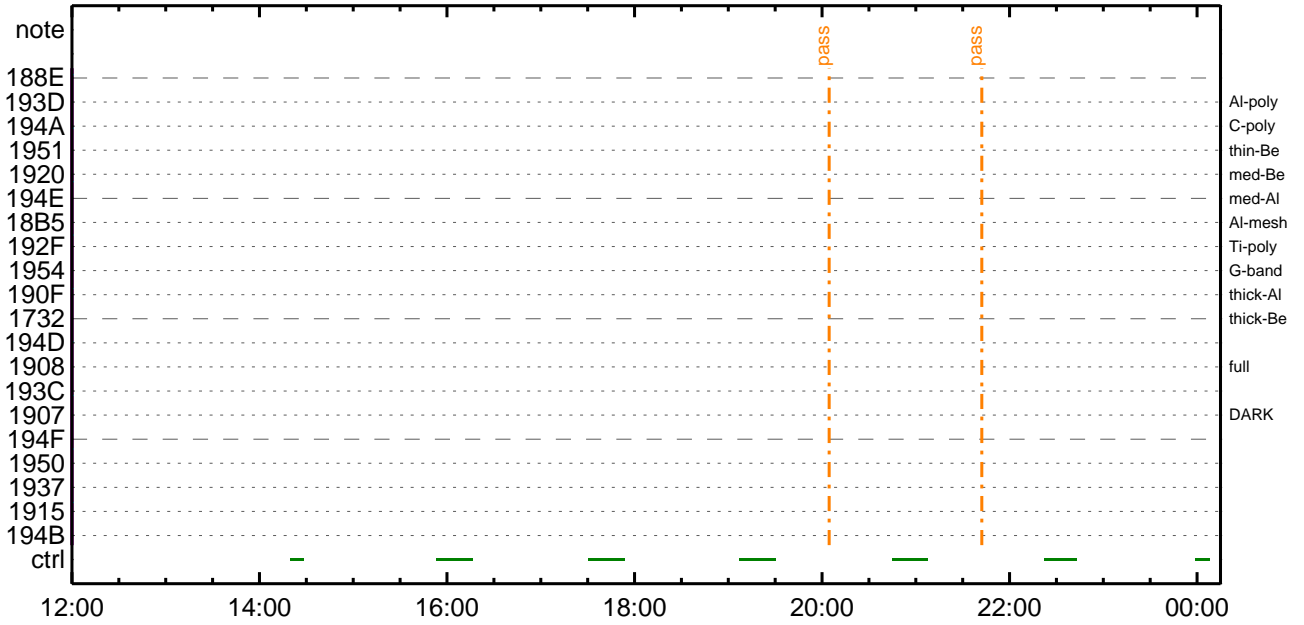
CMDI #0111 2012/12/11



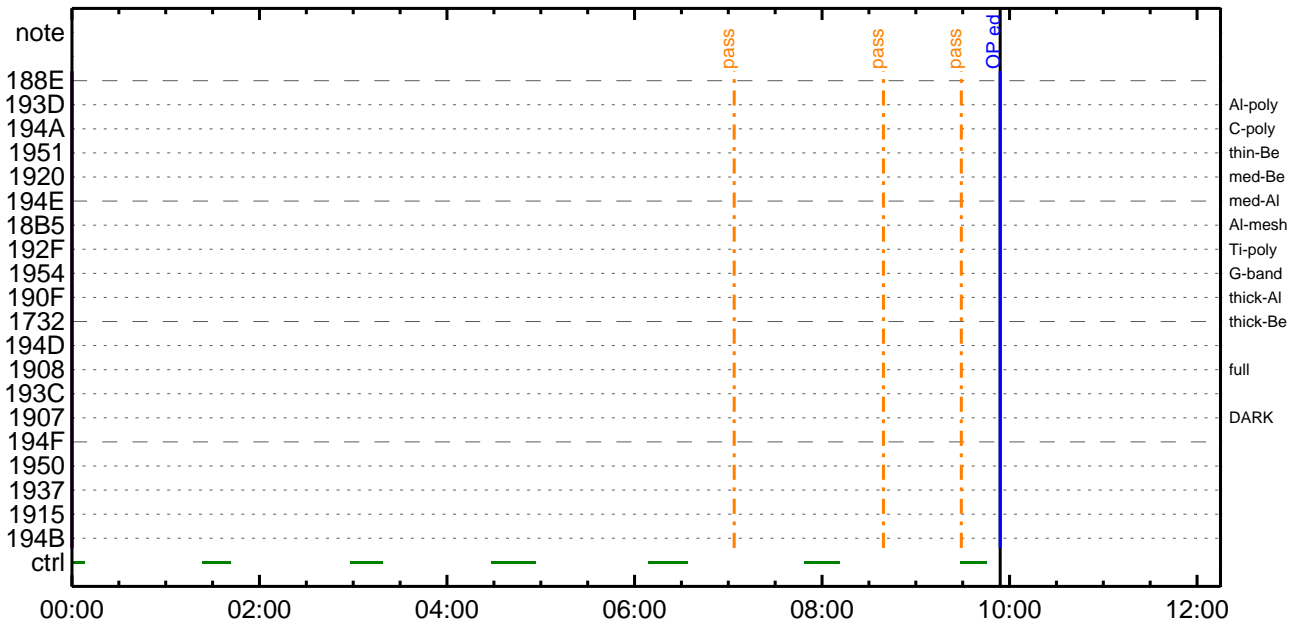
CMDI #0111 2012/12/12



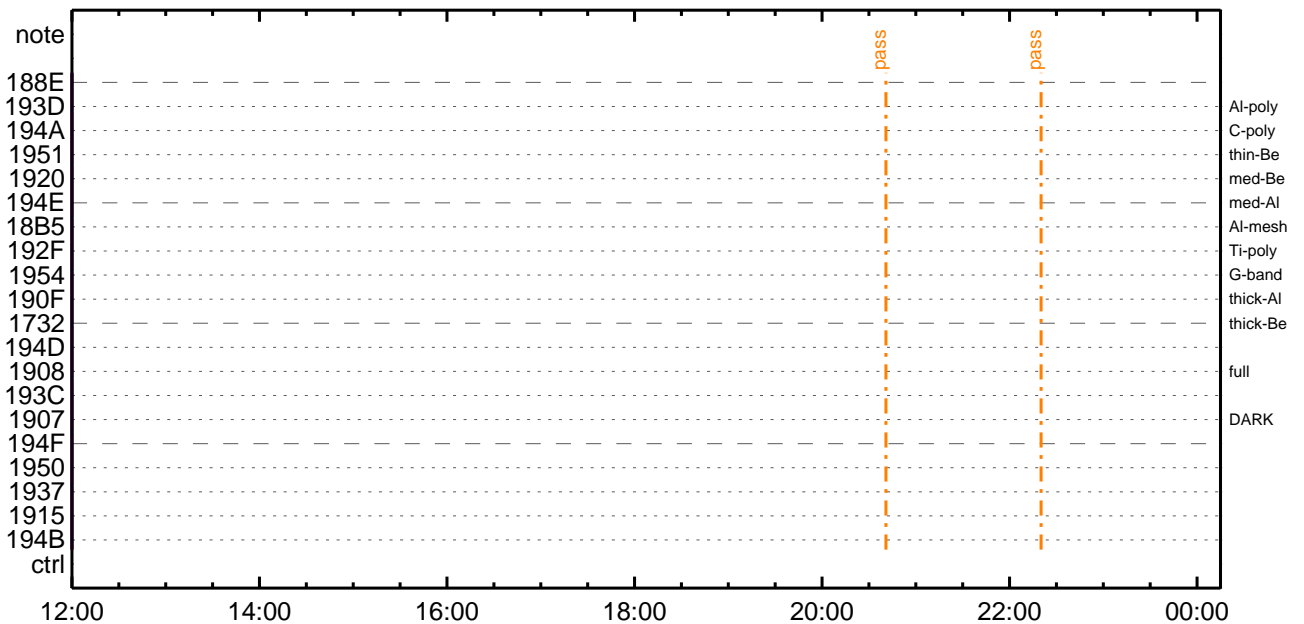
CMDI #0111 2012/12/12



CMDI #0111 2012/12/13



CMDI #0111 2012/12/13




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-319:OP
0104 ( )
0105 S. OG og-319:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÔæòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹çòçòâ³òE;f
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYóYÉòðÁDî¿(UT)
0183 +. TI 2012-12-08 09:59:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-12-08 09:59:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-12-08 09:59:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 20s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2012-12-08 10:03:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP ūãîîî»ö¼ýðéâðð¹èDCBC•×²è *****
0153 C. (%ã°îŷ0ŷãŷÉŷÏŷÉŷäŷçŷèè¾¼¼¼¼»Û¹èè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ŷÐŷ¹•İ Daily±;İŊñÈ'ø¹èDCBC•×²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSŷÁŷſŷĀŷ-¼Ā»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-321 2012-12-08 13:19:43 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY-¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÁÍ;Èò¿òÀò•µ°®×Í×ÁÇòÍYçYÁY×YÍ;¼YÉ;ÈÈèµ•ÍÍÉ;ÈòÈ¼°ÇÓò•ò¿¼Í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;è
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG _____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_DIS
0059 BC (d9)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0061 BC (c9)
0062 + DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 + DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 03)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 10)
0072 . C. ----- Success Verify ? OK / NG _____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2012-12-08 10:03:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG _____
0083 C.
0084 C. ***** XRT END *****
0085 . C. *****
0086 C. SOT table upload
0087 C. *****
0088 . C. < Stop FG table >
0089 +. DC 07-F0 MDP_FG_CTRL_MANU
0090 BC (51)
0091 . C. -----
0092 C. MDP_FG_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 . C. <Upload FG Observation Table>

```

```
0096 . S. RAM ram-263:MDP_OBS_F
0097 ( )
0098 C.
0099 . C. < Dump RAMID=MDP_OBS_F >
0100 +. DC 07-F0 MDP_DUMP_FGTBL
0101 BC (82 07 00 00 00 38 b8)
0102 C. -----
0103 C. MDP_OBS_F verify = OK/NG [ ]
0104 C. -----
0105 C.
0106 C. *****
0107 C. SOT TI command set
0108 C. *****
0109 C. Execute, after the success of TBL upload.
0110 +. TI 2012-12-08 10:03:18.0
0111 DC 07-F0 MDP_SOT_MODE_OBSV
0112 BC (40)
0113 C. -----
0114 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0115 C. -----
0116 C.
0117 C.
0118 . C. ***** MDP `ûÃïñî»ö%ÿñÊÃðñ¹ñèDCBC•×²è *****
0119 C. (%ã°ïŸÖŸÃŸÊŸÏŸÈŸáŸçŸèñ%ç%ã%Ûñ¹ñè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ŸDŸ¹·İ Daily±;îÑñÊ'Øñ¹ñèDCBC•×²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSŸÃŸŸŸÛŸ¹ŸÛñ¹ñè
0130 C.
0131 . C. ***** LOS *****
0132 C.
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*** OP Sequence for XRT ***

2012/12/08	10:13:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	10:14:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 b1 b3 01 3f				
2012/12/08	10:16:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/08	10:16:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/08	10:16:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/08	10:16:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/08	10:16:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/08	10:16:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	10:16:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/12/08	10:16:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/08	10:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	15:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	15:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	15:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	15:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	15:18:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	15:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	15:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	15:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	15:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	15:27:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	15:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	15:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	16:13:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	16:14:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/12/08	16:16:26.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/12/08	16:20:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/08	16:20:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/08	16:20:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/08	16:20:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/08	16:20:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/08	16:20:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	16:20:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/12/08	16:20:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/08	16:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	16:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	16:40:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	16:40:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	16:43:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	17:04:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	17:05:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	17:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/12/08	17:54:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/12/08	17:54:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/08	17:54:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/08	17:54:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

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2012/12/08	17:56:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2012/12/08	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	18:03:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	18:04:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02	00	00	00	00
2012/12/08	18:06:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/12/08	18:06:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/08	18:06:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/08	18:06:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/08	18:06:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/08	18:06:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	18:06:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/12/08	18:06:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/12/08	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	18:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	18:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	18:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	18:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	18:40:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	18:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	19:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	19:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	19:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	19:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	20:18:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	20:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	21:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	21:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	21:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	21:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	21:54:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	21:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/08	23:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/08	23:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/08	23:09:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/08	23:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/08	23:25:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/08	23:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/09	00:42:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/09	00:42:02.5	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/09	00:42:04.5	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/09	00:45:14.5	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/09	00:53:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/09	00:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/09	01:03:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/09	01:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/12/09	01:06:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/12/09	01:06:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/09	01:06:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/09	01:06:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2012/12/09	01:06:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/12/09	01:06:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/09	01:06:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2012/12/09	01:06:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/12/09	01:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/09	02:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/09	02:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/09	02:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/09	02:29:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/09	02:30:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	03:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/09	03:37:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/09	03:37:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/09	03:40:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/09	04:06:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/09	04:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	05:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/09	05:17:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/09	05:17:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/09	05:20:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/09	05:44:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/09	05:45:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/09	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/12/09	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00
2012/12/09	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/12/09	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/12/09	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/12/09	06:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2012/12/09	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	06:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/09	06:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00	00	00
2012/12/09	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/12/09	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/12/09	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/12/09	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/12/09	06:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/12/09	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/09	06:12:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2012/12/09	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/12/09	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	06:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/09	06:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/09	06:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/09	07:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/09	07:21:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/09	07:22:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/09	08:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2012/12/09	08:37:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	08:37:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/09	08:40:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/09	08:57:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/09	08:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/09	09:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/09	10:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 53 74 01 3f		
2012/12/09	10:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/12/09	10:02:46.0	XRT_FLD_ENA_418_OG [0x1a2]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/12/09	10:02:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/12/09	10:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/12/09	10:02:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/12/09	10:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	10:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2012/12/09	10:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/12/09	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/09	10:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/09	10:18:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	10:18:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/09	10:21:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/09	10:27:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/09	10:28:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/09	14:07:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/09	14:07:32.5	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	14:07:34.5	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/09	14:10:44.5	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/09	14:13:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/09	14:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/09	15:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/09	15:40:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	15:40:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/09	15:43:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/09	15:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/09	16:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 f4 fe ac e6		
2012/12/09	16:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/12/09	16:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/12/09	16:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/12/09	16:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/12/09	16:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/12/09	16:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	16:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2012/12/09	16:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/12/09	16:03:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/09	16:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/09	17:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/09	17:17:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/09	17:17:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/09	17:20:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		

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2012/12/09	17:40:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/09	17:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/09	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/09	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/09	17:58:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/12/09	17:58:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/09	17:58:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/09	17:58:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/09	18:00:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/09	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/09	18:07:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/09	18:08:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	02 00 00 00 00
2012/12/09	18:10:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/09	18:10:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/09	18:10:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/09	18:10:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/09	18:10:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/09	18:10:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/09	18:10:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2012/12/09	18:10:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/09	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/09	18:54:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/09	18:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/09	18:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/09	18:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/09	19:17:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/09	19:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/09	20:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/09	20:31:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/09	20:31:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/09	20:34:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/09	20:54:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/09	20:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/09	22:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/09	22:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/09	22:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/09	22:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/09	22:30:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/09	22:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/09	23:07:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/09	23:08:00.0	AOCS_ORe-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	00 f4 fe ac e6
2012/12/09	23:10:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/09	23:10:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/09	23:10:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/09	23:10:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/09	23:10:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/09	23:10:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/09	23:10:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/12/09	23:10:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			

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2012/12/09	23:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/09	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/09	23:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/09	23:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/09	23:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/09	23:56:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/09	23:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	01:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	01:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	01:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	01:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	01:29:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	01:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	02:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	02:45:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	02:45:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	02:48:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	03:06:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	03:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	04:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	04:14:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	04:14:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	04:17:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	04:43:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	04:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	05:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	05:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	05:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	06:21:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	06:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	06:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	06:30:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/12/10	06:31:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00 00
2012/12/10	06:31:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/12/10	06:31:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/12/10	06:31:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/10	06:33:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2012/12/10	06:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	06:40:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	06:41:00.0	AOCs_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 f4 fe ac	e6
2012/12/10	06:43:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/12/10	06:43:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/12/10	06:43:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/12/10	06:43:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/12/10	06:43:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/10	06:43:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	06:43:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2012/12/10	06:43:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					

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2012/12/10	06:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	07:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	07:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	07:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	07:58:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	07:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	09:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	09:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	09:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	09:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	09:33:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	09:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	14:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	14:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	14:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	14:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	14:53:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	14:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	16:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	16:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	16:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	16:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	16:40:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/10	16:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	17:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	17:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	17:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/10	17:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/10	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	18:16:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/12/10	18:17:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2012/12/10	18:17:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/12/10	18:17:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/12/10	18:17:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/10	18:19:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2012/12/10	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/10	18:26:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/10	18:27:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 00 00 00	00
2012/12/10	18:29:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/12/10	18:29:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/12/10	18:29:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/12/10	18:29:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/12/10	18:29:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/10	18:29:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/10	18:29:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2012/12/10	18:29:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/12/10	18:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2012/12/10	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/10	19:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/10	19:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/10	19:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/10	19:54:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/10	19:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/10	21:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/10	21:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/10	21:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/10	21:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/10	21:31:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/10	21:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/10	22:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/10	22:45:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/10	22:45:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/10	22:48:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/10	23:06:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/10	23:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/10	23:26:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/10	23:27:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 f4 fe ac e6				
2012/12/10	23:29:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/10	23:29:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/10	23:29:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/10	23:29:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/10	23:29:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/10	23:29:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/10	23:29:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/12/10	23:29:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/10	23:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/11	00:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/11	00:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/11	00:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/11	00:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/11	00:30:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/11	00:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/11	01:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/11	01:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/11	01:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/11	01:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/11	02:05:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/11	02:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/11	03:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/11	03:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/11	03:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/11	03:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/11	03:42:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/11	03:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/11	04:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/11	04:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/11	04:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							

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2012/12/11	04:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/11	05:20:00.0	XRT_Custom_434_OG [0x1b2]				
2012/12/11	05:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/11	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/11	06:02:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/11	06:03:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00
2012/12/11	06:03:16.0	XRT_FLD_DIS_404_OG [0x194]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/11	06:03:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/11	06:03:20.0	XRT_ARS_DIS_406_OG [0x196]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/11	06:05:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/11	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/11	06:12:54.0	XRT_CTRL_MANU_447_OG [0x1bf]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/11	06:13:00.0	AOCS_ORe-point_Start_6_OG [0x09c]				
			AOCU_NM	5	02-76	00 f4 fe ac e6
2012/12/11	06:15:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/11	06:15:46.0	XRT_FLD_ENA_428_OG [0x1ac]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/11	06:15:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/11	06:15:50.0	XRT_AEC_RESET_423_OG [0x1a7]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/11	06:15:52.0	XRT_ARS_DIS_438_OG [0x1b6]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/11	06:15:54.0	XRT_FLD_RESET_424_OG [0x1a8]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/11	06:15:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/12/11	06:15:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/11	06:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/11	06:32:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/11	06:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/11	06:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/11	06:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/11	06:57:30.0	XRT_Custom_434_OG [0x1b2]				
2012/12/11	06:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/11	08:12:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/11	08:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/11	08:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/11	08:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/11	08:34:30.0	XRT_Custom_434_OG [0x1b2]				
2012/12/11	08:35:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/11	09:50:00.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/11	10:27:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00