

XRT Timeline to be uploaded on 2012/12/13

Period: 2012/12/13 09:54:00 - 2012/12/18 09:43:00

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Normal mode

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XOB #1905: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long-2 - w leak image																
Term			Pointing (x, y)					Comment								
12/14 13:03:00 - 12/14 13:09:54			Fixed (-528.0, -528.0)					# XRT post bake-out quadrant pointings 1/4.								
PROG= 17			1-time(s)													
Subr= 1		1-time(s)		12.0sec												
Seqn= 1		1-time(s)		12.0sec												
		Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)			Q=90	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)			Q=90	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)			Q=98	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)			Q=98	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec												
Seqn= 7		2-time(s)		2.0sec												
		Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Subr= 3		2-time(s)		2.0sec												
Seqn= 15		1-time(s)		2.0sec												
		Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)			DPCM	0	0	2.0sec
Seqn= 8		1-time(s)		2.0sec												
		Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

XOB #1906: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long-w leak image																
Term			Pointing (x, y)					Comment								
12/14 13:13:00 - 12/14 13:19:54			Fixed (528.0, -528.0)					# 2/4								
PROG= 04			1-time(s)													
Subr= 1			1-time(s)		12.0sec											
Seqn= 2			1-time(s)		12.0sec											
Open/G-band			Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)			Q=90	0	0	2.0sec
Open/G-band			Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)			Q=90	0	0	2.0sec
Open/thick-Be			Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)			Q=98	0	0	2.0sec
Open/thick-Be			Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)			Q=98	0	0	2.0sec
Subr= 2			1-time(s)		2.0sec											
Seqn= 7			2-time(s)		2.0sec											
Open/Al-mesh			Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Open/Ti-poly			Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Subr= 3			2-time(s)		2.0sec											
Seqn= 15			1-time(s)		2.0sec											
Open/G-band			Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)			DPCM	0	0	2.0sec
Seqn= 8			1-time(s)		2.0sec											
Open/G-band			Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

XOB #1907: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long-w leak image															
Term			Pointing (x, y)					Comment							
12/14 13:23:00 - 12/14 13:29:54			Fixed (528.0, 528.0)					# 3/4							
PROG= 06			1-time(s)												
Subr= 1			1-time(s)			12.0sec									
Seqn= 3			1-time(s)			12.0sec									
Open/G-band			Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)		Q=90	0	0	2.0sec
Open/G-band			Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)		Q=90	0	0	2.0sec
Open/thick-Be			Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)		Q=98	0	0	2.0sec
Open/thick-Be			Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)		Q=98	0	0	2.0sec
Subr= 2			1-time(s)			2.0sec									
Seqn= 7			2-time(s)			2.0sec									
Open/Al-mesh			Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Ti-poly			Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 3			2-time(s)			2.0sec									
Seqn= 15			1-time(s)			2.0sec									
Open/G-band			Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 8			1-time(s)			2.0sec									
Open/G-band			Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1908: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long-w leak image														
Term			Pointing (x, y)			Comment								
12/14 13:33:00 - 12/14 13:39:54			Fixed (-528.0, 528.0)			# 4/4								
PROG= 08 1-time(s)														
└ Subr= 1 1-time(s) 12.0sec														
└└ Seqn= 4 1-time(s) 12.0sec														
└└└ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec														
└└└ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec														

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 7	2-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	1-time(s)	2.0sec											
Seqn= 15	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #193C: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
12/14 13:43:00 - 12/14 18:01:24	Fixed (920.0, 202.4)	# Observe returning AR 11589 for EIS.
12/14 18:14:30 - 12/15 06:19:54	Track (-755.4, 156.3) @ 12/14 18:11:30	# Return to AR near East limb.]
12/15 06:33:00 - 12/15 09:04:00	Track (-678.9, 158.5) @ 12/15 06:30:00	# AR cont.

PROG= 07	1-time(s)	2.0sec											
Subr= 1	1-time(s)	2.0sec											
Seqn= 19	2-time(s)	2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec											
Seqn= 92	1-time(s)	2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32	4-time(s)	2.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 93	30-time(s)	60.0sec											
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18

Term	Pointing (x, y)	Comment
12/14 18:04:30 - 12/14 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
12/15 06:23:00 - 12/15 06:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min

PROG= 13	1-time(s)	14.0sec											
Subr= 1	1-time(s)	14.0sec											
Seqn= 64	1-time(s)	4.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 70	1-time(s)	4.0sec											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 68	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
12/14 13:43:00 - 12/14 18:01:24	Fixed (920.0, 202.4)	# Observe returning AR 11589 for EIS.
12/14 18:14:30 - 12/15 06:19:54	Track (-755.4, 156.3) @ 12/14 18:11:30	# Return to AR near East limb.]
12/15 06:33:00 - 12/15 09:04:00	Track (-678.9, 158.5) @ 12/15 06:30:00	# AR cont.

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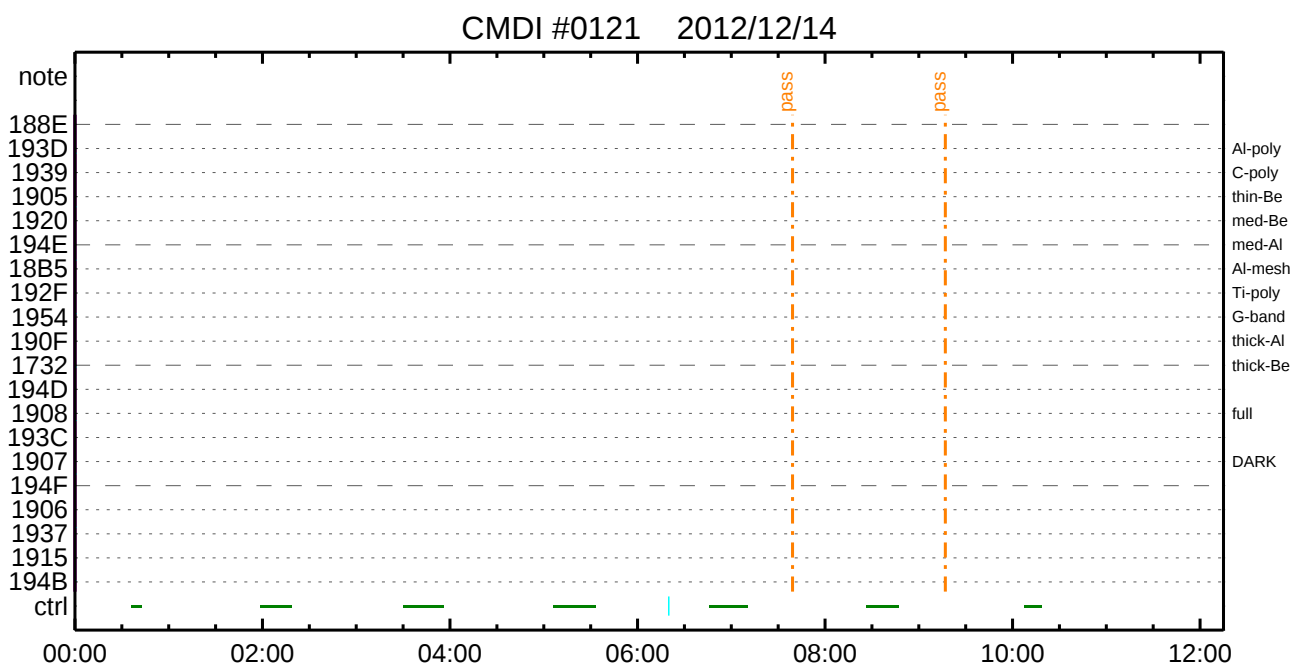
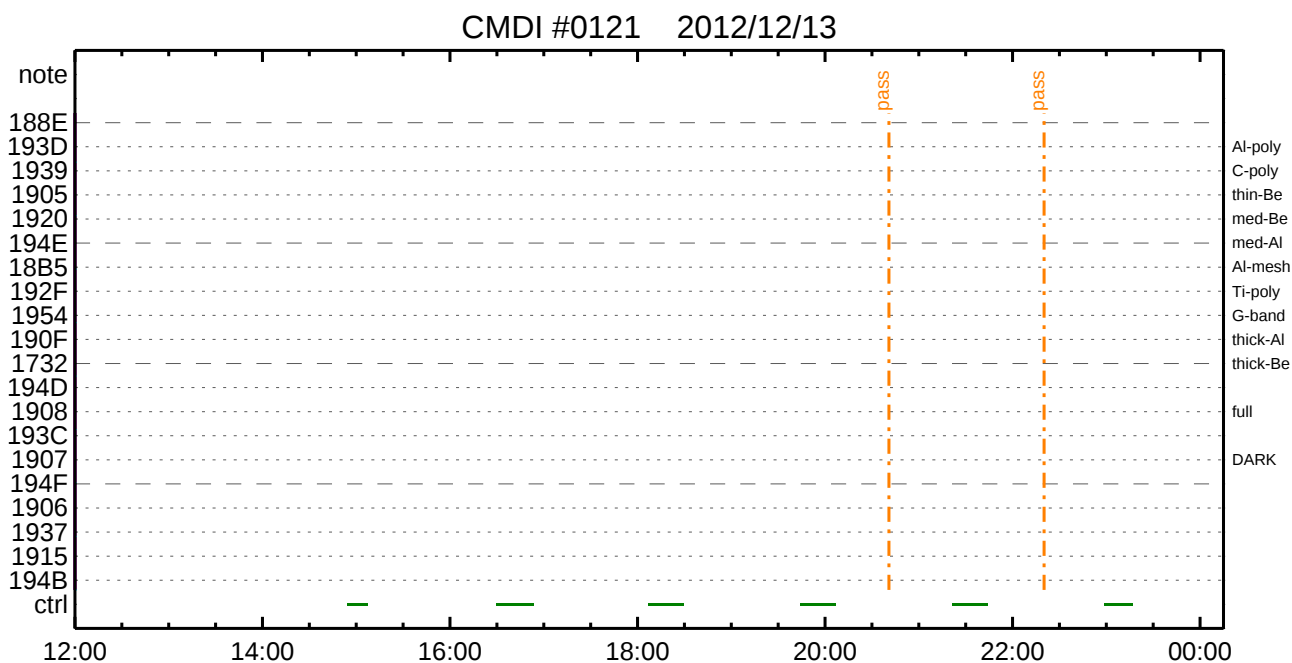
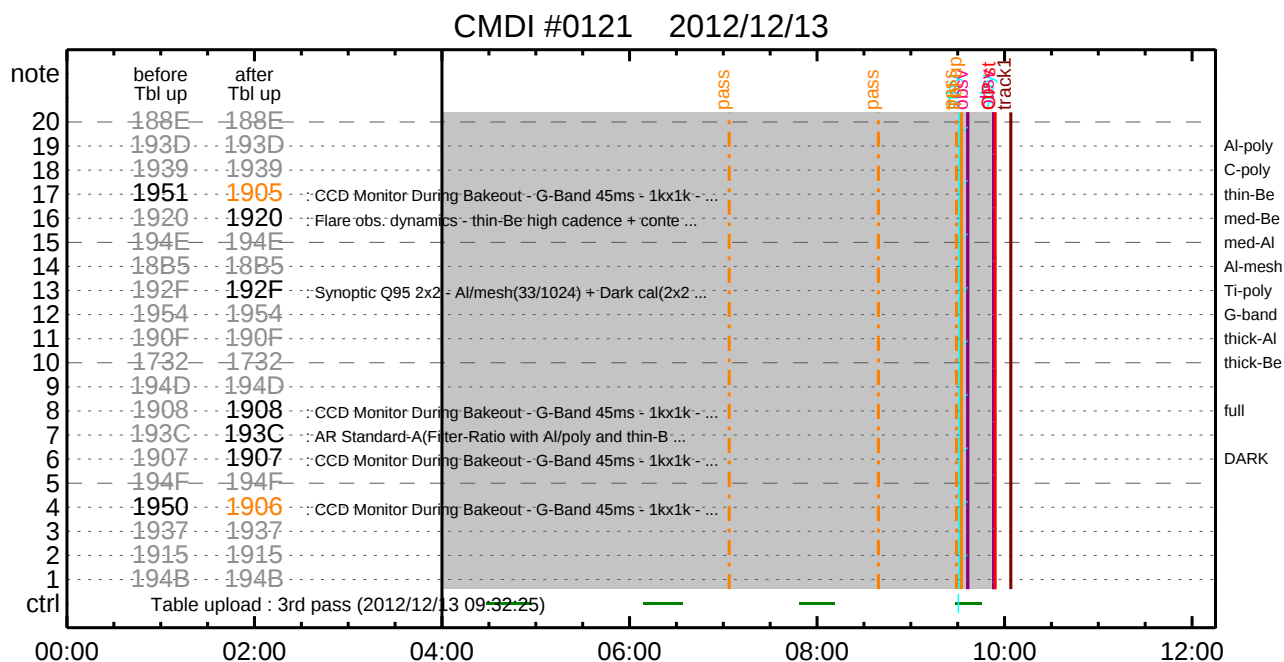
Active Region Search * * * * *

NOT USED

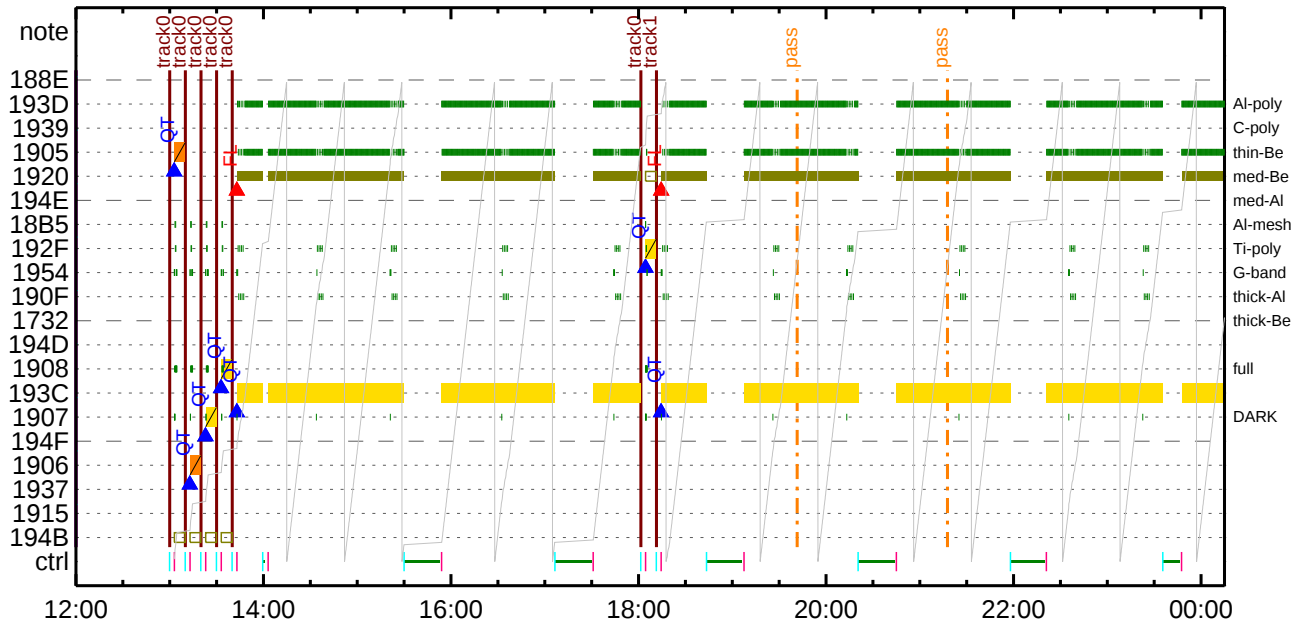
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Flare Detection * * * * *

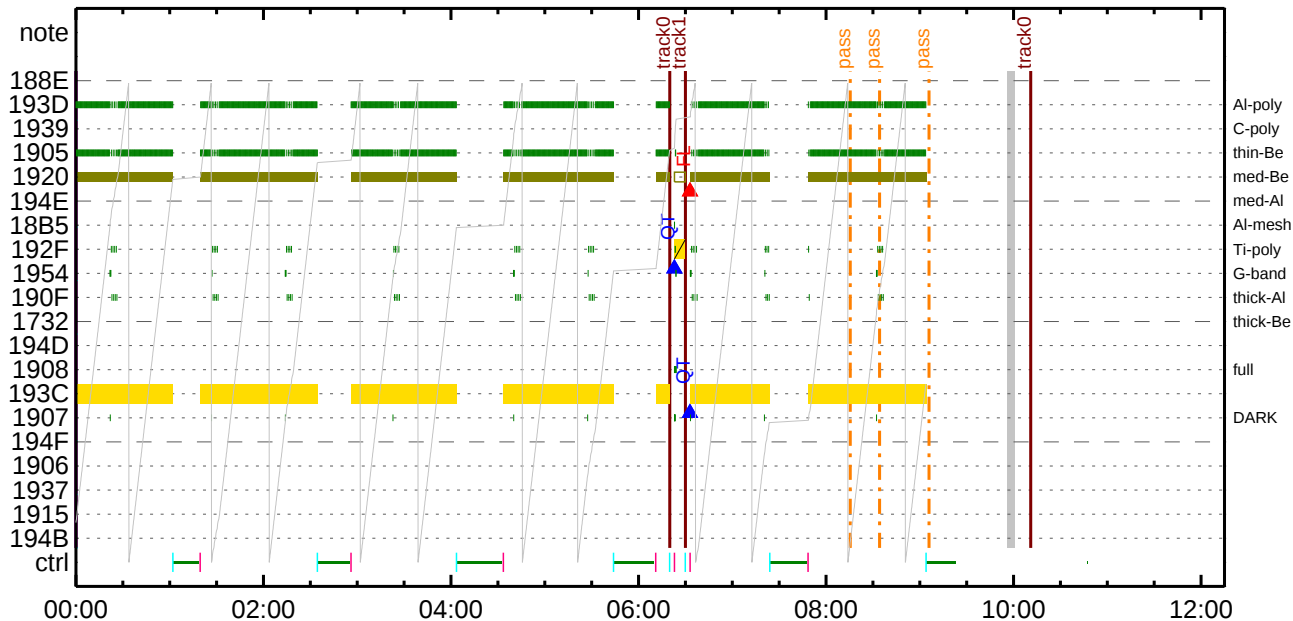
FLD Patrol												
Term		Pointing (x, y)					Comment					
12/14 13:42:46 - 12/14 18:01:46		Fixed (920.0, 202.4)					# Observe returning AR 11589 for EIS.					
12/14 18:14:16 - 12/15 06:20:16		Track (-755.4, 156.3) @ 12/14 18:11:30					# Return to AR near East limb.]					
12/15 06:32:46 - 12/18 09:43:00		Track (-678.9, 158.5) @ 12/15 06:30:00					# AR cont.					
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp. AEC Buffer Interval



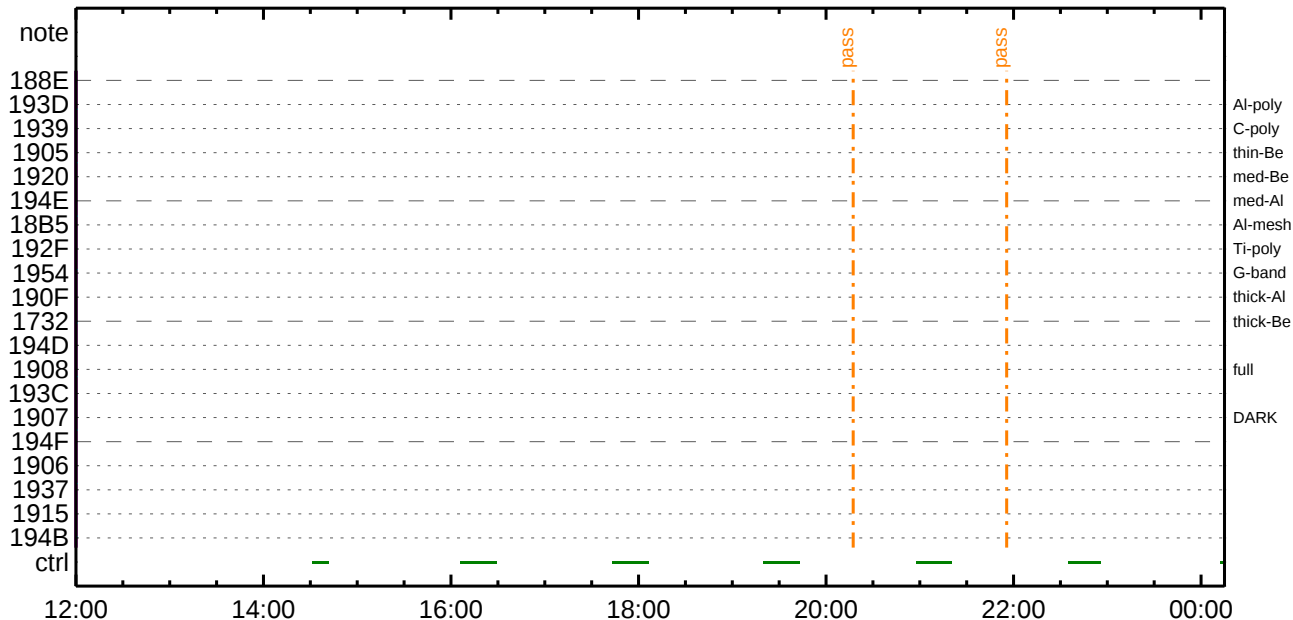
CMDI #0121 2012/12/14



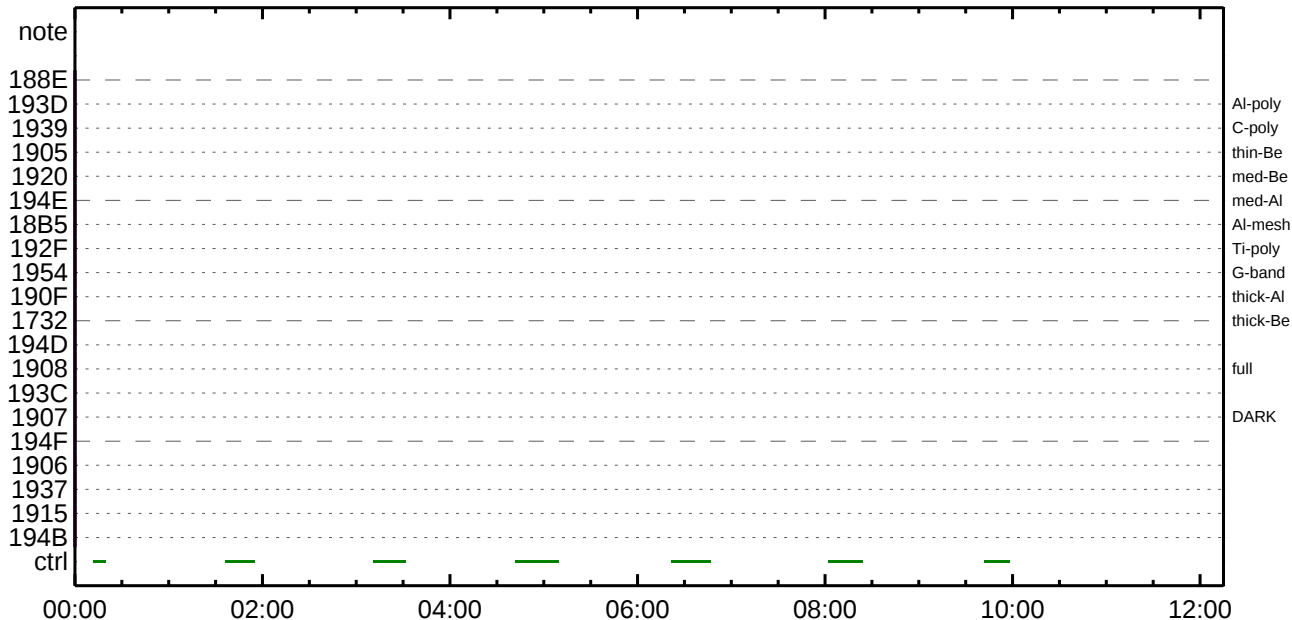
CMDI #0121 2012/12/15



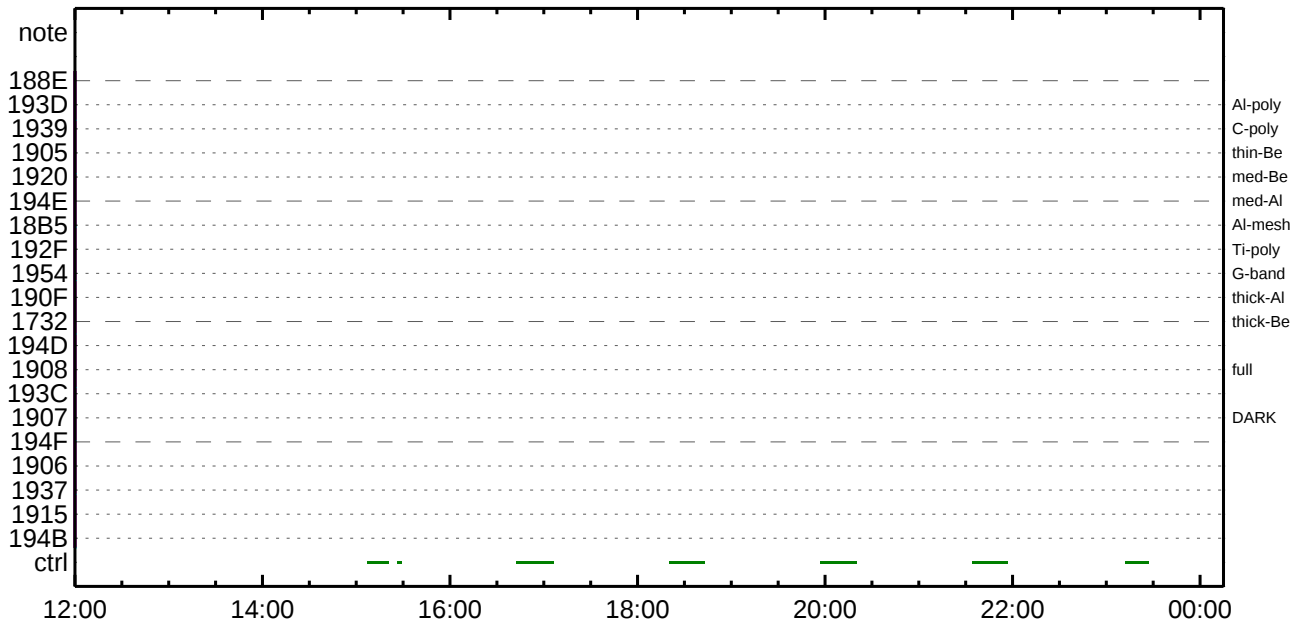
CMDI #0121 2012/12/15



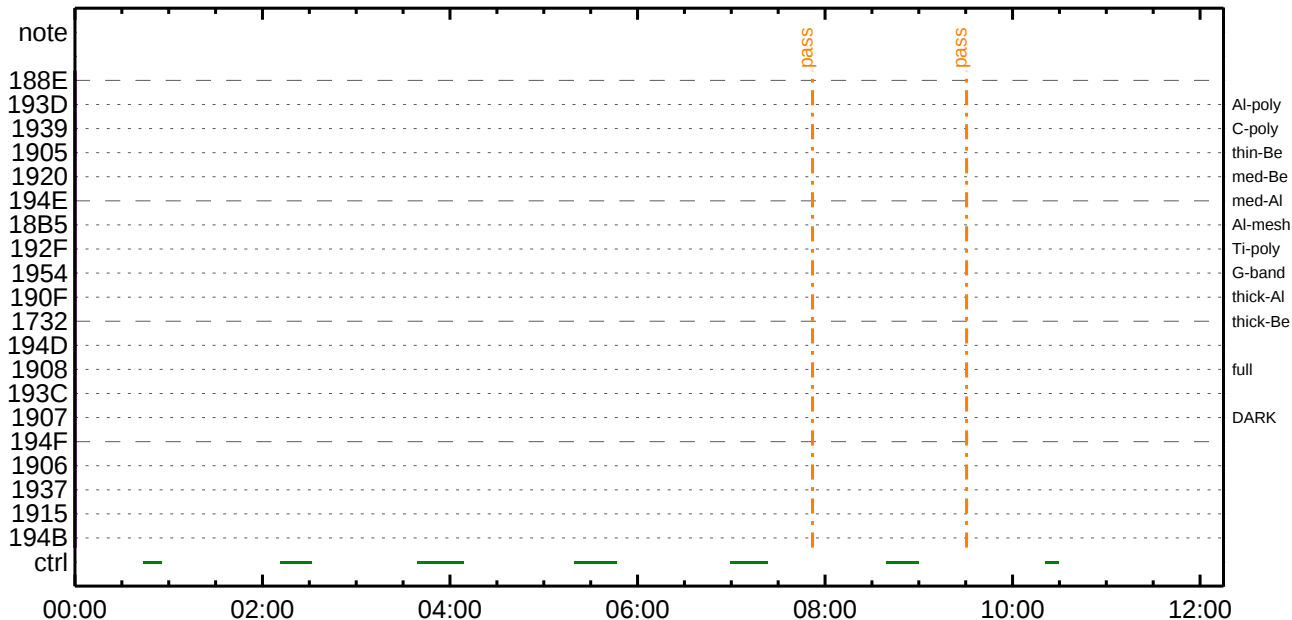
CMDI #0121 2012/12/16



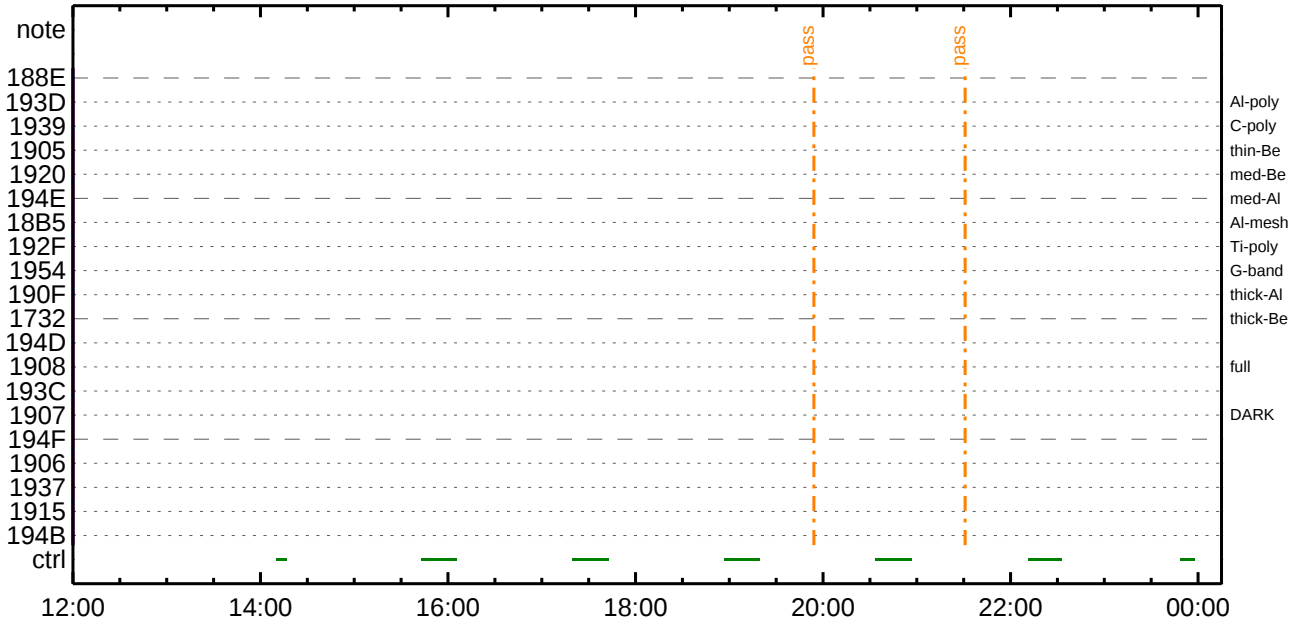
CMDI #0121 2012/12/16



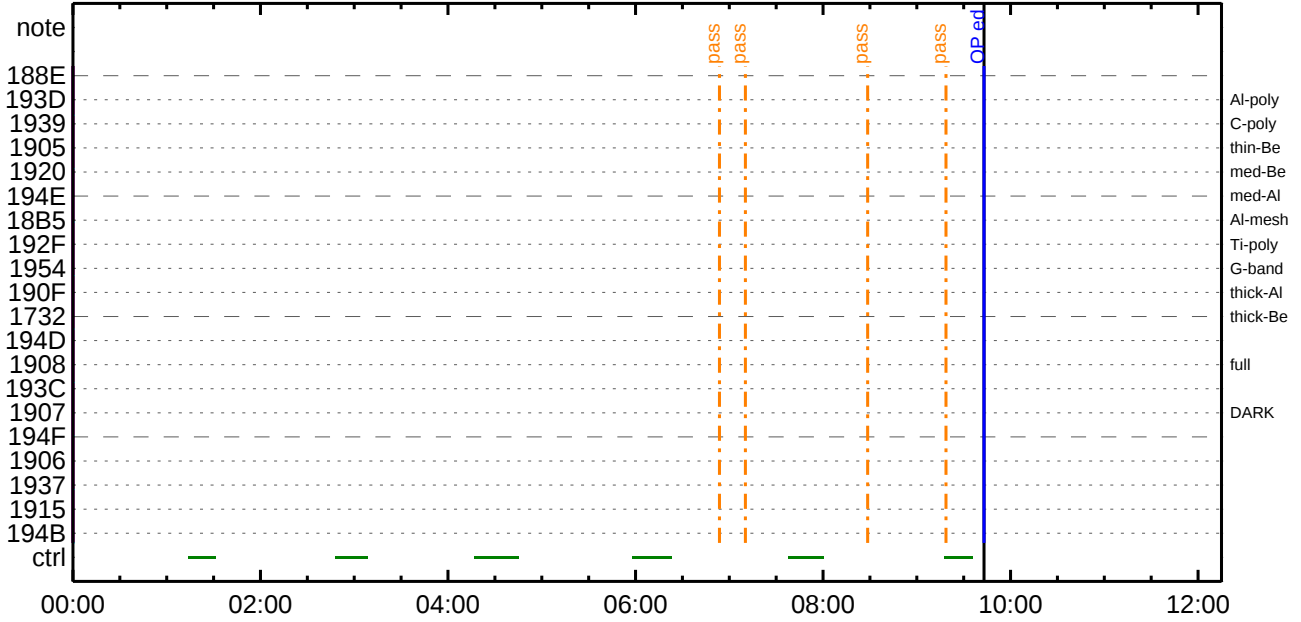
CMDI #0121 2012/12/17



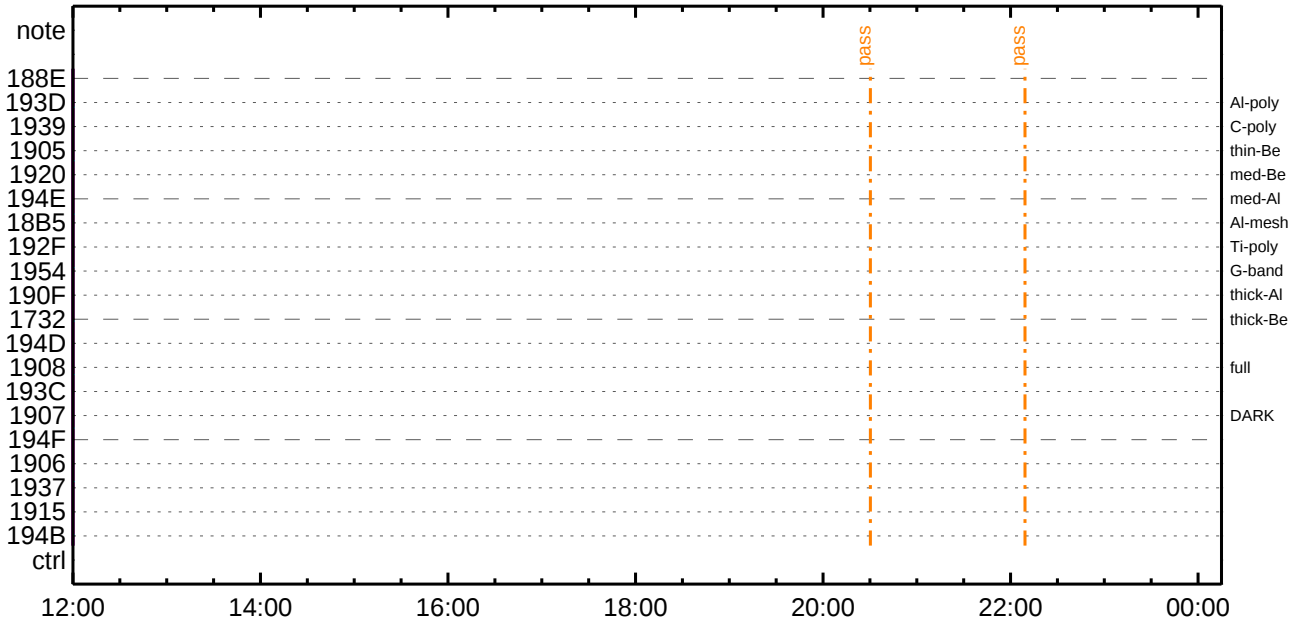
CMDI #0121 2012/12/17



CMDI #0121 2012/12/18



CMDI #0121 2012/12/18



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main-328 2012-12-13 12:36:30 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥$¥Ã¥¬¼Å»Üjã
0005 C.
0006 C. ¥Ä¥ßj¼¥³¥D¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÄijË□¿□À□•µ°Æ»İ×AC□İçF¥Ä¥×¥İj¼¥É;ÊÈè½µ•îĬ;Ë◊Ê¼°ÇÖ□•□¿%ī¹Ç□İjÇÀ®, ù□¹□è□▷□ÇÄ÷¿®□•□Ê□□□³□È;j£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆÄ, İxÊÝ□āLOS□▷□Ç□İ»þ´Ô□ò¹ĬĬ, □•jÇÊŎİ×□EXÄŐON□İ¹Ô□Ê□İ□Ê□□□³□È;j£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/L0] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥D¥Ó¥É¥İ¥Ä¥¬¼ÖÄŐ□¬¼Äê□•□¿□ējç°Ê²¼□İ°ÆÄ, ¼ê½Ç□Ò¼Ä¹Ô□¹□ē;j£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äİ¼ı°ÆÄ,
0033 C. *****
0034 C. ¨ RESTARTjÊPT1jË□•□¿□□%ī¹Ç□İjç°Ê²¼□İ¼Ä¹Ô□»□°jçDCBC-150□Ø¿Ê□à;j£
0035 C.
0036 . C. jãPT1°ÆÄ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ŏ, j¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, j¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, j¼Ú)
0045 C.
0046 . C. jã¥Ç¥ó¥Æ¥ÊÄÜÄŐjÊÄ•Äº²ôÊöjË, ä□İ°ÆÄ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ŏ, j¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, j¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, j¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, □¬¼«Æ°Ää»ß□•□¿, äjç°Ê²¼□Ò¼Ä¹Ô□¹□ē;j£
0055 C. ¥Ç¥ó¥Æ¥ÊÄÜÄŐ□ÄÄ•Äº²ôÊö□¬¼á□□%ī¹Ç□İ'İ»□¹□è□▷□ÇÄŐ□Äj£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äİ¼ı°ÆÄ,
0059 C. *****
0060 C. ¨ RESTARTjÊPT2jË□•□¿□□%ī¹Ç□İjç°Ê²¼□İ¼Ä¹Ô□»□°jçDCBC-151□Ø¿Ê□à;j£
0061 C.
0062 . C. jãPT2°ÆÄ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ŏ, j¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, j¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, j¼Ú)
0071 C.
0072 . C. jã¥Ç¥ó¥Æ¥ÊÄÜÄŐjÊÄ•Äº²ôÊöjË, ä□İ°ÆÄ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ŏ, j¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, j¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, j¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ßjçXÄ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆÄ, Ää»ßjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÄ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

[illegible]

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0194 C.
0195 +. TI 2012-12-13 09:53:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. C#[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼□İÄê¾İİŇ□İ¥Á¥$¥Á¥¹äİÜ
0200 C. C#[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. C#[HK1_TI_CMD_NUM] EQ 4
0202 C. C#[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. C#[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°è¥À¥Ó¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. C#[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. C#[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. C#[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. C#[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. C#[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. C#[HK1_PKT_FORM_NO] EQ 7
0220 C. C#[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. C#[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. C#[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. C#[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ¥À¥Ó¥×½ªİ»ð³İÇ$
0226 C. C#[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBL□İ¾¹Ç•è²İOKð³İÇ$
0229 C.
0230 C. DHU¥âİ;¼¥ÉİÊ¼Ý½, ¥İİ;¼¥ÉİËðİá□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. C#[HK1_PKT_FORM_NO] EQ 2
0234 C. C#[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. C#[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. C#[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-12-13 09:53:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2012-12-13 09:53:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C. C#[HK1_TI_CMD_NUM] EQ 2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2012-12-13 09:53:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 C. C#[HK1_TI_CMD_NUM] EQ 1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ÜÄİ□İ»Ö¾Ý□ĒÄð□¹ēDCBC•×²è *****
0265 C. (¾ä°İ¥Ö¥Á¥Ē¥ð¥Ē¥ä¥ç¥ē¾¼□¼¥Ä»Ü□¹ē)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ¥Ð¥¹•İ Daily±¿İŇ□Ē·Ø□¹ēDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. İäLOS¥Á¥$¥Á¥ ¼Ä»Üİä
0276 C.
0277 C. ***** LOS *****
0278 C.

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main-329 2012-12-13 12:36:30 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥Ä¥ßj¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÄijË¤¿¤À¤•µ°Æ»I×AÇ¤I¥¢¥Ä¥×¥íj¼¥ÉjËËè½µ•îîÉjËë¼°Ç¤¤•¤¿¼¹Ç¤¤IjÇÄ®, ù¤¹¤ð¤¤¤¤ÇÄ÷¿®¤•¤Ê¤¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆÄ, î×ËÝ¤äLÔS¤¤¤Ç¤¤I»p´Ö¤¤¹îî, ¤•jÇÉÖî×¤ÊXÄÓON¤¤î¹Ö¤¤Ê¤¤¤¤¤¤³¤Èj£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥í¥Ã¥¼ÖÄÖ¤¬°ÄÄê¤•¤¿¤¤éjÇ°Ê²¼¤¤î¹°ÆÄ, ¼ê½Ç¤¤ò¼Ä¹Ö¤¤¹¤¤èj£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äîî¹°ÆÄ
0033 C. *****
0034 C. ¨ RESTARTjËPT1jË¤¤•¤¿¤¤¤¼¹Ç¤¤IjÇ°Ê²¼¤¤î¹Ä¹Ö¤¤»¤¤jÇDCBC-150¤Ø¿Ê¤¤àj£
0035 C.
0036 . C. jãPT1°ÆÄ, ³«»îjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, i¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ú)
0045 C.
0046 . C. jã¥¢¥ó¥Æ¥ÉÄÜÂØjÊÄ•Äº²ÖÈòjË, ä¤î¹°ÆÄ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, i¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»ß¤¤¤¿, äjÇ°Ê²¼¤¤ò¼Ä¹Ö¤¤¹¤¤èj£
0055 C. ¥¢¥ó¥Æ¥ÉÄÜÂØ¤¤Ä•Äº²ÖÈò¤¬¶á¤¤¤¼¹Ç¤¤I °î»¤¤¹¤ð¤¤¤¤ÇÄ¤¤¤¤j£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äîî¹°ÆÄ
0059 C. *****
0060 C. ¨ RESTARTjËPT2jË¤¤•¤¿¤¤¤¼¹Ç¤¤IjÇ°Ê²¼¤¤î¹Ä¹Ö¤¤»¤¤jÇDCBC-151¤Ø¿Ê¤¤àj£
0061 C.
0062 . C. jãPT2°ÆÄ, ³«»îjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, i¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ú)
0071 C.
0072 . C. jã¥¢¥ó¥Æ¥ÉÄÜÂØjÊÄ•Äº²ÖÈòjË, ä¤î¹°ÆÄ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, i¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ Ää»ßjÇXÄ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆÄ Ää»ßjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÄ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 7s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2012-12-13 09:53:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'ûÃÎï»ö¼ÝðĒĀð¹ðĒDCBC•x²è *****
0153 C. (%ā°İ¥Ō¥Ā¥Ē¥ð¥Ē¥Ā¥Ĉ¥Ē¥Ē¼¼¼¼Ā»Ū¹¼Ē)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¥Đ¥¹•İ Daily±¿İŃðĒ'ð¹¼ĒDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ¡āLOS¥Ā¥$¥Ā¥¬¼Ā»Ū¡ä
0164 C.
0165 . C. ***** LOS *****
0166 C.

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main-330 2012-12-13 12:36:30 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥$¥Ã¥¬¼Å»Üjã
0005 C.
0006 C. ¥Ã¥ßj¼¥³¥Ð¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀījĒ□¿□A□•μ°A»Ī×AÇ□Ī¥Ç¥Ã¥×¥īj¼¥ÉjĒĒē½μ•īĒjĒ□É¼°Ç0□•□¿¼¹Ç□ījÇÀ®, ù□¹□ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 c0 c0 10 10)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 40 c0 10 10)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 40 40 10 10)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a c0 40 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 85 83 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 80 80 20 08)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0e 80 80 08 20)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 + DC 07-F0 MDP_XRT_FLD_DIS
0065 BC (d9)
0066 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0067 BC (c9)
0068 + DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 + DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 + DC 07-F0 MDP_XRT_FLD_RESET
0073 BC (da)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2012-12-13 09:53:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP ´ŪĀī□ī»Ō¼Ÿ□ĒĀ□¹□ēDCBC•×²ē *****
0089 C. (¼Ā°īŸŌ¥Ā¥Ē¥Ð¥É¥Ā¥Ç¥ē□Ē¼□□¼A»Ū□¹□ē)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** ¥Ð¥¹•Ī Daily÷¿īŸ□Ēē´Ō□¹□ēDCBC•×²ē *****
0095 . S. DC-BC dcbc-153:DCBC

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0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. i äLOS¥Á¥S¥Ã¥¼Ä»Ü i ä
0100 C.
0101 . C. ***** LOS *****
0102 C.

Dec 13, 12 12:36

XRT_OGLIST_0121.chk

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*** OP Sequence for XRT ***

2012/12/13	10:04:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/12/14	06:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/14	06:20:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_431_OG [0x1af]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2012/12/14	12:59:54.0	XRT_CTRL_MANU_443_OG [0x1bb]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/14	13:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	2e	f1	2e	f1
2012/12/14	13:02:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/12/14	13:02:52.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2012/12/14	13:02:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/14	13:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/14	13:02:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/14	13:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/14	13:09:54.0	XRT_CTRL_MANU_443_OG [0x1bb]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/14	13:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	2e	f1	d1	0f
2012/12/14	13:12:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/12/14	13:12:52.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2012/12/14	13:12:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/14	13:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/14	13:12:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/14	13:13:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/14	13:19:54.0	XRT_CTRL_MANU_443_OG [0x1bb]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/14	13:20:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	d1	0f	d1	0f
2012/12/14	13:22:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/12/14	13:22:52.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2012/12/14	13:22:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/14	13:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/14	13:22:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/14	13:23:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/14	13:29:54.0	XRT_CTRL_MANU_443_OG [0x1bb]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/14	13:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d1	0f	2e	f1
2012/12/14	13:32:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/12/14	13:32:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2012/12/14	13:32:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/14	13:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/14	13:32:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/14	13:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/14	13:39:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/14	13:40:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	ee	05	ae	36
2012/12/14	13:42:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/12/14	13:42:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/14	13:42:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/14	13:42:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/14	13:42:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/14	13:42:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/14	13:42:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/12/14	13:42:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/12/14	13:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2012/12/14	13:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	13:59:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	13:59:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	14:02:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	15:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	15:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	15:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	15:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	15:53:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	15:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	17:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	17:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	17:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	17:30:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	17:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/14	18:01:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/14	18:01:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/14	18:01:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/14	18:01:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/14	18:04:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/14	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	18:11:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	18:11:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2012/12/14	18:13:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/14	18:14:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/14	18:14:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/14	18:14:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/14	18:14:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/14	18:14:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	18:14:26.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/12/14	18:14:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/14	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	18:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	18:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	18:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	18:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	19:06:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	19:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	20:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	20:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	20:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	20:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	20:44:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	20:45:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2012/12/14	21:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	21:58:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	21:58:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	22:01:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	22:20:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/14	22:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/14	23:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/14	23:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/14	23:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/14	23:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/14	23:46:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/14	23:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	01:02:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	01:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	01:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	01:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	01:18:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/15	01:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	02:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	02:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	02:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	02:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	02:55:00.5	XRT_Custom_434_OG [0x1b2]			
2012/12/15	02:56:00.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	04:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	04:03:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	04:03:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	04:06:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	04:32:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/15	04:33:30.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	05:44:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	05:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	05:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	05:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	06:10:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/15	06:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	06:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/15	06:20:00.0	AOCs_0Re-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/12/15	06:20:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/15	06:20:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/15	06:20:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/15	06:22:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/15	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	06:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	06:30:00.0	AOCs_0Re-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2012/12/15	06:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/15	06:32:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/15	06:32:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/15	06:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]			

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2012/12/15	06:32:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/15	06:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/15	06:32:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	06:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/12/15	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/15	07:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	07:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	07:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	07:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	07:47:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	07:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	09:04:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	10:11:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00