

Period: 2012/12/15 10:01:00 - 2012/12/20 10:56:00

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(183)														
Term			Pointing (x, y)				Comment							
12/15 18:09:30 - 12/15 18:16:24			Fixed (0.0, 0.0)				synoptic, shifted 6.5 min							
12/16 05:56:30 - 12/16 06:03:24			Fixed (0.0, 0.0)				synoptic, shifted -6.5 min							
12/16 17:59:30 - 12/16 18:06:24			Fixed (0.0, 0.0)				synoptic, shifted -3.5 min							
12/17 04:13:00 - 12/17 04:19:54			Fixed (0.0, 0.0)				synoptic, shifted - 2 hours.							
12/17 18:03:00 - 12/17 18:09:54			Fixed (0.0, 0.0)				synoptic							
12/18 04:03:00 - 12/18 04:09:54			Fixed (0.0, 0.0)				synoptic, shifted 1.5 hours.							
PROG= 13 1-time(s)														
Subr= 1 1-time(s) 14.0sec														
Seqn= 64 1-time(s) 4.0sec														
└ Open/Al-mesh Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Open/Al-mesh Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 6 1-time(s) 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec														
Seqn= 70 1-time(s) 4.0sec														
└ Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 67 1-time(s) 2.0sec														
└ thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 69 1-time(s) 2.0sec														
└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Subr= 2 1-time(s) 2.0sec														
Seqn= 68 1-time(s) 2.0sec														
└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec														
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval														

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al, thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops		
Term	Pointing (x, y)	Comment

12/15 10:14:00 - 12/15 17:43:00	Track (-945.8, -214.8)	@ 12/15 10:11:00	# OP start + 10min, track AR
12/15 18:19:30 - 12/16 05:53:24	Track (-936.0, -213.5)	@ 12/15 18:16:30	# AR cont.
12/16 06:06:30 - 12/16 17:56:24	Track (-911.5, -211.4)	@ 12/16 06:03:30	# AR cont.
12/16 18:09:30 - 12/17 01:59:54	Track (-874.4, -209.1)	@ 12/16 18:06:30	# AR cont.
12/17 02:03:00 - 12/17 03:39:00	Track (797.5, 323.1)	@ 12/17 02:00:00	# HOP 173, AR 11631
12/17 04:23:00 - 12/17 08:09:54	Track (-833.6, -207.1)	@ 12/17 04:20:00	# HOP 173 until 6UT...
12/17 08:13:00 - 12/17 17:59:54	Track (-816.2, -206.3)	@ 12/17 08:10:00	AR cont.
12/17 18:13:00 - 12/18 01:59:54	Track (-765.6, -204.2)	@ 12/17 18:10:00	# AR cont.
12/18 02:03:00 - 12/18 03:59:54	Track (882.3, 319.3)	@ 12/18 02:00:00	# HOP 173
12/18 04:13:00 - 12/18 09:17:30	Track (-707.8, -202.1)	@ 12/18 04:10:00	# HOP 173 until 6UT...

PROG= 16 15-time(s)											
Subr= 1		45-time(s)		10.0sec							
Seqn= 35		1-time(s)		2.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Subr= 2		1-time(s)		10.0sec							
Seqn= 36		1-time(s)		2.0sec							
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Seqn= 37		1-time(s)		2.0sec							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0 2.0sec
Seqn= 38		1-time(s)		2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

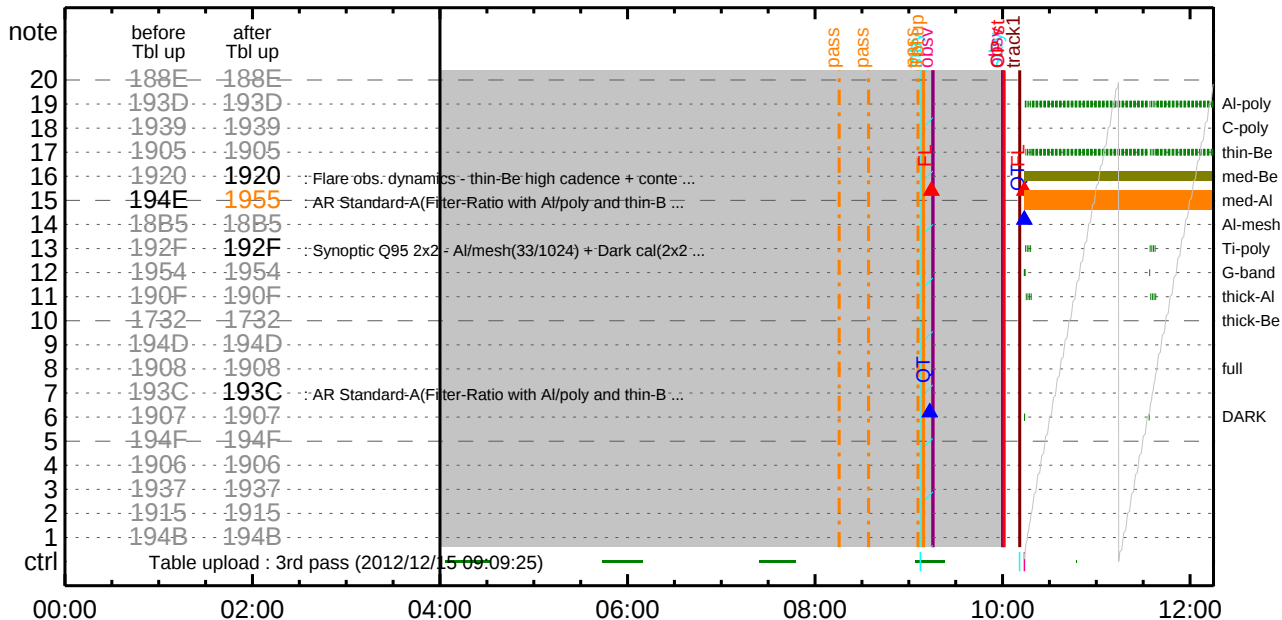
* * * * *

Flare Detection

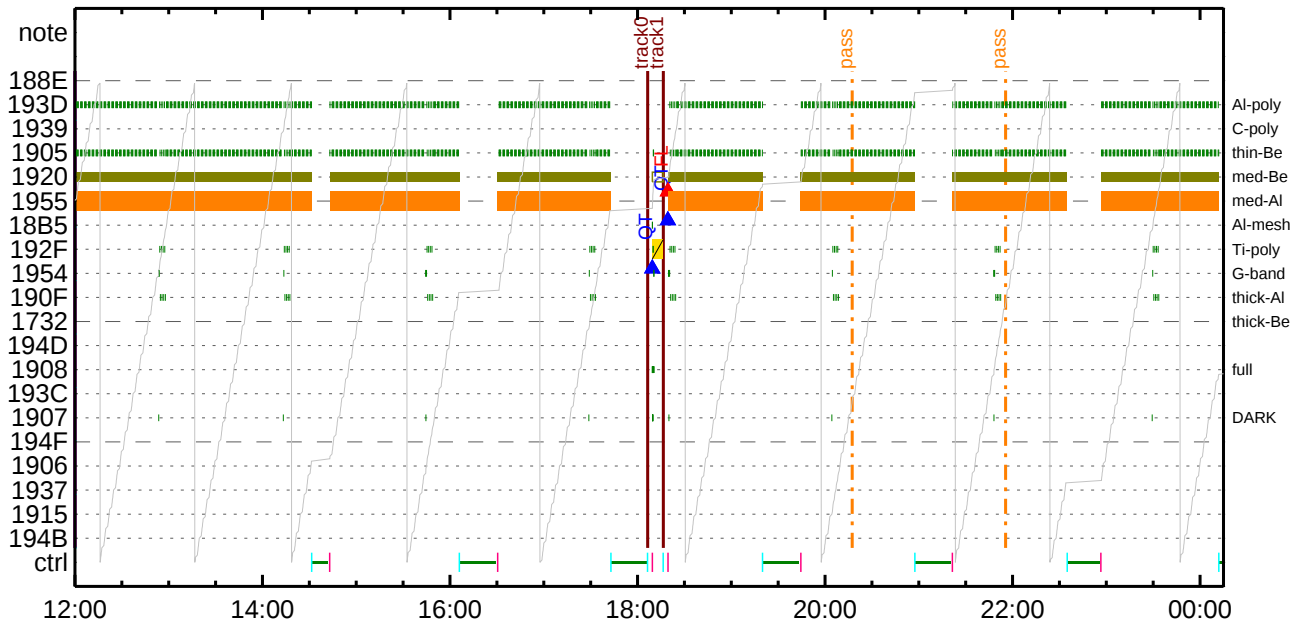
* * * * *

FLD Patrol											
Term		Pointing (x, y)						Comment			
12/15 18:19:16 - 12/16 05:53:46		Track (-936.0, -213.5)		@ 12/15 18:16:30		# AR cont.					
12/16 06:06:16 - 12/16 17:56:46		Track (-911.5, -211.4)		@ 12/16 06:03:30		# AR cont.					
12/16 18:09:16 - 12/17 04:10:16		Track (-874.4, -209.1)		@ 12/16 18:06:30		# AR cont.					
12/17 04:22:46 - 12/17 18:00:16		Track (-833.6, -207.1)		@ 12/17 04:20:00		# HOP 173 until 6UT...					
12/17 18:12:46 - 12/18 04:00:16		Track (-765.6, -204.2)		@ 12/17 18:10:00		# AR cont.					
12/18 04:12:46 - 12/20 10:56:00		Track (-707.8, -202.1)		@ 12/18 04:10:00		# HOP 173 until 6UT...					
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	8ms	Obs	8x8	Q=50 30sec	
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center) Comp. AEC Buffer Interval	

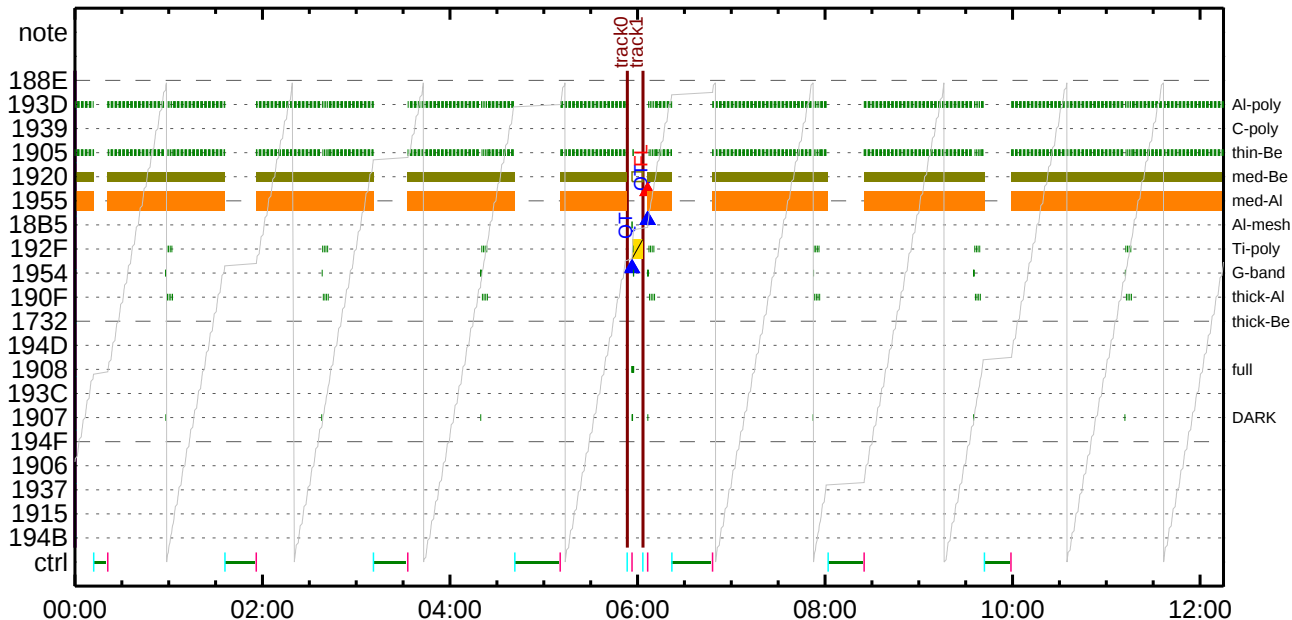
CMDI #0125 2012/12/15



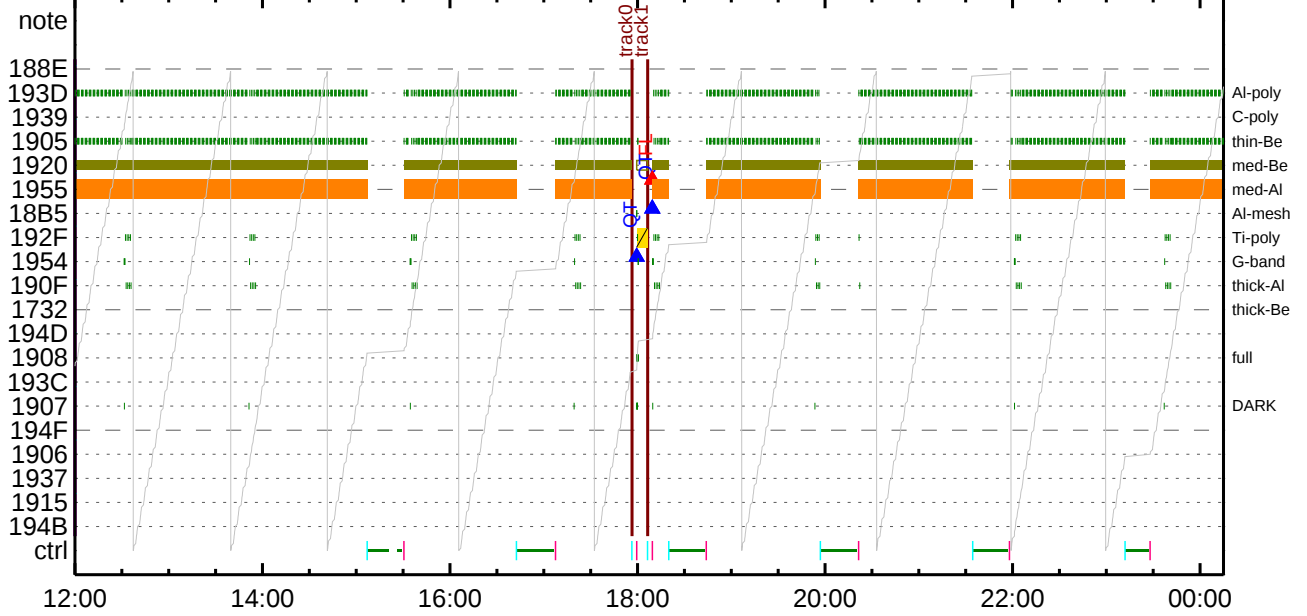
CMDI #0125 2012/12/15



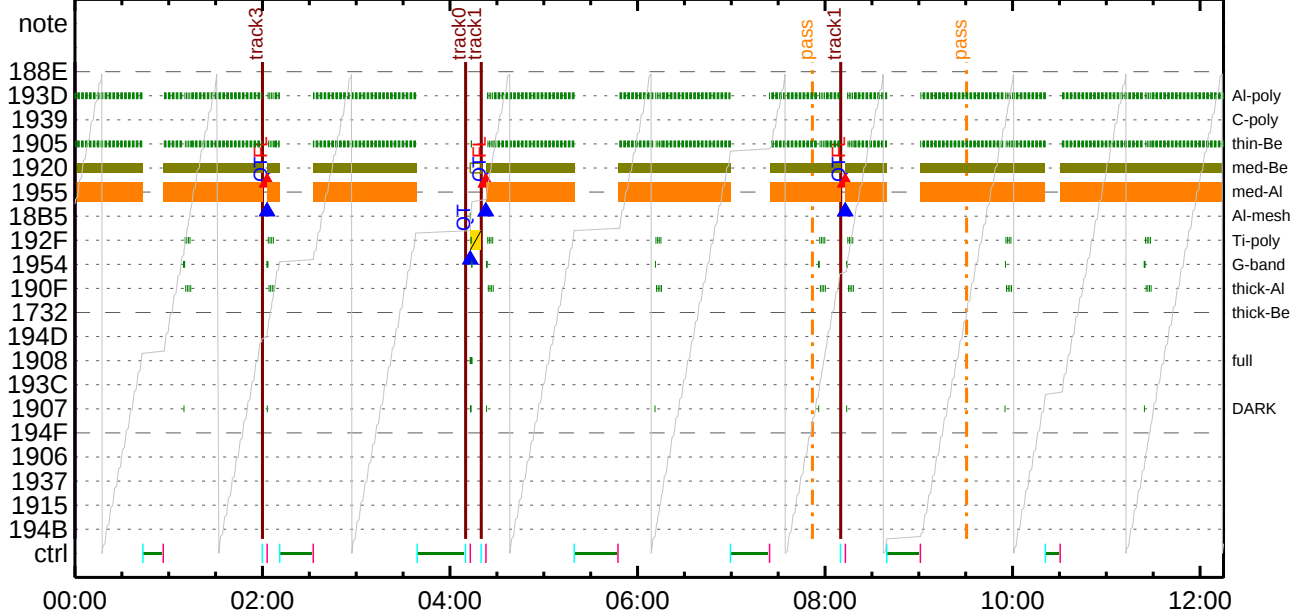
CMDI #0125 2012/12/16



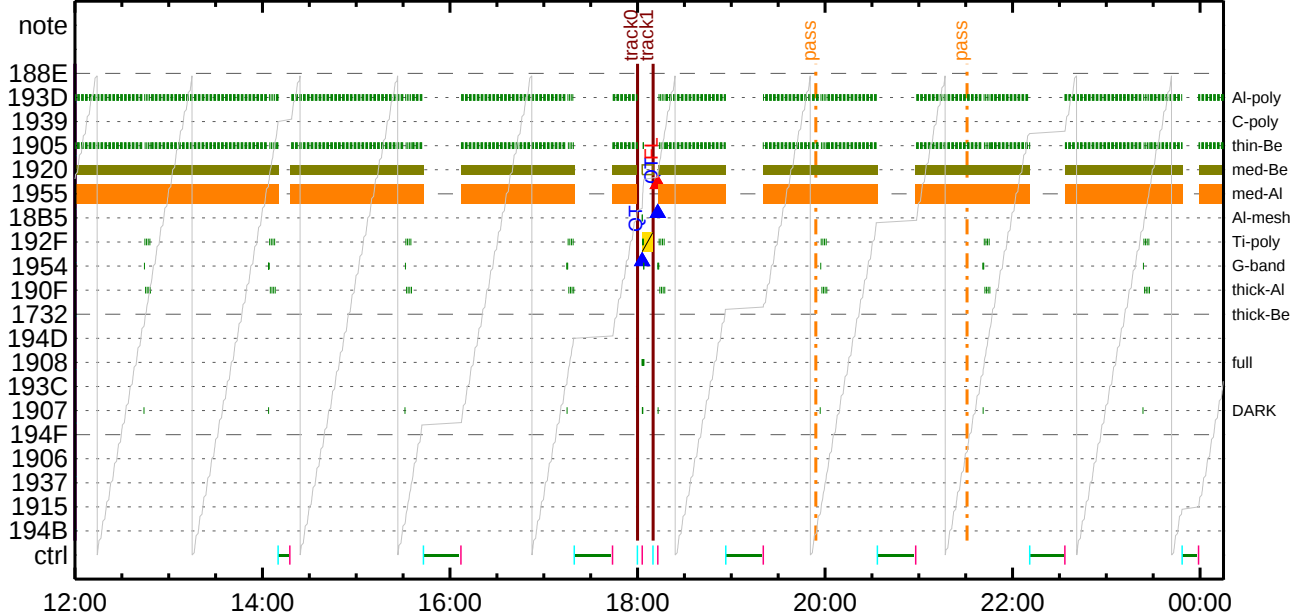
CMDI #0125 2012/12/16



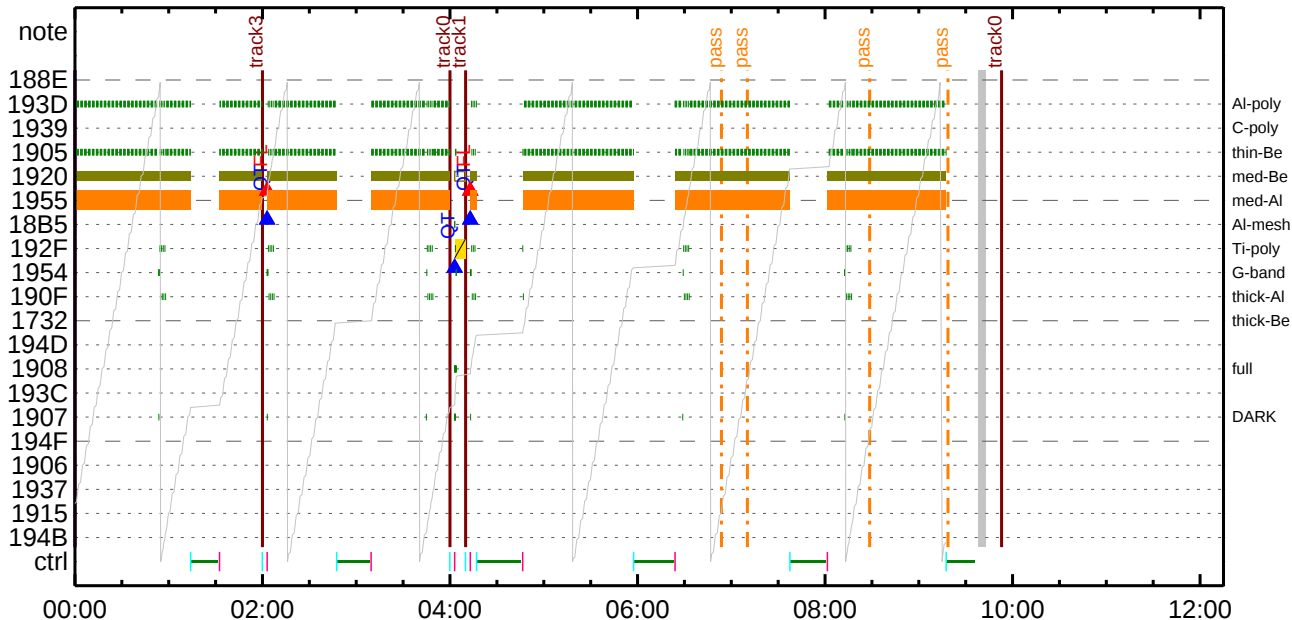
CMDI #0125 2012/12/17



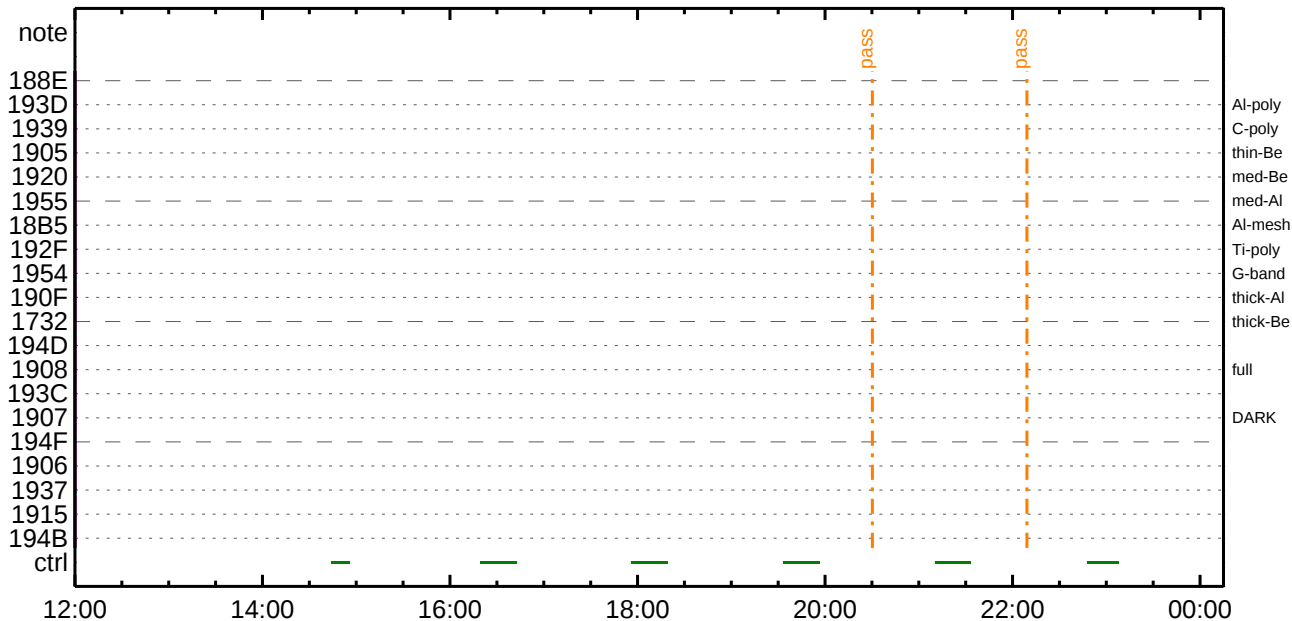
CMDI #0125 2012/12/17



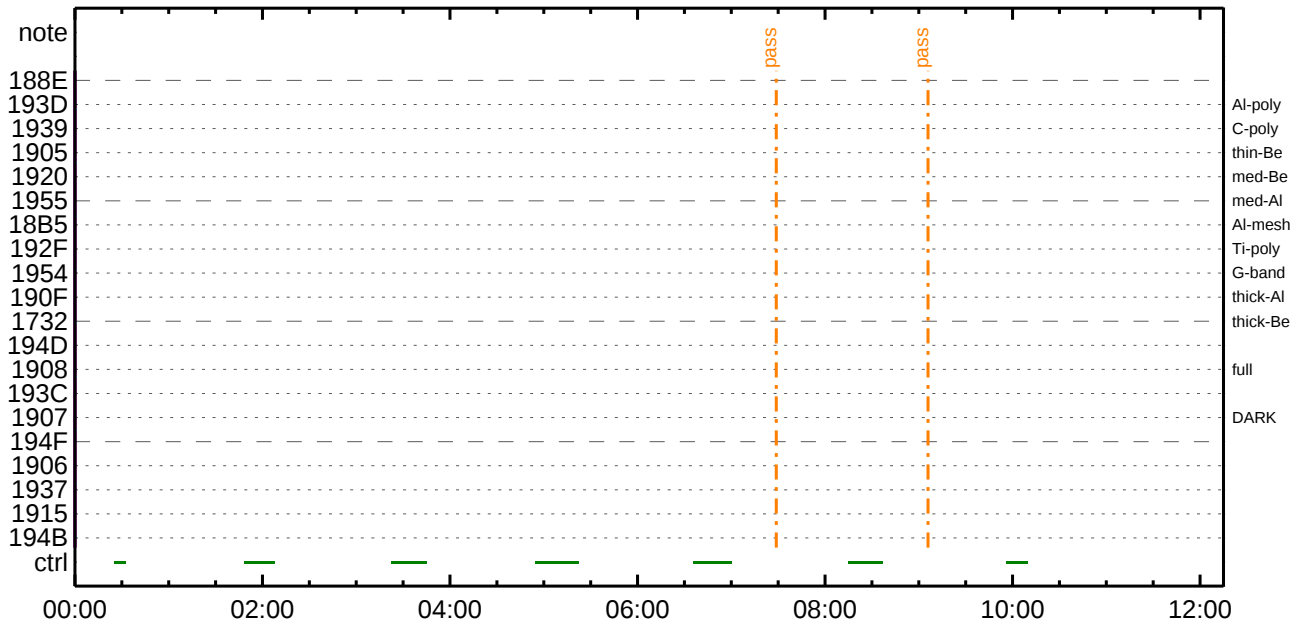
CMDI #0125 2012/12/18



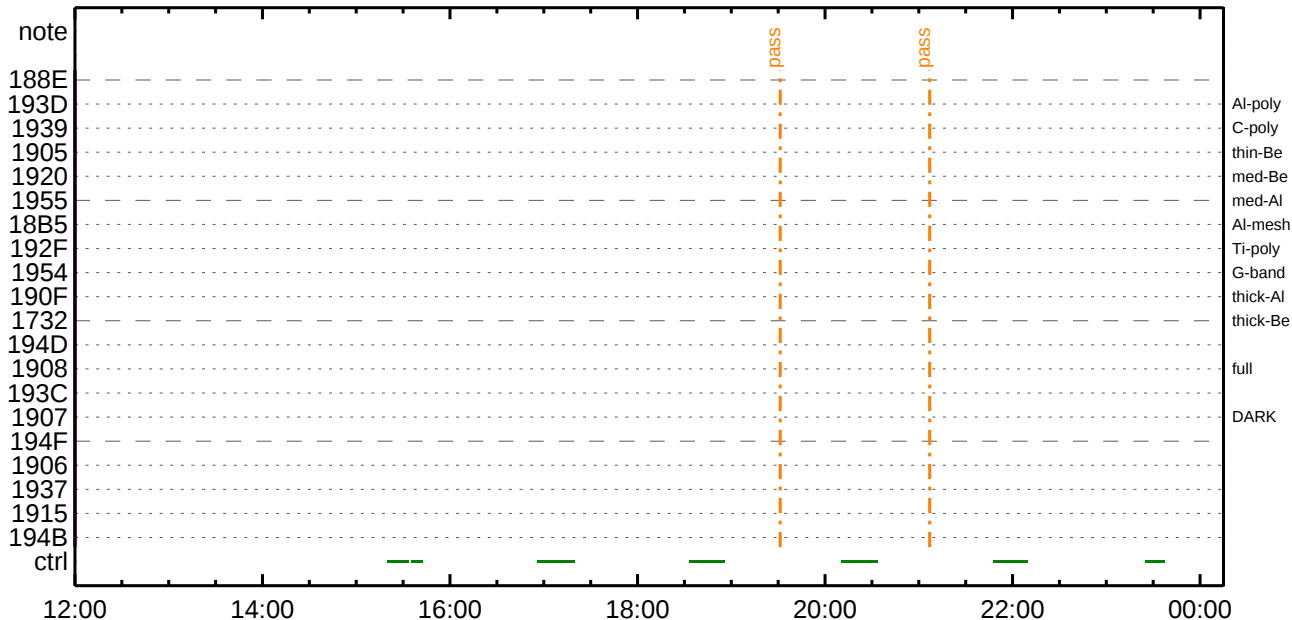
CMDI #0125 2012/12/18



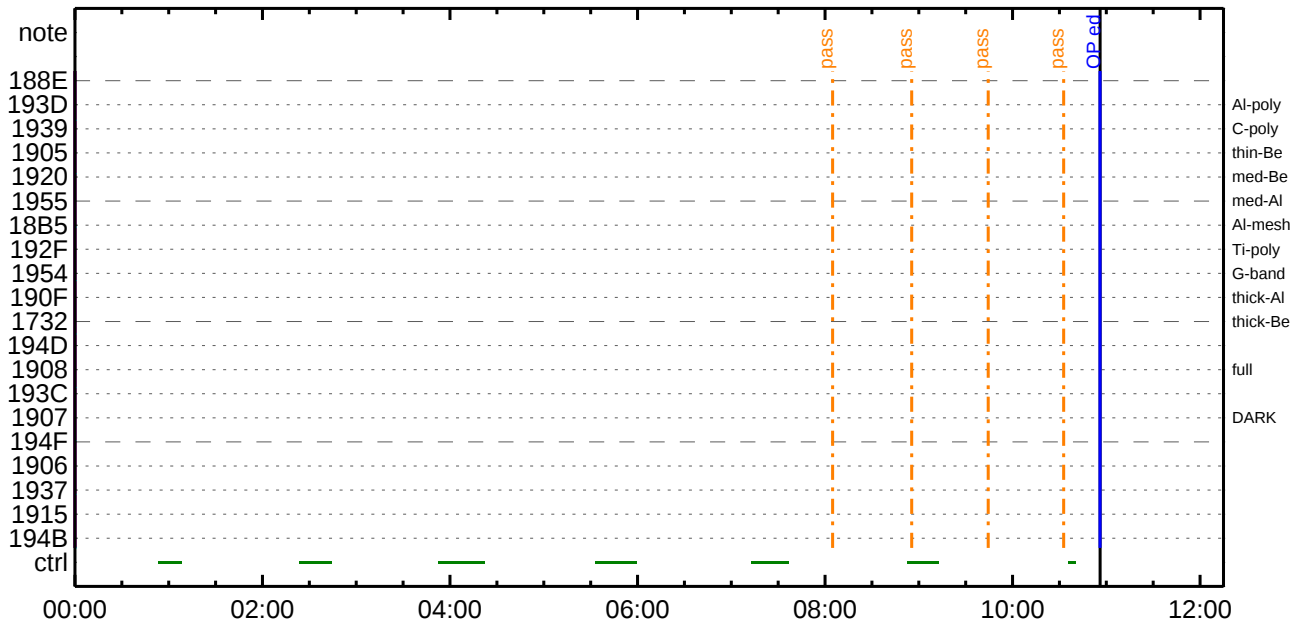
CMDI #0125 2012/12/19



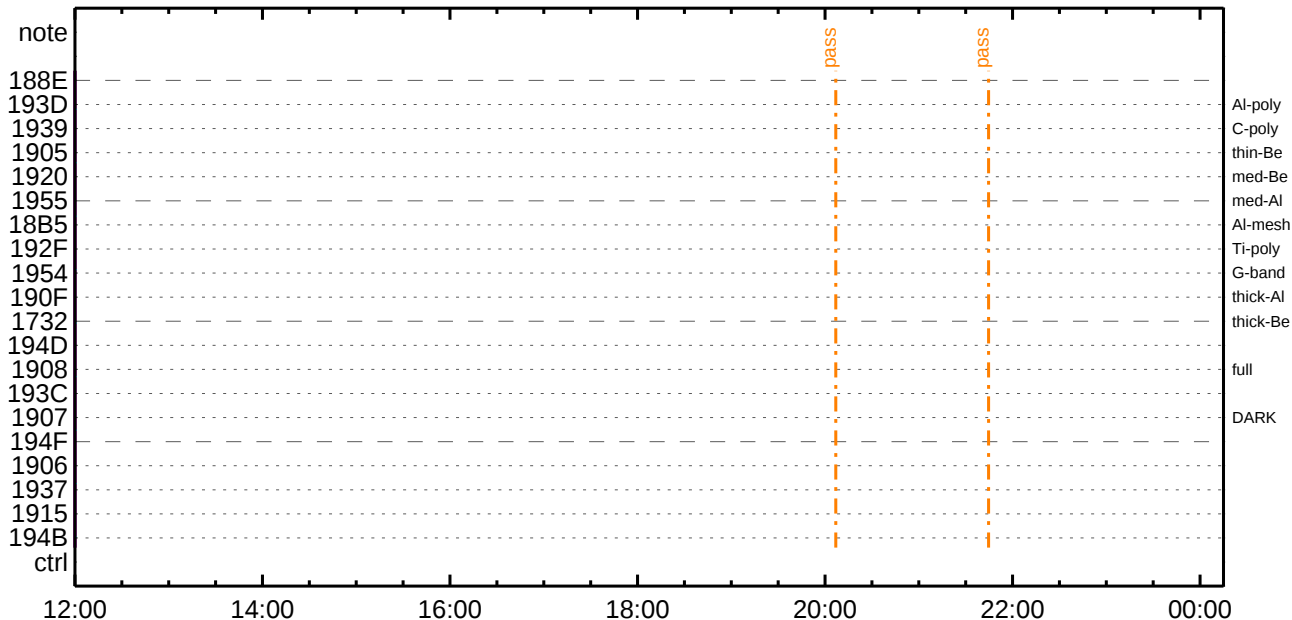
CMDI #0125 2012/12/19



CMDI #0125 2012/12/20



CMDI #0125 2012/12/20



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main-333 2012-12-15 12:29:27 289 33 SOLAR-B MAIN //
0001 C.
0002 C. ***** AOS *****
0003 C.
0004 C. ;ãAOS¥Á¥$¥Ã¥¬¼Ä»Ü;ä
0005 C.
0006 C. ¥Ä¥ß;¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 + DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¿À«•µ°Æ»Î×ÁÇ¿Í¥¢¥Ä¥×¥¹;¼¥Ê;ËËè½µ•îÊ;ËÈ¼°ÇÔ«•¿¿ì¹Ç¿İ;ÇÄ®, ù«¹«ë««ÇÄ÷¿®«•«Ê«««³«Ê;£
0011 + DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C. *****
0015 C. XÄ÷¿®µ;ON
0016 C. *****
0017 C. ¢¨ °ÆÄ, İ×ÊÝ«àLOS««Ç¿İ»Þ´Ô«ò¹İİ, «•;ÇÉÔİ×«EXÄÓON«İ¹Ô«Ê«İ«Ê«««³«Ê;£
0018 C.
0019 + DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 C. X¥Ð¥Ó¥É¥¹¥Ã¥¬¼ÖÄÔ«¬¼ÄÄê«•«¿««Ê;Ç°Ê²¼«İ°ÆÄ, ¼ê½Ç«ò¼Ä¹Ô«¹««Ê;£
0030 C.
0031 C. *****
0032 C. DR PT1 Äİ¼İ°ÆÄ
0033 C. *****
0034 C. ¢¨ RESTART;ËPT1;Ë«•«¿««ì¹Ç¿İ;Ç°Ê²¼«İ¼Ä¹Ô«»«°;ÇDCBC-150«Ø¿Ê«à;£
0035 C.
0036 C. ;ãPT1°ÆÄ, ³«»İ;ä
0037 + DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¼Ú)
0045 C.
0046 C. ;ã¥¢¥ó¥Æ¥ÉÄÜÄØ;ËÄ•Äº²óÊò;Ë, ä«İ°ÆÄ, °Æ³«;ä
0047 + DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¼Ú)
0052 C.
0053 C.
0054 C. PT1°ÆÄ, «¬¼«Æ°Ää»ß«•«¿, ä;Ç°Ê²¼«ò¼Ä¹Ô«¹««Ê;£
0055 C. ¥¢¥ó¥Æ¥ÉÄÜÄØ«äÄ•Äº²óÊò«¬¼á««ì¹Ç¿İ°İ»«¹«ë««ÇÄÔ«à;£
0056 C.
0057 C. *****
0058 C. DR PT2 Äİ¼İ°ÆÄ
0059 C. *****
0060 C. ¢¨ RESTART;ËPT2;Ë«•«¿««ì¹Ç¿İ;Ç°Ê²¼«İ¼Ä¹Ô«»«°;ÇDCBC-151«Ø¿Ê«à;£
0061 C.
0062 C. ;ãPT2°ÆÄ, ³«»İ;ä
0063 + DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¼Ú)
0071 C.
0072 C. ;ã¥¢¥ó¥Æ¥ÉÄÜÄØ;ËÄ•Äº²óÊò;Ë, ä«İ°ÆÄ, °Æ³«;ä
0073 + DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¼Ú)
0078 C.
0079 C. *****
0080 C. DR°ÆÄ, Ää»ß;ÇXÄ÷¿®µ;OFF
0081 C. *****
0082 C.
0083 C. ;ãDR°ÆÄ, Ää»ß;ä
0084 + DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 C. ;ãXÄ÷¿®µ;OFF;ä
0091 + DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.  
0097 C.  
*****  
0098 .C. OP/OG¥¡¼¥É½|¥À¥ó×x  
0099 C.  
*****  
0100 C.  
0101 C.  
0102 .C. ¡ãOP/OG¥¡¼¥É½ä  
0103 .S. OP op-333:OP  
0104 (  
0105 S. OG og-333:OG  
0106 )  
0107 C.  
0108 .C. ¡änMOG&OPIî°è¥À¥ó×x¡ä  
0109 C. NMOG(0x200000~0x207FFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0111 BC (20 00 7f 01 02)  
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40  
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0118 BC (07 0b f8)  
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0124 .C. ¥À¥ó×x½ªI»ð³IC$  
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0126 .C. RAM ID=NMOG¤I%Ê¹Ç•ë²IOKð³IC$  
0127 C.  
0128 C. NMOG(0x208000~0x20FFFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0130 BC (20 80 7f 01 02)  
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41  
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0137 BC (07 0b f8)  
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0143 .C. ¥À¥ó×x½ªI»ð³IC$  
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0145 .C. RAM ID=NMOG¤I%Ê¹Ç•ë²IOKð³IC$  
0146 C.  
0147 C. NMOG(0x210000~0x2100FF;$ 256byte)+OP(0x210100~0x2141FF: 16.25kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0149 BC (21 00 41 01 02)  
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42  
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65  
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0156 BC (07 0b f8)  
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0162 .C. ¥À¥ó×x½ªI»ð³IC$  
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0164 .C. RAM ID=NMOG,RAM ID=OPA I%Ê¹Ç•ë²IOKð³IC$  
0165 C.  
0166 C. ***** °Ê²¼qI%A´¶Á°¤ËÈ¬oÁ÷¿® (%âµ-¥À¥ó×x¼ê½ÇðdÃÖæmÇ¾am¨¸ë%ì¹ÇqÇqa) *****  
0167 C. DHU¥â¼¥É½E¥Y½ ¥l¼¥É½EðòÎá¤¹  
+ DC 01-22 DHU_MODE_CHNG  
0169 BC (02 0a f8)  
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2  
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S  
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K  
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0174 C.  
*****  
0175 C.  
0176 C. TI-CMD SET (POPG STOP/COPY/START)  
0177 C. *****  
0178 C.  
0179 .C. NOTICE i$ OPOG UPLOAD¤A÷¿®NG¤I%Ì¹Çj¢°Ê²¼qITi-CMDÁ÷¿®¤I%Ä¹Ô¤¤¤E¤¤¤¤Ej£  
0180 C. ¤¤¤¿çj¢SET¤EDUMP¤IA±°ìN¥¹qÇ¹Ô¤¤¤¤Ej£  
0181 C.  
0182 .C. TI¥³¥¤¥ó¥ÉðdÂBII¿(UT)  
+ TI 2012-12-15 09:56:00.0  
0184 DC 01-B3 DHU_OP_STOP  
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0186 C.  
+ TI 2012-12-15 09:56:01.0  
0188 DC 01-B4 DHU_OP_COPY  
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0190 C.  
+ TI 2012-12-15 09:56:01.0  
0192 DC 01-B5 DHU_OPOG_COPY  
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2012-12-15 10:00:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.
0198 C.
0199 C. °Ê²¼□İÄê%İİÎñİ¥Á¥$¥Á¥¹àİÜ
0200 C.
0201 C.
0202 C.
0203 C.
0204 C.
0205 C. *****
0206 C. TIIÎ°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.
0213 C.
0214 C.
0215 C.
0216 C.
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.
0220 C.
0221 C.
0222 C.
0223 C.
0224 C.
0225 C. ¥À¥Ö¥×½ªİ»ð³İÇ$
0226 C.
0227 C.
0228 C. RAM ID=TI_TBL□İ%Ê¹Ç•è²İOKð³İÇ$
0229 C.
0230 C. DHU¥â;¼¥Ê;Ê¼¥½, ¥ì;¼¥Ê;Ëð³İä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.
0234 C.
0235 C.
0236 C.
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-12-15 10:00:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C.
0246 C.
0247 C.
0248 C.
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-12-15 10:00:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2012-12-15 10:00:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C.
0262 C.
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-12-15 10:00:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C.
0272 C.
0273 C.
0274 C.
0275 C. ***** MDP `ÜÄİ□İ»ð¾Ý□ĒÄð¹¹ēDCBC•×²è *****
0276 C. (%â°İ¥Ö¥Á¥Ē¥ð¥Ē¥â¥Ç¥ē□Ē¾¼□□¼A»Ü□¹□ē)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•İ Daily±çİÎñĒ`Ø□¹ēDCBC•×²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ĵaLOS¥Á¥$¥Á¥¼A»Üĵa
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-334 2012-12-15 12:49:27 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi;Ê¿¿A¤•µ°Æ»I×AÇ¤I¥C¥A¥×¥i;¼¥ÉjÊÊè½µ•iîÊjÊ¤E¼°ÇÖ¤•¤¿¼i¹Ç¤IjÇÀ®, ù¤¹¤è¤¤¤¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 14s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 C.
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-12-15 10:00:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP ´ÜÄi¤I»Ö%Y¤EÄ¤¤¹¤èDCBC•x²è *****
0069 C. (%Ä°i¥Ó¥Ä¥E¥P¥E¥Ä¥C¥è¤E½¼¤¤¼Ä»Ü¤¹¤è)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤E´Ø¤¹¤èDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. iãLOS¥Á¥S¥Ã¥¼Å»Üjã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-335 2012-12-15 12:49:27 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē□¿□A□•μ°£»İ×AÇ□İ¥C¥A¥×¥ıj¼¥ÉjĒĒē½μ•īĒjĒ□Ē¼°Ç□□•□¿¼ı¹Ç□İjÇÀ®, ū□¹□ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-265:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-281:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2012-12-15 10:00:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 +. DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR ____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 85 83 08 08)
```

```

00096 + DC 07-F0 MDP_XRT_ROI_SET
00097 BC (cd 08 80 80 20 20)
00098 + DC 07-F0 MDP_XRT_ROI_SET
00099 BC (cd 09 80 80 20 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 08 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0f 80 80 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 10 80 80 08 08)
0106 + DC 07-F0 MDP_XRT_FLD_ENA
0107 BC (d8)
0108 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0109 BC (c8)
0110 + DC 07-F0 MDP_XRT_AEC_RESET
0111 BC (d0)
0112 + DC 07-F0 MDP_XRT_ARS_DIS
0113 BC (d5)
0114 + DC 07-F0 MDP_XRT_FLD_RESET
0115 BC (da)
0116 + DC 07-F0 MDP_XRT_QT_PROG_SET
0117 BC (c4 07)
0118 + DC 07-F0 MDP_XRT_FL_PROG_SET
0119 BC (c5 10)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C.
0123 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0124 C.
0125 + DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 + TI 2012-12-15 10:00:02.0
0128 DC 07-F0 MDP_XRT_MODE_OBSV
0129 BC (c2)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C. ***** XRT END *****
0133 C.
0134 . C. ***** MDP `0Aïaî»0%ÿ0ÊAð1aèDCBC•x²è *****
0135 C. (%â°î¥0¥A¥E¥p¥E¥â¥C¥è0E½%00¥A»Û01aè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ¥0¥1•î Daily±¿îÑaÉ´001aèDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ¡ãLOS¥Á¥S¥Ã¥¼A»Û¡ã
0146 C.
0147 . C. ***** LOS *****
0148 C.

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*** OP Sequence for XRT ***

2012/12/15	10:10:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	10:11:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2012/12/15	10:13:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/15	10:13:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/15	10:13:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/15	10:13:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/15	10:13:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/15	10:13:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	10:13:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/15	10:13:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/15	10:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	14:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	14:31:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	14:31:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	14:34:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	14:42:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/15	14:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	16:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	16:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	16:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	16:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	16:29:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/15	16:30:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	17:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	17:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	17:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	17:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	18:06:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/15	18:06:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/12/15	18:06:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/15	18:06:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/15	18:06:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/15	18:09:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/15	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	18:16:24.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	18:16:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2012/12/15	18:18:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/15	18:19:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/15	18:19:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/15	18:19:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/15	18:19:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/15	18:19:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	18:19:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/15	18:19:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/15	18:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	19:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			

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2012/12/15	19:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	19:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	19:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	19:43:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	19:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	20:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	20:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	20:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	21:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	21:20:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	21:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/15	22:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/15	22:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/15	22:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/15	22:55:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/15	22:56:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	00:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	00:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	00:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	00:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	00:20:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	00:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	01:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	01:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	01:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	01:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	01:55:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	01:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	03:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	03:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	03:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	03:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	03:32:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	03:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	04:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	04:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	04:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	04:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	05:09:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	05:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	05:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	05:53:30.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	05:53:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	05:53:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	05:53:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	05:56:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	05:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	06:03:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2012/12/16	06:03:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	01 00 00 00 00
2012/12/16	06:05:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/16	06:06:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/16	06:06:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/16	06:06:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/16	06:06:22.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/16	06:06:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	06:06:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/16	06:06:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/16	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	06:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	06:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	06:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	06:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	06:47:00.0	XRT_Custom_434_OG [0x1b2]				
2012/12/16	06:48:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	08:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	08:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	08:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	08:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	08:24:00.0	XRT_Custom_434_OG [0x1b2]				
2012/12/16	08:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	09:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	09:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	09:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	09:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	09:58:00.0	XRT_Custom_434_OG [0x1b2]				
2012/12/16	09:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	15:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	15:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	15:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	15:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	15:29:30.0	XRT_Custom_434_OG [0x1b2]				
2012/12/16	15:30:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	16:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	16:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	16:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	16:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	17:06:30.0	XRT_Custom_434_OG [0x1b2]				
2012/12/16	17:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	17:56:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/16	17:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/16	17:56:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/16	17:56:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/16	17:56:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/16	17:59:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/16	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	18:06:24.0	XRT_CTRL_MANU_447_OG [0x1bf]				

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2012/12/16	18:06:30.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCs_Or-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2012/12/16	18:08:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/16	18:09:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/16	18:09:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/16	18:09:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/16	18:09:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/16	18:09:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	18:09:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/16	18:09:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/16	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	18:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	18:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	18:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	18:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	18:43:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/16	18:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	19:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	19:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	20:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	20:20:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/16	20:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	21:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	21:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	21:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	21:57:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/16	21:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/16	23:12:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/16	23:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/16	23:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/16	23:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/16	23:27:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/16	23:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	00:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	00:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	00:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	00:55:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/17	00:56:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	01:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	02:00:00.0	AOCs_Or-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/12/17	02:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/17	02:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/17	02:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/17	02:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/17	02:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/17	02:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	02:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]			

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2012/12/17	02:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2012/12/17	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/12/17	02:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/17	02:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	02:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/17	02:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/17	02:31:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/17	02:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
2012/12/17	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/17	03:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	03:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/17	03:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/17	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/17	04:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	04:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2012/12/17	04:10:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00	00 00 00 00
2012/12/17	04:10:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/12/17	04:10:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/12/17	04:12:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/17	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2012/12/17	04:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/17	04:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	04:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	01	00 00 00 00
2012/12/17	04:22:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/12/17	04:22:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/12/17	04:22:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/12/17	04:22:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/12/17	04:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/17	04:22:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/17	04:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2012/12/17	04:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/12/17	05:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/17	05:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	05:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/17	05:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/17	05:46:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/17	05:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
2012/12/17	06:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/17	06:59:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	06:59:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/17	07:02:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/17	07:23:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/17	07:24:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
2012/12/17	08:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/17	08:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/17	08:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	01	00 00 00 00
2012/12/17	08:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/12/17	08:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	

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2012/12/17	08:12:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/17	08:12:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/17	08:12:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/17	08:12:56.0	XRT_QT_PROG_SET_435_0G [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	08:12:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/17	08:13:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/17	08:39:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	08:39:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	08:39:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	08:42:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	09:00:00.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	09:01:00.0	XRT_CTRL_AUTO_413_0G [0x19d]				
2012/12/17	10:21:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	10:21:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	10:21:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	10:24:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	10:29:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	10:30:30.0	XRT_CTRL_AUTO_413_0G [0x19d]				
2012/12/17	14:10:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	14:10:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	14:10:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	14:13:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	14:16:30.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	14:17:30.0	XRT_CTRL_AUTO_413_0G [0x19d]				
2012/12/17	15:43:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	15:43:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	15:43:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	15:46:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	16:06:00.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	16:07:00.0	XRT_CTRL_AUTO_413_0G [0x19d]				
2012/12/17	17:19:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	17:19:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	17:19:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	17:22:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	17:43:00.0	XRT_Custom_434_0G [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	17:44:00.0	XRT_CTRL_AUTO_413_0G [0x19d]				
2012/12/17	17:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	17:59:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	18:00:00.0	AOCS_ORe-point_Start_2_0G [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/17	18:00:16.0	XRT_FLD_DIS_404_0G [0x194]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/17	18:00:18.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/17	18:00:20.0	XRT_ARS_DIS_406_0G [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/17	18:02:58.0	XRT_QT_PROG_SET_419_0G [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/17	18:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/17	18:09:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	18:10:00.0	AOCS_ORe-point_Start_1_0G [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	18:12:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	AOCU_NM	5	02-76	01 00 00 00 00
2012/12/17	18:12:46.0	XRT_FLD_ENA_428_0G [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/17	18:12:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8

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2012/12/17	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/17	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/17	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/17	18:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/17	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/17	18:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	18:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	18:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	18:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	19:19:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	19:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/17	20:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	20:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	20:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	20:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	20:57:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	20:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/17	22:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	22:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	22:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	22:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	22:32:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	22:33:30.5	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/17	23:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/17	23:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/17	23:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/17	23:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/17	23:58:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/17	23:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/18	01:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	01:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	01:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	01:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/18	01:31:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	01:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
2012/12/18	01:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	02:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	02:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	03 00 00 00 00
2012/12/18	02:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/18	02:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/18	02:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/18	02:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/18	02:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/18	02:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	02:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/18	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/18	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	02:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2012/12/18	02:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	02:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/18	03:08:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	03:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/18	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/18	04:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/18	04:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/18	04:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/18	04:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/18	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	04:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	04:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2012/12/18	04:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/18	04:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/18	04:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/18	04:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/18	04:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/18	04:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	04:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2012/12/18	04:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/18	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	04:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	04:17:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	04:17:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/18	04:20:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	04:45:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	04:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	05:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	05:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	05:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/18	06:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	06:23:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	06:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	07:37:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	07:37:32.5	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	07:37:34.5	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/18	07:40:44.5	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	08:00:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	08:01:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/18	09:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/18	09:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/18	09:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/18	09:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/18	09:53:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00