

XRT Timeline to be uploaded on 2012/12/18

Period: 2012/12/18 09:43:00 - 2012/12/22 10:33:00

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Normal mode

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XOB #1957: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
12/18 10:00:00 - 12/18 10:15:54	Track (-566.1, -107.3) ^{Ⓜ 12/18 09:53:00}	# OP start + 10min : track AR 11633
12/18 22:13:00 - 12/19 01:48:30	Track (-468.9, -104.9) ^{Ⓜ 12/18 22:10:00}	# track AR 11633
12/19 02:09:00 - 12/19 03:59:54	Track (-437.1, -104.1) ^{Ⓜ 12/19 02:00:00}	HOP 173 - AR 11633
12/19 04:13:00 - 12/19 05:59:54	Track (-418.8, -103.8) ^{Ⓜ 12/19 04:10:00}	# HOP 173 - AR 11633
12/19 06:03:00 - 12/19 17:59:54	Track (-403.2, -103.4) ^{Ⓜ 12/19 06:00:00}	Track AR 11633
12/19 18:13:00 - 12/20 01:59:54	Track (-296.8, -101.3) ^{Ⓜ 12/19 18:10:00}	# track AR 11633
12/20 02:03:00 - 12/20 03:39:54	Track (-226.0, -100.1) ^{Ⓜ 12/20 02:00:00}	HOP 173 - AR 11633
12/20 04:23:00 - 12/20 05:33:00	Track (-209.2, -99.9) ^{Ⓜ 12/20 03:50:00}	# HOP 173 - AR 11633
12/20 06:03:00 - 12/20 10:29:42	Track (-189.3, -99.6) ^{Ⓜ 12/20 06:00:00}	track AR 11633

PROG= 05 Inf.-time(s)

Subr=	Seqn=	1-time(s)	2.0sec										
1	18	Open/Ti-poly	Open/thick-Al close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
19	65	Open/G-band	Open/G-band close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
20	66	Open/thick-Al	Open/thick-Al close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		Al-poly/Open	Al-poly/thick-Be close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
21	67	C-poly/Open	C-poly/thick-Al close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		thin-Be/Open	med-Be/Open close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
22	68	med-Be/Open	med-Be/Open close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		med-Al/Open	med-Al/thick-Al close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
2	94	thin-Be/Open	med-Be/Open close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
		thin-Be/Open	med-Be/Open close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
		thin-Be/Open	med-Be/Open close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
		thin-Be/Open	med-Be/Open close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18

Term	Pointing (x, y)	Comment
12/18 22:03:00 - 12/18 22:09:54	Fixed (0.0, 0.0)	synoptic, shifted to 22:00
12/19 04:03:00 - 12/19 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted to 4:00
12/19 18:03:00 - 12/19 18:09:54	Fixed (0.0, 0.0)	synoptic
12/20 03:43:00 - 12/20 03:49:54	Fixed (0.0, 0.0)	synoptic, shifted to 3:40

PROG= 13 1-time(s)

Subr=	Seqn=	1-time(s)	14.0sec										
1	64	Open/Al-mesh	Open/Ti-poly close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Al-mesh	Open/Ti-poly close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
6	70	Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
70	67	Open/Ti-poly	Open/Ti-poly close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly	Open/Ti-poly close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
67	69	thin-Be/Open	thin-Be/Open close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		thin-Be/Open	thin-Be/Open close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
69	68	Open/G-band	Open/G-band open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/G-band	Open/G-band close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
12/18 10:00:00 - 12/18 10:15:54	Track (-566.1, -107.3) ^{Ⓜ 12/18 09:53:00}	# OP start + 10min : track AR 11633
12/18 22:13:00 - 12/19 01:48:30	Track (-468.9, -104.9) ^{Ⓜ 12/18 22:10:00}	# track AR 11633
12/19 02:09:00 - 12/19 03:59:54	Track (-437.1, -104.1) ^{Ⓜ 12/19 02:00:00}	HOP 173 - AR 11633
12/19 04:13:00 - 12/19 05:59:54	Track (-418.8, -103.8) ^{Ⓜ 12/19 04:10:00}	# HOP 173 - AR 11633

12/19 06:03:00 - 12/19 17:59:54 Track (-403.2, -103.4) @ 12/19 06:00:00 Track AR 11633
 12/19 18:13:00 - 12/20 01:59:54 Track (-296.8, -101.3) @ 12/19 18:10:00 # track AR 11633
 12/20 02:03:00 - 12/20 03:39:54 Track (-226.0, -100.1) @ 12/20 02:00:00 HOP 173 - AR 11633
 12/20 04:23:00 - 12/20 05:33:00 Track (-209.2, -99.9) @ 12/20 03:50:00 # HOP 173 - AR 11633
 12/20 06:03:00 - 12/20 10:29:42 Track (-189.3, -99.6) @ 12/20 06:00:00 track AR 11633

PROG= 16 15-time(s)

Subr= 1		45-time(s)	10.0sec										
Seqn= 35		1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Subr= 2		1-time(s)	10.0sec										
Seqn= 36		1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 37		1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 38		1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

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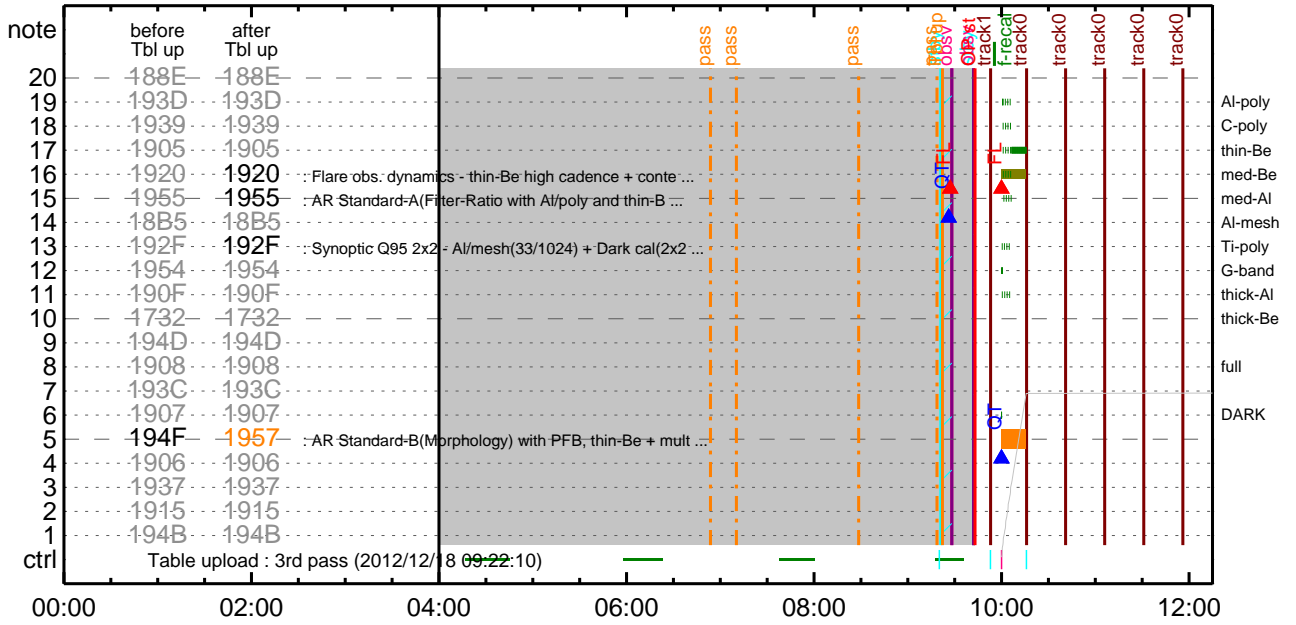
Flare Detection

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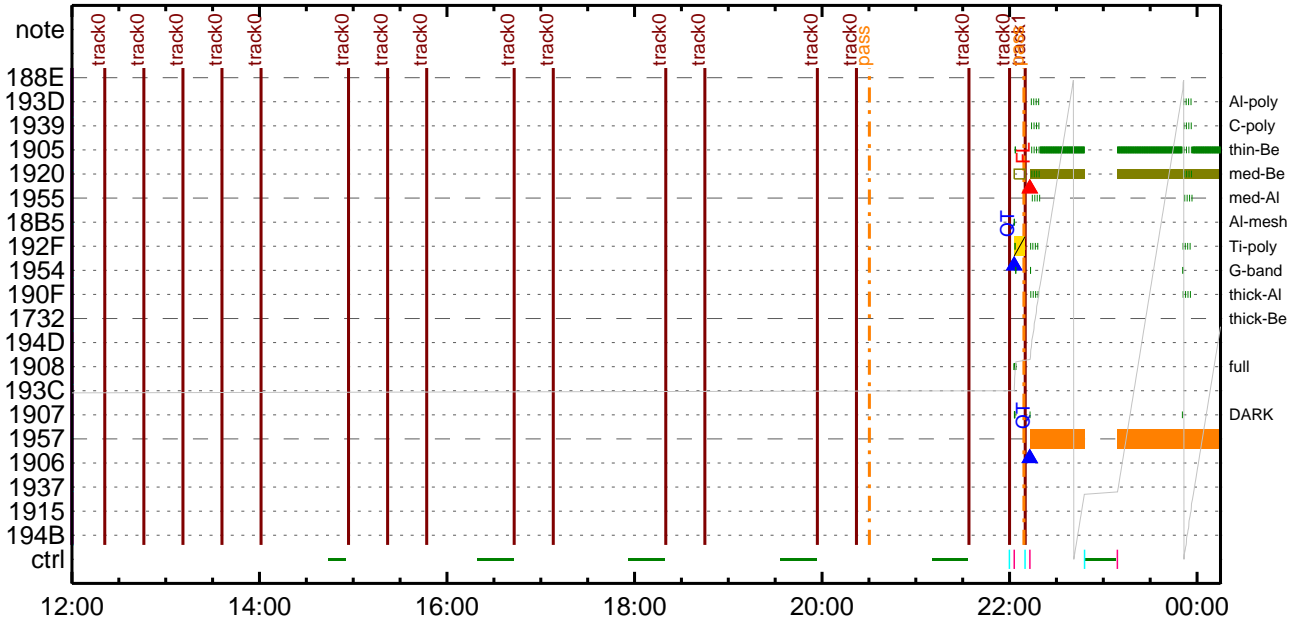
FLD Patrol

Term	Pointing (x, y)	Comment
12/18 22:12:46 - 12/19 04:00:16	Track (-468.9, -104.9) @ 12/18 22:10:00	# track AR 11633
12/19 04:12:46 - 12/19 18:00:16	Track (-418.8, -103.8) @ 12/19 04:10:00	# HOP 173 - AR 11633
12/19 18:12:46 - 12/20 03:40:16	Track (-296.8, -101.3) @ 12/19 18:10:00	# track AR 11633
12/20 04:22:46 - 12/22 10:33:00	Track (-209.2, -99.9) @ 12/20 03:50:00	# HOP 173 - AR 11633
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

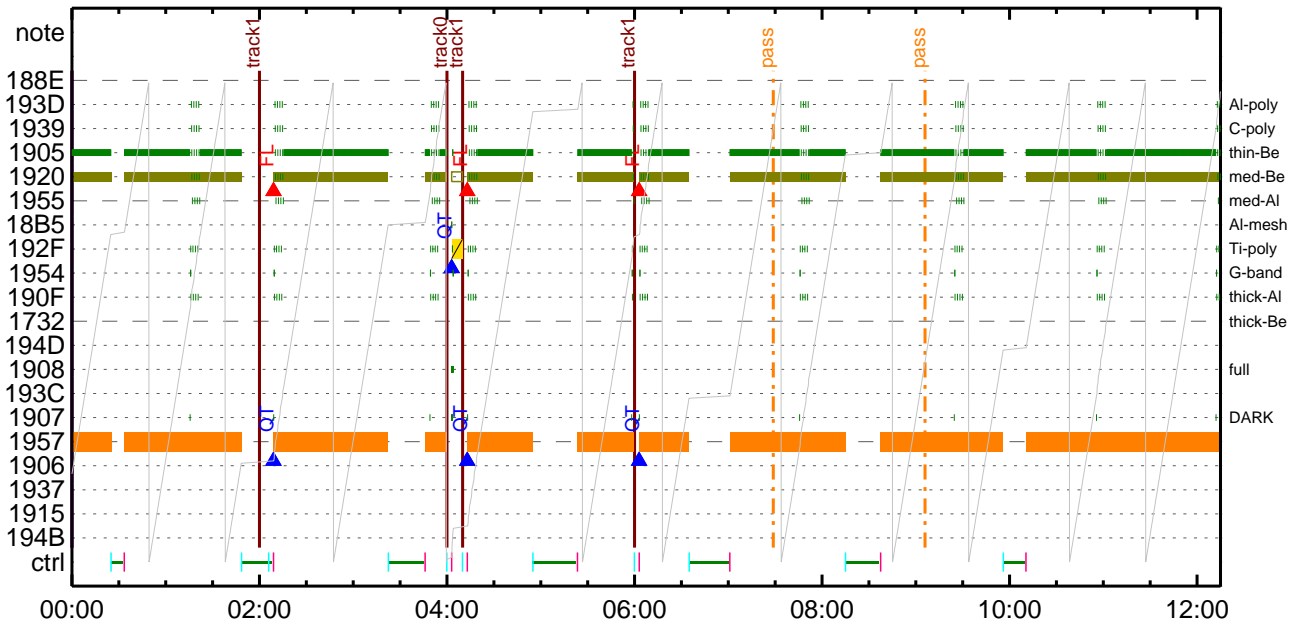
CMDI #0131 2012/12/18



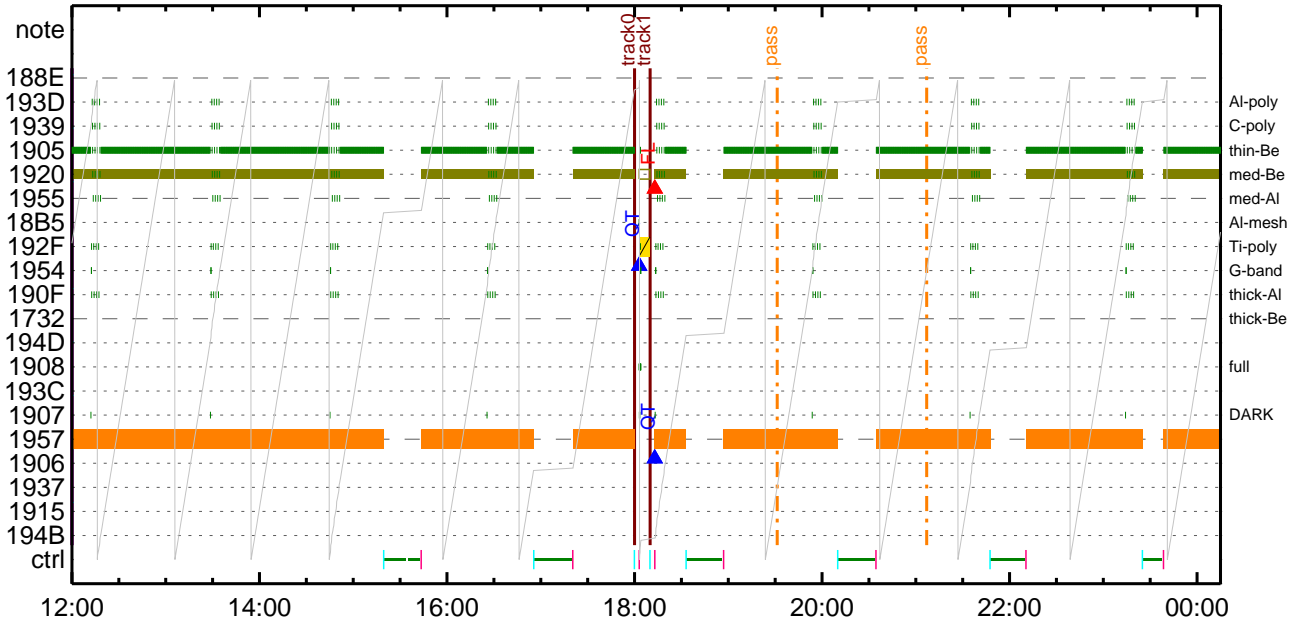
CMDI #0131 2012/12/18



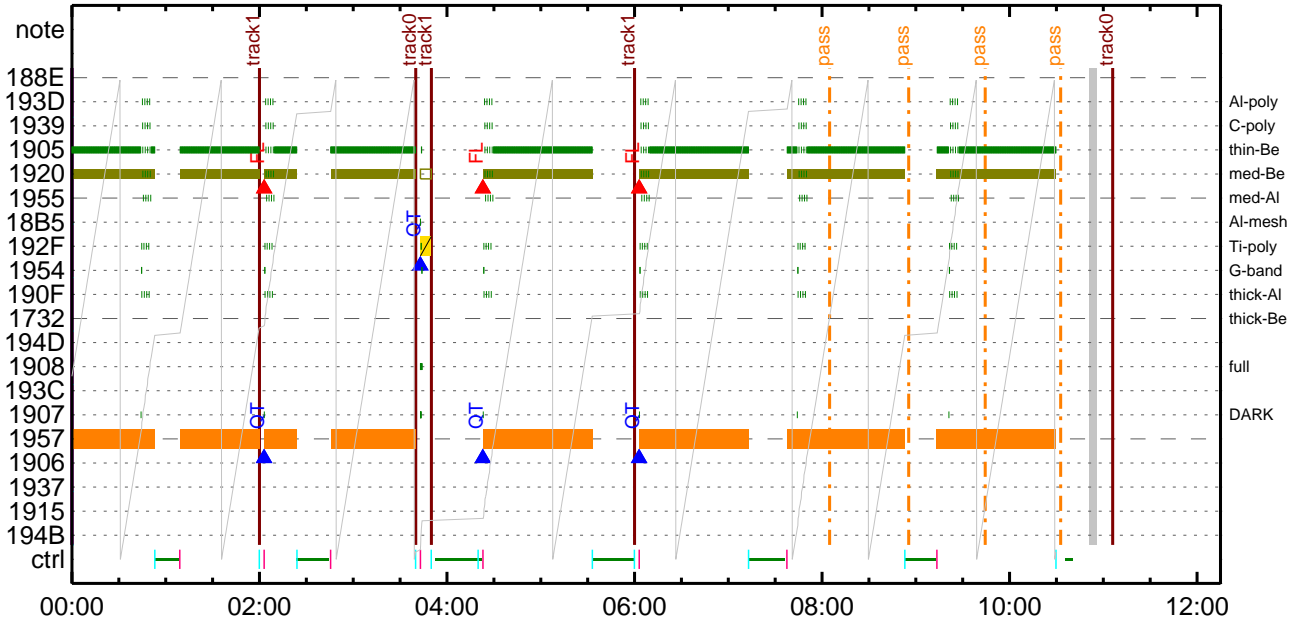
CMDI #0131 2012/12/19



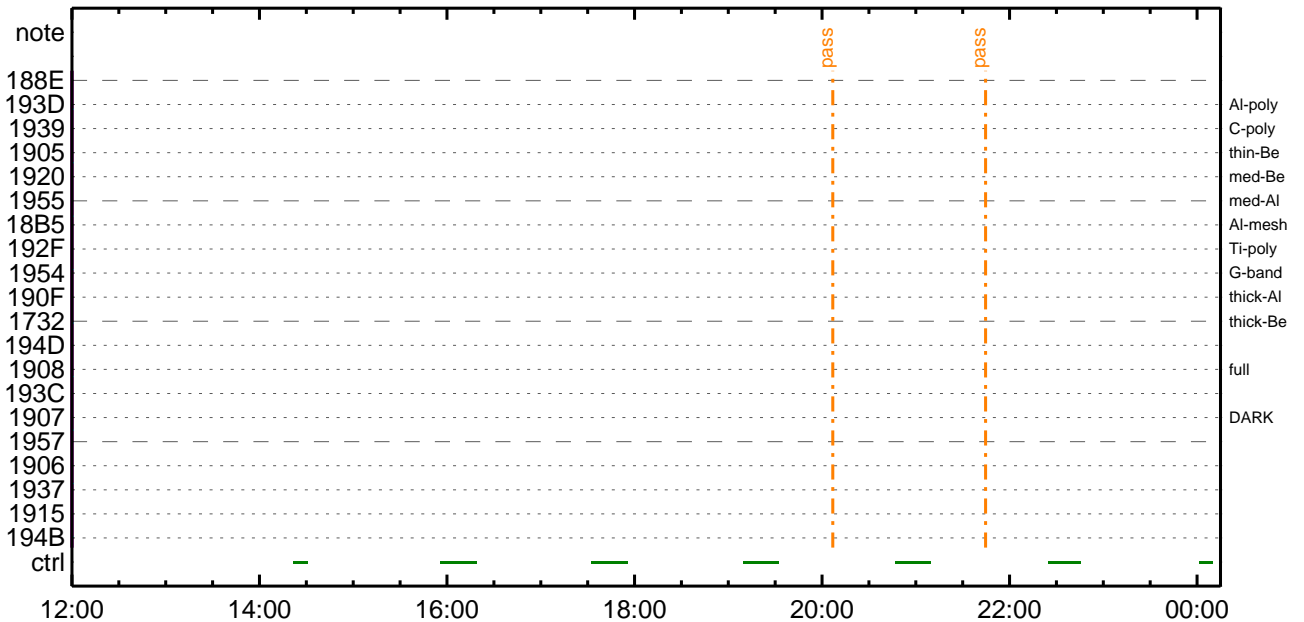
CMDI #0131 2012/12/19



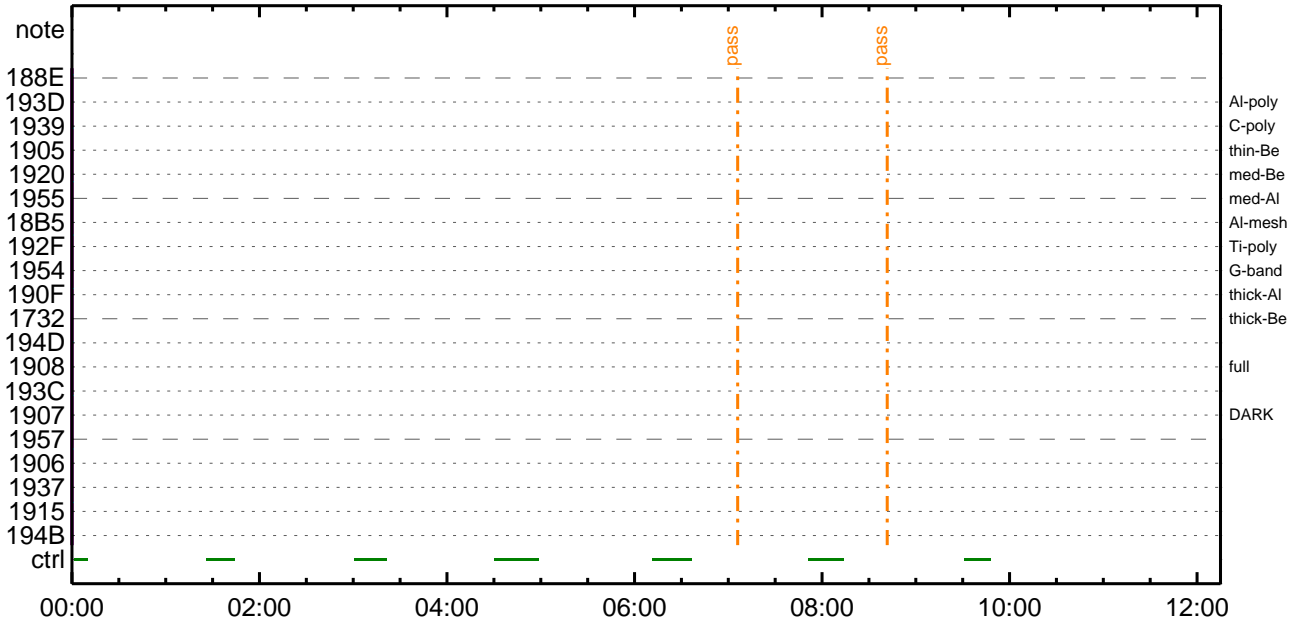
CMDI #0131 2012/12/20



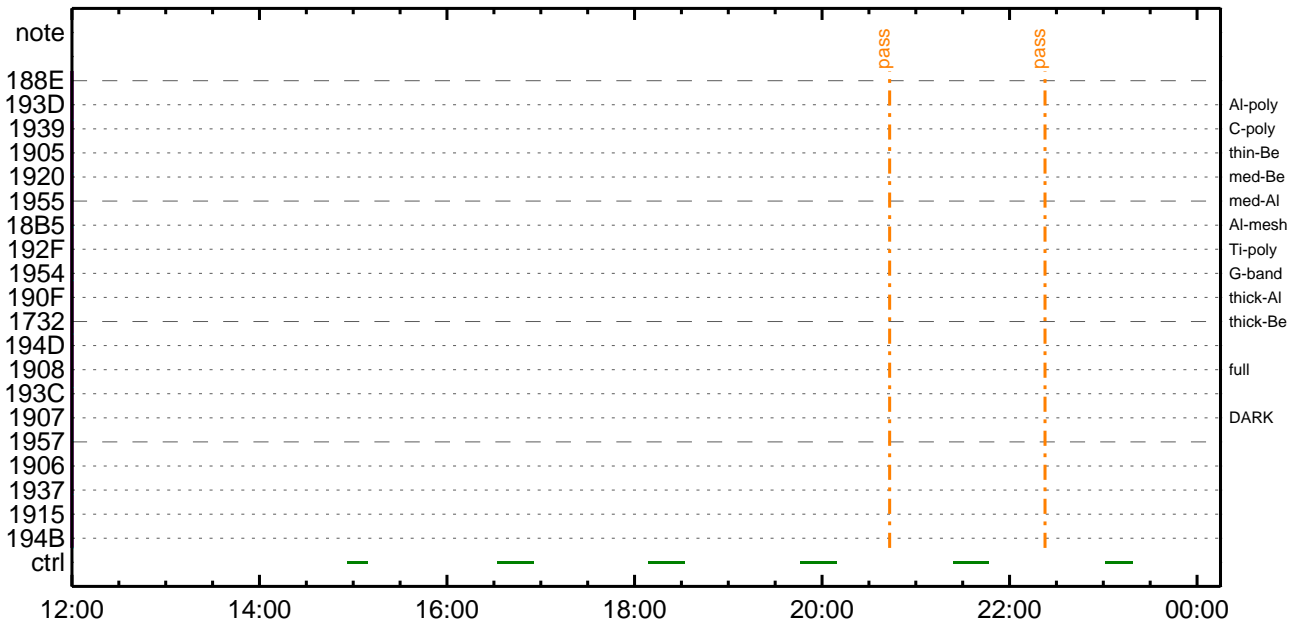
CMDI #0131 2012/12/20



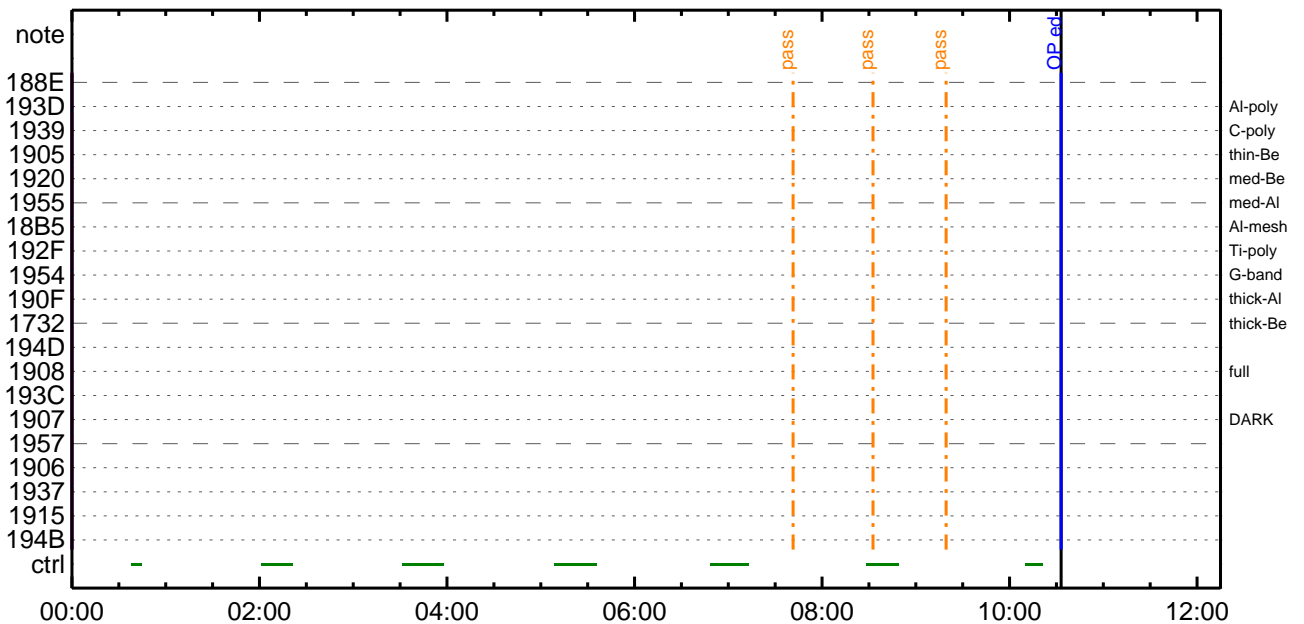
CMDI #0131 2012/12/21



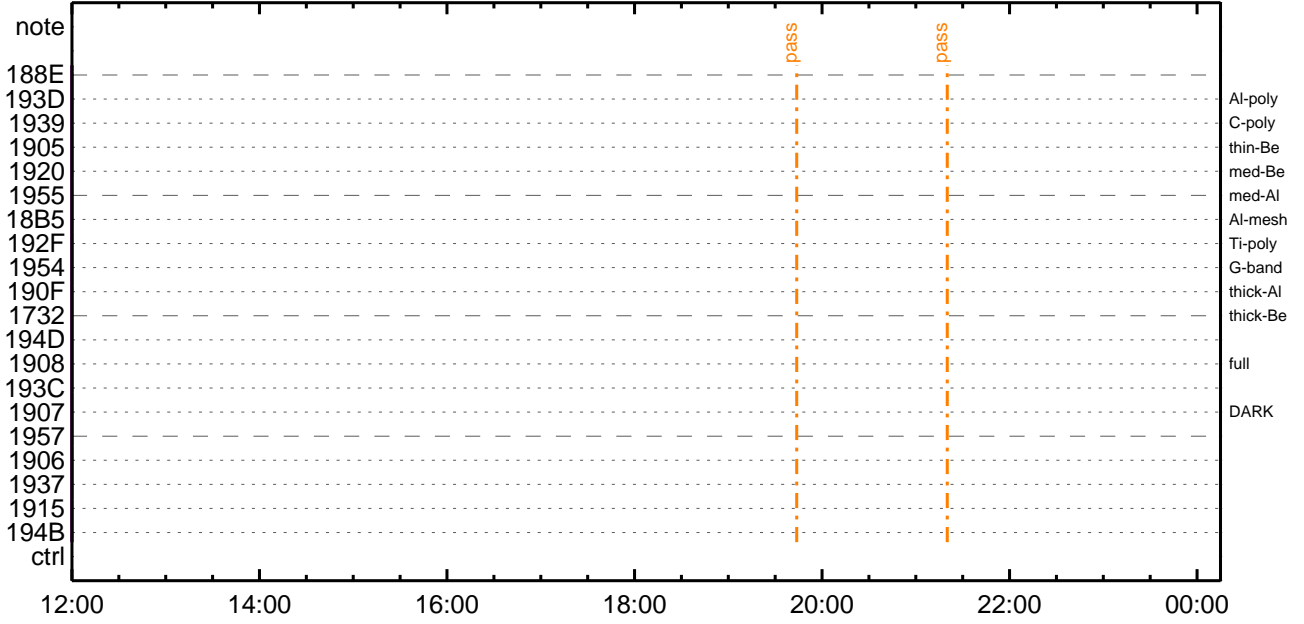
CMDI #0131 2012/12/21



CMDI #0131 2012/12/22



CMDI #0131 2012/12/22




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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_ENA
0113 BC (d8)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0115 BC (c8)
0116 + DC 07-F0 MDP_XRT_AEC_RESET
0117 BC (d0)
0118 + DC 07-F0 MDP_XRT_ARS_DIS
0119 BC (d5)
0120 + DC 07-F0 MDP_XRT_FLD_RESET
0121 BC (da)
0122 + DC 07-F0 MDP_XRT_QT_PROG_SET
0123 BC (c4 0f)
0124 + DC 07-F0 MDP_XRT_FL_PROG_SET
0125 BC (c5 10)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C.
0129 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0130 C.
0131 + DC 07-F0 MDP_XRT_MODE_OBSV
0132 BC (c2)
0133 + TI 2012-12-18 09:42:02.0
0134 DC 07-F0 MDP_XRT_MODE_OBSV
0135 BC (c2)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C. ***** XRT END *****
0139 C.
0140 . C. ***** MDP 'uÃîqî»ö¼ÝqÊÄð¹ñèDCBC•x²è *****
0141 C. (%á°îÿÓÿÄÿËÿÏÿÑÿÑÿçÿèñ¼q¼Ä»Û¹ñè)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ÿÐÿ¹•ï Daily±¿îÑñÉ´Ø¹ñèDCBC•x²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ;ãLOSÿÁÿÏÿÄÿÑÿ¼Ä»Û;ä
0152 C.
0153 . C. ***** LOS *****
0154 C.
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Dec 18, 12 12:49

XRT_OGLIST_0131.chk

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*** OP Sequence for XRT ***

2012/12/18	09:52:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/18	09:53:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/12/18	09:55:26.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/12/18	09:59:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/18	09:59:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/18	09:59:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/18	09:59:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/18	09:59:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/18	09:59:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/18	09:59:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/12/18	09:59:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/18	10:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/18	10:15:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/18	10:16:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 54 00 01 58				
2012/12/18	10:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 4c 9b 01 58				
2012/12/18	11:06:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 43 be 01 58				
2012/12/18	11:31:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 3a d9 01 58				
2012/12/18	11:56:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 31 f3 01 58				
2012/12/18	12:21:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 29 0e 01 58				
2012/12/18	12:46:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 20 31 01 58				
2012/12/18	13:11:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 17 4b 01 58				
2012/12/18	13:36:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 0e 66 01 58				
2012/12/18	14:01:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 05 81 01 58				
2012/12/18	14:57:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 fd 82 01 58				
2012/12/18	15:22:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 f4 9c 01 58				
2012/12/18	15:47:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 eb bf 01 58				
2012/12/18	16:43:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 e2 da 01 58				
2012/12/18	17:08:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 d9 f4 01 58				
2012/12/18	18:20:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 d1 0f 01 58				
2012/12/18	18:45:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 c8 32 01 58				
2012/12/18	19:57:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00 bf 4c 01 58				
2012/12/18	20:22:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00 b6 67 01 58				
2012/12/18	21:34:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00 ad 82 01 58				
2012/12/18	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/18	21:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/12/18	22:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/12/18	22:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/18	22:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/18	22:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/18	22:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/12/18	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/18	22:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/18	22:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/12/18	22:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/18	22:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/18	22:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							

2012/12/18	22:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/12/18	22:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/18	22:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/18	22:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2012/12/18	22:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/12/18	22:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/18	22:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/18	22:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/18	22:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/18	22:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/18	23:08:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/18	23:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/19	00:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/19	00:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/19	00:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/19	00:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/19	00:32:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/19	00:33:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/19	01:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/19	01:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/19	01:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/19	01:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/19	02:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2012/12/19	02:05:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/19	02:08:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/12/19	02:08:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/12/19	02:08:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/12/19	02:08:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/12/19	02:08:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/19	02:08:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/19	02:08:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2012/12/19	02:08:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/12/19	02:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/19	03:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/19	03:22:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/19	03:22:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/19	03:25:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/19	03:45:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/19	03:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/19	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/19	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/12/19	04:00:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/12/19	04:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/12/19	04:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/12/19	04:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/19	04:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2012/12/19	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/19	04:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					

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2012/12/19	04:10:00.0	AOCS_Or-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	01 00 00 00 00
2012/12/19	04:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/19	04:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/19	04:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/19	04:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/19	04:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/19	04:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	04:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/19	04:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/19	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/19	04:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/19	04:55:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	04:55:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/19	04:58:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/19	05:22:30.5	XRT_Custom_434_OG [0x1b2]				
2012/12/19	05:23:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/19	05:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/19	06:00:00.0	AOCS_Or-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	01 00 00 00 00
2012/12/19	06:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/19	06:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/19	06:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/19	06:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/19	06:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/19	06:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	06:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/19	06:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/19	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/19	06:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/19	06:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	06:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/19	06:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/19	07:00:00.0	XRT_Custom_434_OG [0x1b2]				
2012/12/19	07:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/19	08:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/19	08:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	08:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/19	08:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/19	08:36:30.0	XRT_Custom_434_OG [0x1b2]				
2012/12/19	08:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/19	09:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/19	09:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	09:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/19	09:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/19	10:09:30.0	XRT_Custom_434_OG [0x1b2]				
2012/12/19	10:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/19	15:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/19	15:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/19	15:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2012/12/19	15:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/19	15:42:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/19	15:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	16:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	16:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/19	16:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/19	16:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/19	17:19:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/19	17:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/12/19	18:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/12/19	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/19	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/19	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/19	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/12/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/12/19	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/19	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/19	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/19	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/19	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/19	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/19	18:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/12/19	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	18:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	18:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/19	18:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/19	18:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/19	18:56:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/19	18:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	20:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	20:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/19	20:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/19	20:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/19	20:33:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/19	20:34:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	21:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	21:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/19	21:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/19	21:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/19	22:09:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/19	22:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/19	23:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/19	23:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/19	23:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2012/12/19	23:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/19	23:37:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/19	23:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/20	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	00:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/20	00:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/20	00:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/20	01:08:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/20	01:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/20	01:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	02:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/12/20	02:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/20	02:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/20	02:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/20	02:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/20	02:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/20	02:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/20	02:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/12/20	02:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/20	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/20	02:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	02:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/20	02:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/20	02:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/20	02:44:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/20	02:45:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/20	03:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	03:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/12/20	03:40:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/12/20	03:40:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/20	03:40:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/20	03:40:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/20	03:42:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/12/20	03:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/20	03:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	03:50:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/12/20	04:19:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	04:22:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/12/20	04:22:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/20	04:22:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/20	04:22:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/20	04:22:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/20	04:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/20	04:22:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/12/20	04:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/12/20	04:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/20	05:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/20	05:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/12/20	05:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/12/20	05:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/12/20	05:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/12/20	06:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	01 00 00 00 00					
2012/12/20	06:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2012/12/20	06:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/12/20	06:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/12/20	06:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/12/20	06:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/12/20	06:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/12/20	06:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05					
2012/12/20	06:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2012/12/20	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/12/20	07:13:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/12/20	07:13:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/12/20	07:13:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/12/20	07:16:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/12/20	07:36:30.0	XRT_Custom_434_OG [0x1b2]								
2012/12/20	07:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/12/20	08:53:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/12/20	08:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/12/20	08:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/12/20	08:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/12/20	09:12:30.0	XRT_Custom_434_OG [0x1b2]								
2012/12/20	09:13:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/12/20	10:29:42.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/12/20	11:06:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]								
		AOCU_NM	5	02-76	00 00 00 00 00					