

XRT Timeline to be uploaded on 2012/12/20

Period: 2012/12/20 10:56:00 - 2012/12/25 10:46:00

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Normal mode

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XOB #1957: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad w/ G-Band VLS Closed Test													
Term		Pointing (x, y)				Comment							
12/20 15:14:00 - 12/20 17:32:30		Track (-81.5, -105.8) @ 12/20 15:11:00				# Observe AR 11633							
12/20 18:11:00 - 12/21 03:59:54		Track (-53.9, -105.5) @ 12/20 18:08:00				# AR 11633 (HOP-173 from 2UT)							
12/21 04:13:00 - 12/21 05:59:54		Track (40.2, -104.6) @ 12/21 04:10:00				# HOP 173							
12/21 06:03:00 - 12/21 11:09:54		Track (57.4, -104.5) @ 12/21 06:00:00				# AR 11633							
12/21 13:33:00 - 12/21 17:45:54		Track (127.4, -104.0) @ 12/21 13:30:00				# AR 11633							
12/21 17:59:00 - 12/22 03:31:30		Track (168.5, -103.8) @ 12/21 17:56:00				# AR 11633 (HOP 173 from 2UT).							
12/22 04:13:00 - 12/22 05:59:54		Track (262.1, -103.6) @ 12/22 04:10:00				# HOP 173							
12/22 06:03:00 - 12/22 09:16:00		Track (278.6, -103.6) @ 12/22 06:00:00				# AR 11633							
PROG= 05 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 18		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 19		1-time(s)		2.0sec									
Open/G-band		Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 65		4-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open		Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open		C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 94		70-time(s)		2.0sec									
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1958: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33) - 1 loop													
Term		Pointing (x, y)				Comment							
12/20 18:01:00 - 12/20 18:07:54		Fixed (0.0, 0.0)				synoptic, shifted -2.0 min							
PROG= 20													
Subr= 1		1-time(s)		85.0sec									
Seqn= 82		1-time(s)		7.0sec									
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 6		1-time(s)		22.0sec									
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 76		1-time(s)		8.0sec									
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 77		1-time(s)		12.0sec									
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 79		1-time(s)		10.0sec									
└ C-poly/Open		C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ C-poly/Open		C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 75		1-time(s)		10.0sec									
└ Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 78		1-time(s)		15.0sec									
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)		150.0sec									
Seqn= 34		1-time(s)		29.0sec									
└ med-Al/Open		med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 83		1-time(s)		84.0sec									
└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 69		2-time(s)		13.0sec									
└ Open/G-band		Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 68		1-time(s)		10.0sec									
└ Open/G-band		Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(183

Term	Pointing (x, y)	Comment
12/21 04:03:00 - 12/21 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted a couple of hours.
12/21 17:49:00 - 12/21 17:55:54	Fixed (0.0, 0.0)	synoptic, shifted -14.0 min
12/22 04:03:00 - 12/22 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted 2 hours.

PROG= 13

1-time(s)

Subr= 1		1-time(s)		14.0sec											
Seqn= 64		1-time(s)		4.0sec											
Open/Al-mesh		Open/Ti-poly		close		Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Ti-poly		close		Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6		1-time(s)		2.0sec											
Open/Ti-poly		Open/thick-Al		close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close		Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close		Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close		Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close		Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 70		1-time(s)		4.0sec											
Open/Ti-poly		Open/Ti-poly		close		Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly		Open/Ti-poly		close		Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67		1-time(s)		2.0sec											
thin-Be/Open		thin-Be/Open		close		Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open		close		Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open		Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec											
Seqn= 68		1-time(s)		2.0sec											
Open/G-band		Open/G-band		close		Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter		Thicker Filter		VLS		mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
12/20 15:14:00 - 12/20 17:32:30	Track (-81.5, -105.8) @ 12/20 15:11:00	# Observe AR 11633
12/20 18:11:00 - 12/21 03:59:54	Track (-53.9, -105.5) @ 12/20 18:08:00	# AR 11633 (HOP-173 from 2UT)
12/21 04:13:00 - 12/21 05:59:54	Track (40.2, -104.6) @ 12/21 04:10:00	# HOP 173
12/21 06:03:00 - 12/21 11:09:54	Track (57.4, -104.5) @ 12/21 06:00:00	# AR 11633
12/21 13:33:00 - 12/21 17:45:54	Track (127.4, -104.0) @ 12/21 13:30:00	# AR 11633
12/21 17:59:00 - 12/22 03:31:30	Track (168.5, -103.8) @ 12/21 17:56:00	# AR 11633 (HOP 173 from 2UT).
12/22 04:13:00 - 12/22 05:59:54	Track (262.1, -103.6) @ 12/22 04:10:00	# HOP 173
12/22 06:03:00 - 12/22 09:16:00	Track (278.6, -103.6) @ 12/22 06:00:00	# AR 11633

PROG= 16

15-time(s)

Subr= 1		45-time(s)		10.0sec											
Seqn= 35		1-time(s)		2.0sec											
thin-Be/Open		med-Be/Open		close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		10.0sec											
Seqn= 36		1-time(s)		2.0sec											
med-Al/Open		med-Al/thick-Al		close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be		close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 37		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/thick-Al		close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open		Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close		Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close		Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter		VLS		mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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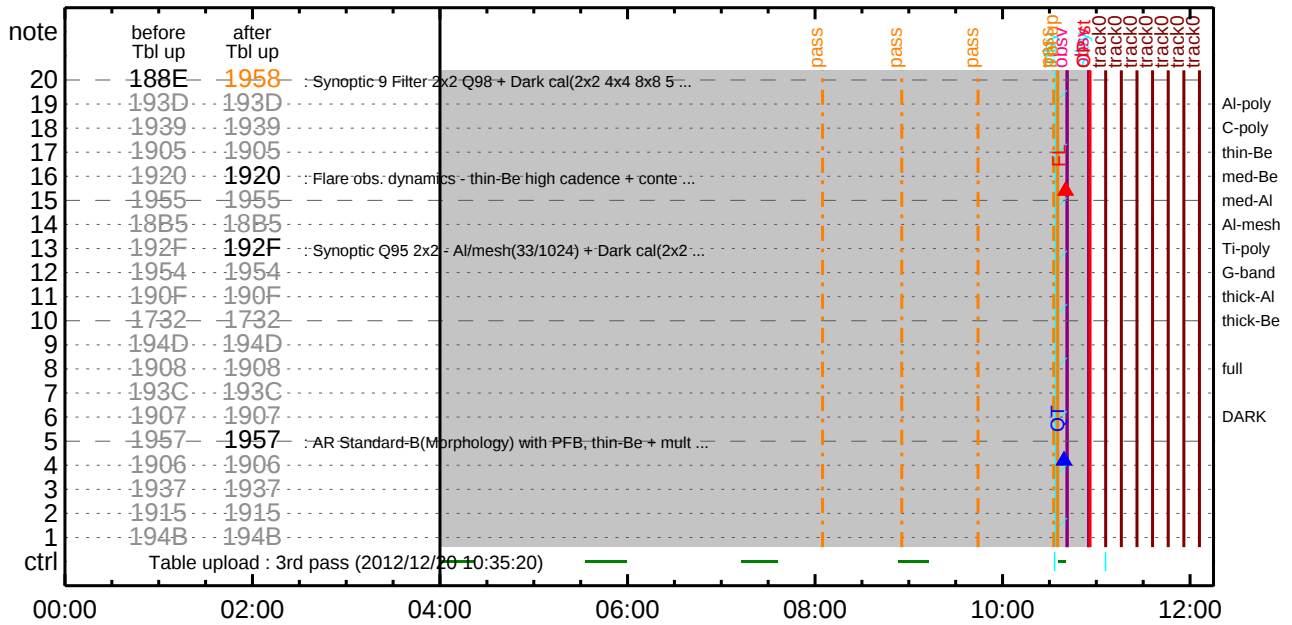
Flare Detection

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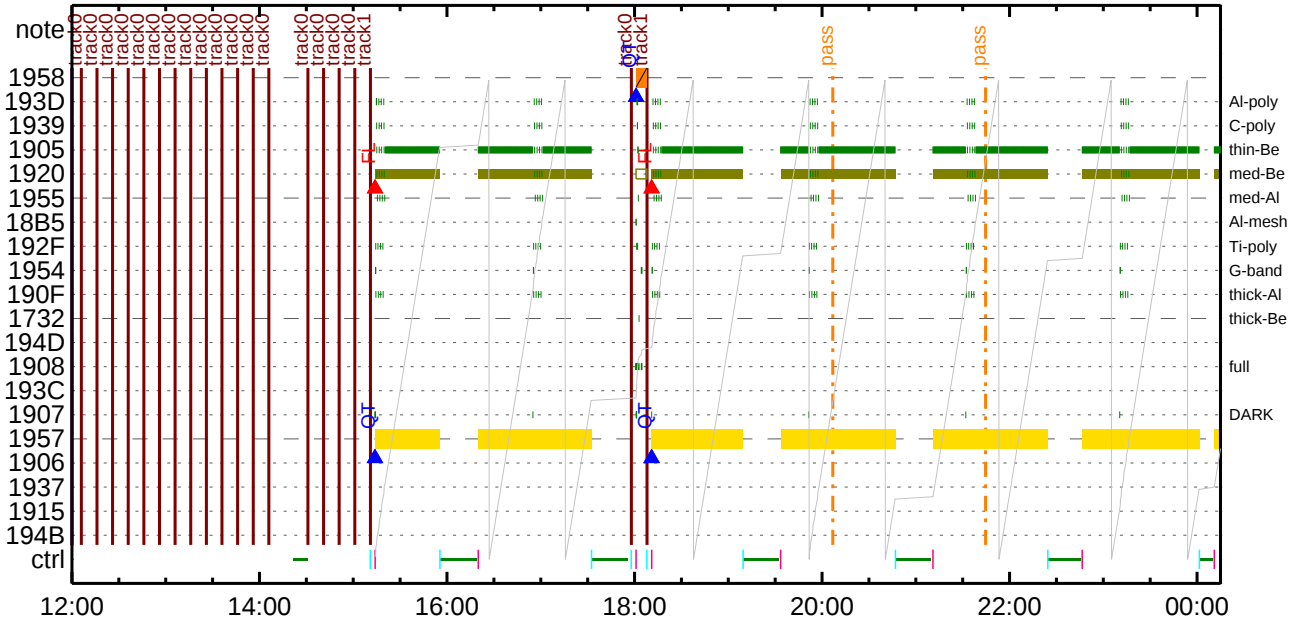
FLD Patrol

Term	Pointing (x, y)								Comment					
12/20 18:10:46 - 12/21 04:00:16	Track (	-53.9,	-105.5)	@ 12/20 18:08:00	# AR 11633 (HOP-173 from 2UT)									
12/21 04:12:46 - 12/21 17:46:16	Track (	40.2,	-104.6)	@ 12/21 04:10:00	# HOP 173									
12/21 17:58:46 - 12/22 04:00:16	Track (	168.5,	-103.8)	@ 12/21 17:56:00	# AR 11633 (HOP 173 from 2UT).									
12/22 04:12:46 - 12/25 10:46:00	Track (	262.1,	-103.6)	@ 12/22 04:10:00	# HOP 173									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50				30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)				Comp.	AEC Buffer	Interval

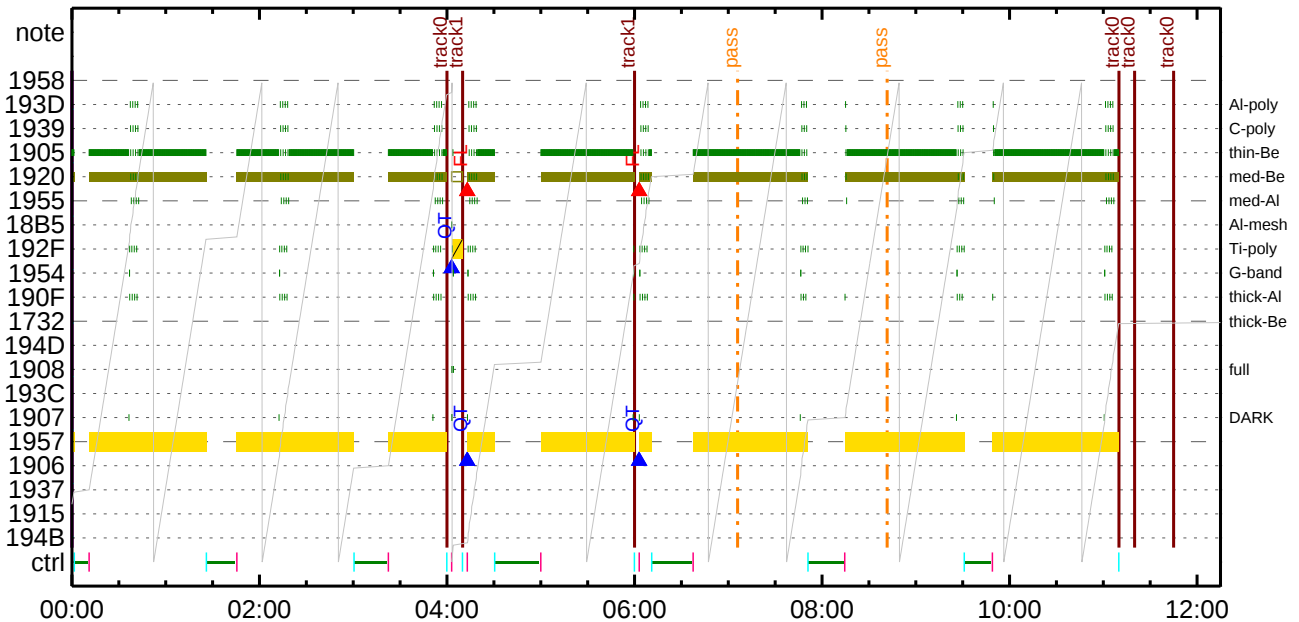
CMDI #0135 2012/12/20



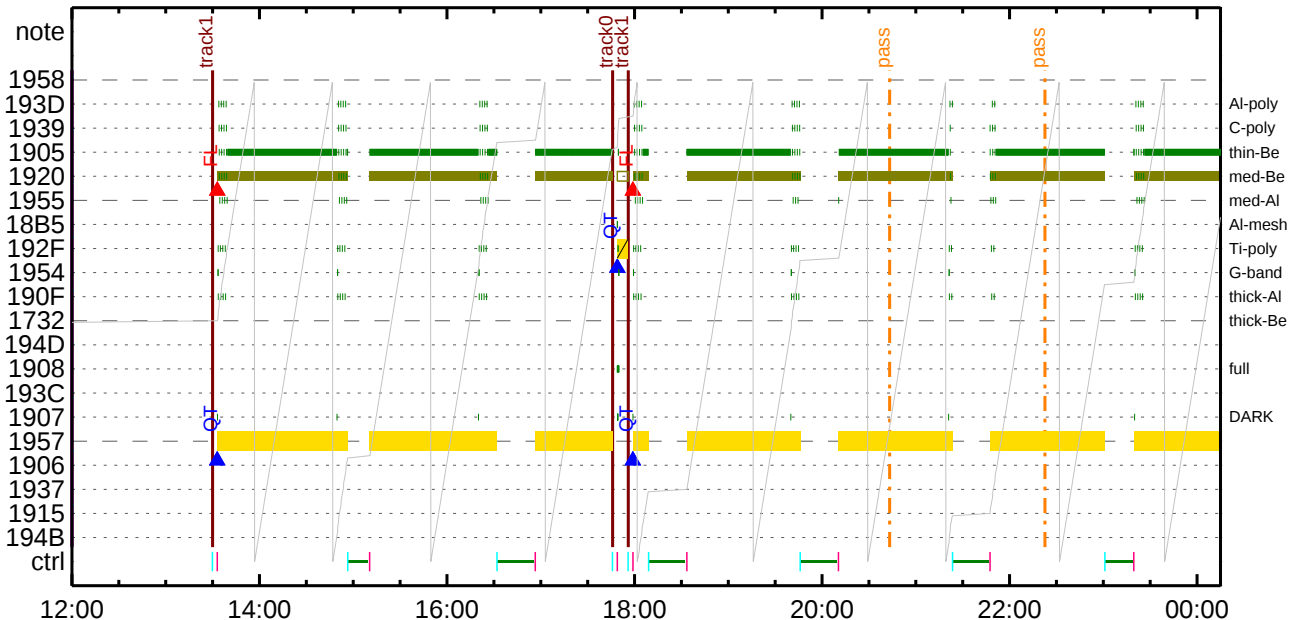
CMDI #0135 2012/12/20



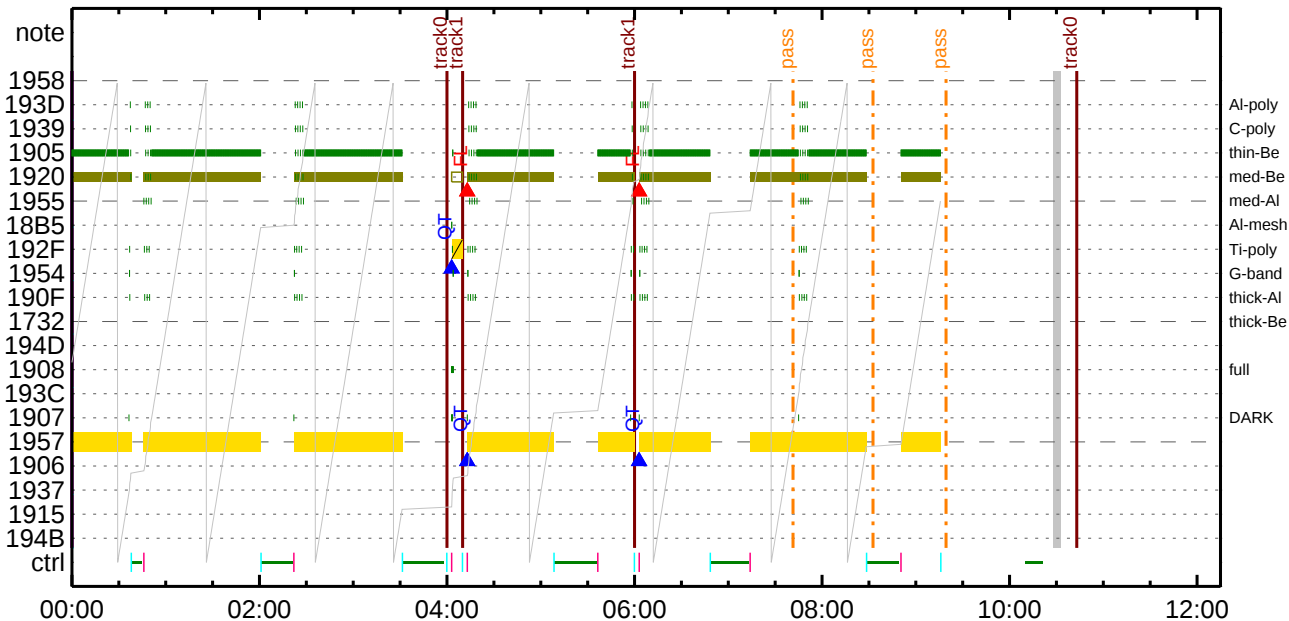
CMDI #0135 2012/12/21



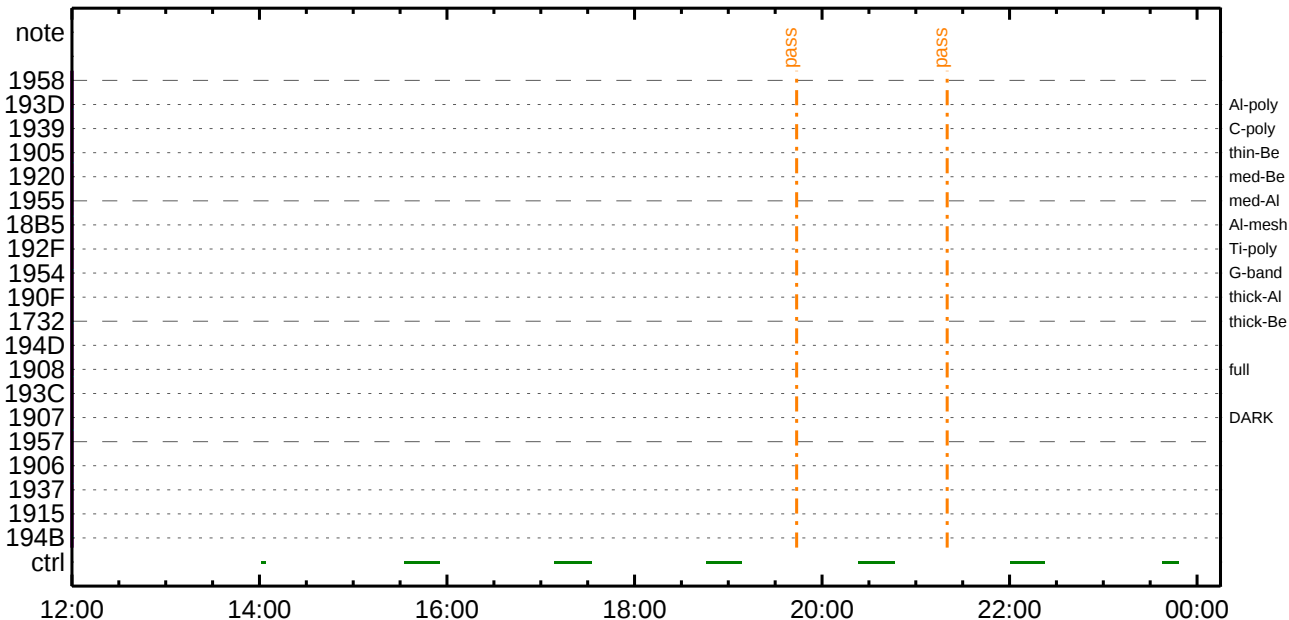
CMDI #0135 2012/12/21



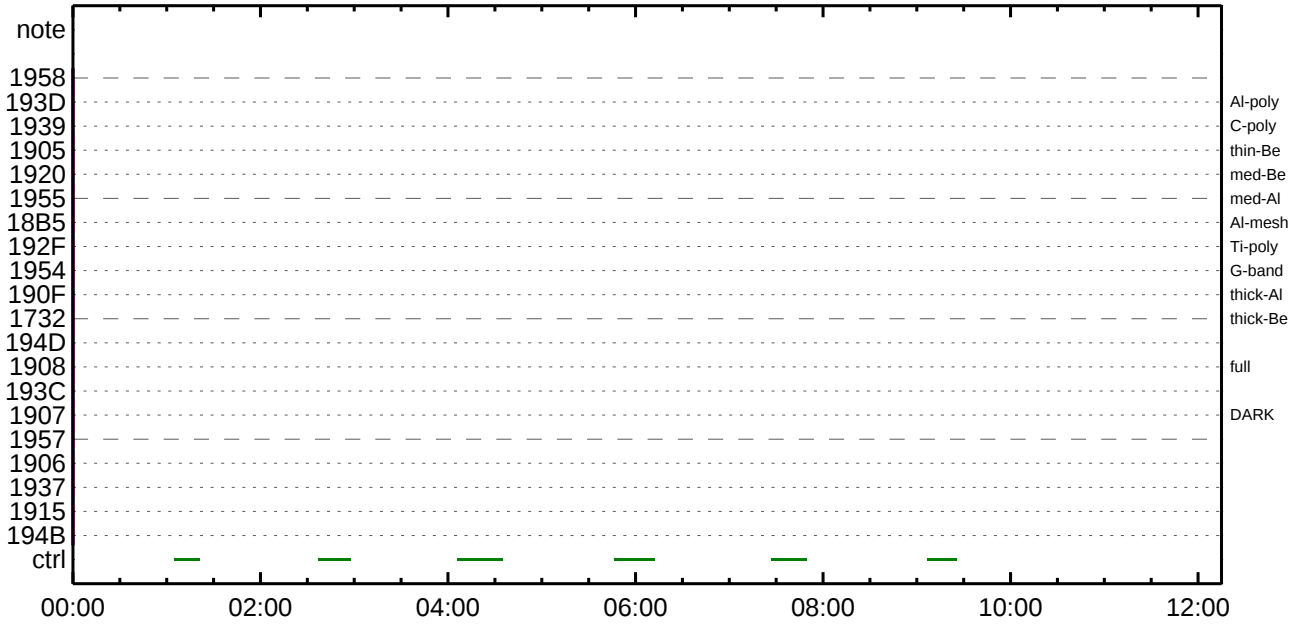
CMDI #0135 2012/12/22



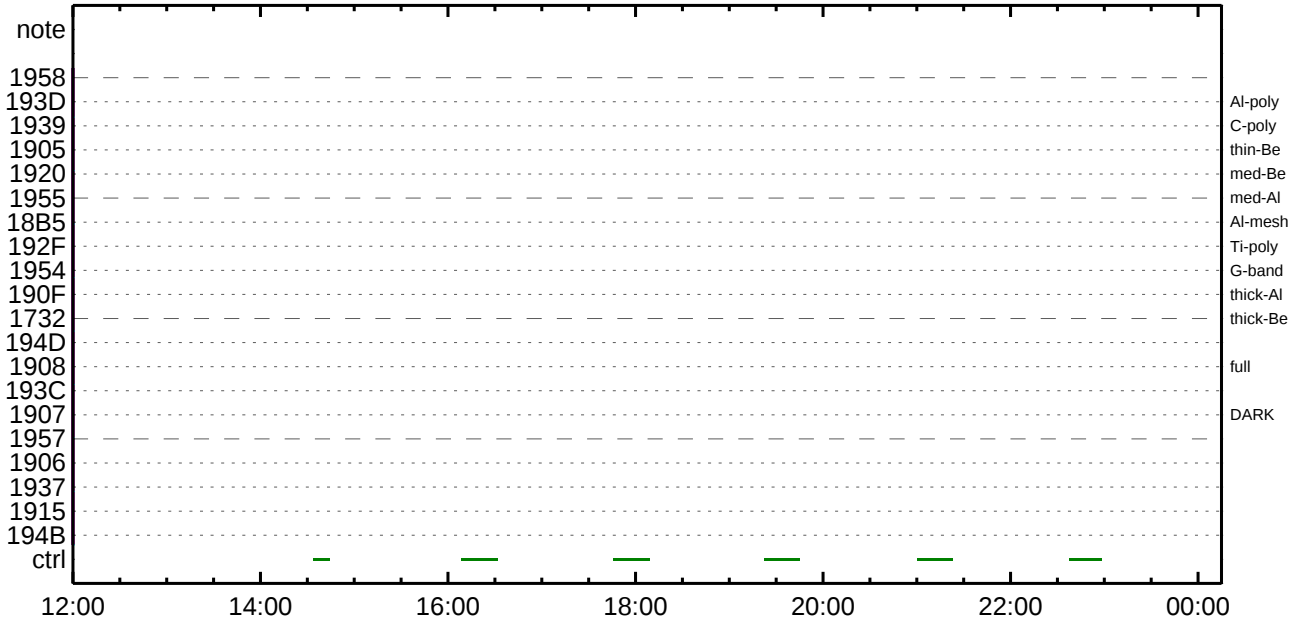
CMDI #0135 2012/12/22



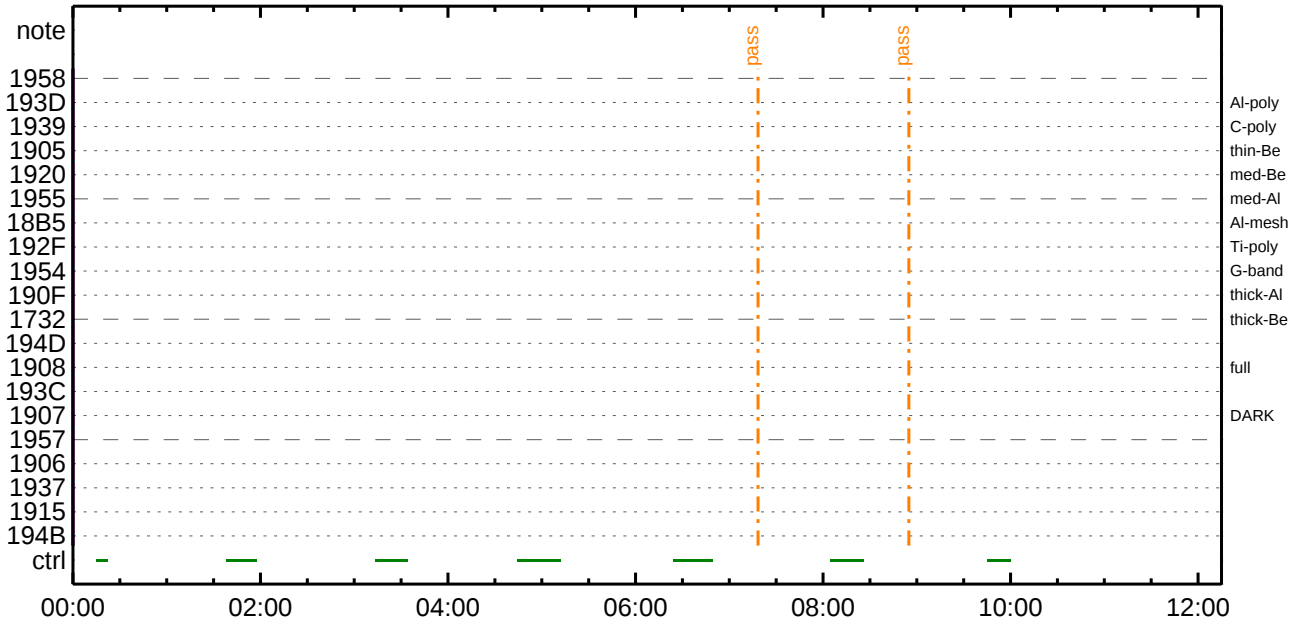
CMDI #0135 2012/12/23



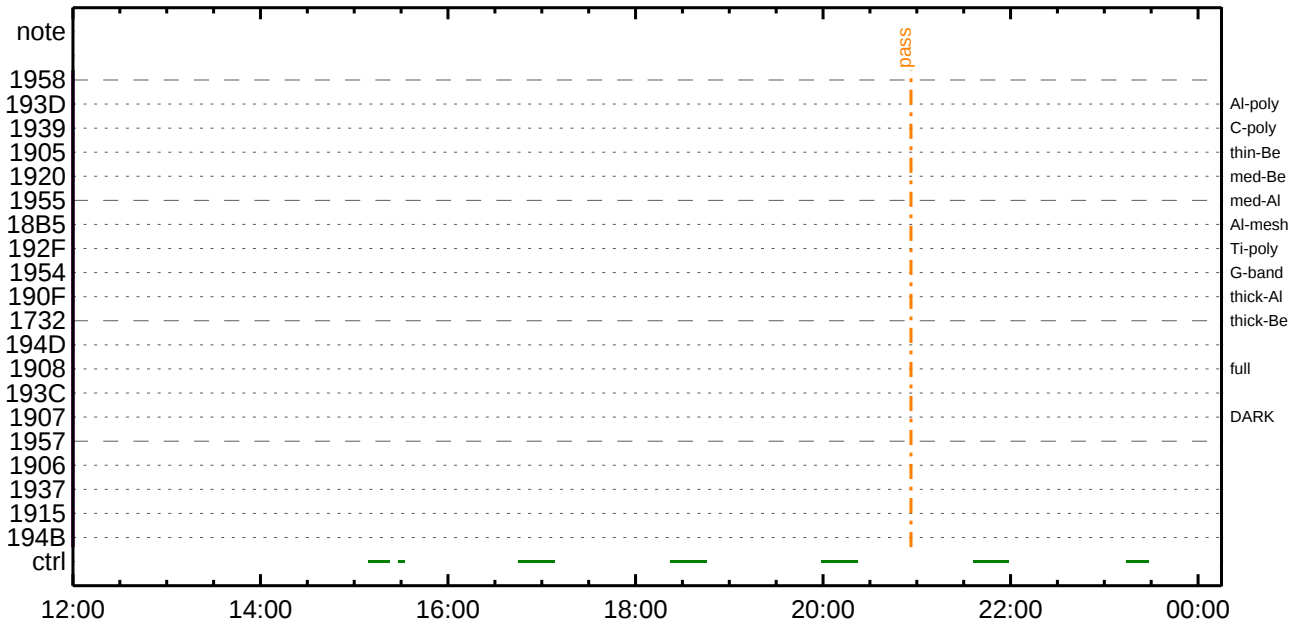
CMDI #0135 2012/12/23



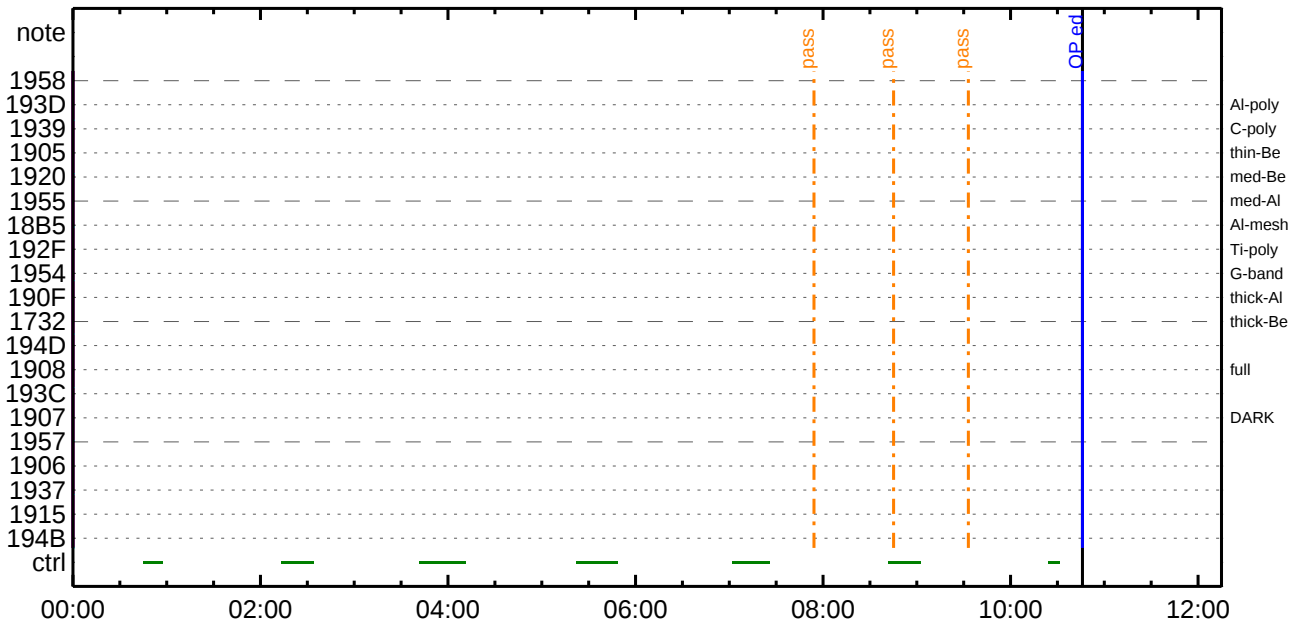
CMDI #0135 2012/12/24



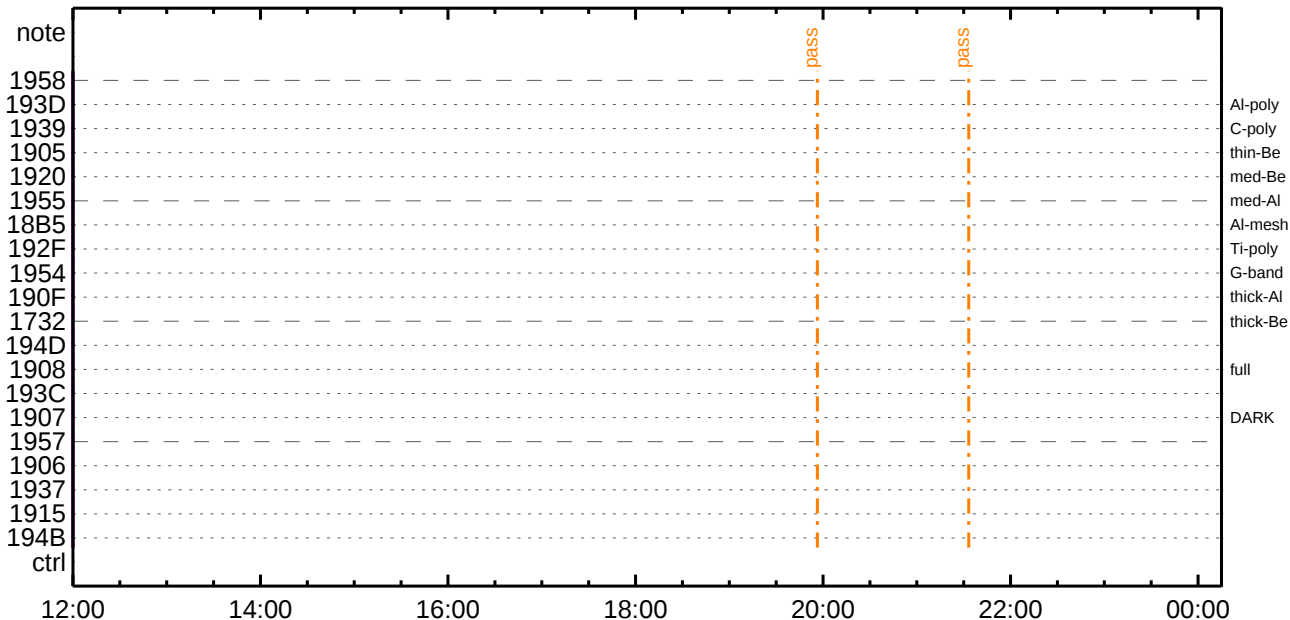
CMDI #0135 2012/12/24



CMDI #0135 2012/12/25



CMDI #0135 2012/12/25



(a) Spacecraft Operation Procedure (real-commands)

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main-343 2012-12-20 12:26:28 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¤İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ĒĒ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p´Œ¤¤¹İİ, ¤•jÇĒŒİ×¤ĒXĒŒŒON¤İ¹Œ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŒĒŒ¤-°ĒĒĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ĒĒ, ¼Ē½Ç¤¤Œ¼Ē¹Œ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ĒĒ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ē¹Œ¤¤¤¤•jÇDCBC-150¤Œ¿Ē¤¤¤jĒ
0035 C.
0036 . C. jãPT1°ĒĒ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Œ, j¼Œ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0045 C.
0046 . C. jã¥Ç¥Ó¥Æ¥ĒĒŒŒjĒĒ•Ē°²ŒĒjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Œ, j¼Œ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0052 C.
0053 C.
0054 . C. PT1°ĒĒ, ¤-¼«Æ°ĒĒ»¤¤•¤¿, ĒjÇ°Ē²¼¤¤Œ¼Ē¹Œ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ó¥Æ¥ĒĒŒŒŒŒŒ•Ē°²ŒĒŒ¤Œ¤¤¼¹Ç¤İ°İ°İ»¤¹¤Ē¤¤ÇĒŒŒjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ĒĒ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ē¹Œ¤¤¤¤•jÇDCBC-151¤Œ¿Ē¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ĒĒ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Œ, j¼Œ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0071 C.
0072 . C. jã¥Ç¥Ó¥Æ¥ĒĒŒŒjĒĒ•Ē°²ŒĒjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Œ, j¼Œ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0078 C.
0079 . C. *****
0080 C. DR°ĒĒ, Ē¤»BjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ĒĒ, Ē¤»Bjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OG¥¡¡¼¥É¡¼¥Á¥ó¥×
0100 C. *****
0101 C.
0102 C. ¡ãOP/OG¥¡¡¼¥É¡ä
0103 S. OP op-343:OP
0104 ()
0105 S. OG og-343:OG
0106 ()
0107 C.
0108 C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109 C. NMOG(0x200000-0x207FFF;S 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥À¥ó¥×½ªÎ»ð³İÇŞ
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤Î¥Ê¹Ç•ë²İOK¤ð³İÇŞ
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;S 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥À¥ó¥×½ªÎ»ð³İÇŞ
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤Î¥Ê¹Ç•ë²İOK¤ð³İÇŞ
0146 C.
0147 C. NMOG(0x210000-0x2100FF;S 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥À¥ó¥×½ªÎ»ð³İÇŞ
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP¤Î¥Ê¹Ç•ë²İOK¤ð³İÇŞ
0165 C.
0166 C. ***** °Ê²¼¤İ¾Á´¶Á¤¤ÊË-¤¤Á÷¿® (%âµ-¥À¥ó¥×½ê½Ç¤¤ÃŒ¤¤Ç½¤¤¨¤¤ë¾¼¹Ç¤Ç¤â) *****
0167 C. DHU¥â¡¼¥É¡¼¥Y½, ¥¡¼¼¥É¡¼¤İ¤¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ¡§ OPOG UPLOAD¤-Á÷¿®NG¤¤Î¼¹Ç¡¢°Ê²¼¤İTI-CMDÁ÷¿®¤İ¾Á¹Œ¤¤•¤Ê¤¤¤³¤¤ë¡£
0180 C. ¤¤¤¿¡¢¡¢SET¤EDUMP¤İÆ±°¡¼N¥¹¡¢Ç¹Œ¤¤³¤¤ë¡£
0181 C.
0182 C. TI¥³¥¤¥ó¥É¤¤ð¤Đİ¿(UT)
0183 +. TI 2012-12-20 10:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-12-20 10:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-12-20 10:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

0194	C.			
0195	+. TI	2012-12-20 10:55:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	☐☐[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê¼☐İÄê%İİŇ☐İ¥Á¥\$¥Ä¥¹äİÜ		
0200	C.	☐☐[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	☐☐[HK1_TI_CMD_NUM]	EQ	4
0202	C.	☐☐[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	☐☐[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİ°è¥Ä¥0¥×		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;S 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	☐☐[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	☐☐[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	☐☐[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	☐☐[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	☐☐[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	☐☐[HK1_PKT_FORM_NO]	EQ	7
0220	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Ä¥0¥×¼½İ»☐0³İÇ\$		
0226	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL☐İ¥Ê¹Ç•è²İOK☐0³İÇ\$		
0229	C.			
0230	C.	DHU¥âİ¼¥ÉİÊ¼Ý½, ¥İİ¼¥ÈİÊ☐0İä☐¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	☐☐[HK1_PKT_FORM_NO]	EQ	2
0234	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2012-12-20 10:55:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2012-12-20 10:55:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2012-12-20 10:55:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2012-12-20 10:55:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP ´ÜÄİ☐İ»0¼Ý☐ĒÄĐ☐¹☐ēDCBC•×²è *****		
0276	C.	(¼Ä°İ¥0¥Ä¥Ē¥Đ¥Ē¥Ç¥ē☐Ē¼☐☐¼Ä»Ü☐¹☐ē)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥Đ¥¹•İ Daily±çİŇ☐Ē´Đ☐¹☐ēDCBC•×²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	İäLOS¥Ä¥\$¥Ä¥¼Ä»Üİä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			

```

main-344 2012-12-20 12:26:28 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥$¥Ã¥¬¼Á»Üjã
0005 C.
0006 C. ¥Ã¥ßj¼¥³¥p¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀījĒ□¿Á□•μ°Æ»Ī×ÁÇ□Ī¥Ç¥Ã¥×¥īj¼¥ĒjĒĒ½μ•īĒjĒ□Ē¼°Ç□□•□¿¼ī¹Ç□ījÇÀ®, ū□¹□Ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 30s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 C.
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-12-20 10:55:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ŪĀĪ□Ī»Ō¼Ÿ□ĒĀ□¹□ĒDCBC•×²Ē *****
0069 C. (¼Ā°ĪŸŌ¼Ÿ¥ĒĒp¥Ē¥Ē¥Ē¥ĒĒĒ¼¼□□¼A»Ū□¹□Ē)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** ¥Đ¥¹•Ī Daily±¿ĪŃ□Ē´Ō□¹□ĒDCBC•×²Ē *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. jãLOS¥Á¥$¥Ã¥¬¼Á»Üjã
0080 C.
0081 . C. ***** LOS *****
0082 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-345 2012-12-20 12:26:28 128 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĒ¿¿Aa•µ°£»İ×AÇaİ¥C¥A¥×¥i¼¥ÉjĒĒµ•iĒĒjĒĒ°ÇÖa•a¿i¹ÇaİjÇÀ®, ũa¹aĒaPaÇÁ÷¿®a•aĒaPa³aĒj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2012-12-20 10:55:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0f 80 80 06 06)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 10 80 80 08 08)
0086 +. DC 07-F0 MDP_XRT_FLD_ENA
0087 BC (d8)
0088 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0089 BC (c8)
0090 +. DC 07-F0 MDP_XRT_AEC_RESET
0091 BC (d0)
0092 +. DC 07-F0 MDP_XRT_ARS_DIS
0093 BC (d5)
0094 +. DC 07-F0 MDP_XRT_FLD_RESET
0095 BC (da)
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00096 +. DC 07-F0 MDP_XRT_QT_PROG_SET
00097 BC (c4 05)
00098 +. DC 07-F0 MDP_XRT_FL_PROG_SET
00099 BC (c5 10)
0100 . C. ----- Success Verify ? OK / NG ____
0101 C.
0102 C.
0103 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0104 C.
0105 +. DC 07-F0 MDP_XRT_MODE_OBSV
0106 BC (c2)
0107 +. TI 2012-12-20 10:55:02.0
0108 DC 07-F0 MDP_XRT_MODE_OBSV
0109 BC (c2)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C. ***** XRT END *****
0113 C.
0114 . C. ***** MDP `0ÃïaÎ»0%YpÊÄð1aèDCBC•x²è *****
0115 C. (%ã°i¥0¥Ã¥Ê¥p¥Ê¥ã¥ç¥è¥½¥q¥ã¥Ü01aè)
0116 . S. DC-BC dcbc-402:DCBC
0117 (MDP_known_event)
0118 C.
0119 C.
0120 . C. ***** ¥Ð¥¹•İ Daily±¿İÑaĖ'001aèDCBC•x²è *****
0121 . S. DC-BC dcbc-153:DCBC
0122 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123 C.
0124 C.
0125 . C. jãLOS¥Ã¥S¥Ã¥¼Ã¥Üjã
0126 C.
0127 . C. ***** LOS *****
0128 C.

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Dec 20, 12 12:27

XRT_OGLIST_0135.chk

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*** OP Sequence for XRT ***

2012/12/20	11:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/12/20	11:06:00.0	AOCS_ORe-point_Start_1_OG [0x097]	
		AOCU_NM	5 02-76 00 00 cc 54 00
2012/12/20	11:16:00.0	AOCS_ORe-point_Start_2_OG [0x098]	
		AOCU_NM	5 02-76 00 00 00 45 0e
2012/12/20	11:26:00.0	AOCS_ORe-point_Start_3_OG [0x099]	
		AOCU_NM	5 02-76 00 ff 8e 34 e5
2012/12/20	11:36:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	
		AOCU_NM	5 02-76 00 ff 4c 24 41
2012/12/20	11:46:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	
		AOCU_NM	5 02-76 00 ff 1b 13 64
2012/12/20	11:56:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	
		AOCU_NM	5 02-76 00 ff 03 02 24
2012/12/20	12:06:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	
		AOCU_NM	5 02-76 00 ff 03 00 72
2012/12/20	12:16:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	
		AOCU_NM	5 02-76 00 ff 1b ef 4c
2012/12/20	12:26:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	
		AOCU_NM	5 02-76 00 ff 4c de 67
2012/12/20	12:36:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	
		AOCU_NM	5 02-76 00 ff 8e cd cb
2012/12/20	12:46:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	
		AOCU_NM	5 02-76 00 00 00 bd 9a
2012/12/20	12:56:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	
		AOCU_NM	5 02-76 00 00 cc ad dc
2012/12/20	13:06:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	
		AOCU_NM	5 02-76 00 ea 67 52 0c
2012/12/20	13:16:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	
		AOCU_NM	5 02-76 00 e9 9a 42 35
2012/12/20	13:26:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	
		AOCU_NM	5 02-76 00 e9 27 31 f3
2012/12/20	13:36:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	
		AOCU_NM	5 02-76 00 e8 e6 21 68
2012/12/20	13:46:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	
		AOCU_NM	5 02-76 00 e8 cd 10 72
2012/12/20	13:56:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	
		AOCU_NM	5 02-76 00 e8 b5 ff 34
2012/12/20	14:06:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]	
		AOCU_NM	5 02-76 00 e8 cd f2 36
2012/12/20	14:31:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]	
		AOCU_NM	5 02-76 00 e8 e6 e1 40
2012/12/20	14:41:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	
		AOCU_NM	5 02-76 00 e9 27 d0 b5
2012/12/20	14:51:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	
		AOCU_NM	5 02-76 00 e9 9a c0 73
2012/12/20	15:01:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	
		AOCU_NM	5 02-76 00 ea 67 b0 9c
2012/12/20	15:10:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/12/20	15:11:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	
		AOCU_NM	5 02-76 01 00 00 00 00
2012/12/20	15:13:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	
		XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2012/12/20	15:13:46.0	XRT_FLD_ENA_428_OG [0x1ac]	
		MDP_XRT_FLD_ENA	1 07-F0 d8
2012/12/20	15:13:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	
		MDP_XRT_FLRCTRL_ENA	1 07-F0 c8
2012/12/20	15:13:50.5	XRT_AEC_RESET_423_OG [0x1a7]	
		MDP_XRT_AEC_RESET	1 07-F0 d0
2012/12/20	15:13:52.5	XRT_ARS_DIS_438_OG [0x1b6]	
		MDP_XRT_ARS_DIS	1 07-F0 d5
2012/12/20	15:13:54.5	XRT_FLD_RESET_424_OG [0x1a8]	
		MDP_XRT_FLD_RESET	1 07-F0 da
2012/12/20	15:13:56.5	XRT_QT_PROG_SET_417_OG [0x1a1]	
		MDP_XRT_QT_PROG_SET	2 07-F0 c4 05
2012/12/20	15:13:58.5	XRT_FL_PROG_SET_444_OG [0x1bc]	
		MDP_XRT_FL_PROG_SET	2 07-F0 c5 10
2012/12/20	15:14:00.5	XRT_CTRL_AUTO_408_OG [0x198]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2012/12/20	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/12/20	15:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]	
		MDP_XRT_FLD_RESET	1 07-F0 da
2012/12/20	15:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	
		MDP_XRT_PREFLR_STRT	1 07-F0 e8
2012/12/20	15:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	
		MDP_XRT_PREFLR_STOP	1 07-F0 e9
2012/12/20	16:19:00.0	XRT_Custom_434_OG [0x1b2]	
2012/12/20	16:20:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2012/12/20	17:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2012/12/20	17:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]	
		MDP_XRT_FLD_RESET	1 07-F0 da
2012/12/20	17:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	
		MDP_XRT_PREFLR_STRT	1 07-F0 e8
2012/12/20	17:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	
		MDP_XRT_PREFLR_STOP	1 07-F0 e9
2012/12/20	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1

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2012/12/20	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2012/12/20	17:58:00.0	AOCs_Or-point_Start_25_OG [0x0af]					
		AOCU_NM	5	02-76	00	00	00 00 00
2012/12/20	17:58:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/12/20	17:58:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/12/20	17:58:20.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/12/20	18:00:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2012/12/20	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/20	18:07:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/20	18:08:00.0	AOCs_Or-point_Start_24_OG [0x0ae]					
		AOCU_NM	5	02-76	01	00	00 00 00
2012/12/20	18:10:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2012/12/20	18:10:46.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/12/20	18:10:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/12/20	18:10:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/12/20	18:10:52.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/12/20	18:10:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/20	18:10:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2012/12/20	18:10:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2012/12/20	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/20	19:09:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/20	19:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/20	19:09:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/20	19:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/20	19:32:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/20	19:33:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/20	20:47:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/20	20:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/20	20:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/20	20:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/20	21:10:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/20	21:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/20	22:24:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/20	22:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/20	22:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/20	22:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/20	22:45:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/20	22:46:30.5	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/21	00:01:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/21	00:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/21	00:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/21	00:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/21	00:10:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/21	00:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/21	01:26:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/21	01:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/21	01:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/21	01:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/21	01:44:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/21	01:45:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/21	03:00:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2012/12/21	03:00:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/21	03:00:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/21	03:03:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/21	03:21:30.0	XRT_Custom_434_0G [0x1b2]				
2012/12/21	03:22:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/21	03:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/21	03:59:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/21	04:00:00.0	AOCS_ORe-point_Start_25_0G [0x0af]	AOCU_NM	5	02-76	00 00 00 00 00
2012/12/21	04:00:16.0	XRT_FLD_DIS_404_0G [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/21	04:00:18.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/21	04:00:20.0	XRT_ARS_DIS_406_0G [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/21	04:02:58.0	XRT_QT_PROG_SET_419_0G [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/21	04:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/21	04:09:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/21	04:10:00.0	AOCS_ORe-point_Start_24_0G [0x0ae]	AOCU_NM	5	02-76	01 00 00 00 00
2012/12/21	04:12:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/21	04:12:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/21	04:12:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/21	04:12:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/21	04:12:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/21	04:12:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/21	04:12:56.0	XRT_QT_PROG_SET_417_0G [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/21	04:12:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/21	04:13:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/21	04:30:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/21	04:30:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/21	04:30:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/21	04:33:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/21	04:59:00.0	XRT_Custom_434_0G [0x1b2]				
2012/12/21	05:00:00.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/21	05:59:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/21	06:00:00.0	AOCS_ORe-point_Start_24_0G [0x0ae]	AOCU_NM	5	02-76	01 00 00 00 00
2012/12/21	06:02:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/21	06:02:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/21	06:02:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/21	06:02:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/21	06:02:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/21	06:02:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/21	06:02:56.0	XRT_QT_PROG_SET_417_0G [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/21	06:02:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/21	06:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/21	06:11:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/21	06:11:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/21	06:11:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/21	06:14:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/21	06:36:30.0	XRT_Custom_434_0G [0x1b2]				
2012/12/21	06:37:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/21	07:51:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2012/12/21	07:51:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/21	07:51:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/21	07:54:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/21	08:13:30.0	XRT_Custom_434_0G [0x1b2]					
2012/12/21	08:14:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/21	09:31:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	09:31:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/21	09:31:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/21	09:34:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/21	09:48:00.0	XRT_Custom_434_0G [0x1b2]					
2012/12/21	09:49:00.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/21	11:09:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	11:10:00.0	AOCS_ORe-point_Start_26_0G [0x0b0]	AOCU_NM	5	02-76	00 22 d9 cc 00	
2012/12/21	11:20:00.0	AOCS_ORe-point_Start_27_0G [0x0b1]	AOCU_NM	5	02-76	00 22 d9 c0 e6	
2012/12/21	11:45:00.0	AOCS_ORe-point_Start_28_0G [0x0b2]	AOCU_NM	5	02-76	00 24 31 b5 cb	
2012/12/21	13:29:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	13:30:00.0	AOCS_ORe-point_Start_24_0G [0x0ae]	AOCU_NM	5	02-76	01 00 00 00 00	
2012/12/21	13:32:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/12/21	13:32:46.0	XRT_FLD_ENA_428_0G [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/12/21	13:32:48.0	XRT_FLRCTRL_ENA_429_0G [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/12/21	13:32:50.0	XRT_AEC_RESET_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/12/21	13:32:52.0	XRT_ARS_DIS_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/21	13:32:54.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/21	13:32:56.0	XRT_QT_PROG_SET_417_0G [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/12/21	13:32:58.0	XRT_FL_PROG_SET_444_0G [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2012/12/21	13:33:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/21	14:56:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	14:56:32.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/21	14:56:34.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/21	14:59:44.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/21	15:09:30.0	XRT_Custom_434_0G [0x1b2]					
2012/12/21	15:10:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/21	16:32:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	16:32:02.0	XRT_FLD_RESET_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/12/21	16:32:04.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/12/21	16:35:14.0	XRT_PREFLR_STOP_433_0G [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/12/21	16:55:30.0	XRT_Custom_434_0G [0x1b2]					
2012/12/21	16:56:30.0	XRT_CTRL_AUTO_413_0G [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/21	17:45:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	17:45:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/12/21	17:46:00.0	AOCS_ORe-point_Start_25_0G [0x0af]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/12/21	17:46:16.0	XRT_FLD_DIS_404_0G [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/12/21	17:46:18.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/12/21	17:46:20.0	XRT_ARS_DIS_406_0G [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/12/21	17:48:58.0	XRT_QT_PROG_SET_419_0G [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/12/21	17:49:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/12/21	17:55:54.0	XRT_CTRL_MANU_447_0G [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/12/21	17:56:00.0	AOCS_ORe-point_Start_24_0G [0x0ae]	AOCU_NM	5	02-76	01 00 00 00 00	
2012/12/21	17:58:26.0	XRT_FOCUS_POSITION_420_0G [0x1a4]					

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2012/12/21	17:58:46.0	XRT_FLD_ENA_428_OG [0x1ac]	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/12/21	17:58:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/12/21	17:58:50.0	XRT_AEC_RESET_423_OG [0x1a7]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/12/21	17:58:52.0	XRT_ARS_DIS_438_OG [0x1b6]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/12/21	17:58:54.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/21	17:58:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	2	07-F0	c4	05		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2012/12/21	17:58:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	2	07-F0	c5	10		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2012/12/21	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/21	18:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/21	18:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/21	18:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/21	18:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/21	18:32:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/21	18:33:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/21	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/21	19:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/21	19:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/21	19:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/21	20:09:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/21	20:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/21	21:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/21	21:23:32.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/21	21:23:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/21	21:26:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/21	21:46:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/21	21:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/21	23:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/21	23:01:02.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/21	23:01:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/21	23:04:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/21	23:18:30.0	XRT_Custom_434_OG [0x1b2]						
2012/12/21	23:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/22	00:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/22	00:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/22	00:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/22	00:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/22	00:45:00.0	XRT_Custom_434_OG [0x1b2]						
2012/12/22	00:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/22	02:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/22	02:01:02.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/22	02:01:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/22	02:04:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/22	02:21:00.0	XRT_Custom_434_OG [0x1b2]						
2012/12/22	02:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/12/22	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/12/22	03:31:32.0	XRT_FLD_RESET_424_OG [0x1a8]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/12/22	03:31:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/12/22	03:34:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/12/22	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2012/12/22	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/12/22	04:00:00.0	AOCs_Or-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/12/22	04:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/12/22	04:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/12/22	04:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/22	04:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2012/12/22	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/22	04:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/22	04:10:00.0	AOCs_Or-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/12/22	04:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/12/22	04:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/22	04:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/22	04:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/22	04:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/22	04:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/22	04:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2012/12/22	04:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/12/22	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/22	05:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/22	05:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/22	05:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/22	05:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/22	05:35:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/22	05:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/22	05:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/22	06:00:00.0	AOCs_Or-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/12/22	06:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/12/22	06:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/12/22	06:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/12/22	06:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/12/22	06:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/12/22	06:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/22	06:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2012/12/22	06:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/12/22	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/22	06:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/22	06:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/22	06:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/22	06:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/22	07:13:00.0	XRT_Custom_434_OG [0x1b2]							
2012/12/22	07:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/22	08:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/12/22	08:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/12/22	08:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/12/22	08:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/12/22	08:49:30.0	XRT_Custom_434_OG [0x1b2]							
2012/12/22	08:50:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/12/22	09:16:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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XRT_OGLIST_0135.chk

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2012/12/22 10:43:00.0 A0CS_0Re-point_Start_25_0G [0x0af]
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