

XRT Timeline to be uploaded on 2012/12/29

Period: 2012/12/29 09:25:00 - 2013/01/05 11:01:00

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Normal mode

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XOB #1957: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
12/29 09:50:36 - 12/29 17:47:54	Track (844.1, 188.6) @ 12/29 09:35:00	# OP start + 10min, AR11635
12/29 18:01:00 - 12/30 05:59:54	Track (878.5, 185.5) @ 12/29 17:58:00	AR11635
12/30 06:13:00 - 12/30 18:05:54	Fixed (925.2, 180.5)	AR11635 with fixed pointing
12/30 18:19:00 - 12/31 06:24:24	Fixed (925.2, 180.5)	AR11635
12/31 06:37:30 - 12/31 17:47:30	Fixed (925.2, 180.5)	AR11635
12/31 18:24:00 - 01/01 05:58:24	Track (205.6, 254.9) @ 12/31 18:21:00	AR11636 & 11637

PROG= 05 Inf-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 18 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Seqn= 19 1-time(s) 2.0sec													
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Seqn= 65 4-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 94 70-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)

Term	Pointing (x, y)	Comment
12/29 17:51:00 - 12/29 17:57:54	Fixed (0.0, 0.0)	synoptic, shifted -12.0 min
12/30 06:03:00 - 12/30 06:09:54	Fixed (0.0, 0.0)	synoptic
12/30 18:09:00 - 12/30 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min
12/31 06:27:30 - 12/31 06:34:24	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min
12/31 18:15:24 - 12/31 18:20:54	Fixed (0.0, 0.0)	synoptic, shifted 11.0 min
01/01 06:01:30 - 01/01 06:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min
01/01 17:46:36 - 01/01 18:00:00	Track (-224.3, -3.4) @ 01/01 06:08:30	HOP217, +synoptic
01/02 06:05:06 - 01/02 06:15:00	Track (-224.3, -3.4) @ 01/01 06:08:30	HOP217, +synoptic
01/02 18:05:30 - 01/02 18:15:00	Track (-224.3, -3.4) @ 01/01 06:08:30	HOP217, +synoptic
01/03 05:37:30 - 01/03 07:13:00	Fixed (0.0, 0.0)	synoptic, shifted -25.5 min

PROG= 13 1-time(s)													
Subr= 1 1-time(s) 14.0sec													
Seqn= 64 1-time(s) 4.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 6 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 70 1-time(s) 4.0sec													
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 67 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 69 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 68 1-time(s) 2.0sec													
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #195B: AR Band during disk center pointing HOP217, 1024x1024 at 1024 1024, 5m-cad Al-poly w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
01/01 06:11:30 - 01/01 17:43:30	Track (-224.3, -3.4) @ 01/01 06:08:30	HOP217, +synoptic
01/01 18:03:06 - 01/02 06:02:00	Track (-224.3, -3.4) @ 01/01 06:08:30	HOP217, +synoptic
01/02 06:18:06 - 01/02 18:05:00	Track (-224.3, -3.4) @ 01/01 06:08:30	HOP217, +synoptic

PROG= 14 Inf.-time(s)												
Subr= 1		1-time(s)	2.0sec									
Seqn= 66		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	1024x1024 (1024, 1024)	DPCM	0	0 2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 95		12-time(s)	300.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	1024x1024 (1024, 1024)	Q=95	3	0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	1024x1024 (1024, 1024)	Q=95	2	0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1958: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33) - 1 loop

Term			Pointing (x, y)					Comment							
01/03 07:16:06 - 01/03 08:30:00			Fixed (0.0, 0.0)					SOT flat field							
PROG= 20 1-time(s)															
Subr= 1		1-time(s)		85.0sec											
Seqn= 82		1-time(s)		7.0sec											
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 6		1-time(s)		22.0sec											
└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 76		1-time(s)		8.0sec											
└ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 77		1-time(s)		12.0sec											
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 79		1-time(s)		10.0sec											
└ C-poly/Open		C-poly/Open		close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ C-poly/Open		C-poly/Open		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 75		1-time(s)		10.0sec											
└ Al-poly/Ti-poly		Al-poly/thick-Al		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 78		1-time(s)		15.0sec											
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Subr= 2		1-time(s)		150.0sec											
Seqn= 34		1-time(s)		29.0sec											
└ med-Al/Open		med-Al/Open		close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 83		1-time(s)		84.0sec											
└ Open/thick-Be		Open/thick-Be		close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 69		2-time(s)		13.0sec											
└ Open/G-band		Open/G-band		open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 68		1-time(s)		10.0sec											
└ Open/G-band		Open/G-band		close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		DPCM	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term		Pointing (x, y)		Comment								
12/29 09:50:36 - 12/29 17:47:54		Track (844.1, 188.6) @ 12/29 09:35:00		# OP start + 10min, AR11635								
12/29 18:01:00 - 12/30 05:59:54		Track (878.5, 185.5) @ 12/29 17:58:00		AR11635								
12/30 06:13:00 - 12/30 18:05:54		Fixed (925.2, 180.5)		AR11635 with fixed pointing								
12/30 18:19:00 - 12/31 06:24:24		Fixed (925.2, 180.5)		AR11635								
12/31 06:37:30 - 12/31 17:47:30		Fixed (925.2, 180.5)		AR11635								
12/31 18:24:00 - 01/01 05:58:24		Track (205.6, 254.9) @ 12/31 18:21:00		AR11636 & 11637								
01/01 06:11:30 - 01/01 17:43:30		Track (-224.3, -3.4) @ 01/01 06:08:30		HOP217, +synoptic								
01/01 18:03:06 - 01/02 06:02:00		Track (-224.3, -3.4) @ 01/01 06:08:30		HOP217, +synoptic								
01/02 06:18:06 - 01/02 18:05:00		Track (-224.3, -3.4) @ 01/01 06:08:30		HOP217, +synoptic								
01/02 18:18:06 - 01/03 05:34:24		Track (-224.3, -3.4) @ 01/01 06:08:30		HOP217, +synoptic								

PROG= 16 15-time(s)

Subr= 1		45-time(s)	10.0sec									
Seqn= 35		1-time(s)	2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Subr= 2		1-time(s)	10.0sec									
Seqn= 36		1-time(s)	2.0sec									
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Seqn= 37		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0 2.0sec
Seqn= 38		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Active Region Search

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NOT USED

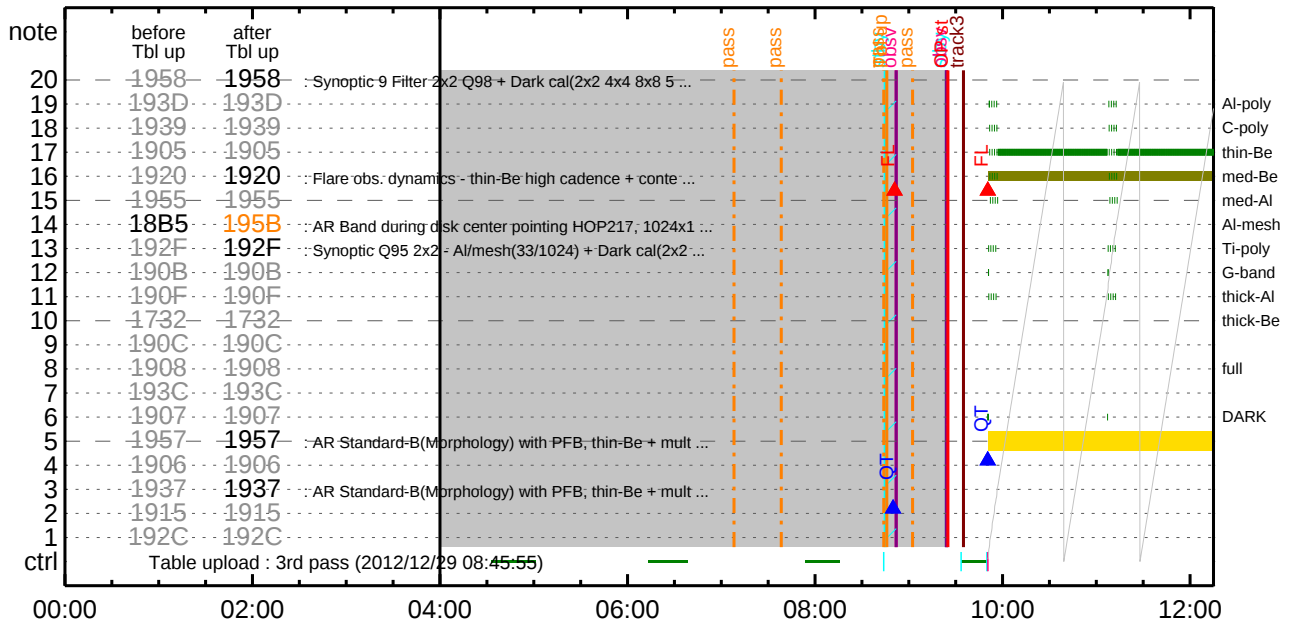
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Flare Detection

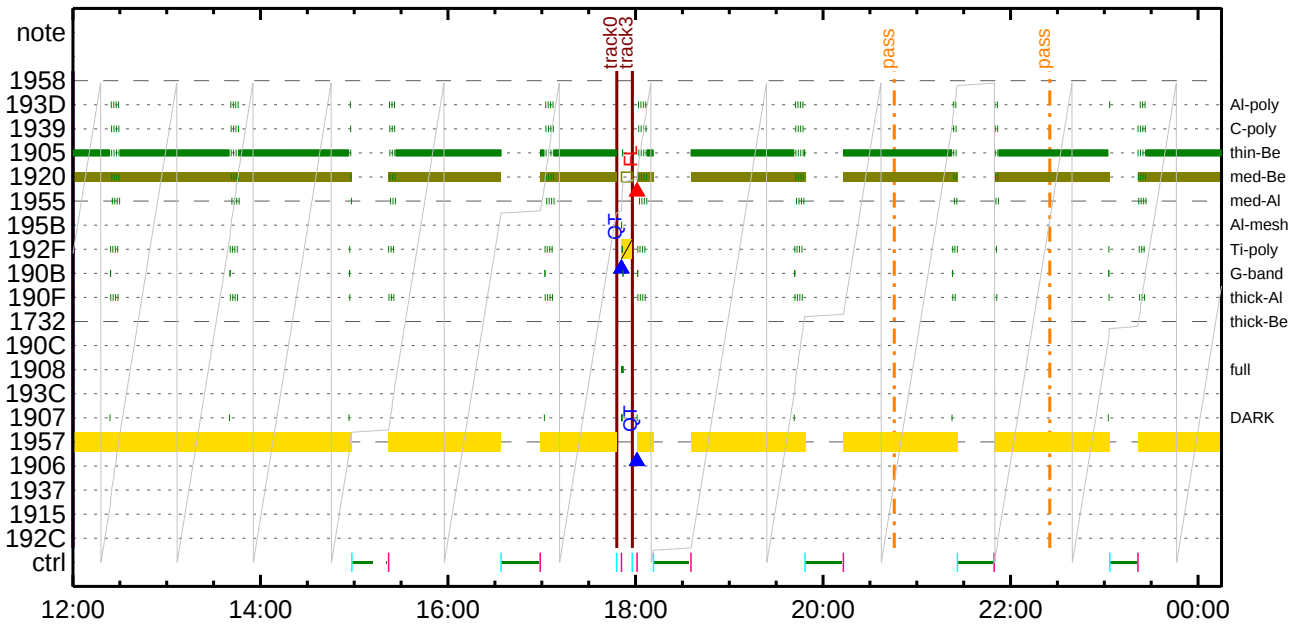
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FLD Patrol											
Term			Pointing (x, y)					Comment			
12/29 18:00:46 - 12/30 06:00:16			Track (878.5, 185.5) @ 12/29 17:58:00					AR11635			
12/30 06:12:46 - 12/30 18:06:16			Fixed (925.2, 180.5)					AR11635 with fixed pointing			
12/30 18:18:46 - 12/31 06:24:46			Fixed (925.2, 180.5)					AR11635			
12/31 06:37:16 - 12/31 18:15:16			Fixed (925.2, 180.5)					AR11635			
12/31 18:23:46 - 01/01 05:58:46			Track (205.6, 254.9) @ 12/31 18:21:00					AR11636 & 11637			
01/01 06:11:16 - 01/01 17:43:52			Track (-224.3, -3.4) @ 01/01 06:08:30					HOP217, +synoptic			
01/01 18:02:52 - 01/02 06:02:22			Track (-224.3, -3.4) @ 01/01 06:08:30					HOP217, +synoptic			
01/02 06:17:52 - 01/02 18:05:22			Track (-224.3, -3.4) @ 01/01 06:08:30					HOP217, +synoptic			
01/02 18:17:52 - 01/03 05:34:46			Track (-224.3, -3.4) @ 01/01 06:08:30					HOP217, +synoptic			
Open/Ti-poly			Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

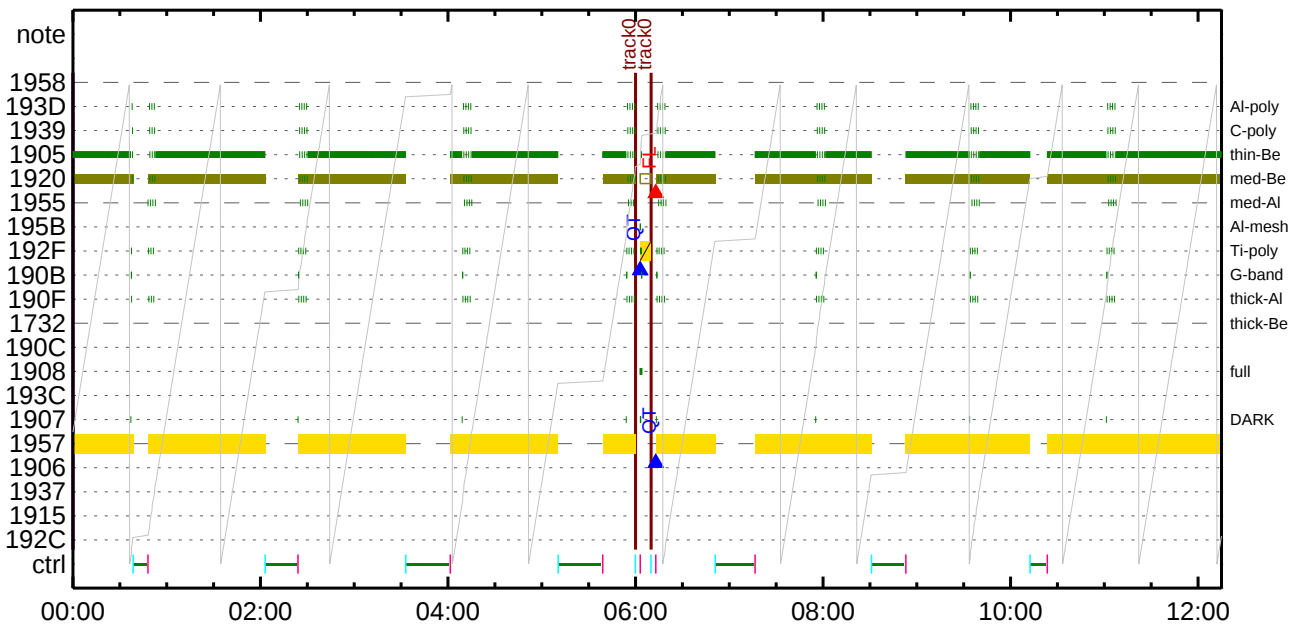
CMDI #0153 2012/12/29



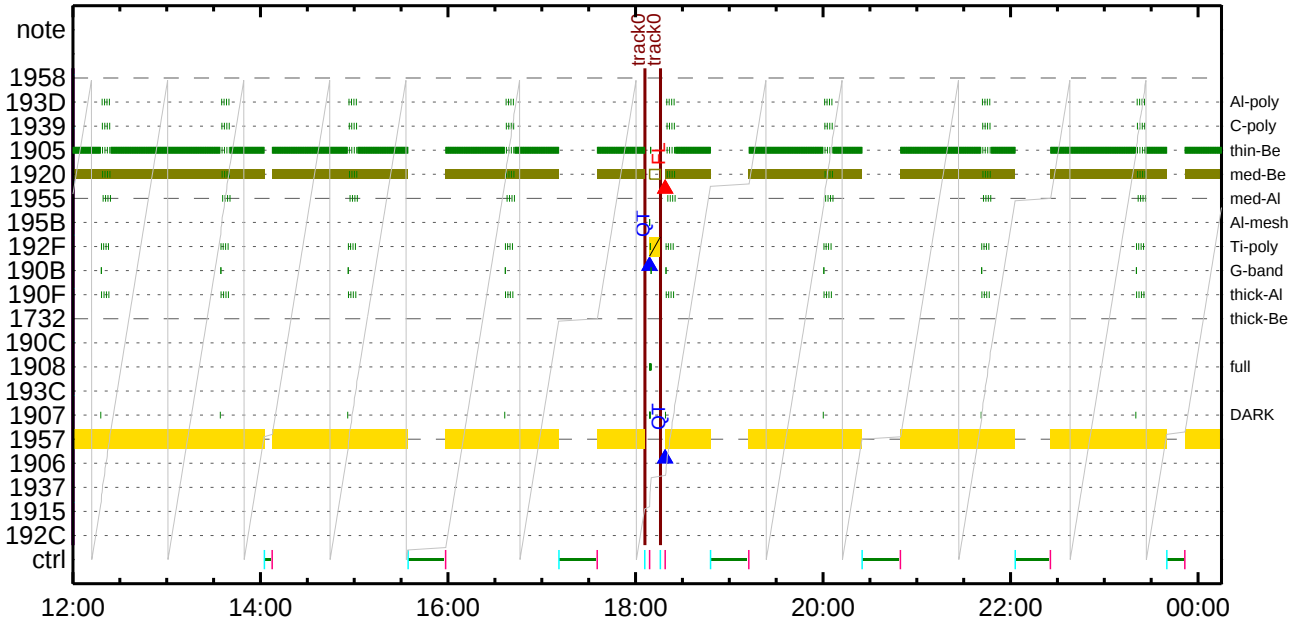
CMDI #0153 2012/12/29



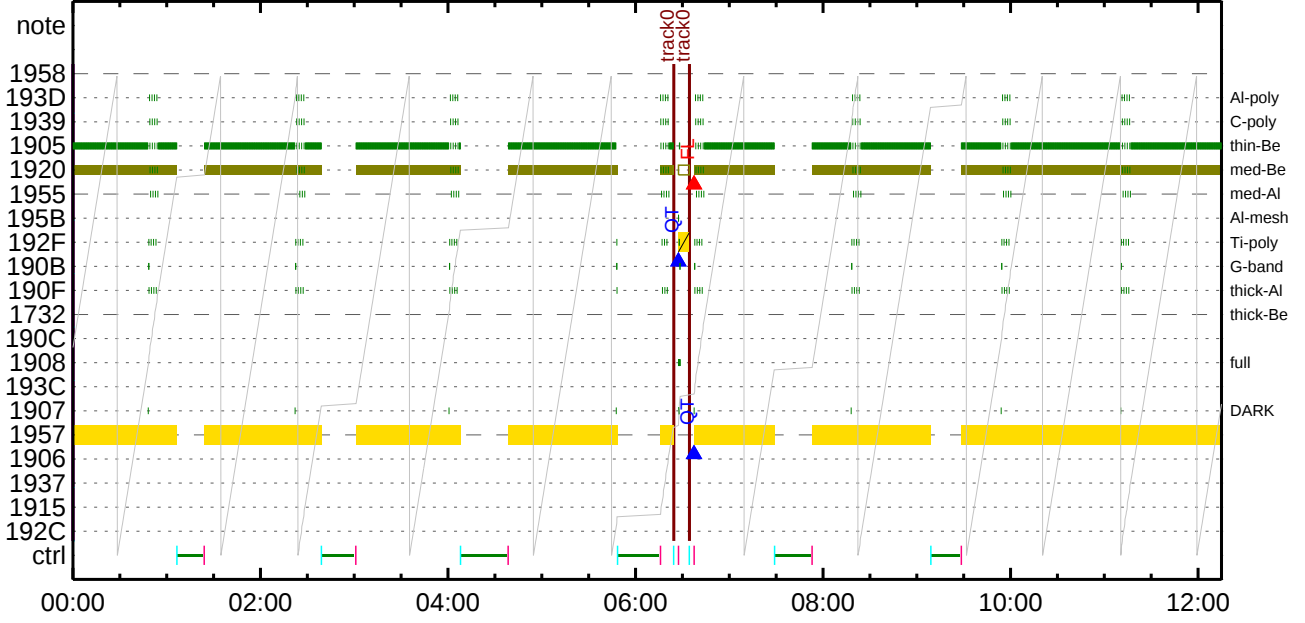
CMDI #0153 2012/12/30



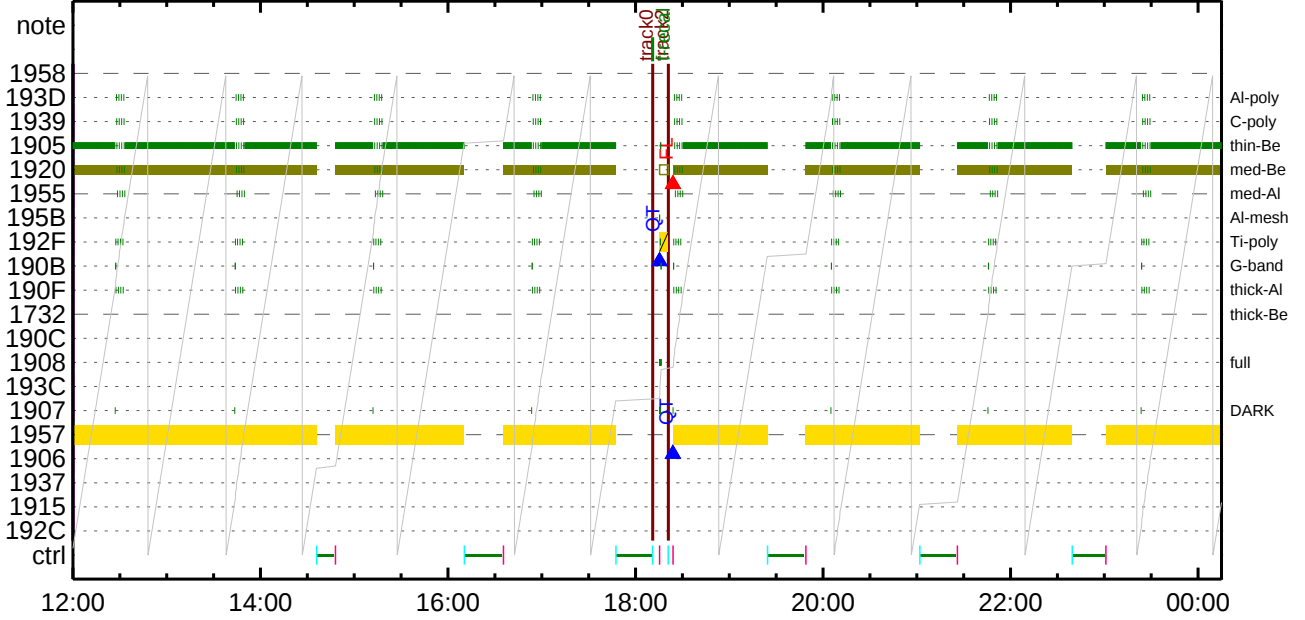
CMDI #0153 2012/12/30



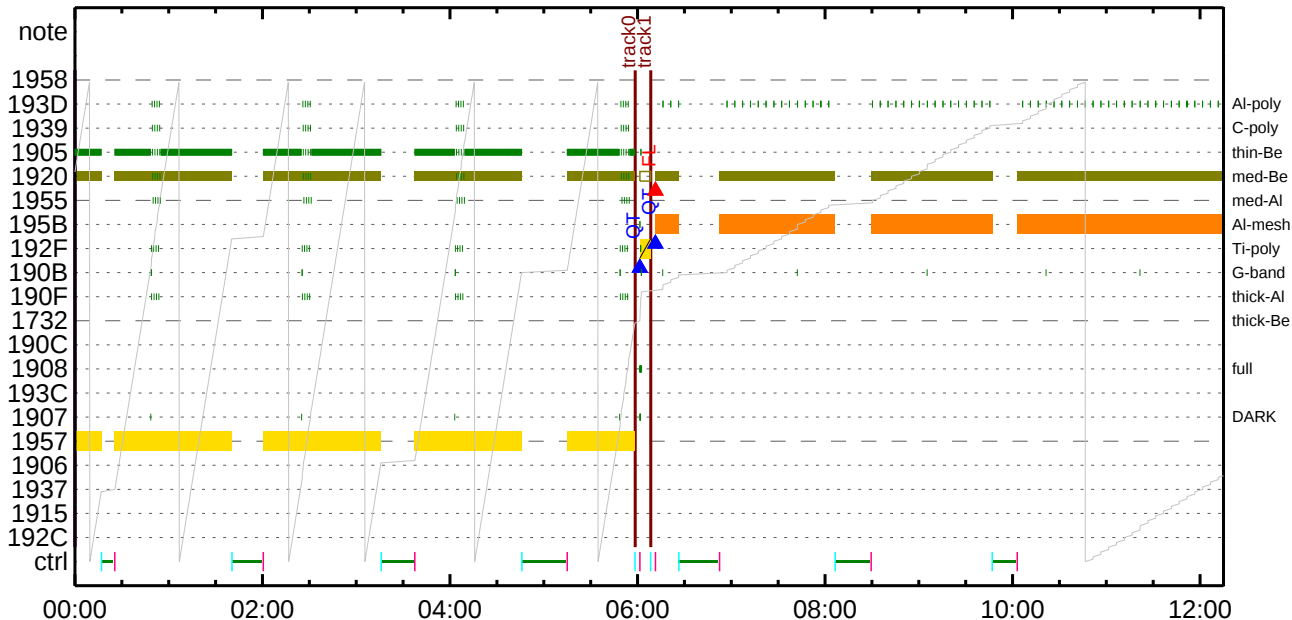
CMDI #0153 2012/12/31



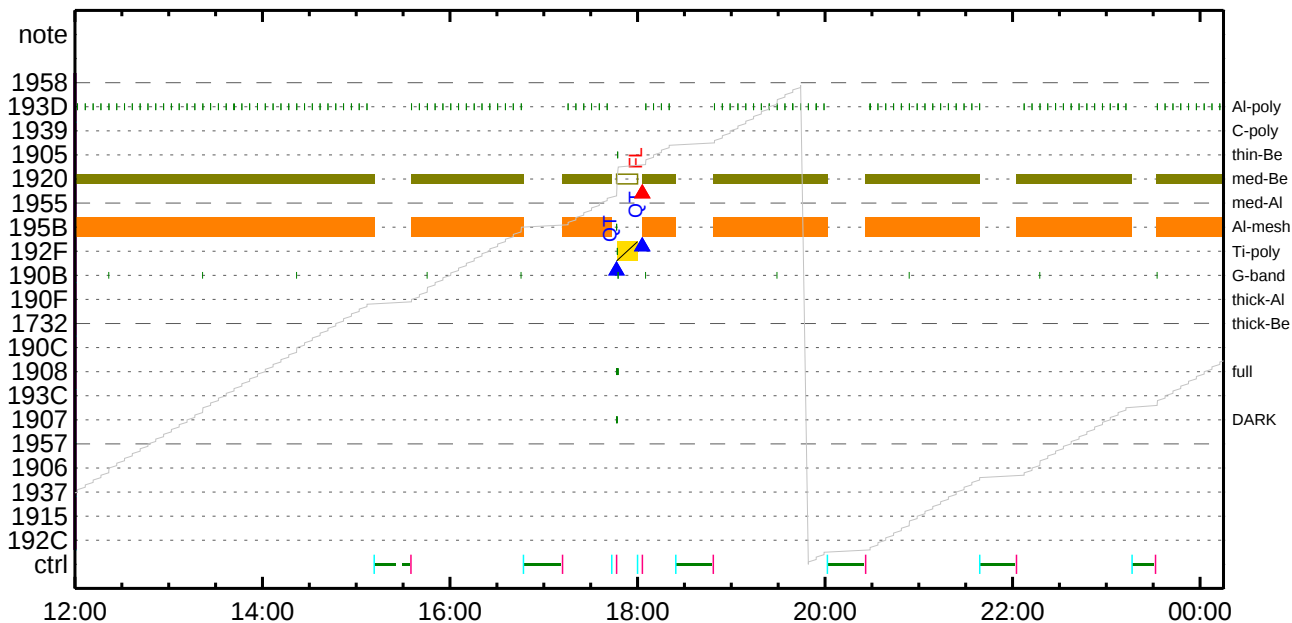
CMDI #0153 2012/12/31



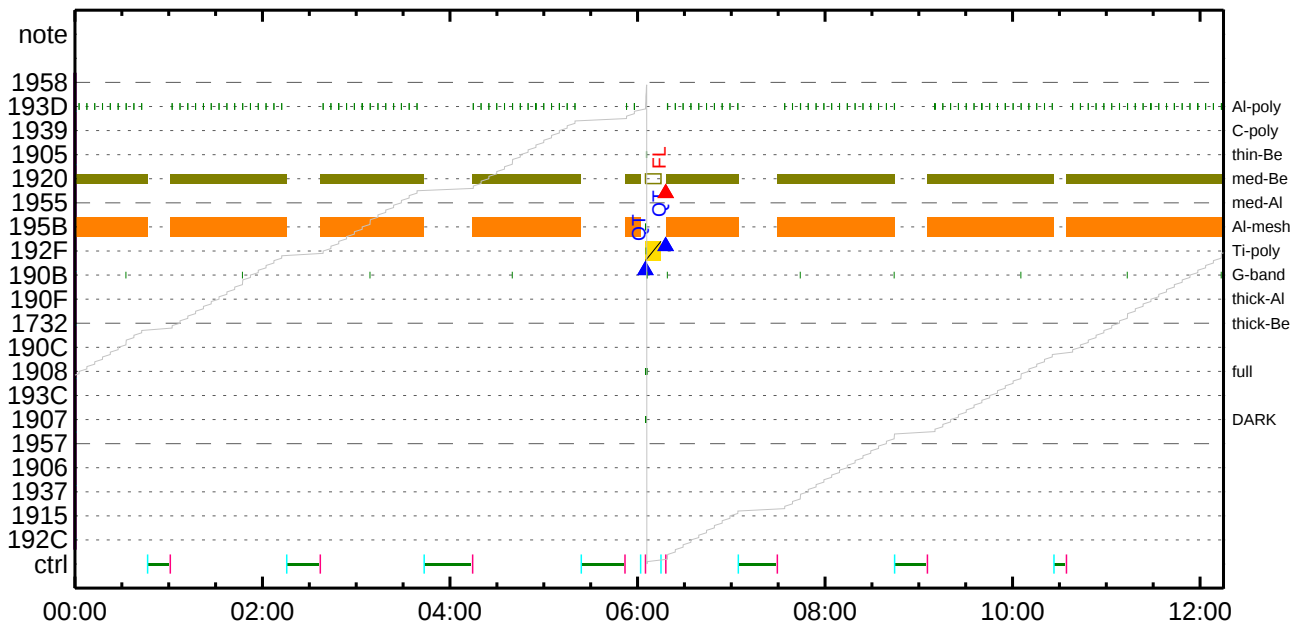
CMDI #0153 2013/01/01



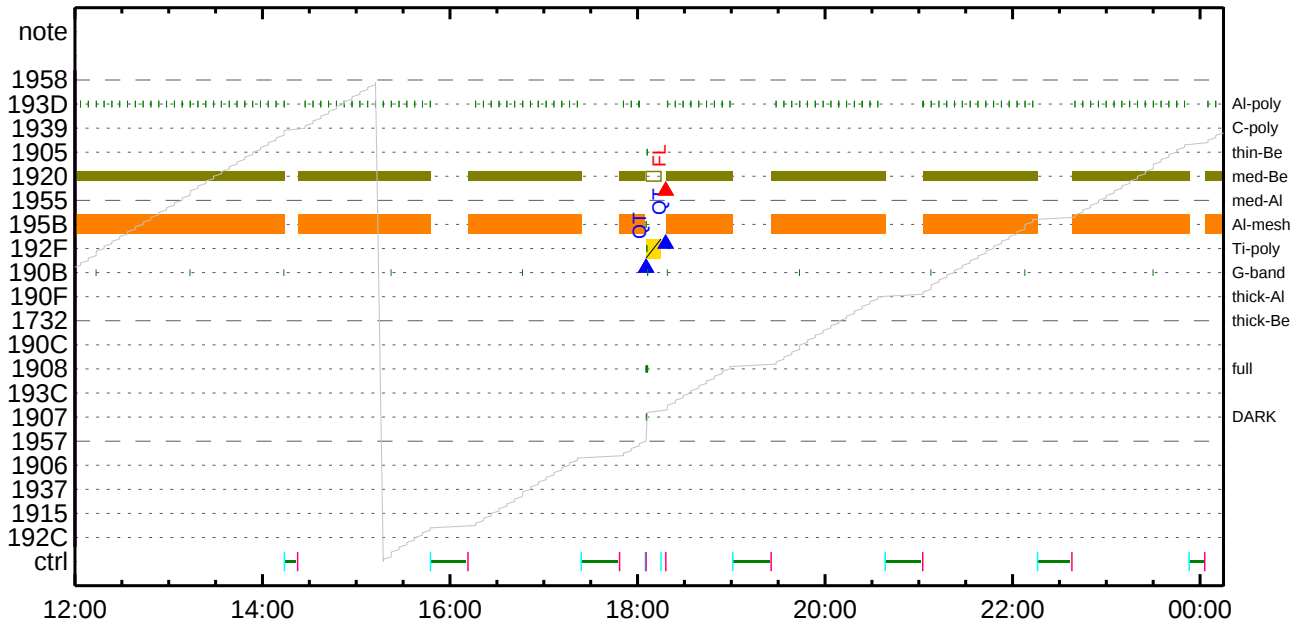
CMDI #0153 2013/01/01



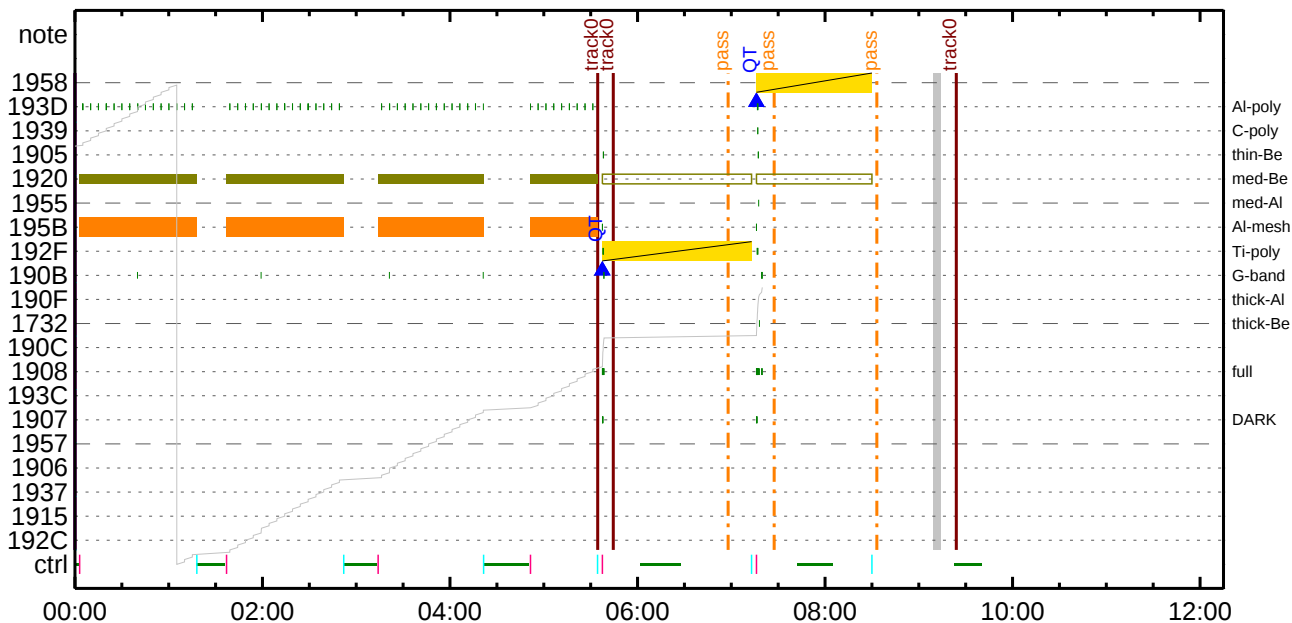
CMDI #0153 2013/01/02



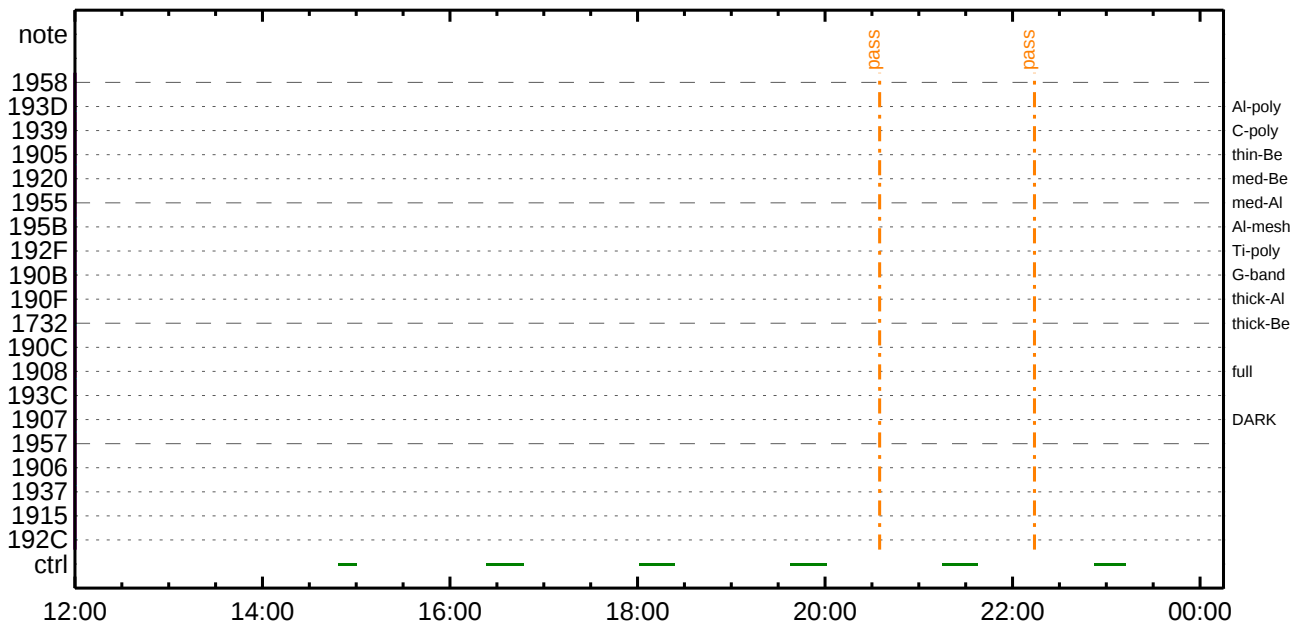
CMDI #0153 2013/01/02



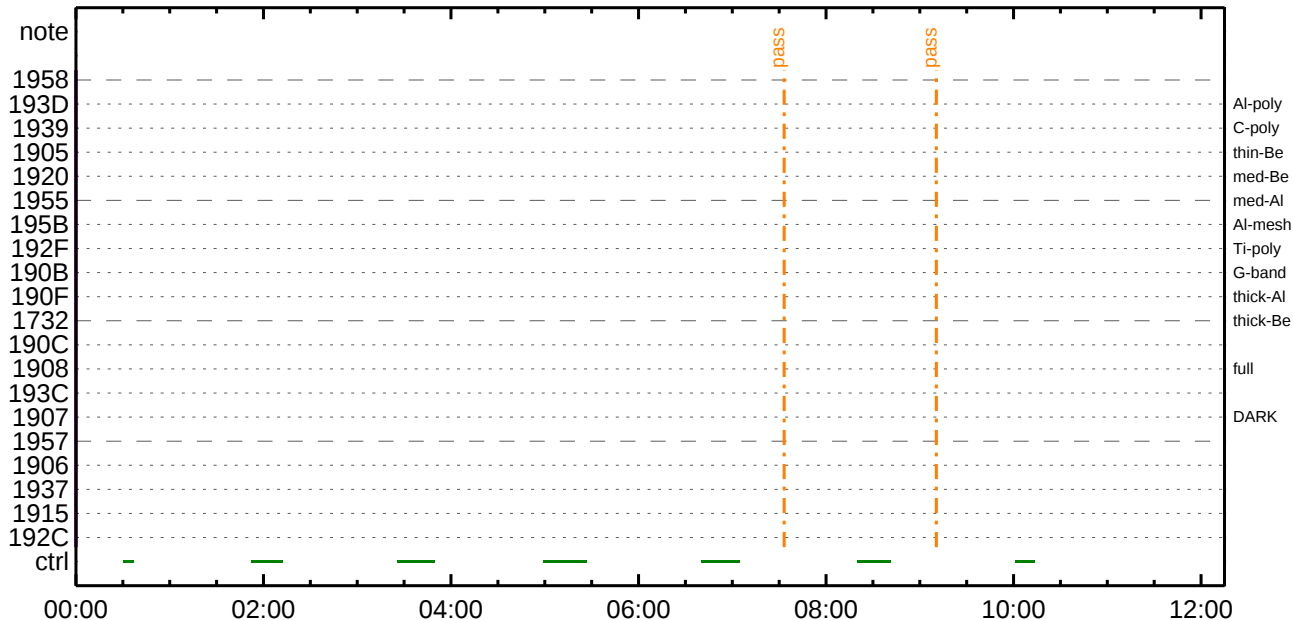
CMDI #0153 2013/01/03



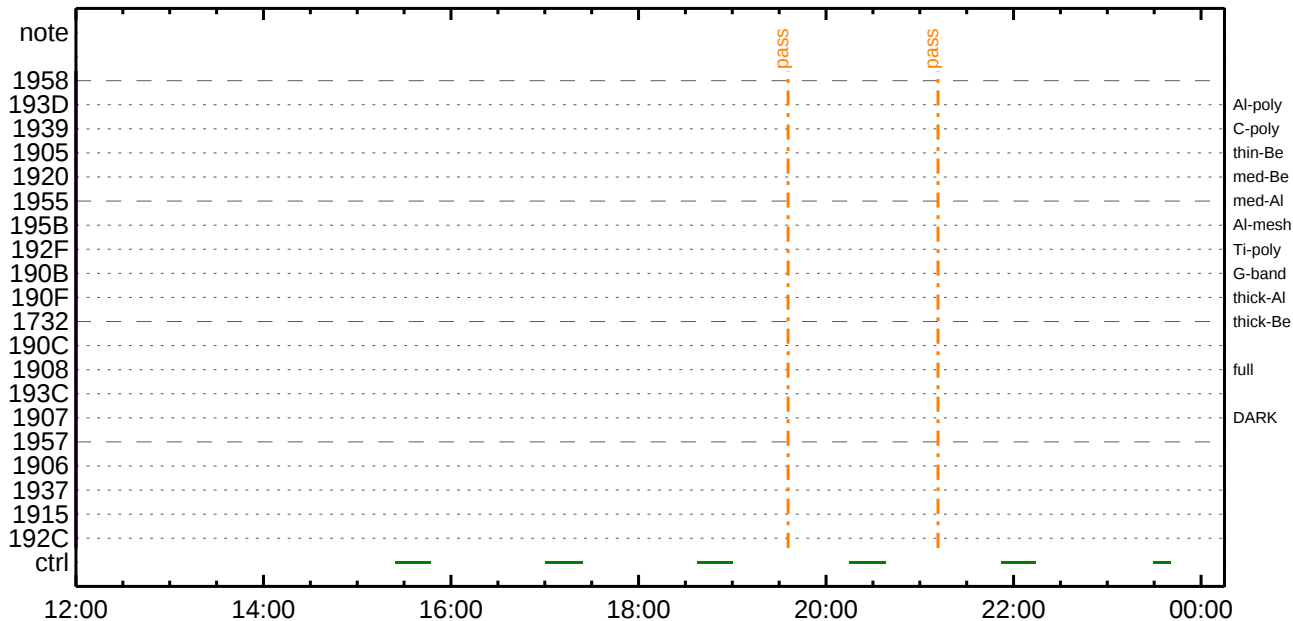
CMDI #0153 2013/01/03



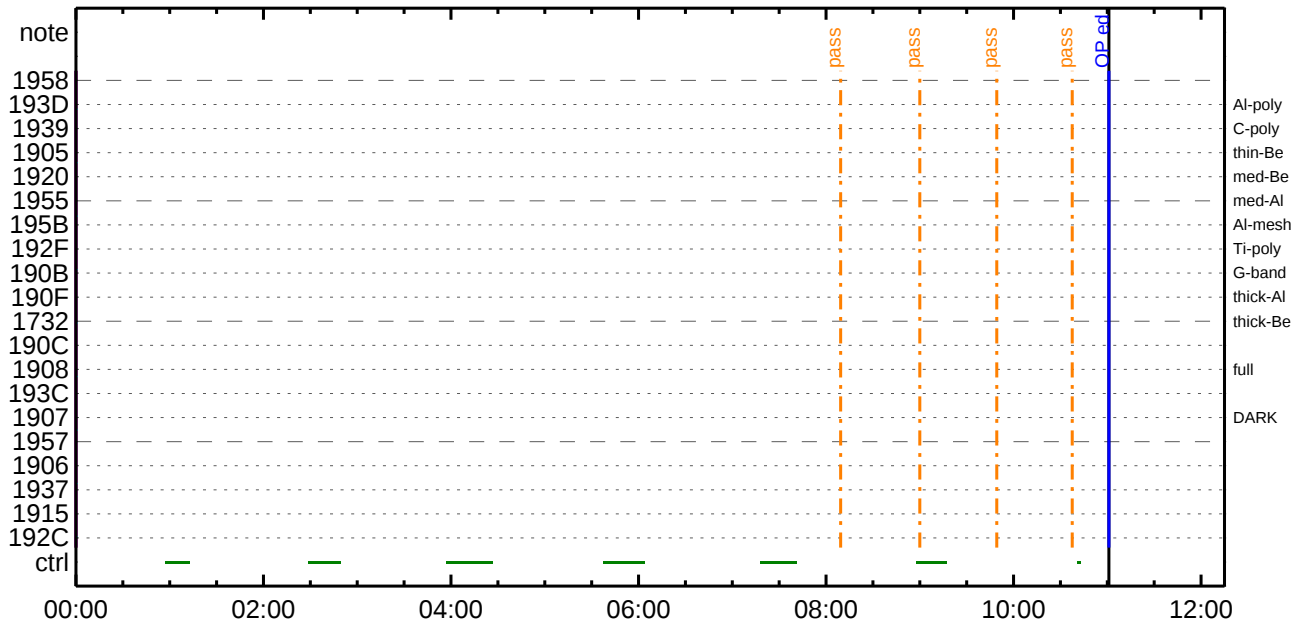
CMDI #0153 2013/01/04



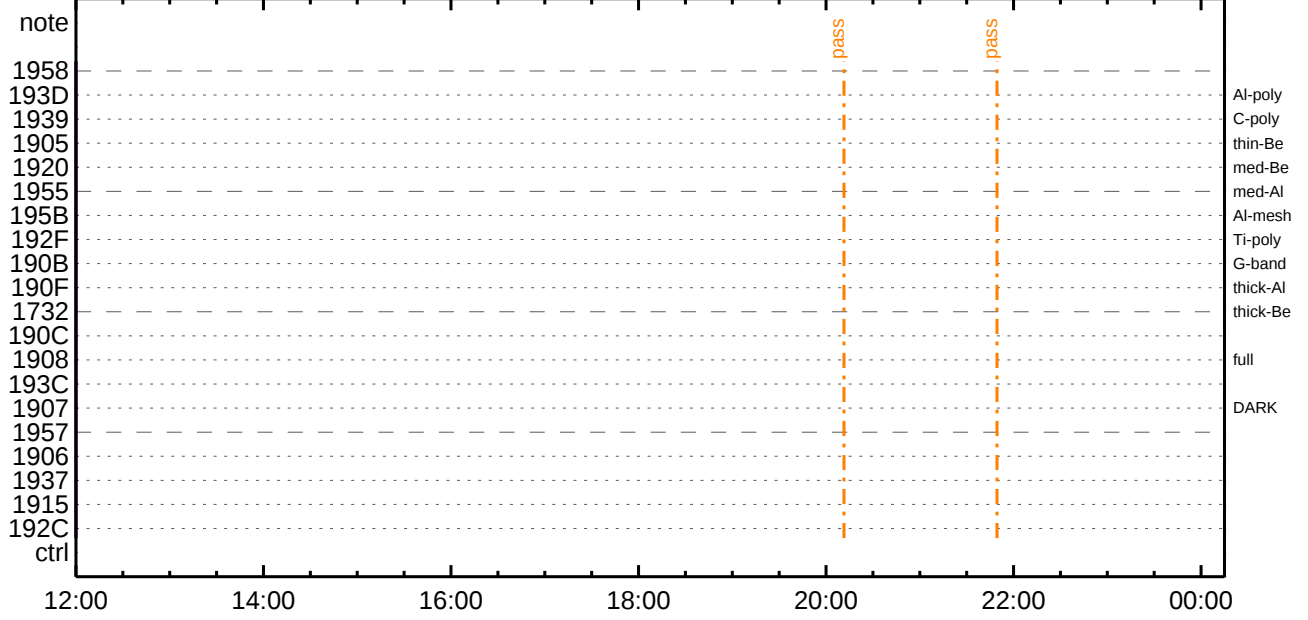
CMDI #0153 2013/01/04



CMDI #0153 2013/01/05



CMDI #0153 2013/01/05



(a) Spacecraft Operation Procedure (real-commands)

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main-362 2012-12-29 13:12:47 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¤İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p`Œ¤¤¹İİ, ¤•jÇĒŒİ×¤ĒXĀŒŒON¤İ¹Œ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŒĀŒ¤-°ĀĀĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ÆĀ, ¼Ē½Ç¤¤Œ¼Ā¹Œ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ā¹Œ¤¤¤¤¤jÇDCBC-150¤Œ¿Ē¤¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Œ, j¼Ū)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ, j¼Ū)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ, j¼Ū)
0045 C.
0046 . C. jã¥Ç¥Ó¥Æ¥ĒĀŪĀŒjĒĀ•Ā²ŒĒŒjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Œ, j¼Ū)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ, j¼Ū)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ, j¼Ū)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤-¼«Æ°ĀĀ»Œ¤•¤¿, ĀjÇ°Ē²¼¤¤Œ¼Ā¹Œ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ó¥Æ¥ĒĀŪĀŒ¤ĀĀ•Ā²ŒĒŒŒŒ¤¤¼¹Ç¤İ °İ»¤¹¤Ē¤¤¤ÇĀŒ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ā¹Œ¤¤¤¤¤jÇDCBC-151¤Œ¿Ē¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Œ, j¼Ū)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ, j¼Ū)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ, j¼Ū)
0071 C.
0072 . C. jã¥Ç¥Ó¥Æ¥ĒĀŪĀŒjĒĀ•Ā²ŒĒŒjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Œ, j¼Ū)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Œ, j¼Ū)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Œ, j¼Ū)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Āã»ŒjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Āã»Œjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

[illegible]

0194	C.			
0195	+. TI	2012-12-29 09:24:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	☐☐[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê²¼☐İÄê%ıİŇ☐İ¥Á¥\$¥Ä¥¹äİÜ		
0200	C.	☐☐[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	☐☐[HK1_TI_CMD_NUM]	EQ	4
0202	C.	☐☐[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	☐☐[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİ°è¥Ä¥0¥x		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;S 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	☐☐[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	☐☐[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	☐☐[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	☐☐[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	☐☐[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	☐☐[HK1_PKT_FORM_NO]	EQ	7
0220	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Ä¥0¥x¥½ªİ»☐ò³İÇ\$		
0226	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL☐İ¥Ê¹Ç•ê²İOK☐ò³İÇ\$		
0229	C.			
0230	C.	DHU¥âı¥¥ÉıÊ¼¥½, ¥İı¥¥ÊıÊ☐òİä☐¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	☐☐[HK1_PKT_FORM_NO]	EQ	2
0234	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2012-12-29 09:24:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2012-12-29 09:24:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2012-12-29 09:24:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2012-12-29 09:24:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP ´ÜÄİ☐İ»ò¼¥☐ÊÄĐ☐¹☐êDCBC•x²è *****		
0276	C.	(%â°İ¥0¥Ä¥Ê¥½¥Ê¥â¥Ç¥ê☐Ê¼☐☐¥Ä»Ü☐¹☐ê)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥Đ¥¹•İ Daily±çİŇ☐É´Đ☐¹☐êDCBC•x²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	ıäLOS¥Ä¥\$¥Ä¥¼Ä»Üıä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			

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main-363 2012-12-29 13:12:47 82 33 SOLAR-B MAIN //
0001 C.
0002 C. ***** AOS *****
0003 C.
0004 C. jãAOS¥Á¥$¥Ã¥¬¼Á»Üjã
0005 C.
0006 C. ¥Ã¥ßj¼¥³¥p¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀījĒ□¿Á□•μ°Æ»Ī×ÁÇ□Ī¥Ç¥Ã¥×¥īj¼¥ĒjĒĒ½μ•īĒjĒ□Ē¼°Ç□□•□¿¼ī¹Ç□ījÇÀ®, ū□¹□Ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 C. Verify EIS in MANUAL mode
0049 C. Estimated OBSTBL upload time is 33s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 S. RAM ram-820:EIS_OBSTBL
0054 C.
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-12-29 09:24:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 C. ***** MDP `ŪĀĪ□Ī»Ō¼Ÿ□ĒĀ□¹□ĒDCBC•×²è *****
0069 C. (¼Ā°ĪŸŌ¥Ā¥Ē¥p¥Ē¥Ā¥Ç¥Ēē¼¼□□¼A»Ū□¹□Ē)
0070 S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 C. ***** ¥Ð¥¹•Ī Daily±¿ĪŃ□Ē´Ō□¹□ĒDCBC•×²è *****
0075 S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 C. jãLOS¥Á¥$¥Ã¥¬¼Á»Üjã
0080 C.
0081 C. ***** LOS *****
0082 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ()
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 +. DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 +. DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 +. DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 +. DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 +. DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 +. DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 +. DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 10 10)
0136 +. DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 +. DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 +. DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 +. DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 +. DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 +. DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 03)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 10)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2012-12-29 09:24:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-261:MDP_OBS_F
0179 ()
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 C. *****
0189 C. SOT TI command set
0190 C. *****
0191 C. Execute, after the success of TBL upload.
0192 +. TI 2012-12-29 09:24:18.0
0193 DC 07-F0 MDP_SOT_MODE_OBSV

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```
0194 BC (40)
0195 . C. -----
0196 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0197 C. -----
0198 C.
0199 C.
0200 . C. ***** MDP ´ûÃîï»ö%ýðĖâð¹ðĕDCBC•x²è *****
0201 C. (¾ă°î¥ŒŒ¥Ė¥ð¥Ė¥ă¥ċ¥ĕðĖ½¼ð¼Ā»Ŭ¹ðĕ)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ¥ð¥¹•İ Daily±ċİÑðĖ´Œ¹ðĕDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ĩăLOS¥Á¥S¥Ā¥¼Ā»Ŭĭă
0212 C.
0213 . C. ***** LOS *****
0214 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-365 2012-12-29 13:12:47 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ¡ãAOS¥Á¥S¥Ã¥¼Å»Ü¡ä
0005 C.
0006 C. ¥À¥B¡¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOS : Reload orbital element (send every contact) *****
0010 C. Å¡¡Ë¿¿À¤•µ°£»Î×AÇ¤Î¥¢¥Á¥×¥¡¡¼¥É¡ËËè½µ•îËËË°ÇÔ¤•¤¿¼¹¿¤Î¡ÇÀ®, ù¤¹¤è¤¤¤ÇÁ÷¿®¤•¤Ë¤¤¤³¤Ë¡£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP ´ÛÃÎ¤Î»Ô¼Ý¤ËÂ¤¤¹¤èDCBC•×²è *****
0016 C. (%ã°Î¥0¥Á¥Ë¥P¥Ë¥ã¥¢¥è¤Ë½¼¤¤¼Å»Ü¤¹¤è)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** ¥Ð¥¹•Î Daily±¿ÎÑ¤Ë´Ø¤¹¤èDCBC•×²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ¡ãLOS¥Á¥S¥Ã¥¼Å»Ü¡ä
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

Dec 29, 12 13:14

XRT_OGLIST_0153.chk

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*** OP Sequence for XRT ***

2012/12/29	09:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	09:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	09:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	09:35:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/12/29	09:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	09:50:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	09:50:02.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/29	09:50:22.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/29	09:50:24.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/29	09:50:26.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/29	09:50:28.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/29	09:50:30.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	09:50:32.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/29	09:50:34.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/29	09:50:36.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	14:58:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	14:58:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	14:58:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	15:01:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	15:21:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/29	15:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	16:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	16:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	16:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	16:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	16:58:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/29	16:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	17:47:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	17:47:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/29	17:48:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/12/29	17:48:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/29	17:48:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/29	17:48:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/29	17:50:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/29	17:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	17:57:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	17:58:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/12/29	18:00:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/29	18:00:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/29	18:00:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/29	18:00:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/29	18:00:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/29	18:00:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	18:00:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/29	18:00:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/29	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	18:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			

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2012/12/29	18:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	18:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	18:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	18:34:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	18:35:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	19:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	19:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	19:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	19:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	20:12:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	20:13:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	21:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	21:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	21:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	21:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	21:48:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	21:49:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/29	23:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/29	23:03:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/29	23:03:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	23:06:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/29	23:20:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/29	23:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	00:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	00:38:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	00:38:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	00:41:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	00:47:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	00:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	02:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	02:03:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	02:03:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	02:06:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	02:23:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	02:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	03:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	03:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	03:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	03:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	04:00:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	04:01:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	05:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	05:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	05:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	05:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	05:38:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	05:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	06:00:00.0	XRT_FOCUS_POSITION	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	06:00:00.0	AOCs_Or-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/30	06:00:00.0	AOCU_NM	AOCU_NM	5	02-76	00 00 00 00 00

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2012/12/30	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/30	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/30	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/30	06:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	06:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 ef f8 ad c3
2012/12/30	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/30	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/30	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/30	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/30	06:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/30	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	06:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/30	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/30	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	06:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	06:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	06:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	06:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	07:15:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/30	07:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	08:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	08:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	08:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	08:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	08:52:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/30	08:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	10:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	10:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	10:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	10:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	10:22:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/30	10:23:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	14:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	14:02:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	14:02:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	14:05:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	14:06:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/30	14:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	15:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	15:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	15:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/30	15:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/30	15:57:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/30	15:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/30	17:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/30	17:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/30	17:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2012/12/30	17:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/30	17:34:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/30	17:35:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/30	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/30	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2012/12/30	18:06:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2012/12/30	18:06:16.0	XRT_FLD_DIS_404_OG [0x194]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2012/12/30	18:06:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2012/12/30	18:06:20.0	XRT_ARS_DIS_406_OG [0x196]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/12/30	18:08:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0d
2012/12/30	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/30	18:15:54.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/30	18:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 ef f8 ad c3
2012/12/30	18:18:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/12/30	18:18:46.0	XRT_FLD_ENA_428_OG [0x1ac]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/12/30	18:18:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/12/30	18:18:50.0	XRT_AEC_RESET_423_OG [0x1a7]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/12/30	18:18:52.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/12/30	18:18:54.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/30	18:18:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2012/12/30	18:18:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 10
2012/12/30	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/30	18:48:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/30	18:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/30	18:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/30	18:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/30	19:11:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/30	19:12:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/30	20:25:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/30	20:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/30	20:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/30	20:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/30	20:48:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/30	20:49:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/30	22:03:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/30	22:03:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/30	22:03:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/30	22:06:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/30	22:24:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/30	22:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/30	23:40:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/30	23:40:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/30	23:40:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/12/30	23:43:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/12/30	23:50:30.0	XRT_Custom_434_OG [0x1b2]		
2012/12/30	23:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/12/31	01:06:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/12/31	01:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/12/31	01:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8

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2012/12/31	01:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	01:23:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/31	01:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	02:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	02:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	02:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/31	02:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	03:00:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/31	03:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	04:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	04:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	04:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/31	04:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	04:37:30.0	XRT_Custom_434_OG [0x1b2]			
2012/12/31	04:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	05:48:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	05:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	05:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/31	05:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	06:15:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/31	06:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	06:24:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	06:24:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/12/31	06:24:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/12/31	06:24:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/12/31	06:24:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/12/31	06:24:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/31	06:27:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/12/31	06:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	06:34:24.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	06:34:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 ef f8 ad c3
2012/12/31	06:36:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/12/31	06:37:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/12/31	06:37:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/12/31	06:37:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/12/31	06:37:22.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/12/31	06:37:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	06:37:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/12/31	06:37:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/12/31	06:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	07:29:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	07:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	07:29:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/31	07:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	07:52:00.0	XRT_Custom_434_OG [0x1b2]			
2012/12/31	07:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	09:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	09:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	09:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2012/12/31	09:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/31	09:27:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/31	09:28:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/31	14:36:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	14:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/31	14:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/31	14:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/31	14:47:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/31	14:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/31	16:10:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	16:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/31	16:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/31	16:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/31	16:34:30.0	XRT_Custom_434_OG [0x1b2]					
2012/12/31	16:35:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/31	17:47:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	17:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/31	17:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/31	17:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/31	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	18:10:56.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		
2012/12/31	18:11:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2012/12/31	18:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/12/31	18:15:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/12/31	18:15:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/12/31	18:15:20.0	XRT_ARS_DIS_412_OG [0x19c]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/12/31	18:15:22.0	XRT_QT_PROG_SET_419_OG [0x1a3]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2012/12/31	18:15:24.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/31	18:20:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	18:21:00.0	AOCS_ORe-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2012/12/31	18:23:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/12/31	18:23:46.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/12/31	18:23:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/12/31	18:23:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/12/31	18:23:52.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/12/31	18:23:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/31	18:23:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2012/12/31	18:23:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/12/31	18:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/31	19:24:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	19:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/31	19:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/31	19:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/12/31	19:48:00.0	XRT_Custom_434_OG [0x1b2]					
2012/12/31	19:49:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/12/31	21:02:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/12/31	21:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2012/12/31	21:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/12/31	21:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					

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2012/12/31	21:25:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	21:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	22:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/12/31	22:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/12/31	22:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/12/31	22:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/12/31	23:00:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/12/31	23:01:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	00:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	00:17:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	00:17:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	00:20:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	00:24:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	00:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	01:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	01:40:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	01:40:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	01:40:36.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	01:43:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	01:59:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	02:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	03:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	03:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	03:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	03:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	03:36:30.5	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	03:37:30.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	04:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	04:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	04:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	04:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	05:14:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	05:15:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	05:58:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	05:58:30.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	05:58:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	05:58:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	05:58:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	06:01:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/01	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2013/01/01	06:08:24.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	06:08:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	06:10:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	06:11:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/01	06:11:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/01	06:11:20.5	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/01	06:11:22.5	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/01	06:11:24.5	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/01	06:11:26.5	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e

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2013/01/01	06:11:28.5	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5	10
2013/01/01	06:11:30.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	06:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	06:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/01/01	06:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/01/01	06:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/01/01	06:51:30.0	XRT_Custom_434_OG [0x1b2]			
2013/01/01	06:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	08:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	08:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/01/01	08:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/01/01	08:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/01/01	08:28:30.0	XRT_Custom_434_OG [0x1b2]			
2013/01/01	08:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	09:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	09:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/01/01	09:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/01/01	09:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/01/01	10:02:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/01	10:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	15:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	15:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/01/01	15:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/01/01	15:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/01/01	15:34:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/01	15:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	16:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	16:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/01/01	16:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/01/01	16:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/01/01	17:11:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/01	17:12:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	17:43:30.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	17:43:32.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa	00
2013/01/01	17:43:52.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2013/01/01	17:43:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2013/01/01	17:43:56.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2013/01/01	17:46:34.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4	0d
2013/01/01	17:46:36.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/01/01	18:00:00.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/01/01	18:02:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97	00
2013/01/01	18:02:52.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2013/01/01	18:02:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2013/01/01	18:02:56.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2013/01/01	18:02:58.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2013/01/01	18:03:00.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/01/01	18:03:02.0	XRT_QT_PROG_SET_415_OG [0x19f]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4	0e
2013/01/01	18:03:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5	10
2013/01/01	18:03:06.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	

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2013/01/01	18:24:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/01	18:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/01	18:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/01	18:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/01	18:47:30.0	XRT_Custom_434_OG [0x1b2]		
2013/01/01	18:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/01	20:01:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/01	20:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/01	20:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/01	20:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/01	20:25:00.0	XRT_Custom_434_OG [0x1b2]		
2013/01/01	20:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/01	21:39:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/01	21:39:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/01	21:39:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/01	21:42:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/01	22:01:30.0	XRT_Custom_434_OG [0x1b2]		
2013/01/01	22:02:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/01	23:16:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/01	23:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/01	23:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/01	23:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/01	23:30:30.0	XRT_Custom_434_OG [0x1b2]		
2013/01/01	23:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/02	00:46:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/02	00:46:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/02	00:46:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/02	00:49:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/02	01:00:01.0	XRT_Custom_434_OG [0x1b2]		
2013/01/02	01:01:01.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/02	02:15:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/02	02:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/02	02:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/02	02:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/02	02:36:00.0	XRT_Custom_434_OG [0x1b2]		
2013/01/02	02:37:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/02	03:43:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/02	03:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/02	03:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/02	03:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/02	04:13:30.0	XRT_Custom_434_OG [0x1b2]		
2013/01/02	04:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/02	05:24:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/02	05:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/01/02	05:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/01/02	05:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/01/02	05:51:00.0	XRT_Custom_434_OG [0x1b2]		
2013/01/02	05:52:00.0	XRT_CTRL_AUTO_413_OG [0x19d]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/01/02	06:02:00.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/01/02	06:02:02.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2013/01/02	06:02:22.0	XRT_FLD_DIS_404_OG [0x194]		

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2013/01/02	06:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/01/02	06:02:26.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/01/02	06:05:04.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/02	06:05:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2013/01/02	06:15:00.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	06:17:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	06:17:52.0	XRT_FLD_ENA_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/01/02	06:17:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/01/02	06:17:56.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/01/02	06:17:58.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/01/02	06:18:00.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/02	06:18:02.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	06:18:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2013/01/02	06:18:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/01/02	07:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	07:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	07:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	07:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	07:28:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	07:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_Custom_434_OG [0x1b2]	1	07-F0	c0
2013/01/02	08:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	08:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	08:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	08:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	09:04:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	09:05:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_Custom_434_OG [0x1b2]	1	07-F0	c0
2013/01/02	10:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	10:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	10:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	10:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	10:33:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	10:34:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_Custom_434_OG [0x1b2]	1	07-F0	c0
2013/01/02	14:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	14:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	14:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	14:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	14:21:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	14:22:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_Custom_434_OG [0x1b2]	1	07-F0	c0
2013/01/02	15:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	15:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	15:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	15:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	16:10:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	16:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_Custom_434_OG [0x1b2]	1	07-F0	c0
2013/01/02	17:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	17:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	17:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	17:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	17:47:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2013/01/02	17:48:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	18:05:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	18:05:02.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/01/02	18:05:22.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/01/02	18:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/01/02	18:05:26.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/02	18:05:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2013/01/02	18:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	18:15:00.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	18:17:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/01/02	18:17:52.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/01/02	18:17:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/01/02	18:17:56.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/01/02	18:17:58.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/02	18:18:00.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	18:18:02.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2013/01/02	18:18:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/01/02	18:18:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	19:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	19:01:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	19:01:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	19:04:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	19:24:30.0	XRT_Custom_434_OG [0x1b2]				
2013/01/02	19:25:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	20:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	20:38:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	20:38:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	20:41:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	21:01:30.0	XRT_Custom_434_OG [0x1b2]				
2013/01/02	21:02:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	22:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	22:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	22:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	22:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/02	22:37:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/02	22:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/02	23:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/02	23:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/02	23:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/02	23:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/03	00:02:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/03	00:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/03	01:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/03	01:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/03	01:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/03	01:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/03	01:36:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/03	01:37:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/03	02:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2013/01/03	02:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/03	02:52:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/03	02:55:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/03	03:13:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/03	03:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/03	04:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/03	04:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/03	04:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/03	04:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/03	04:50:30.0	XRT_Custom_434_OG [0x1b2]			
2013/01/03	04:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/03	05:34:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/03	05:34:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/01/03	05:34:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/01/03	05:34:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/01/03	05:34:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/01/03	05:34:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/03	05:37:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2013/01/03	05:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/03	05:44:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/01/03	07:13:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/03	07:13:02.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/01/03	07:13:22.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/01/03	07:13:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/01/03	07:13:26.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/03	07:16:04.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2013/01/03	07:16:06.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/03	08:30:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/03	09:24:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00