

# XRT Timeline to be uploaded on 2013/01/17

Period: 2013/01/17 10:47:00 - 2013/01/22 10:05:00

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## Normal mode

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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly			
Term	Pointing (x, y)	Comment	
01/17 11:00:00 - 01/17 13:24:54	Fixed ( 0.0, 941.0)	# OP start + 10min, EIS full-disk spectral scan; eight pointings.	
01/17 13:28:00 - 01/17 14:59:54	Fixed ( -411.0, 847.0)		
01/17 15:41:30 - 01/17 16:51:30	Fixed ( -411.0, 847.0)	* EIS scan, cont.	
01/17 17:15:31 - 01/17 18:28:30	Fixed ( -815.0, 470.0)	*	
01/17 18:53:00 - 01/17 20:06:00	Fixed ( 815.0, -470.0)		
01/17 20:33:00 - 01/17 23:21:00	Fixed ( 411.0, -847.0)		
01/17 23:34:01 - 01/18 02:20:00	Fixed ( 0.0, -941.0)		
01/18 02:40:31 - 01/18 05:28:30	Fixed ( -411.0, -847.0)		
01/18 05:55:30 - 01/18 07:09:00	Fixed ( -815.0, -471.0)	* EIS scan, final pointing.	

**PROG= 01 Inf.-time(s)**

Subr= 1	1-time(s)	120.0sec										
└─ Seqn= 12	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

**XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)**

Term	Pointing (x, y)	Comment	
01/17 15:03:00 - 01/17 15:15:30	Fixed ( 0.0, 0.0)	synoptic, shifted manually.	
01/18 07:38:00 - 01/18 07:44:54	Fixed ( 0.0, 0.0)	* Synoptic, shifted manually.	
01/18 17:57:00 - 01/18 18:03:54	Fixed ( 0.0, 0.0)	synoptic, shifted -6.0 min	
01/19 05:42:00 - 01/19 05:48:54	Fixed ( 0.0, 0.0)	synoptic, shifted -21.0 min	

**PROG= 13 1-time(s)**

Subr= 1	1-time(s)	14.0sec										
└─ Seqn= 64	1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 70	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 67	1-time(s)	2.0sec										
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 69	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 68	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

**XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo**

Term	Pointing (x, y)	Comment	
01/18 07:48:00 - 01/18 17:28:30	Track ( 781.9, 130.3) @ 01/18 07:45:00	# Track AR 11654.	

**PROG= 18 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
└─ Seqn=100	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 19	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 65	4-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 94	70-time(s)	2.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

**XOB #1962: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (33ms) w/ G-Band VLS CI**

Term	Pointing (x, y)	Comment
01/18 18:07:00 - 01/19 05:38:54	Track ( 835.6, 123.7) <sup>Ⓜ 01/18 18:04:00</sup>	# Cont.
01/19 05:52:00 - 01/19 09:27:00	Track ( 886.5, 115.5) <sup>Ⓜ 01/19 05:49:00</sup>	# Cont.
<b>PROG= 07 Inf.-time(s)</b>		
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>
<b>Seqn=100</b>	<b>1-time(s)</b>	<b>2.0sec</b>
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 19</b>	<b>1-time(s)</b>	<b>2.0sec</b>
Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
<b>Seqn= 65</b>	<b>4-time(s)</b>	<b>2.0sec</b>
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>
<b>Seqn= 73</b>	<b>70-time(s)</b>	<b>120.0sec</b>
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 12.5sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 12.5sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 12.5sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 12.5sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops**

Term	Pointing (x, y)	Comment
01/17 11:00:00 - 01/17 13:24:54	Fixed ( 0.0, 941.0)	# OP start + 10min, EIS full-disk spectral scan; eight pointings.
01/17 13:28:00 - 01/17 14:59:54	Fixed ( -411.0, 847.0)	
01/17 15:41:30 - 01/17 16:51:30	Fixed ( -411.0, 847.0)	* EIS scan, cont.
01/17 17:15:31 - 01/17 18:28:30	Fixed ( -815.0, 470.0)	*
01/17 18:53:00 - 01/17 20:06:00	Fixed ( 815.0, -470.0)	
01/17 20:33:00 - 01/17 23:21:00	Fixed ( 411.0, -847.0)	
01/17 23:34:01 - 01/18 02:20:00	Fixed ( 0.0, -941.0)	
01/18 02:40:31 - 01/18 05:28:30	Fixed ( -411.0, -847.0)	
01/18 05:55:30 - 01/18 07:09:00	Fixed ( -815.0, -471.0)	* EIS scan, final pointing.
01/18 07:48:00 - 01/18 17:28:30	Track ( 781.9, 130.3) <sup>Ⓜ 01/18 07:45:00</sup>	# Track AR 11654.
01/18 18:07:00 - 01/19 05:38:54	Track ( 835.6, 123.7) <sup>Ⓜ 01/18 18:04:00</sup>	# Cont.
01/19 05:52:00 - 01/19 09:27:00	Track ( 886.5, 115.5) <sup>Ⓜ 01/19 05:49:00</sup>	# Cont.
<b>PROG= 16 15-time(s)</b>		
<b>Subr= 1</b>	<b>45-time(s)</b>	<b>10.0sec</b>
<b>Seqn= 35</b>	<b>1-time(s)</b>	<b>2.0sec</b>
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>10.0sec</b>
<b>Seqn= 36</b>	<b>1-time(s)</b>	<b>2.0sec</b>
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 37</b>	<b>1-time(s)</b>	<b>2.0sec</b>
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 38</b>	<b>1-time(s)</b>	<b>2.0sec</b>
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

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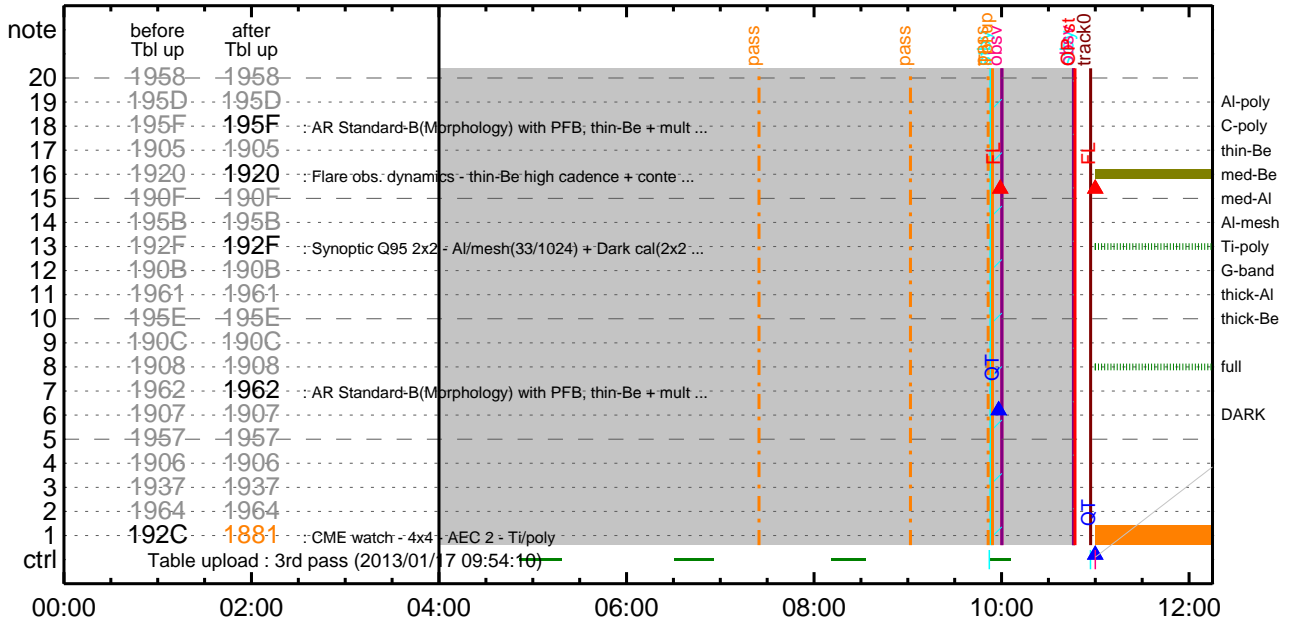
**Flare Detection**

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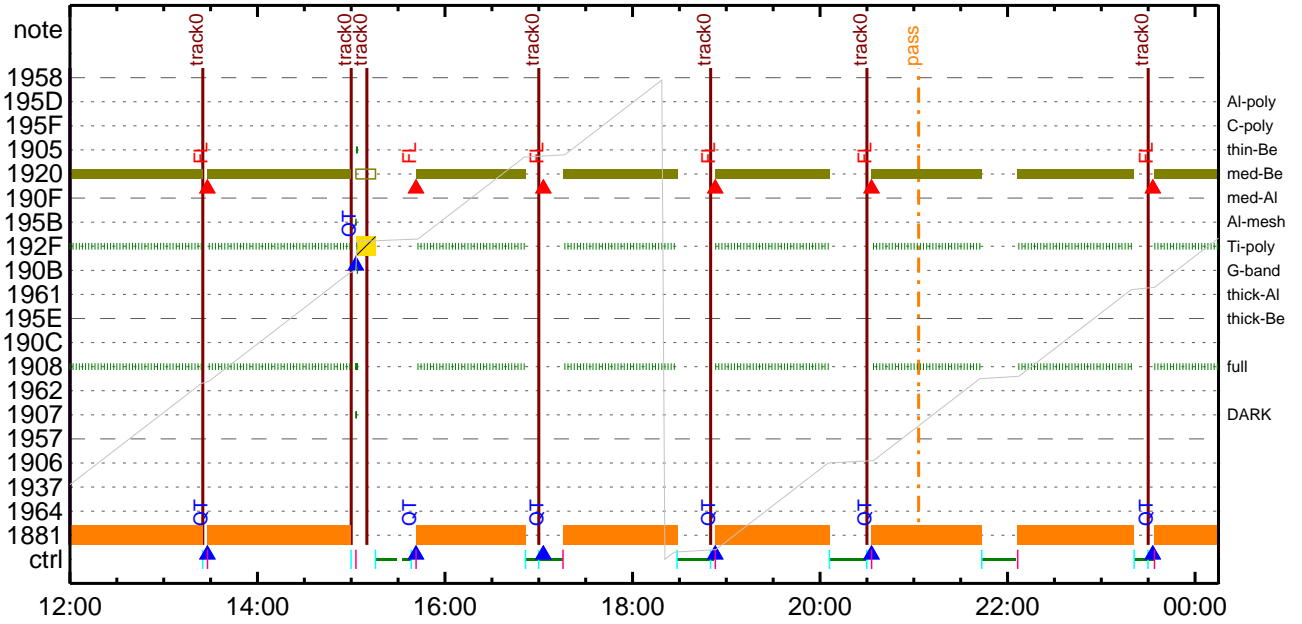
**FLD Patrol**

Term	Pointing (x, y)	Comment
01/17 15:41:16 - 01/18 07:35:16	Fixed ( -411.0, 847.0)	* EIS scan, cont.
01/18 07:45:16 - 01/18 17:54:16	Track ( 781.9, 130.3) <sup>Ⓜ 01/18 07:45:00</sup>	# Track AR 11654.
01/18 18:04:16 - 01/19 05:39:16	Track ( 835.6, 123.7) <sup>Ⓜ 01/18 18:04:00</sup>	# Cont.
01/19 05:49:16 - 01/22 10:05:00	Track ( 886.5, 115.5) <sup>Ⓜ 01/19 05:49:00</sup>	# Cont.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

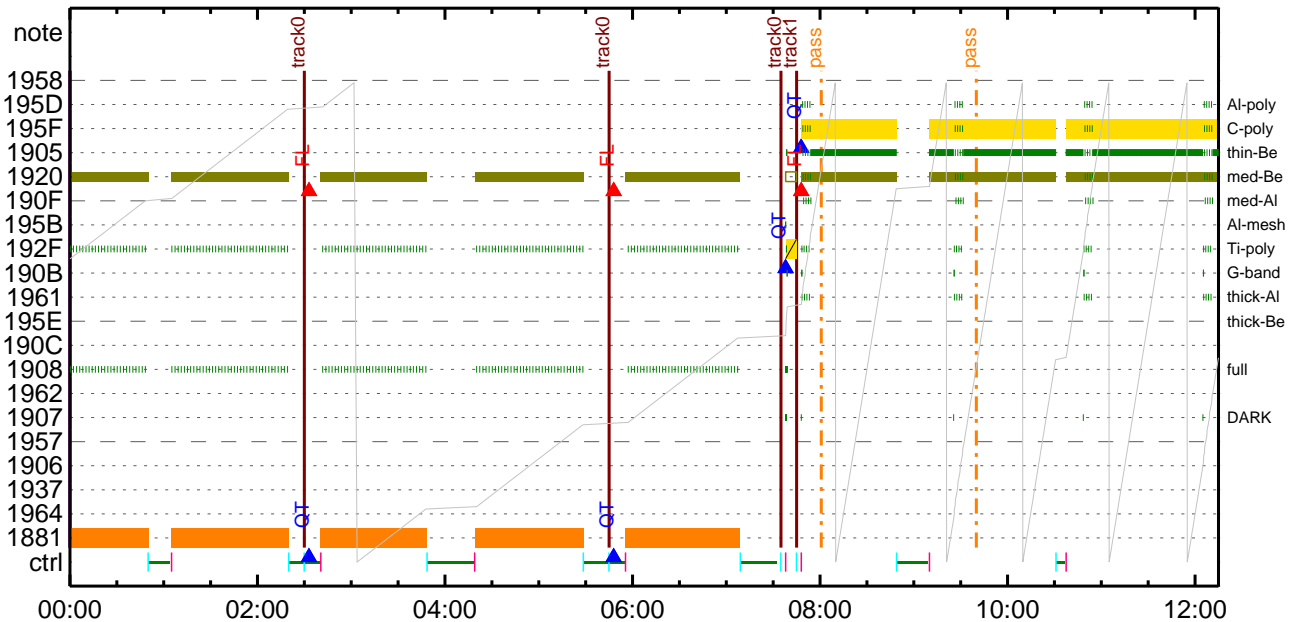
### CMDI #0188 2013/01/17



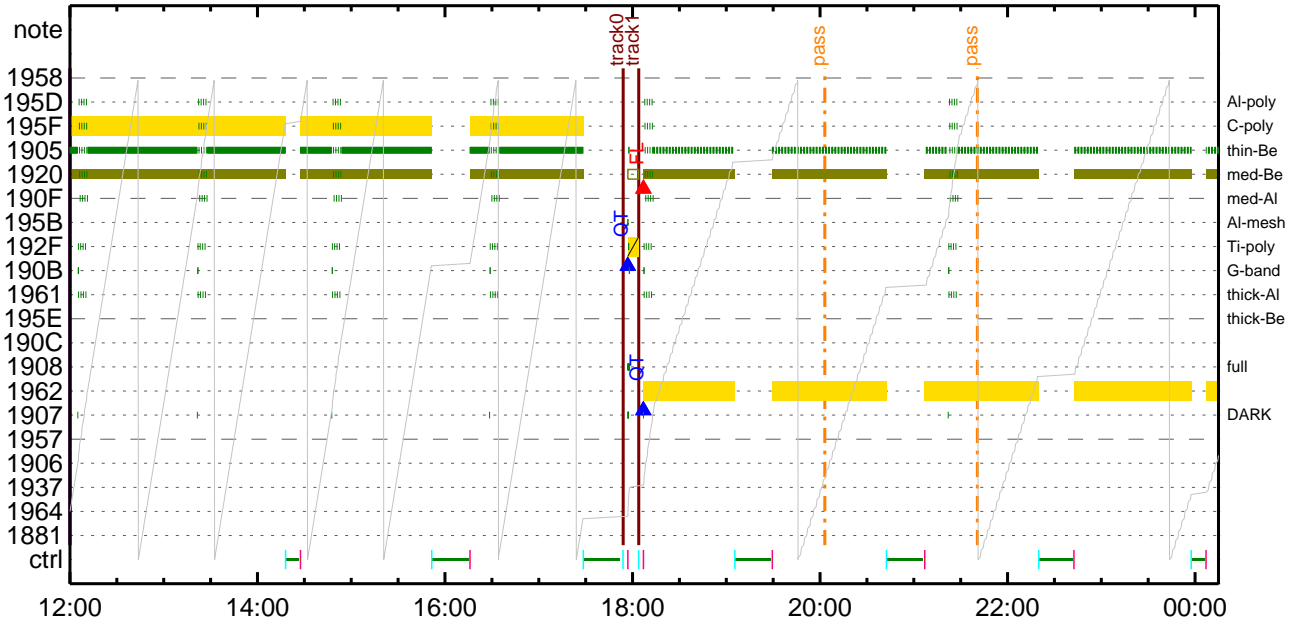
### CMDI #0188 2013/01/17



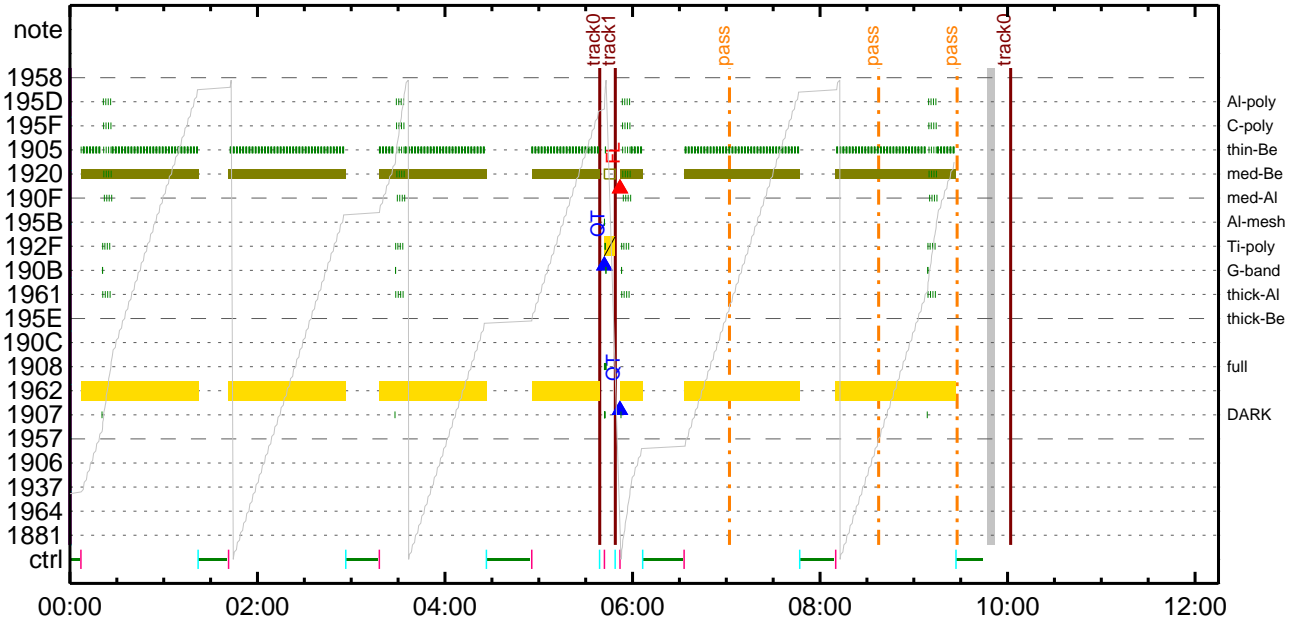
### CMDI #0188 2013/01/18



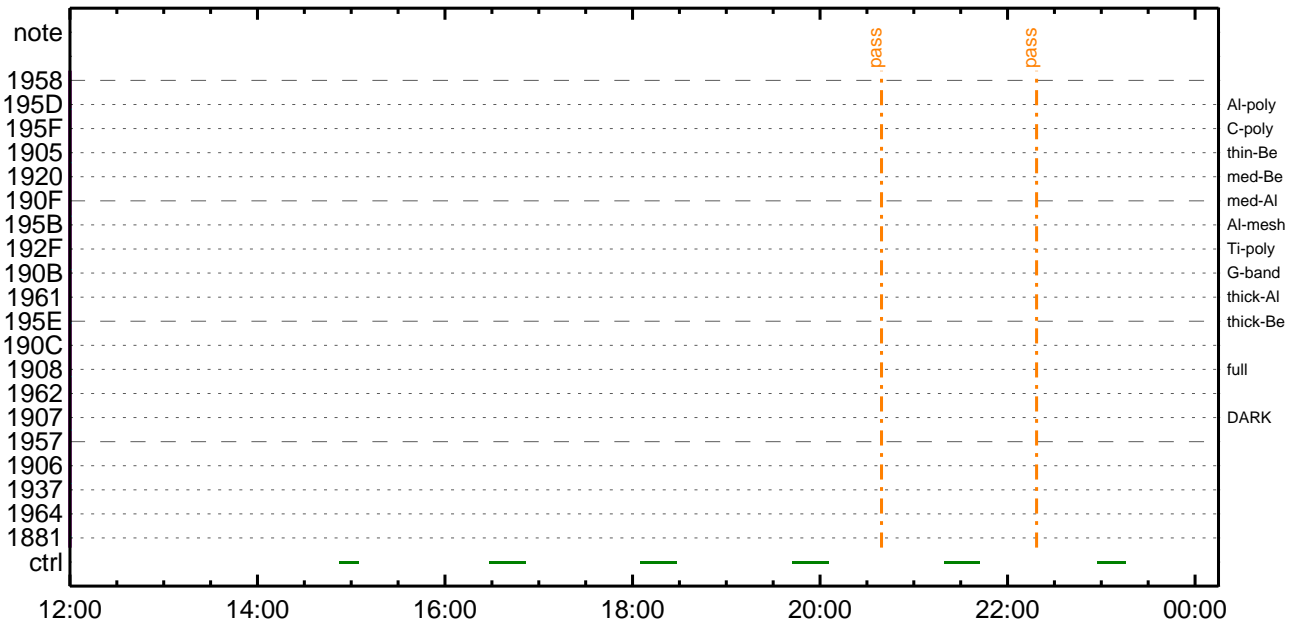
CMDI #0188 2013/01/18



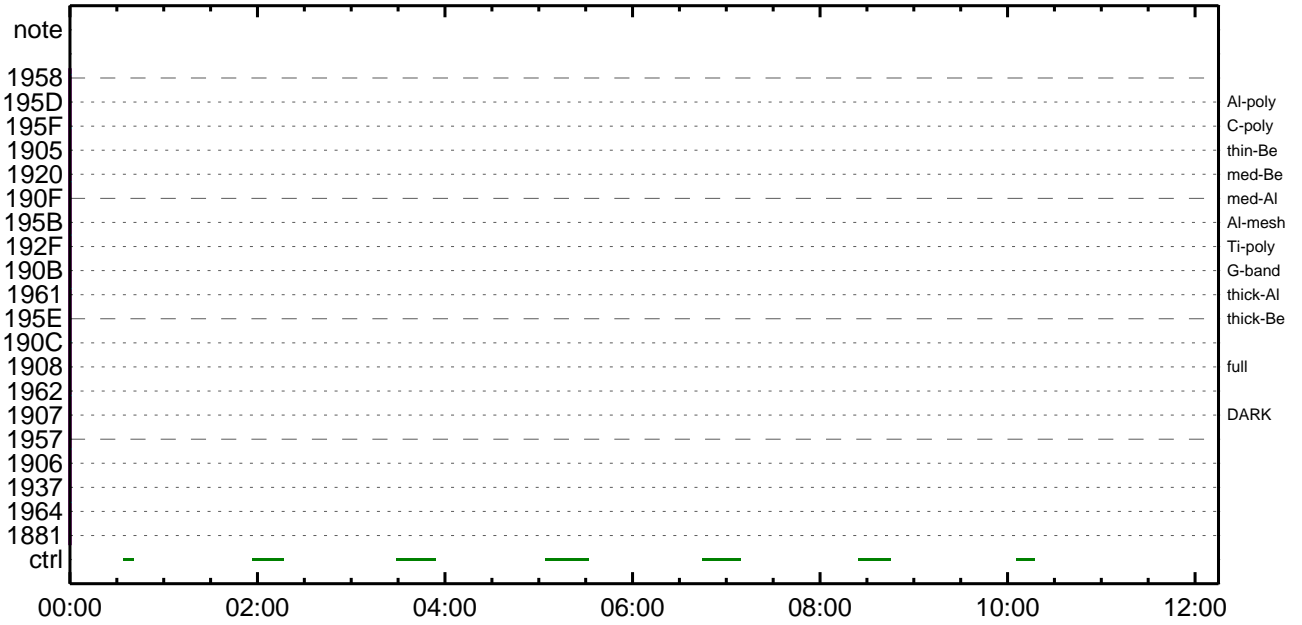
CMDI #0188 2013/01/19



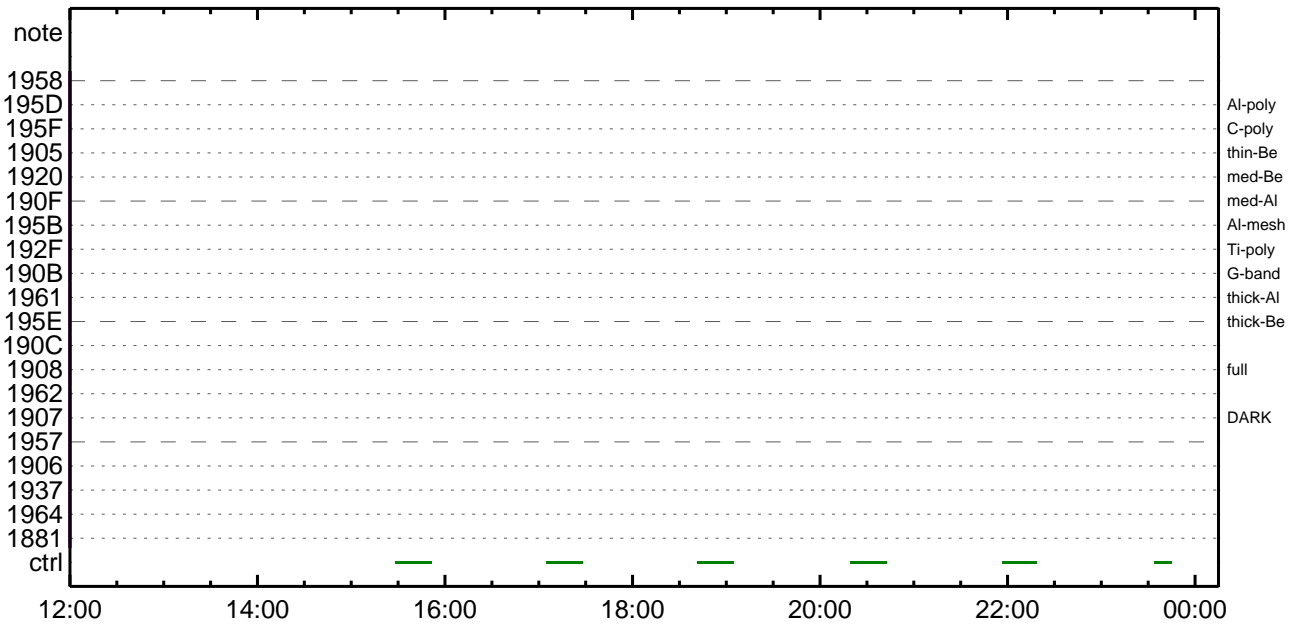
CMDI #0188 2013/01/19



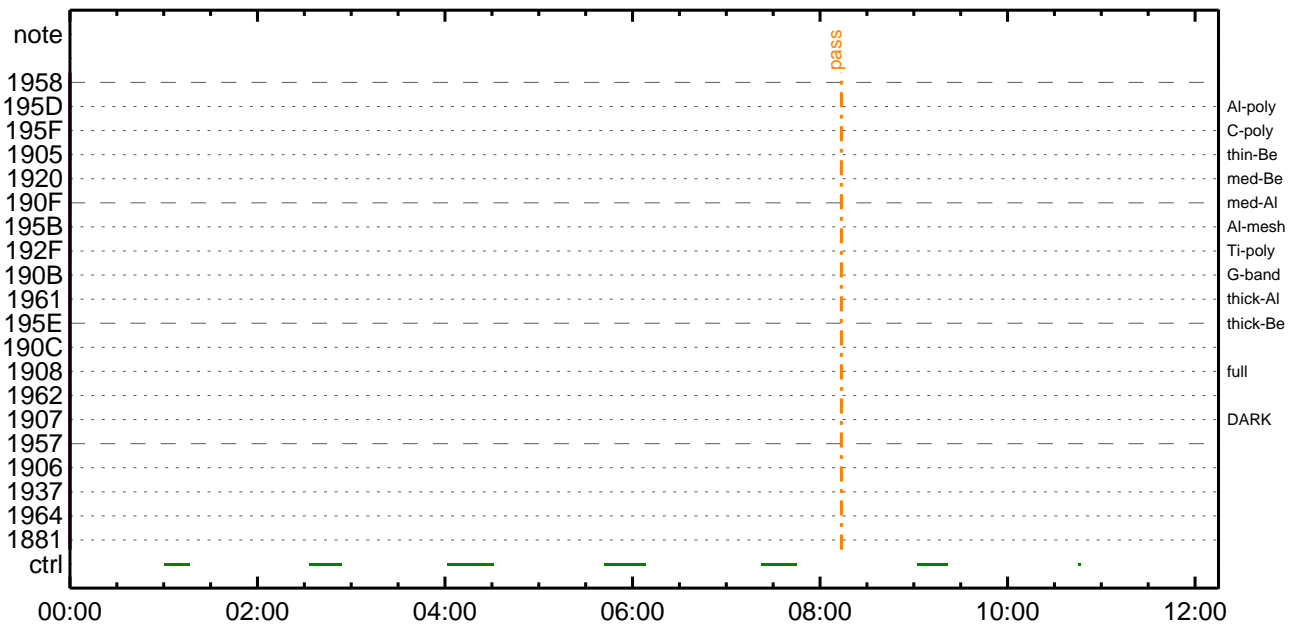
CMDI #0188 2013/01/20



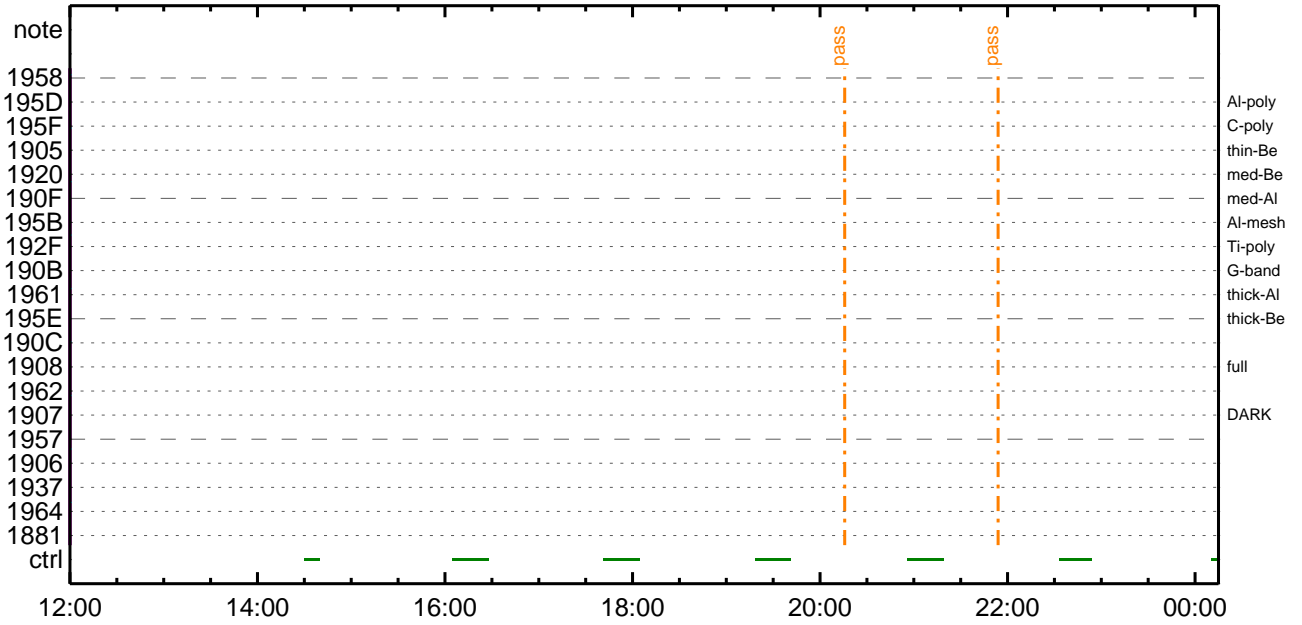
CMDI #0188 2013/01/20



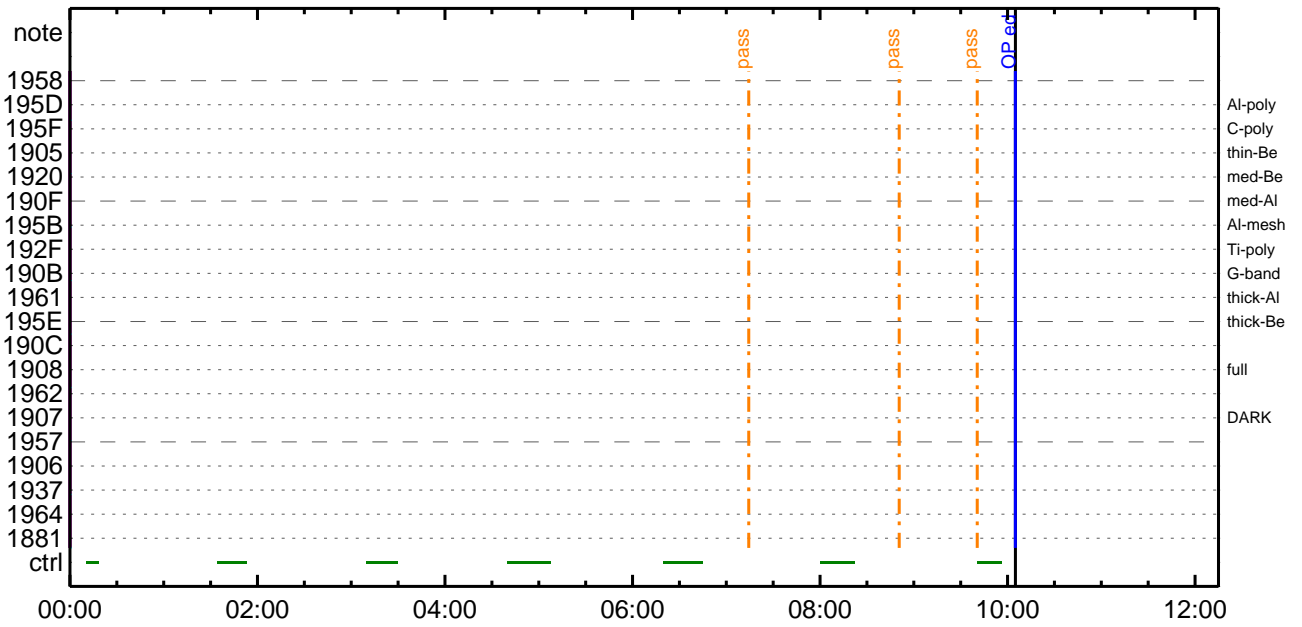
CMDI #0188 2013/01/21



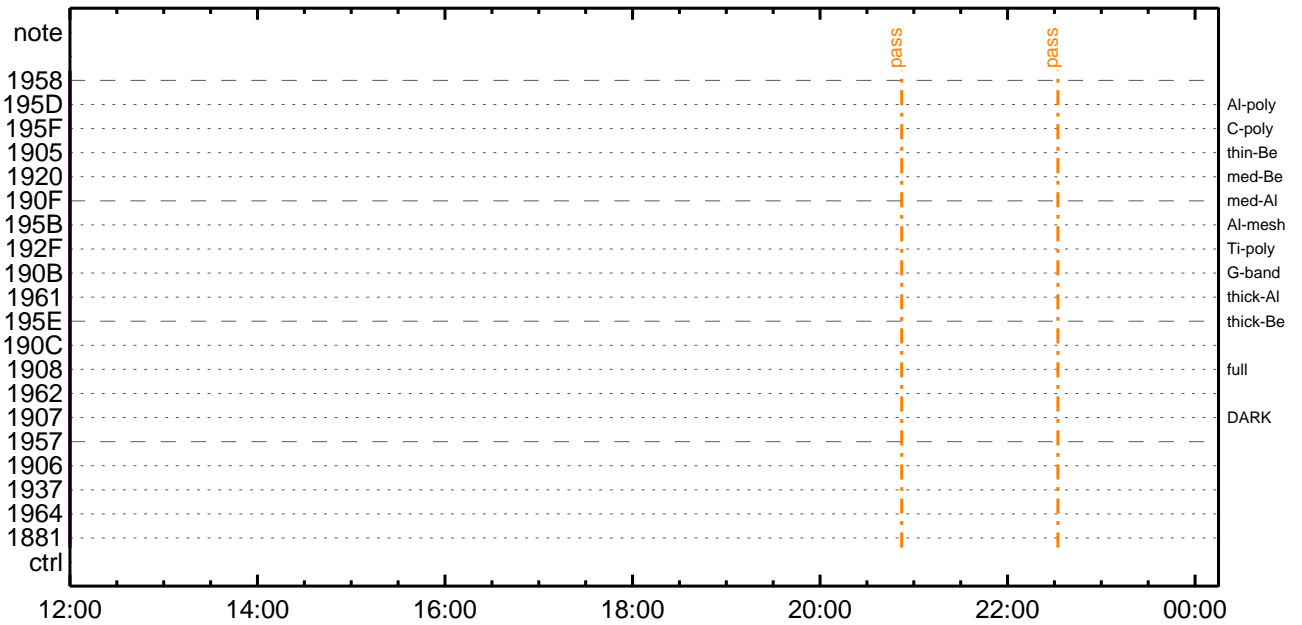
CMDI #0188 2013/01/21



CMDI #0188 2013/01/22



CMDI #0188 2013/01/22





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-395:OP
0104 ( )
0105 S. OG og-395:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ë¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ë¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼Ë¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °Ë²¼òî¼Ã´¶Á°òËË-ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÔËæç¼ª°²òè¼î¹çççâ) *****
0167 C. DHUYâ;4YE;Ë½Y½;Yî;4YE;Ëòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°Ë²¼òîTI-CMDÁ÷¿@²î¼Á¹Ô²°²Ë²²²³²Ë;f
0180 C. ²²²¿;çSET²EDUMPA²Ë±²îYÑY¹²ç¹Ô²²²³²Ë;f
0181 C.
0182 C. TIY³Y²Y³Y²Y³Y²²ððÁDî¿¿(UT)
0183 +. TI 2013-01-17 10:42:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-01-17 10:42:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-01-17 10:42:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-01-17 10:46:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °È²¼¼õîÄè%îíÑõîîŷÅŷ§ŷÅŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]      EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]      EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÅŷóŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC       (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]    EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]    EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC       (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÅŷóŷ×½ªî»ð³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLîîÈ¹çªè²îOKð³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;Èŷ½.ŷî;¼ŷÈ;Èððîäª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC       (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2013-01-17 10:46:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC       (21 02)
0247 +. TI 2013-01-17 10:46:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC       (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2013-01-17 10:46:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC       (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ôÄîõî»ó¼ŷõÈÄð¹ðèDCBC•x²è *****
0265 C. (%ã°îŷóŷÄŷÈŷŷŷÈŷáŷçŷèõÈ¼¼ã¼Ä»Ûð¹ðè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îÑõÈ¹ø¹ðèDCBC•x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. jãLOSŷÄŷŷŷÄŷ-¼Ä»Û;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCS_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS_DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCS_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 17s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-01-17 10:46:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP (uAÎpÎ»ö¼ÝpĚÄĐp¹eDCBC•x²è *****
0153 C. (%ã°İYÓYÁYĚYÏYËYáYçYëpĚ¼p¼Ä»Üp¹e)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** YDŸ¹•İ Daily±¿İÑpĚ´Øp¹eDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSŸÁŸŸŸÄŸ¹¼Ä»Ü;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

Page 1/7

\*\*\* OP Sequence for XRT \*\*\*

2013/01/17	10:56:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	10:57:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2013/01/17	10:59:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/17	10:59:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/17	10:59:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/17	10:59:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/17	10:59:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/17	10:59:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/17	10:59:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/01/17	10:59:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/17	11:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/17	13:24:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	13:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2013/01/17	13:27:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/17	13:27:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/17	13:27:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/17	13:27:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/17	13:27:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/17	13:27:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/17	13:27:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/01/17	13:27:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/17	13:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/17	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	14:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/17	15:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/17	15:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/17	15:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/17	15:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/17	15:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/17	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/17	15:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2013/01/17	15:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	15:15:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/17	15:15:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/17	15:18:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/17	15:38:24.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	15:40:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/17	15:41:16.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/17	15:41:18.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/17	15:41:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/17	15:41:22.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/17	15:41:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/17	15:41:26.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/01/17	15:41:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/17	15:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/17	16:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							

Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

Page 2/7

2013/01/17	16:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17	16:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/17	16:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/01/17	16:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/01/17	17:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17	17:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 d6 36 48 72		
2013/01/17	17:02:46.0	XRT_FLD_ENA_418_OG [0x1a2]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/01/17	17:02:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/01/17	17:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/01/17	17:02:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/17	17:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/17	17:02:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/17	17:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2013/01/17	17:15:31.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/01/17	18:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/17	18:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17	18:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/17	18:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/01/17	18:49:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/01/17	18:50:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17	18:52:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 29 ca b7 8e		
2013/01/17	18:52:46.0	XRT_FLD_ENA_418_OG [0x1a2]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/01/17	18:52:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/01/17	18:52:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/01/17	18:52:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/17	18:52:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/17	18:52:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/17	18:52:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2013/01/17	18:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/01/17	20:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/17	20:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17	20:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/17	20:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/01/17	20:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/01/17	20:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17	20:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	AOCU_NM	5	02-76	00 4b 4b db 75		
2013/01/17	20:32:46.0	XRT_FLD_ENA_418_OG [0x1a2]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/01/17	20:32:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/01/17	20:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/01/17	20:32:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/17	20:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/17	20:32:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/17	20:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2013/01/17	20:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/01/17	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/17	21:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/17			MDP_XRT_FLD_RESET	1	07-F0	da		

Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

2013/01/17	21:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/17	21:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/17	22:05:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/17	22:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/17	23:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	23:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/17	23:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/17	23:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/17	23:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/17	23:30:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2013/01/17	23:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/17	23:32:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/17	23:32:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/17	23:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/17	23:32:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/17	23:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/17	23:32:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/01/17	23:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/17	23:34:01.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	00:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	00:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	00:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	00:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	01:04:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/18	01:05:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	02:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	02:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	02:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	02:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	02:29:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	02:30:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2013/01/18	02:32:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/18	02:32:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/18	02:32:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/18	02:32:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/18	02:32:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/18	02:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	02:32:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/01/18	02:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/18	02:40:31.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	03:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	03:48:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	03:48:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	03:51:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	04:18:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/18	04:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	05:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	05:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				



Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

Page 4/7

2013/01/18	05:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	05:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	05:44:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	05:45:00.0	AOCS_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2013/01/18	05:47:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/18	05:47:46.0	XRT_FLD_ENA_418_OG [0x1a2]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/18	05:47:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/18	05:47:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/18	05:47:52.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/18	05:47:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	05:47:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/01/18	05:47:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/18	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	07:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	07:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	07:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	07:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	07:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	07:34:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/18	07:35:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/18	07:35:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/18	07:35:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/18	07:35:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/18	07:37:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/18	07:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	07:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	07:44:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/18	07:45:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/01/18	07:45:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/18	07:45:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/18	07:45:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/18	07:45:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/18	07:47:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	07:47:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/01/18	07:47:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/18	07:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	08:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	08:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	08:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	08:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	09:09:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/18	09:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	10:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	10:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	10:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	10:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	10:36:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/18	10:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							

Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

2013/01/18	14:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	14:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	14:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	14:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	14:26:30.0	XRT_Custom_434_OG [0x1b2]								
2013/01/18	14:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	15:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	15:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	15:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	15:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	16:15:00.0	XRT_Custom_434_OG [0x1b2]								
2013/01/18	16:16:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	17:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	17:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	17:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	17:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	17:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/18	17:54:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/18	17:54:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/18	17:54:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/18	17:54:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/18	17:56:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/18	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	18:03:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/18	18:04:00.0	AOCs_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	01 00 00 00 00				
2013/01/18	18:04:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/18	18:04:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/18	18:04:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/18	18:04:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/18	18:06:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	18:06:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2013/01/18	18:06:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/18	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	19:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	19:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	19:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	19:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	19:28:30.0	XRT_Custom_434_OG [0x1b2]								
2013/01/18	19:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	20:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	20:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	20:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	20:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	21:06:00.0	XRT_Custom_434_OG [0x1b2]								
2013/01/18	21:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	22:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

2013/01/18	22:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	22:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/18	22:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/18	22:41:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/18	22:42:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/18	23:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/18	23:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/18	23:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	00:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	00:06:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	00:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	01:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	01:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	01:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	01:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	01:40:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	01:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	02:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	02:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	02:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	02:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	03:17:00.5	XRT_Custom_434_OG [0x1b2]							
2013/01/19	03:18:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	04:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	04:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	04:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	04:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	04:54:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	04:55:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	05:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	05:38:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/19	05:39:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/19	05:39:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/19	05:39:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/19	05:39:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/19	05:41:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/19	05:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	05:48:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/19	05:49:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/01/19	05:49:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/19	05:49:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/19	05:49:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/19	05:49:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/19	05:51:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	05:51:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2013/01/19	05:51:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/19	05:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	06:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

Jan 17, 13 12:57

## XRT\_OGLIST\_0188.chk

Page 7/7

2013/01/19	06:06:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	06:06:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	06:09:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	06:32:00.5	XRT_Custom_434_OG [0x1b2]							
2013/01/19	06:33:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	07:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	07:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	07:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	07:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	08:09:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	08:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/19	09:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/19	09:27:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/19	09:27:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/19	09:30:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/19	10:02:00.0	AOCS_Or-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				