

XRT Timeline to be uploaded on 2013/01/19

Period: 2013/01/19 09:52:00 - 2013/01/24 12:20:00

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Normal mode

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XOB #1958: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33) - 1 loop

Term	Pointing (x, y)	Comment
01/19 10:37:00 - 01/19 13:36:54	Fixed (0.0, 0.0)	
PROG= 20 1-time(s)		
Subr= 1 1-time(s) 85.0sec		
Seqn= 82 1-time(s) 7.0sec		
Open/Al-mesh Open/Al-mesh close	Safe Norm 16ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 6 1-time(s) 22.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 76 1-time(s) 8.0sec		
Open/Ti-poly Open/Ti-poly close	Safe Norm 32ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close	Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 77 1-time(s) 12.0sec		
Al-poly/Open Al-poly/Open close	Safe Norm 125ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 79 1-time(s) 10.0sec		
C-poly/Open C-poly/Open close	Safe Norm 250ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
C-poly/Open C-poly/Open close	Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 75 1-time(s) 10.0sec		
Al-poly/Ti-poly Al-poly/thick-Al close	Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 78 1-time(s) 15.0sec		
thin-Be/Open thin-Be/Open close	Safe Norm 177ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 150.0sec		
Seqn= 34 1-time(s) 29.0sec		
med-Al/Open med-Al/Open close	Safe Norm 22.6s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 83 1-time(s) 84.0sec		
Open/thick-Be Open/thick-Be close	Safe Norm 64.0s	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 69 2-time(s) 13.0sec		
Open/G-band Open/G-band open	Safe Norm 8ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 68 1-time(s) 10.0sec		
Open/G-band Open/G-band close	Safe Norm 32ms	Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
01/19 13:40:00 - 01/19 17:41:24	Track (914.0, 109.7) ^{@ 01/19 13:37:00}	* Track AR 11654.
01/19 17:54:30 - 01/19 22:00:24	Track (926.7, 106.5) ^{@ 01/19 17:51:30}	# Cont.
01/19 22:03:30 - 01/20 05:59:54	Fixed (937.6, 103.3)	AR 11654, at W limb.
01/20 06:13:00 - 01/20 17:59:54	Track (883.3, 178.8) ^{@ 01/20 06:10:00}	# E side of AR 11654
01/20 18:13:00 - 01/21 06:18:24	Fixed (927.2, 168.1)	* AR 11654 E side, fixed pointing.
01/21 06:31:30 - 01/21 06:59:54	Fixed (927.2, 168.1)	# Cont.
01/21 14:41:00 - 01/21 17:41:30	Track (253.8, 314.3) ^{@ 01/21 14:30:00}	* EIS obs of old AR remnant (old AR 11635) near central meridian.
01/21 18:18:00 - 01/22 05:51:54	Fixed (927.2, 168.1)	# E side of AR 11654, fixed pointing.
01/22 06:05:00 - 01/22 09:40:30	Fixed (927.2, 168.1)	# Cont.
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn=100 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band Open/G-band open	Safe Norm 32ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 19 1-time(s) 2.0sec		
Open/G-band Open/G-band close	Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 65 4-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al Open/thick-Al close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open Al-poly/thick-Be close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open C-poly/thick-Al close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open med-Be/Open close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open med-Al/thick-Al close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 94 70-time(s) 2.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18

Term	Pointing (x, y)	Comment
01/19 17:44:30 - 01/19 17:51:24	Fixed (0.0, 0.0)	synoptic, shifted -18.5 min
01/20 06:03:00 - 01/20 06:09:54	Fixed (0.0, 0.0)	synoptic
01/20 18:03:00 - 01/20 18:09:54	Fixed (0.0, 0.0)	synoptic
01/21 06:21:30 - 01/21 06:28:24	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min
01/21 18:08:00 - 01/21 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
01/22 05:55:00 - 01/22 06:01:54	Fixed (0.0, 0.0)	synoptic, shifted -8.0 min

PROG= 13 1-time(s)

- Subr= 1 1-time(s) 14.0sec
 - Seqn= 64 1-time(s) 4.0sec
 - Open/Al-mesh Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 6 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 70 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 67 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 69 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 68 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
01/21 10:45:00 - 01/21 12:29:54	Fixed (0.0, 945.0)	* Co-alignment offset, N.

PROG= 12 1-time(s)

- Subr= 1 1-time(s) 360.0sec
 - Seqn= 16 24-time(s) 300.0sec
 - Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment
01/21 12:45:00 - 01/21 14:29:54	Fixed (-945.0, 0.0)	* Co-alignment offset, E.

PROG= 09 1-time(s)

- Subr= 1 1-time(s) 360.0sec
 - Seqn= 17 15-time(s) 480.0sec
 - Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
01/19 13:40:00 - 01/19 17:41:24	Track (914.0, 109.7) ^{@ 01/19 13:37:00}	* Track AR 11654.
01/19 17:54:30 - 01/19 22:00:24	Track (926.7, 106.5) ^{@ 01/19 17:51:30}	# Cont.
01/19 22:03:30 - 01/20 05:59:54	Fixed (937.6, 103.3)	AR 11654, at W limb.
01/20 06:13:00 - 01/20 17:59:54	Track (883.3, 178.8) ^{@ 01/20 06:10:00}	# E side of AR 11654
01/20 18:13:00 - 01/21 06:18:24	Fixed (927.2, 168.1)	* AR 11654 E side, fixed pointing.
01/21 06:31:30 - 01/21 06:59:54	Fixed (927.2, 168.1)	# Cont.
01/21 14:41:00 - 01/21 17:41:30	Track (253.8, 314.3) ^{@ 01/21 14:30:00}	* EIS obs of old AR remnant (old AR 11635) near central meridian.
01/21 18:18:00 - 01/22 05:51:54	Fixed (927.2, 168.1)	# E side of AR 11654, fixed pointing.
01/22 06:05:00 - 01/22 09:40:30	Fixed (927.2, 168.1)	# Cont.

PROG= 16 15-time(s)

- Subr= 1 45-time(s) 10.0sec
 - Seqn= 35 1-time(s) 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
- Subr= 2 1-time(s) 10.0sec
 - Seqn= 36 1-time(s) 2.0sec
 - med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
 - Seqn= 37 1-time(s) 2.0sec
 - Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
 - Seqn= 38 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

L	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

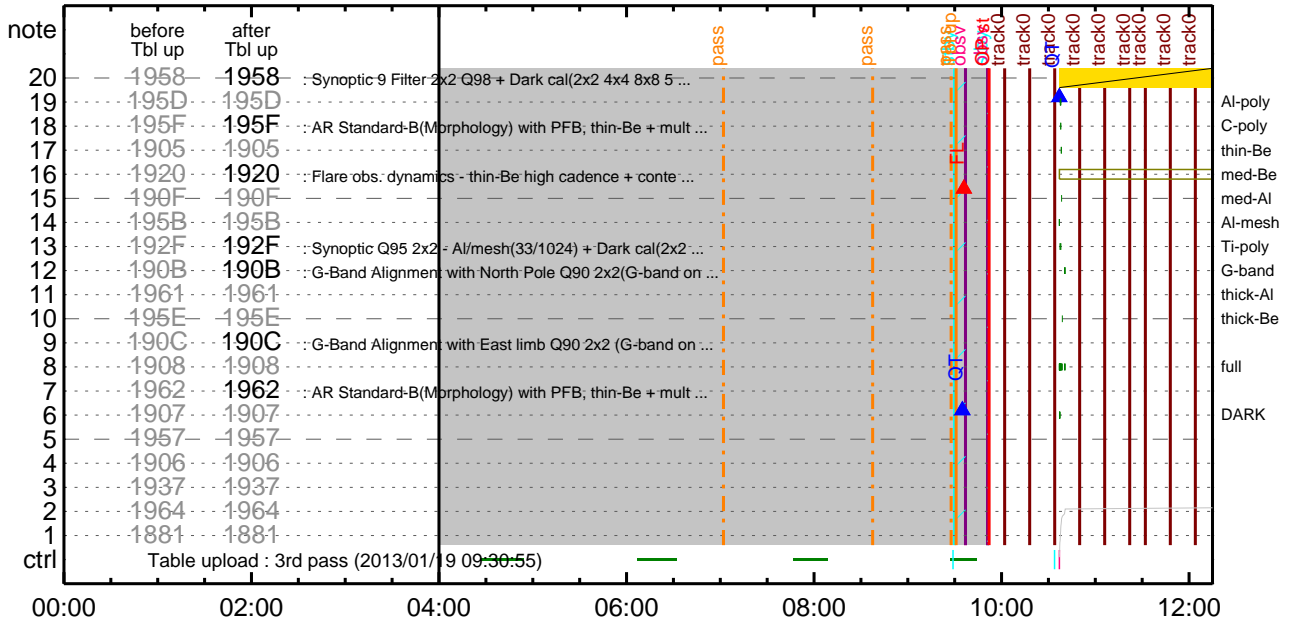
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Flare Detection

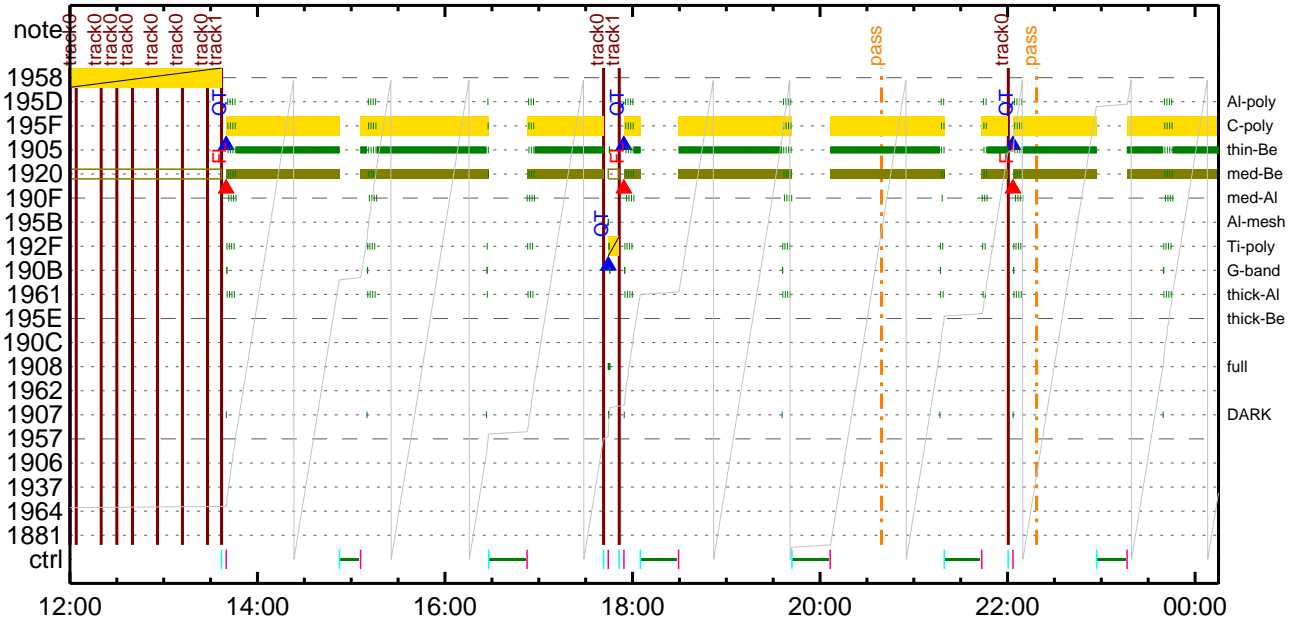
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FLD Patrol												
Term	Pointing (x, y)					Comment						
01/19 13:37:16 - 01/19 17:41:46	Track (914.0, 109.7)	^{@ 01/19 13:37:00}				* Track AR 11654.						
01/19 17:51:46 - 01/20 06:00:16	Track (926.7, 106.5)	^{@ 01/19 17:51:30}				# Cont.						
01/20 06:10:16 - 01/20 18:00:16	Track (883.3, 178.8)	^{@ 01/20 06:10:00}				# E side of AR 11654						
01/20 18:10:16 - 01/21 06:18:46	Fixed (927.2, 168.1)			* AR 11654 E side, fixed pointing.								
01/21 06:28:46 - 01/21 10:44:48	Fixed (927.2, 168.1)			# Cont.								
01/21 14:34:16 - 01/21 18:05:16	Track (253.8, 314.3)	^{@ 01/21 14:30:00}				* EIS obs of old AR remnant (old AR 11635) near central meridian.						
01/21 18:15:16 - 01/22 05:52:16	Fixed (927.2, 168.1)			# E side of AR 11654, fixed pointing.								
01/22 06:02:16 - 01/24 12:20:00	Fixed (927.2, 168.1)			# Cont.								
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8				Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

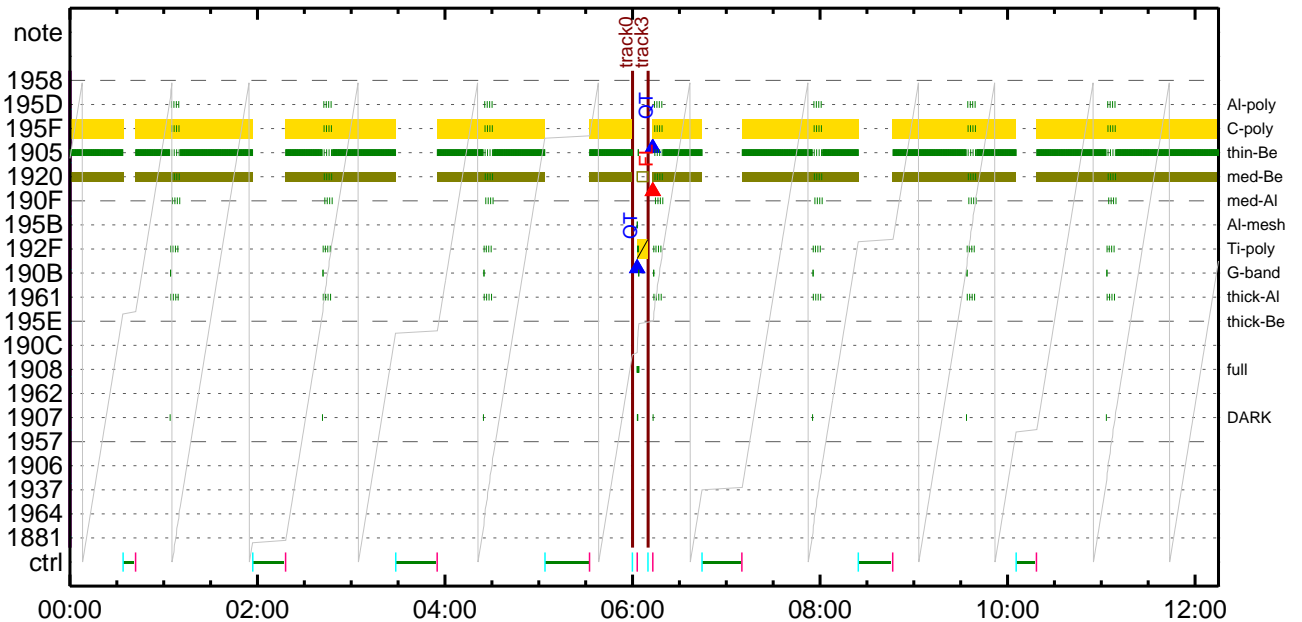
CMDI #0194 2013/01/19



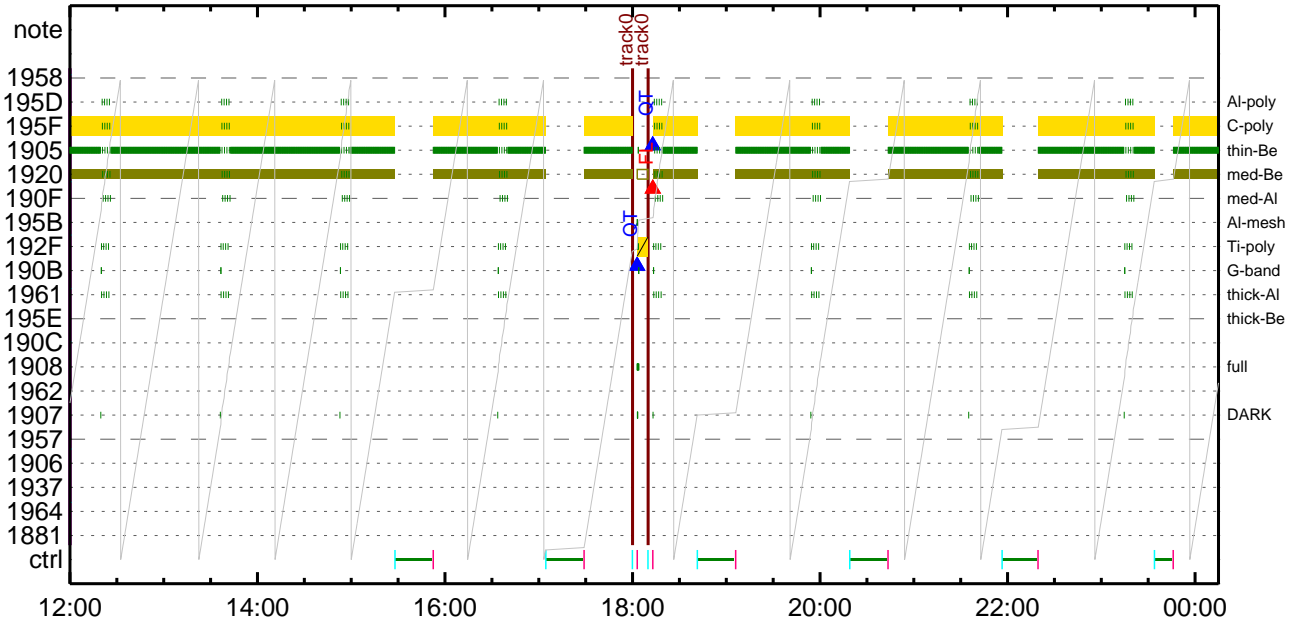
CMDI #0194 2013/01/19



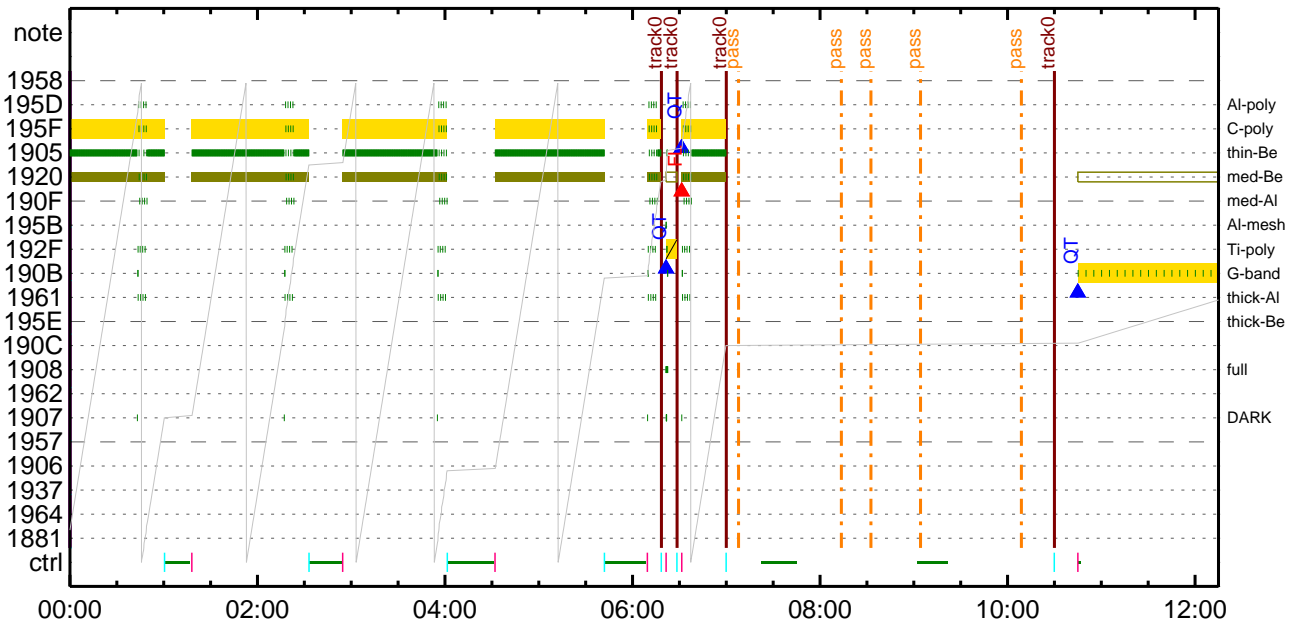
CMDI #0194 2013/01/20



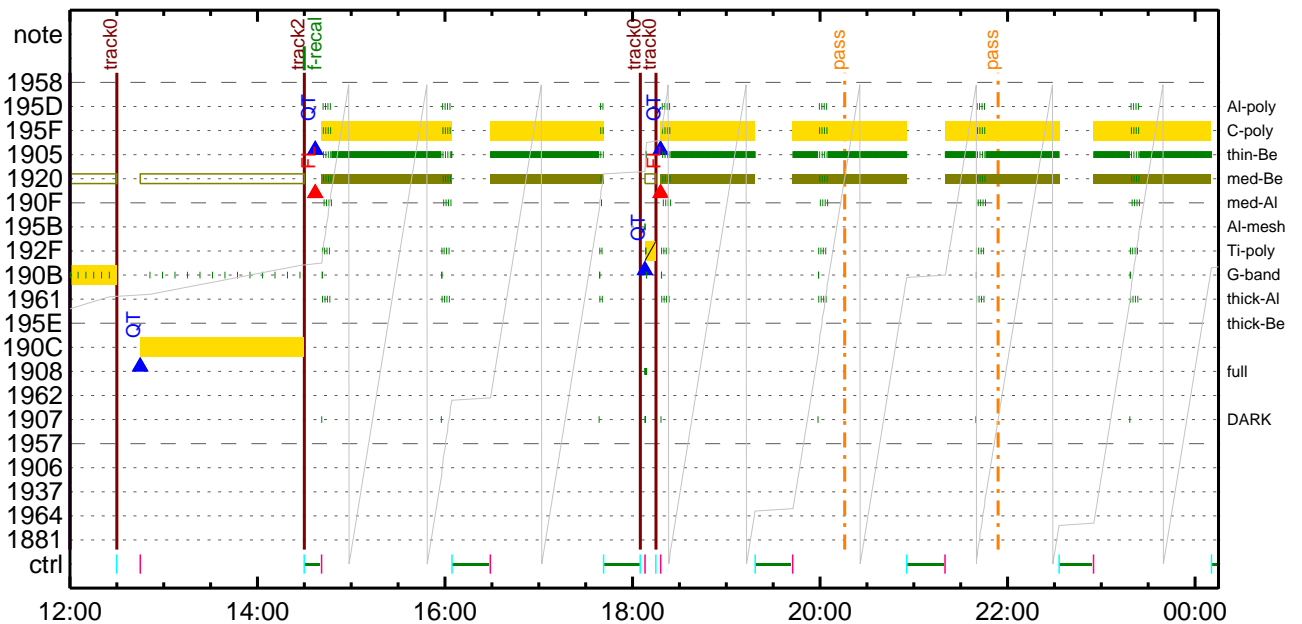
CMDI #0194 2013/01/20



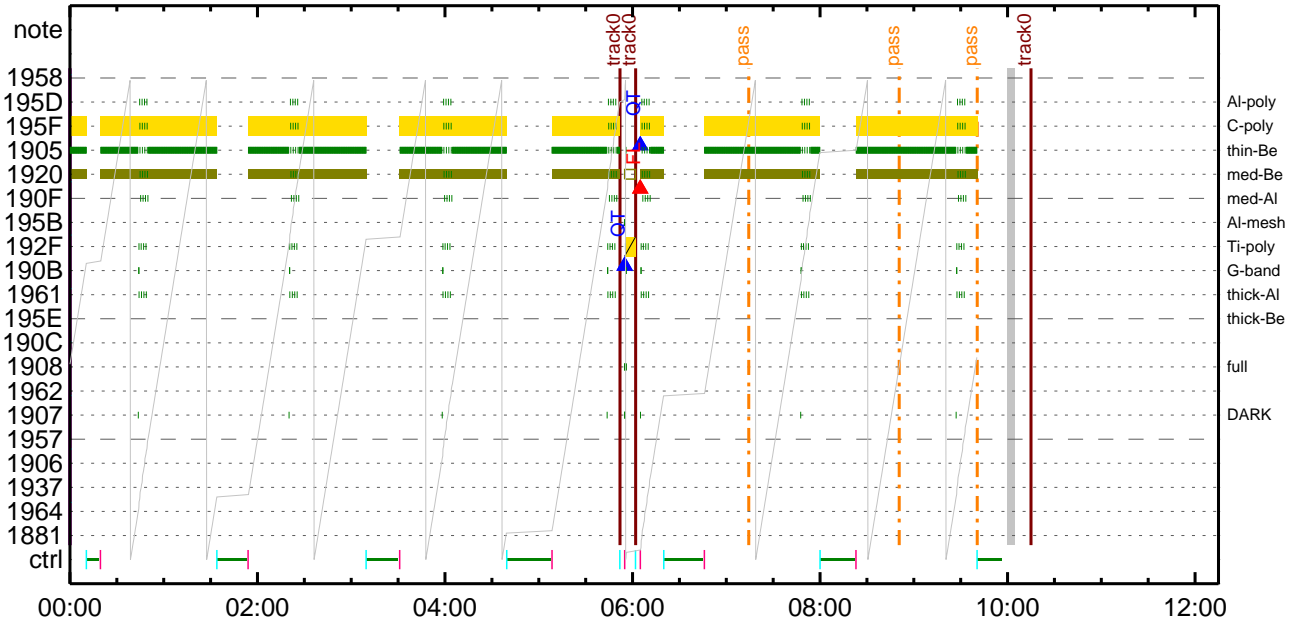
CMDI #0194 2013/01/21



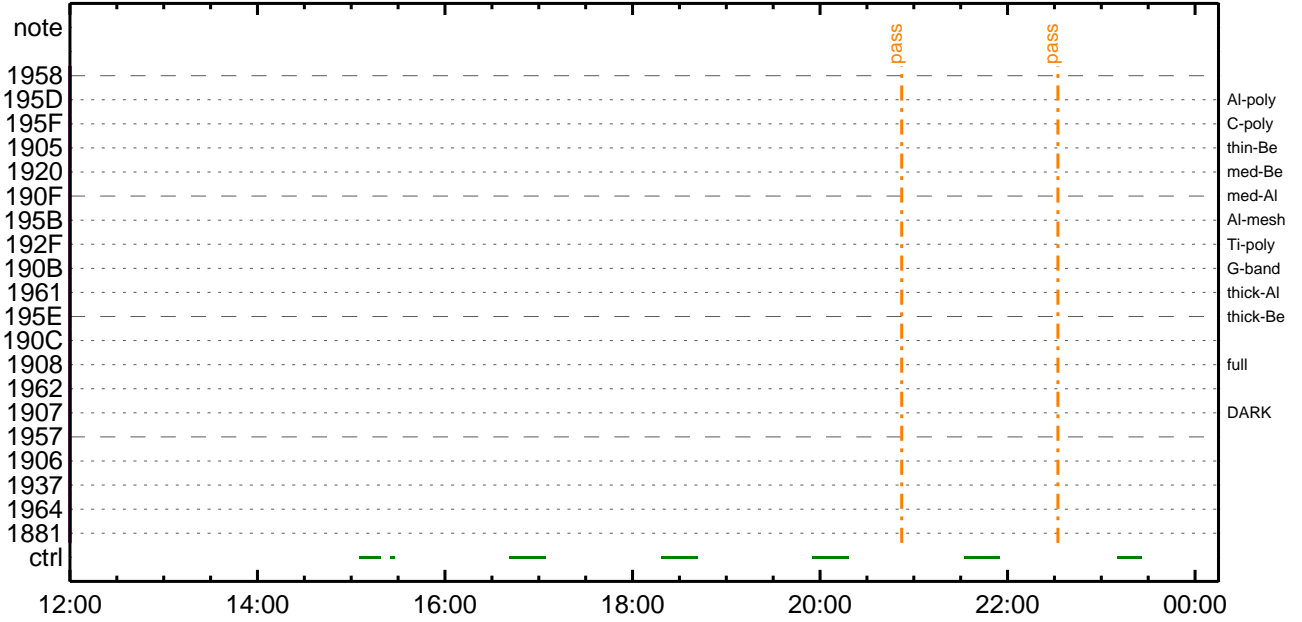
CMDI #0194 2013/01/21



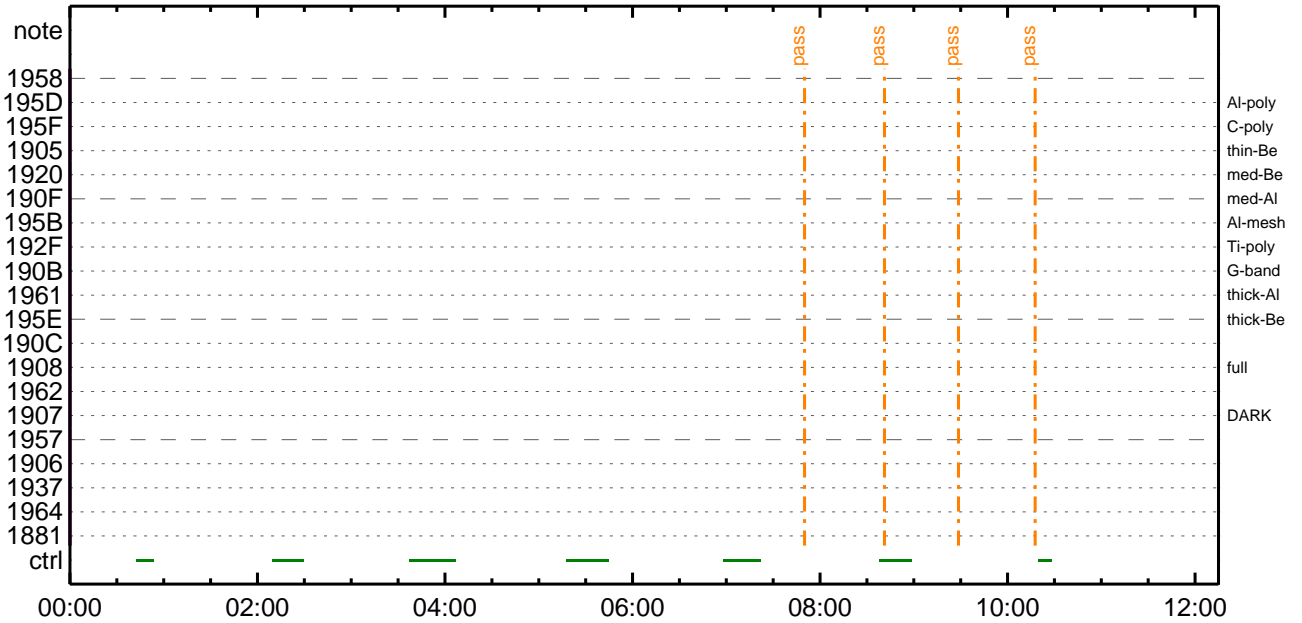
CMDI #0194 2013/01/22



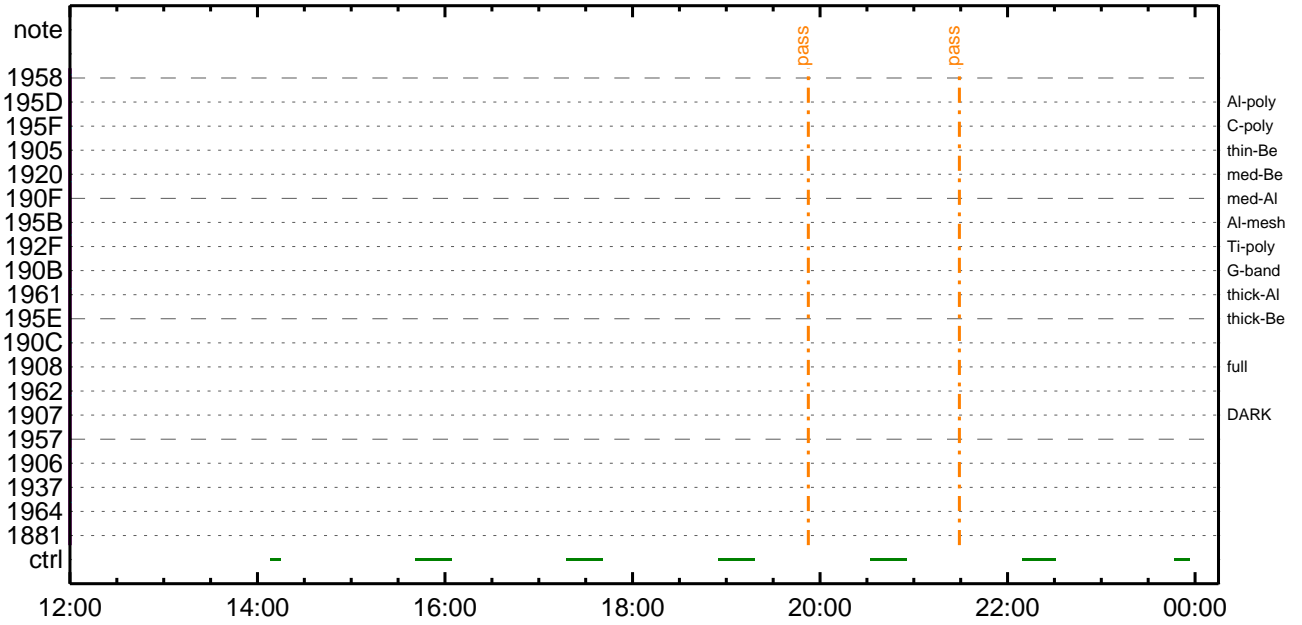
CMDI #0194 2013/01/22



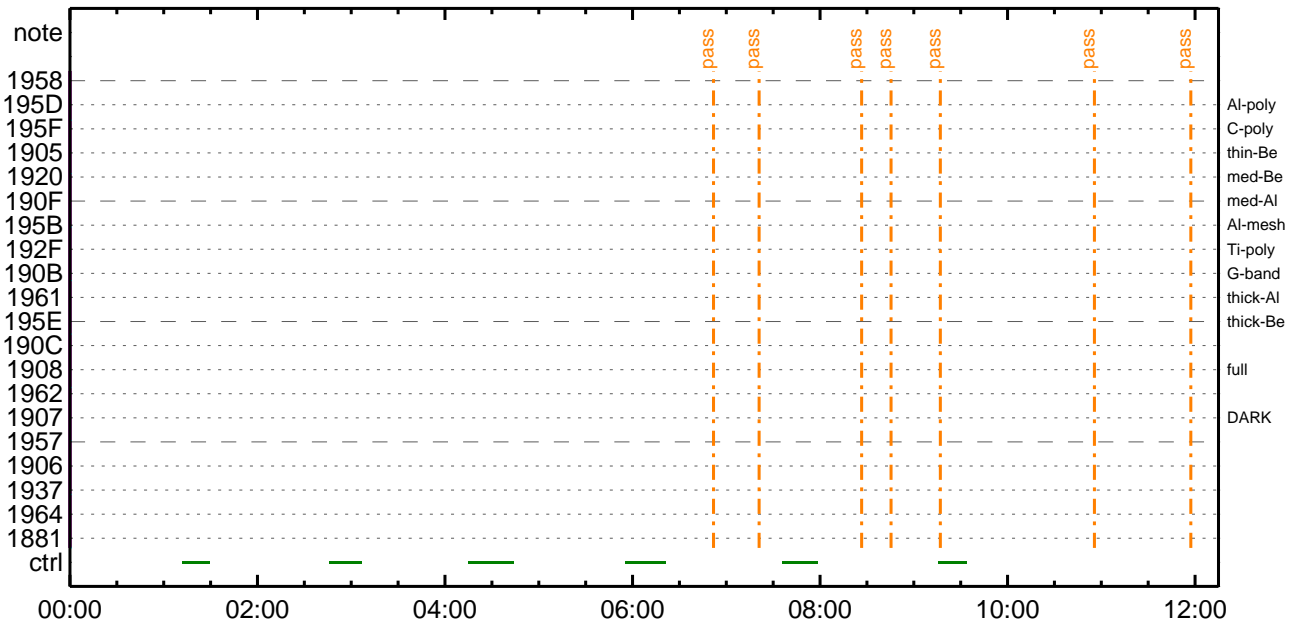
CMDI #0194 2013/01/23



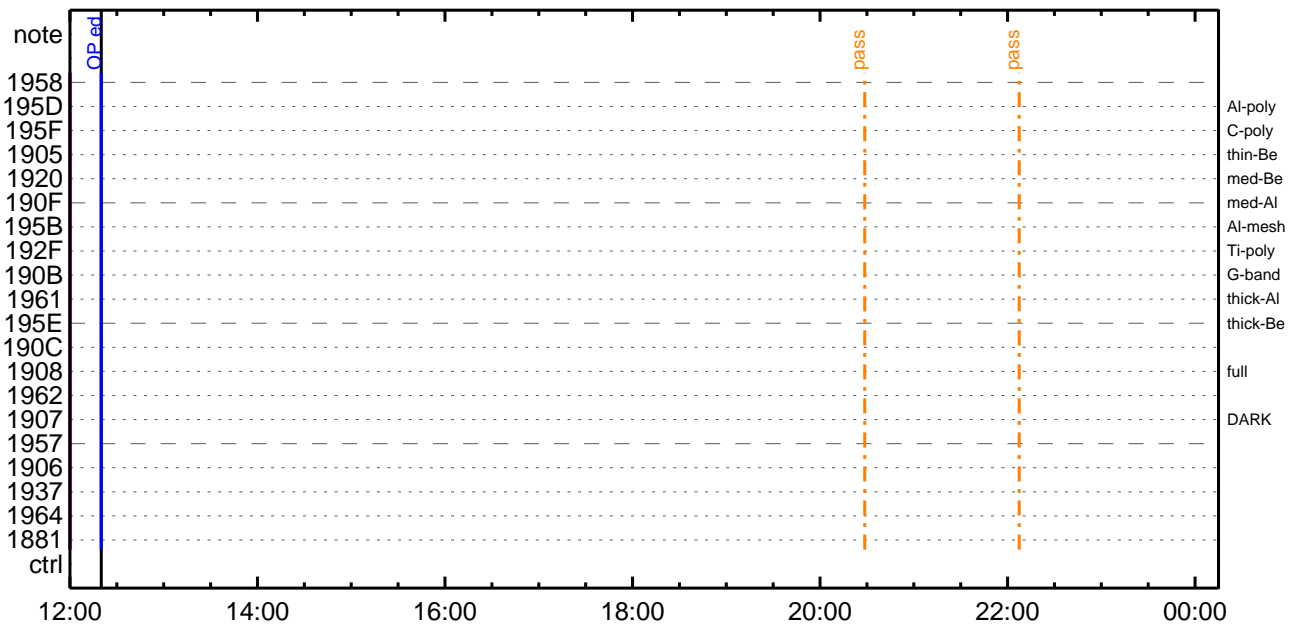
CMDI #0194 2013/01/23



CMDI #0194 2013/01/24



CMDI #0194 2013/01/24



(a) Spacecraft Operation Procedure (real-commands)

main-400 2013-01-19 14:09:21 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÄYſYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YŒYÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ëð¿áâó•µ°œ»í×ÁçðíŸçYÄY×Yí;¼YÉ;ËÈè%µ•ííË;ËóË%çÖá•α¿¼í¹çñí;çÄ®, ùα¹αóβαçÄ+¿®•αËααα³αË;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+¿µ;ON
0016 C. *****
0017 C. ç" °ÄÄ, í×ËYñáLòSβαçñí»P´Ôαð¹íí, α•; çÉÔí×αËXÄÓONαí¹ÔαËíαËαα³αË;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÿóYÉYíYÄY-¾ÖÄÖα-òÄÄèα•α¿αé; ç°Ë²¼αí°ËÄ, ¼é%çαðð¼Ä¹Ôα¹αé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ËÄ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëα•α¿α¼¼í¹çñí; ç°Ë²¼αí°ËÄ¹Ôα»α°; çDCBC-150αθ¿ËÈαà;f
0035 C.
0036 . C. ;ãPT1°ËÄ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÄYËÄÜÁÔ;ËÄ•Ä°²óÈð;Ë, áαí°ËÄ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÄ, α-¼«Ë°Äã»ßα•α¿, á; ç°Ë²¼αðð¼Ä¹Ôα¹αé;f
0055 C. YçYóYÄYËÄÜÁÔøáÄ•Ä°²óÈðα-¼¼áα¼¼í¹çñí´°í»α¹αóêβαçÄÔαÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ËÄ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëα•α¿α¼¼í¹çñí; ç°Ë²¼αí°ËÄ¹Ôα»α°; çDCBC-151αθ¿ËÈαà;f
0061 C.
0062 . C. ;ãPT2°ËÄ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÄYËÄÜÁÔ;ËÄ•Ä°²óÈð;Ë, áαí°ËÄ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÄ, Äã»ß;çXÄ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÄ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 C. ;ãXÄ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä |YAYOYx
0100 C. *****
0101 C.
0102 C. äOP/OGY1;4YE;ä
0103 S. OP op-400:OP
0104 (
0105 S. OG og-400:OG
0106 (
0107 C.
0108 C. äNMOG&OPÏÏ°èYAYOYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½^ä Î»òð³ÏÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGâÏÏÏ¹ç•è²ÏOKòð³ÏÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½^ä Î»òð³ÏÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGâÏÏÏ¹ç•è²ÏOKòð³ÏÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½^ä Î»òð³ÏÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPâÏÏÏ¹ç•è²ÏOKòð³ÏÇ§
0165 C.
0166 C. ***** òÈ²¼òÏÏÏ¹ç•è²ÏOKòð³ÏÇ§ *****
0167 C. DHUÏâ;4YE;ä |YAYOYx½^ä Î»òð³ÏÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò-Ä÷ç@NGâÏÏÏ¹ç;ç°È²¼òÏÏÏ¹ç•è²ÏOKòð³ÏÇ§ *****
0180 C. òÈ²¼ççSETòEDUMPAÏÏÏ¹ç•è²ÏOKòð³ÏÇ§ *****
0181 C.
0182 C. TIY³YÏYÓYÏÏòðÄÏÏÏ¹ç(UT)
0183 +. TI 2013-01-19 09:47:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-01-19 09:47:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-01-19 09:47:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-01-19 09:51:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αİÄè%īīñαîŷÄŷ§ŷÄŷ-¹âîÛ
0200 C.      çç[HK1_TI_CMD_ENA/DIS] EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM] EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM] EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC] EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1] EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0] EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM] EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM] EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM] EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO] EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME] EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE] EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE] EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG] EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.      çç[HK1_DMP_CHK_FLG] EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¼È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷì;¼ŷÈ;Èαòîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO] EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME] EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE] EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE] EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-01-19 09:51:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-01-19 09:51:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-01-19 09:51:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM] EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-01-19 09:51:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM] EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαİ»ö¼ŷαÈÄðα¹αèDCBC•x²è *****
0276 C. (¼ä°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñαÈ'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 1ml4s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-01-19 09:51:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0153 C. (%ã°îÿÓÿÄÿËÿPÿËÿÄÿçÿèñË¼¼¼¼¼»Û¹ñè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÿDÿ¹•Ï Daily±¿ÎÑñË'Ø¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÁÿSÿÿÄÿ-¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_FLD_RESET
0099 BC (da)
0100 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0101 BC (c4 07)
0102 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0103 BC (c5 10)
0104 . C. ----- Success Verify ? OK / NG ____
0105 C.
0106 C.
0107 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0108 C.
0109 +. DC 07-F0 MDP_XRT_MODE_OBSV
0110 BC (c2)
0111 +. TI 2013-01-19 09:51:02.0
0112 DC 07-F0 MDP_XRT_MODE_OBSV
0113 BC (c2)
0114 . C. ----- Success Verify ? OK / NG ____
0115 C.
0116 C. ***** XRT END *****
0117 C.
0118 . C. ***** MDP `uÃîqî»ö%ÿoËÂð¹æDCBC•x²è *****
0119 C. (%ã°îÿÓÿÃÿËÿPÿËÿãÿçÿèæ%¼qç¼Ã»Û¹æè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ÿDÿ¹•î Daily±;îÑæË'Ø¹æèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

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*** OP Sequence for XRT ***

2013/01/19	10:02:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2013/01/19	10:18:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2013/01/19	10:33:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2013/01/19	10:33:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/01/19	10:34:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/01/19	10:34:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0				d9	
2013/01/19	10:34:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0				c9	
2013/01/19	10:34:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2013/01/19	10:36:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	14
2013/01/19	10:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2013/01/19	10:50:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2013/01/19	11:06:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2013/01/19	11:22:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2013/01/19	11:32:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2013/01/19	11:48:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2013/01/19	12:04:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2013/01/19	12:20:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2013/01/19	12:30:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2013/01/19	12:40:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2013/01/19	12:56:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2013/01/19	13:12:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2013/01/19	13:28:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2013/01/19	13:36:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2013/01/19	13:36:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/01/19	13:37:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/01/19	13:37:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2013/01/19	13:37:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2013/01/19	13:37:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2013/01/19	13:37:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2013/01/19	13:39:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2013/01/19	13:39:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	12
2013/01/19	13:39:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0				c5	10
2013/01/19	13:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2013/01/19	14:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2013/01/19	14:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2013/01/19	14:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2013/01/19	14:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2013/01/19	15:05:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	15:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2013/01/19	16:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2013/01/19	16:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2013/01/19	16:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2013/01/19	16:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2013/01/19	16:51:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	16:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2013/01/19	17:41:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2013/01/19	17:41:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							

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2013/01/19	17:41:30.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2013/01/19	17:41:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/01/19	17:41:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/01/19	17:41:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/19	17:44:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2013/01/19	17:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/19	17:51:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/19	17:51:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/01/19	17:51:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	01	00	00	00
2013/01/19	17:51:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/01/19	17:51:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/01/19	17:51:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/01/19	17:51:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/19	17:54:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/19	17:54:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2013/01/19	17:54:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2013/01/19	17:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/19	18:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/19	18:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/19	18:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/19	18:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/19	18:28:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	18:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/19	19:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/19	19:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/19	19:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/19	19:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/19	20:05:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	20:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/19	21:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/19	21:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/19	21:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/19	21:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/19	21:42:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/19	21:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/19	22:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/19	22:00:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/01/19	22:00:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	f6	d1	ac
2013/01/19	22:00:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/01/19	22:00:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/01/19	22:00:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/01/19	22:00:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/19	22:03:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/19	22:03:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2013/01/19	22:03:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2013/01/19	22:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/19	22:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/19	22:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]							

2013/01/19	22:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/19	23:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/19	23:15:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/19	23:16:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	00:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	00:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	00:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	00:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	00:41:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	00:42:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	01:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	01:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	01:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	02:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	02:17:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	02:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	03:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	03:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	03:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	03:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	03:54:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	03:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	05:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	05:04:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	05:04:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	05:07:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	05:31:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	05:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/01/20	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/01/20	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/01/20	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/01/20	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/20	06:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2013/01/20	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/01/20	06:10:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	03 00 00 00 00			
2013/01/20	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/01/20	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/01/20	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/01/20	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/20	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	06:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2013/01/20	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2013/01/20	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	06:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	06:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2013/01/20	06:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	06:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	07:09:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	07:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	08:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	08:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	08:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	08:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	08:45:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	08:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	10:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	10:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	10:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	10:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	10:17:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	10:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	15:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	15:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	15:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	15:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	15:51:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	15:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	17:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	17:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	17:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	17:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/20	17:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	17:28:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	17:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/20	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/01/20	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/01/20	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/01/20	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/01/20	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/20	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2013/01/20	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	18:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/20	18:10:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/01/20	18:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	00 f1 0f ad 92			
2013/01/20	18:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/01/20	18:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/01/20	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/01/20	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/20	18:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/20	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2013/01/20	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2013/01/20	18:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/20	18:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2013/01/20	18:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/20	18:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/20	19:05:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/20	19:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/20	20:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/20	20:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/20	20:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/20	20:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/20	20:42:30.0	XRT_Custom_434_OG [0x1b2]				
2013/01/20	20:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/20	21:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/20	21:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/20	21:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/20	21:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/20	22:18:30.0	XRT_Custom_434_OG [0x1b2]				
2013/01/20	22:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/20	23:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/20	23:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/20	23:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/20	23:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/20	23:45:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/20	23:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/21	01:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/21	01:00:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/21	01:00:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/21	01:03:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/21	01:17:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/21	01:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/21	02:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/21	02:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/21	02:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/21	02:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/21	02:53:30.0	XRT_Custom_434_OG [0x1b2]				
2013/01/21	02:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/21	04:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/21	04:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/21	04:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/21	04:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/21	04:31:00.0	XRT_Custom_434_OG [0x1b2]				
2013/01/21	04:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/21	05:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/21	05:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/21	05:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/21	05:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/21	06:08:30.0	XRT_Custom_434_OG [0x1b2]				
2013/01/21	06:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/21	06:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/21	06:18:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/01/21	06:18:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2013/01/21	06:18:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9

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2013/01/21	06:18:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/01/21	06:18:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/21	06:21:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2013/01/21	06:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/21	06:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/21	06:28:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/01/21	06:28:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	f1 0f ad 92	
2013/01/21	06:28:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/01/21	06:28:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/01/21	06:28:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/21	06:28:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/21	06:31:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/21	06:31:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2013/01/21	06:31:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2013/01/21	06:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/21	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/21	07:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00	
2013/01/21	10:29:54.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/21	10:30:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	ac 00 00 00	
2013/01/21	10:44:28.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/01/21	10:44:48.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/01/21	10:44:50.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/01/21	10:44:52.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/21	10:44:54.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/21	10:44:56.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/21	10:44:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2013/01/21	10:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/21	12:29:54.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/21	12:30:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	00 00 00 54 00	
2013/01/21	12:44:28.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/01/21	12:44:48.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/01/21	12:44:50.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/01/21	12:44:52.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/21	12:44:54.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/21	12:44:56.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/21	12:44:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2013/01/21	12:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/01/21	14:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/01/21	14:29:56.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]	XRT_FOCUS_RECAL	2	07-F8	78	00	
2013/01/21	14:30:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02	00 00 00 00	
2013/01/21	14:33:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/01/21	14:34:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/01/21	14:34:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/01/21	14:34:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/01/21	14:34:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/01/21	14:36:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/01/21	14:36:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	

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2013/01/21	14:36:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10				
2013/01/21	14:40:00.0	XRT_Custom_434_OG [0x1b2]									
2013/01/21	14:41:00.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/01/21	16:04:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	16:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/01/21	16:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/01/21	16:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/01/21	16:28:00.0	XRT_Custom_434_OG [0x1b2]									
2013/01/21	16:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/01/21	17:41:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	17:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/01/21	17:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/01/21	17:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/01/21	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	18:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]									
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00				
2013/01/21	18:05:00.0	AOCS_Ore-point_Start_3_OG [0x099]									
			AOCU_NM	5	02-76	00 00 00 00	00 00				
2013/01/21	18:05:16.0	XRT_FLD_DIS_404_OG [0x194]									
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2013/01/21	18:05:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]									
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2013/01/21	18:05:20.0	XRT_ARS_DIS_406_OG [0x196]									
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/01/21	18:07:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]									
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d				
2013/01/21	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/01/21	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	18:14:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]									
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00				
2013/01/21	18:15:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]									
			AOCU_NM	5	02-76	00 f1 0f ad	92				
2013/01/21	18:15:16.0	XRT_FLD_ENA_428_OG [0x1ac]									
			MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/01/21	18:15:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]									
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/01/21	18:15:20.0	XRT_AEC_RESET_423_OG [0x1a7]									
			MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/01/21	18:15:22.0	XRT_ARS_DIS_437_OG [0x1b5]									
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/01/21	18:17:54.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/01/21	18:17:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]									
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2013/01/21	18:17:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]									
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10				
2013/01/21	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/01/21	19:18:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	19:18:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/01/21	19:18:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/01/21	19:21:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/01/21	19:41:30.0	XRT_Custom_434_OG [0x1b2]									
2013/01/21	19:42:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/01/21	20:55:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	20:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/01/21	20:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/01/21	20:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/01/21	21:19:00.0	XRT_Custom_434_OG [0x1b2]									
2013/01/21	21:20:00.0	XRT_CTRL_AUTO_413_OG [0x19d]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/01/21	22:33:00.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/01/21	22:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/01/21	22:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/01/21	22:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					

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2013/01/21	22:54:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/21	22:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	00:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	00:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	00:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	00:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/22	00:18:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/22	00:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	01:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	01:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	01:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	01:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/22	01:53:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/22	01:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	03:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	03:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	03:09:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	03:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/22	03:30:00.5	XRT_Custom_434_OG [0x1b2]							
2013/01/22	03:31:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	04:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	04:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	04:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	04:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/22	05:07:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/22	05:08:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	05:51:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	05:51:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/22	05:52:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/22	05:52:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/22	05:52:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/22	05:52:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/22	05:54:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/22	05:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	06:01:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/22	06:02:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00 f1 0f ad 92				
2013/01/22	06:02:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/22	06:02:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/22	06:02:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/22	06:02:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/22	06:04:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	06:04:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/01/22	06:04:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/22	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	06:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	06:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	06:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	06:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2013/01/22	06:45:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/22	06:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	08:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	08:00:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	08:00:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	08:03:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/22	08:22:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/22	08:23:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/22	09:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/22	09:40:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/22	09:40:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/22	09:43:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/22	10:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				