

XRT Timeline to be uploaded on 2013/01/24

Period: 2013/01/24 12:20:00 - 2013/01/29 08:57:00

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Normal mode

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XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18

Term	Pointing (x, y)	Comment
01/25 06:06:30 - 01/25 06:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min
01/25 18:03:00 - 01/25 18:09:54	Fixed (0.0, 0.0)	synoptic
01/26 06:10:30 - 01/26 06:17:24	Fixed (0.0, 0.0)	synoptic, shifted 7.5 min
PROG= 13 1-time(s)		
└─ Subr= 1 1-time(s) 14.0sec		
└─ Seqn= 64 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 70 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 67 1-time(s) 2.0sec		
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 69 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 68 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1905: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long-2 - w leak image

Term	Pointing (x, y)	Comment
01/25 06:16:30 - 01/25 06:23:24	Fixed (-528.4, -528.4)	# XRT quadrant 1
PROG= 17 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 1 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 7 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1906: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long-w leak image

Term	Pointing (x, y)	Comment
01/25 06:26:30 - 01/25 06:33:30	Fixed (528.4, -528.4)	# XRT quadrant 2
PROG= 04 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 2 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 7 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1907: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long-w leak image												
Term	Pointing (x, y)		Comment									
01/25 07:01:30 - 01/25 07:08:24	Fixed (528.4, 528.4)		# XRT quadrant 3									
PROG= 06 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 3 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 7 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1908: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long-w leak image												
Term	Pointing (x, y)		Comment									
01/25 07:11:30 - 01/25 07:18:24	Fixed (-528.4, 528.4)		# XRT quadrant 4									
PROG= 08 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 4 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 7 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 1-time(s) 2.0sec												
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1960: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be + multifilter context, 512x512 at 1064 1048, 24s-cad - shorter G-band (33ms) w/ G-Band												
Term	Pointing (x, y)		Comment									
01/25 07:21:30 - 01/25 15:17:30	Track (817.3, 235.1) ^{® 01/25 07:18:30}		# AR 11660									
PROG= 14 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 61 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 27 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 25 150-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo												
Term	Pointing (x, y)		Comment									
01/25 15:43:30 - 01/25 17:59:54	Track (817.3, 235.1) ^{® 01/25 07:18:30}		# AR 11660									
01/25 18:13:00 - 01/26 06:07:24	Track (864.6, 226.8) ^{® 01/25 18:10:00}		# AR 11660									
01/26 06:20:30 - 01/26 09:41:00	Track (906.2, 216.9) ^{® 01/26 06:17:30}		# AR 11660									
PROG= 18 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn=100 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 19 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 65 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 94 70-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment										
01/25 07:21:30 - 01/25 15:17:30	Track (817.3, 235.1) ^{Ⓢ 01/25 07:18:30}	# AR 11660										
01/25 15:43:30 - 01/25 17:59:54	Track (817.3, 235.1) ^{Ⓢ 01/25 07:18:30}	# AR 11660										
01/25 18:13:00 - 01/26 06:07:24	Track (864.6, 226.8) ^{Ⓢ 01/25 18:10:00}	# AR 11660										
01/26 06:20:30 - 01/26 09:41:00	Track (906.2, 216.9) ^{Ⓢ 01/26 06:17:30}	# AR 11660										
PROG= 16 15-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 35 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 36 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

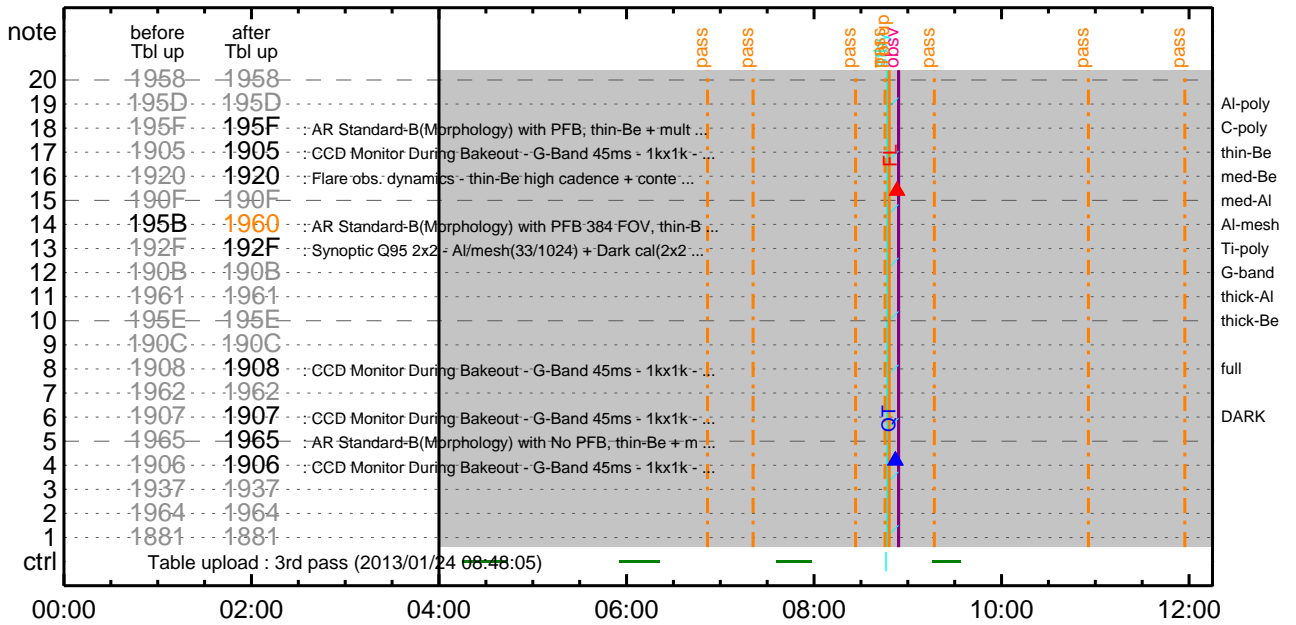
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Flare Detection

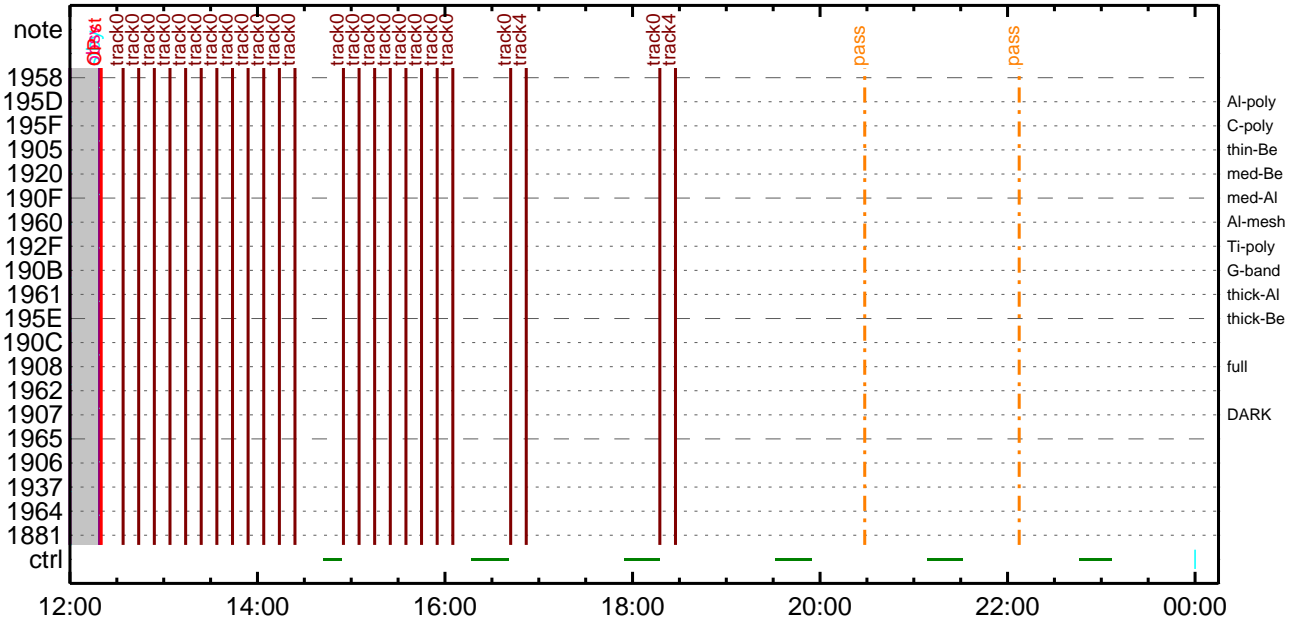
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FLD Patrol												
Term	Pointing (x, y)	Comment										
01/25 07:18:46 - 01/25 18:00:16	Track (817.3, 235.1) ^{Ⓢ 01/25 07:18:30}	# AR 11660										
01/25 18:10:16 - 01/26 06:07:46	Track (864.6, 226.8) ^{Ⓢ 01/25 18:10:00}	# AR 11660										
01/26 06:17:46 - 01/29 08:57:00	Track (906.2, 216.9) ^{Ⓢ 01/26 06:17:30}	# AR 11660										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

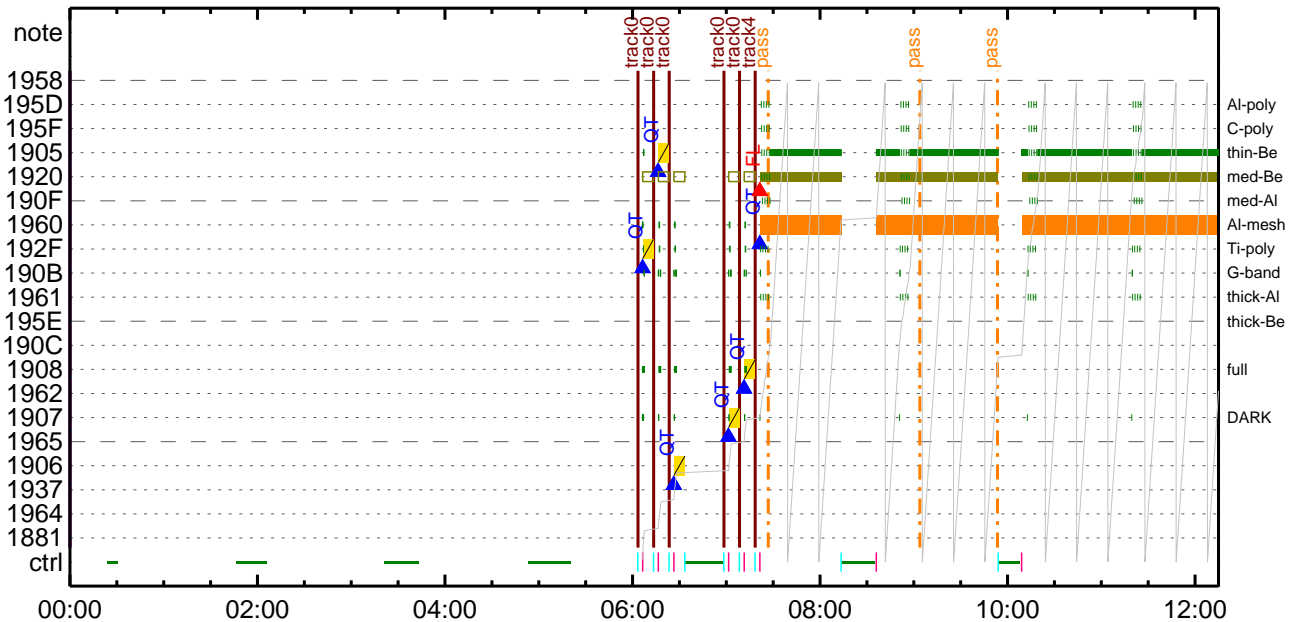
CMDI #0205 2013/01/24



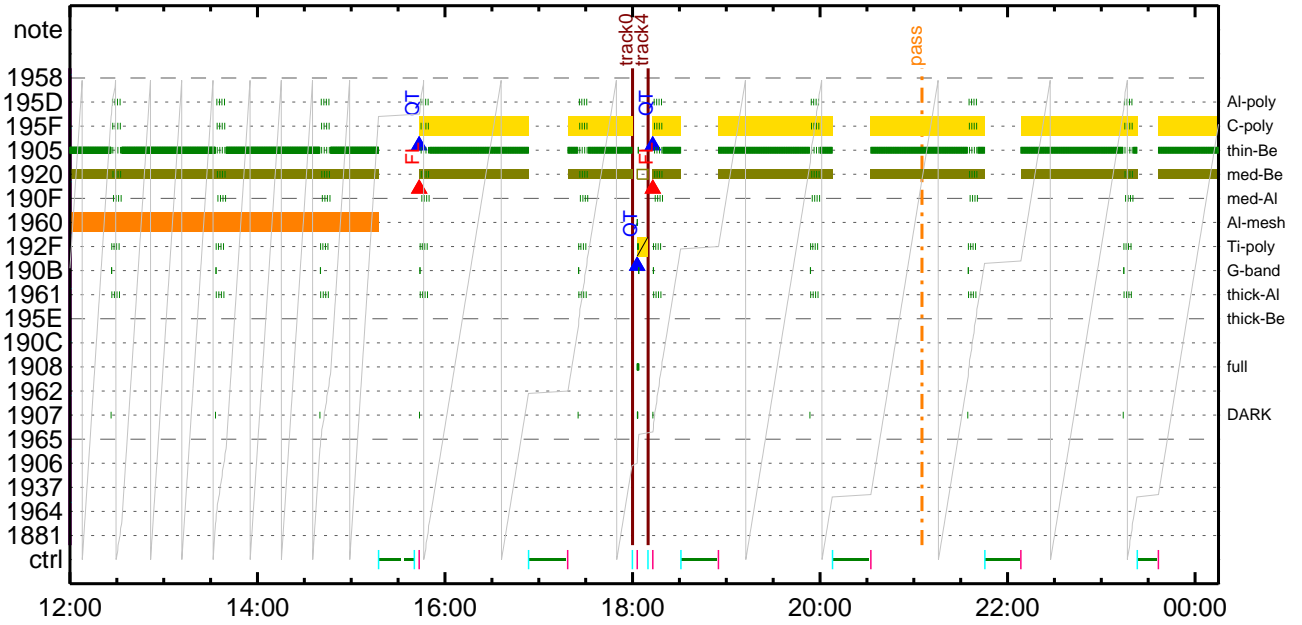
CMDI #0205 2013/01/24



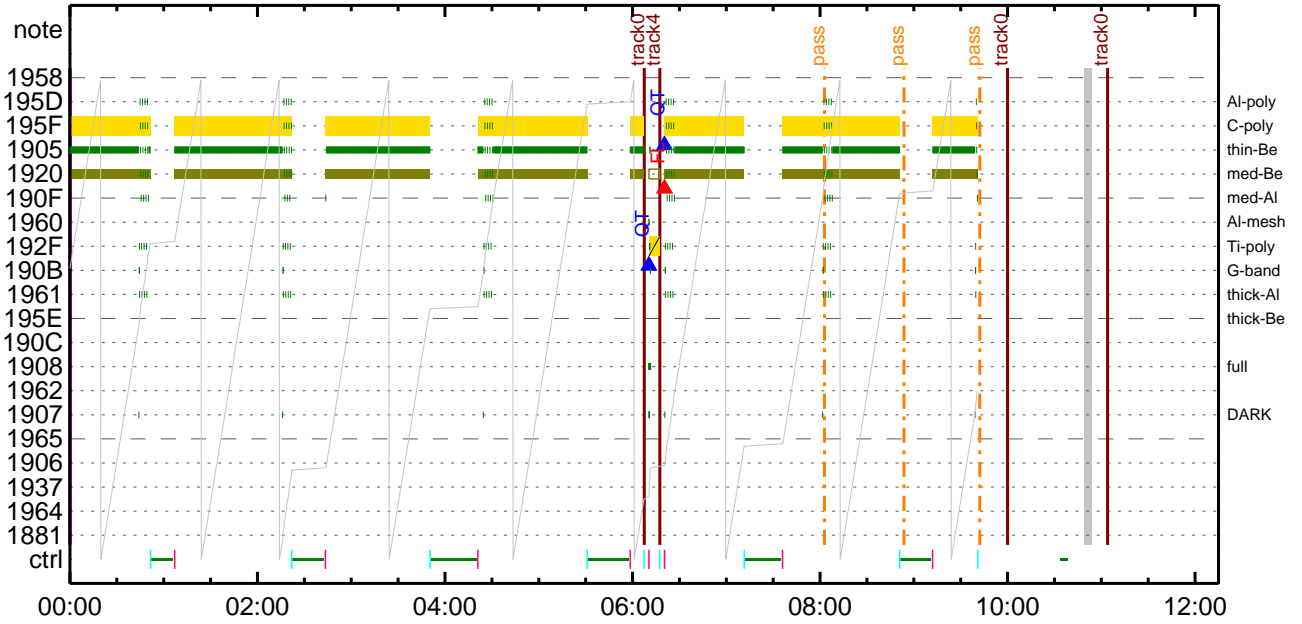
CMDI #0205 2013/01/25



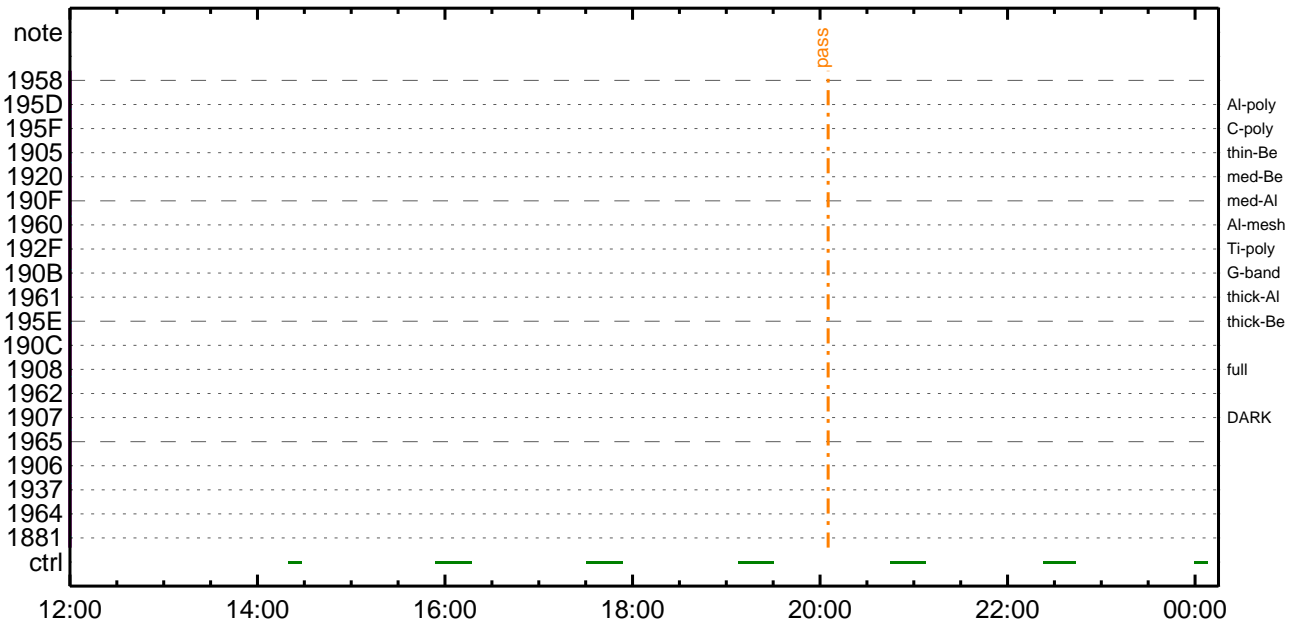
CMDI #0205 2013/01/25



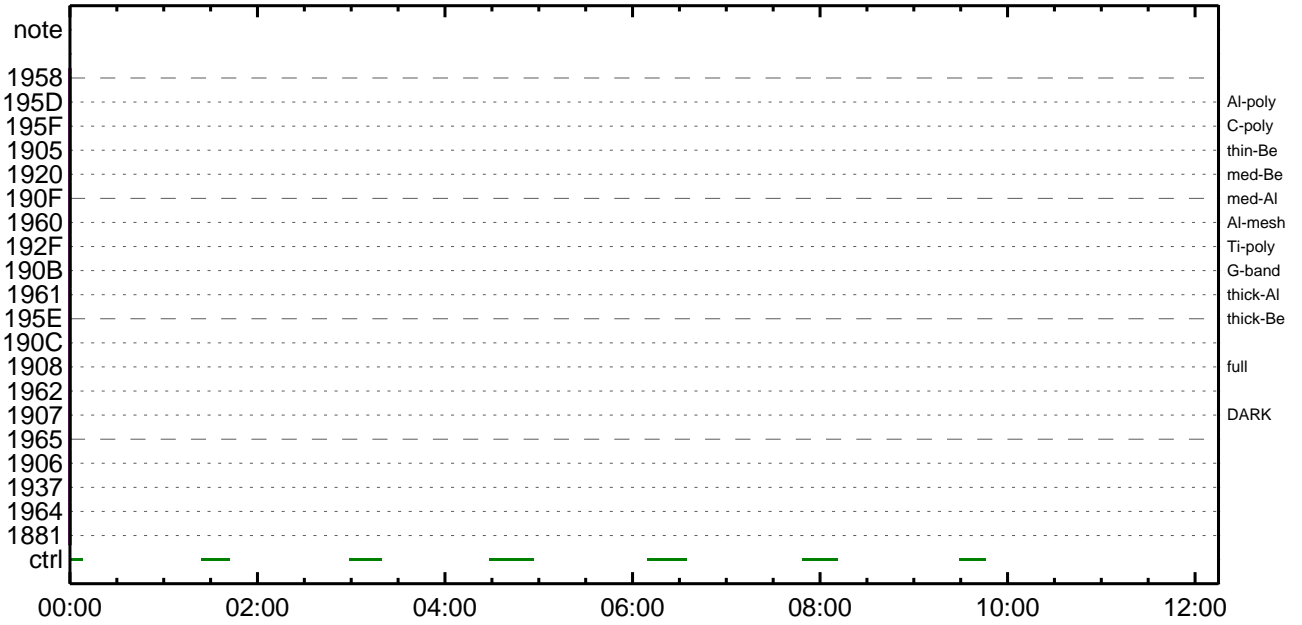
CMDI #0205 2013/01/26



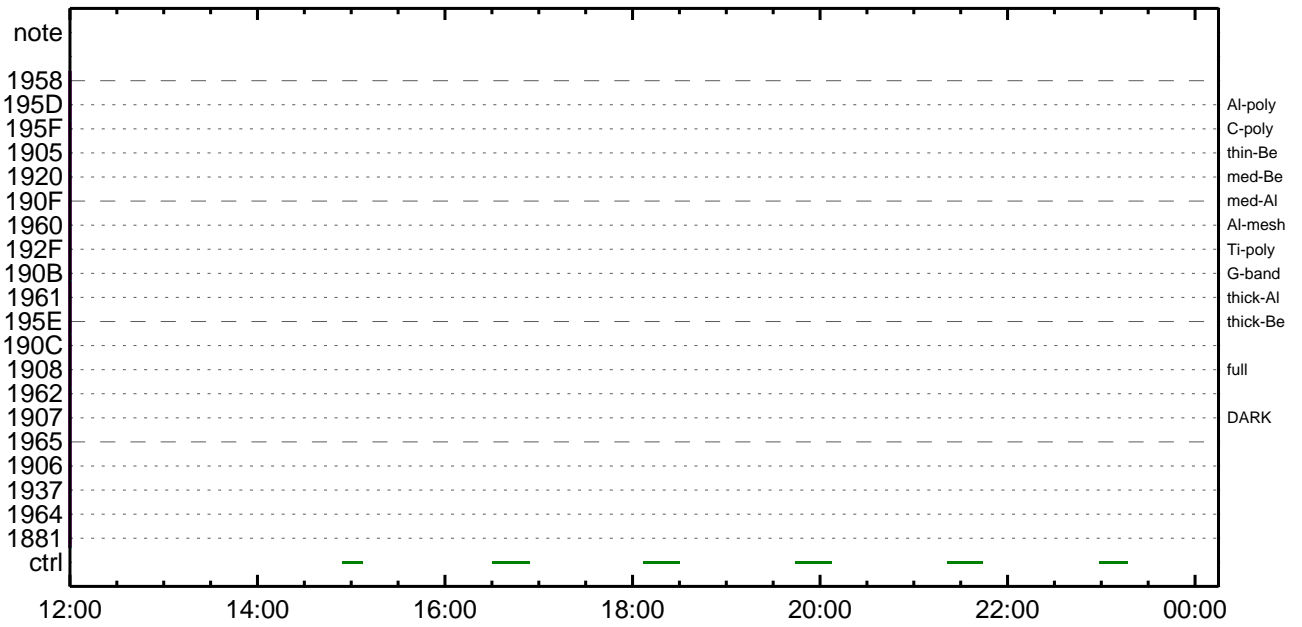
CMDI #0205 2013/01/26



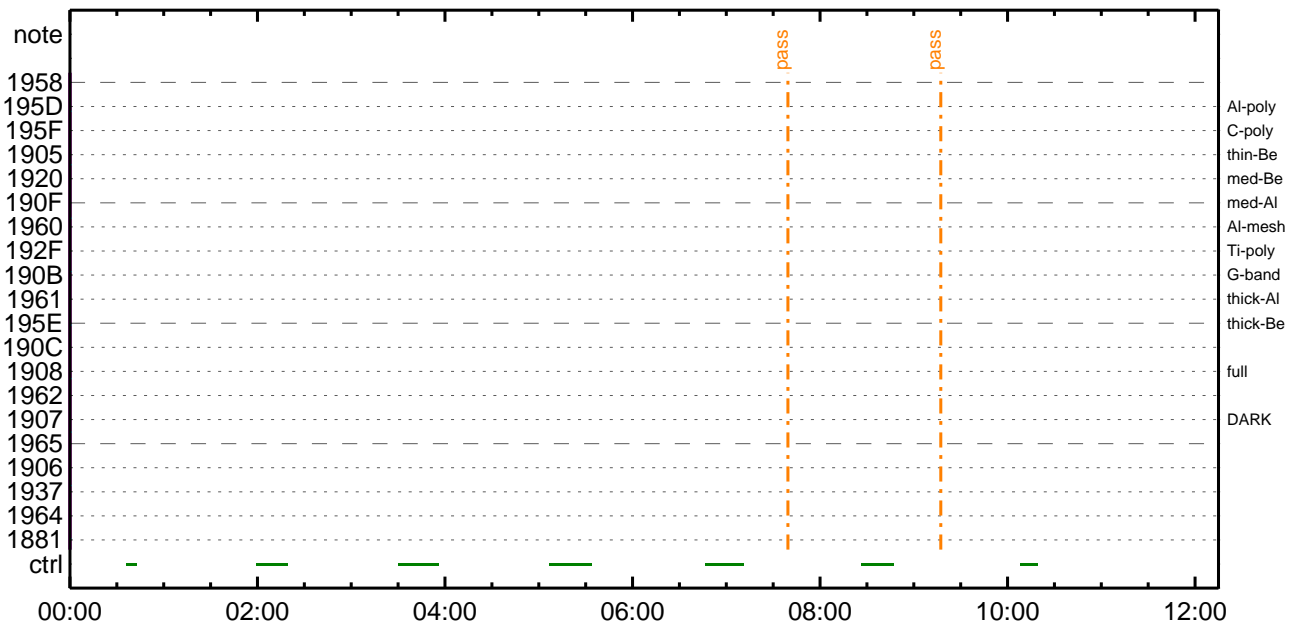
CMDI #0205 2013/01/27



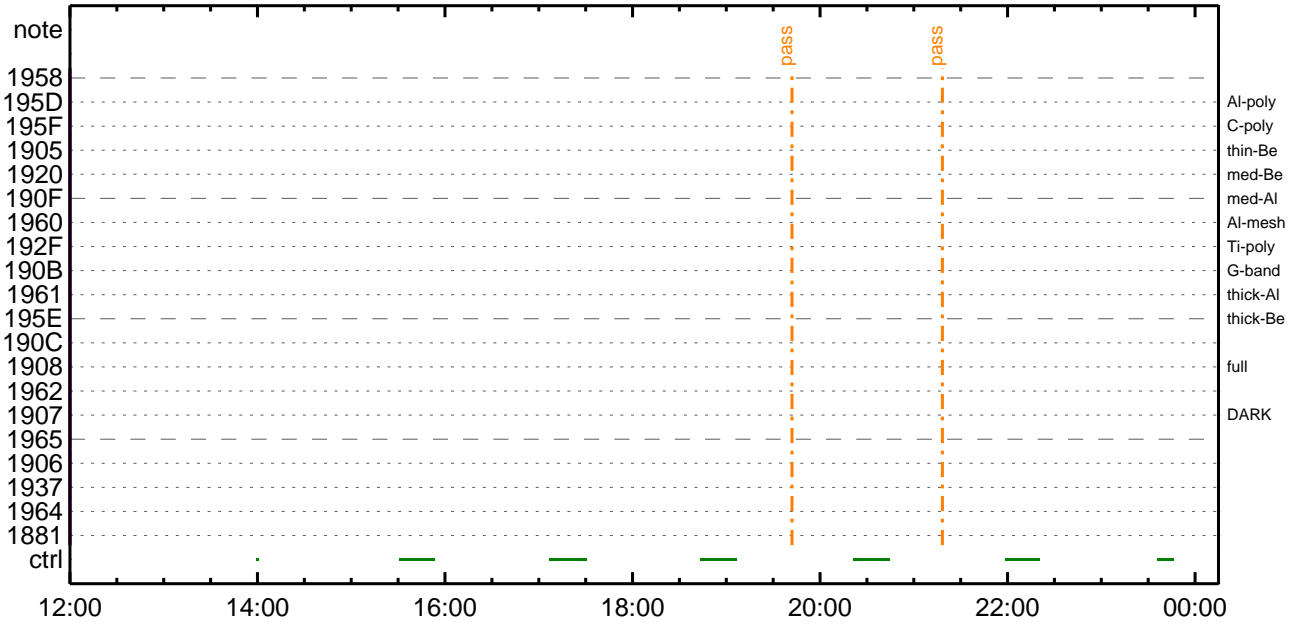
CMDI #0205 2013/01/27



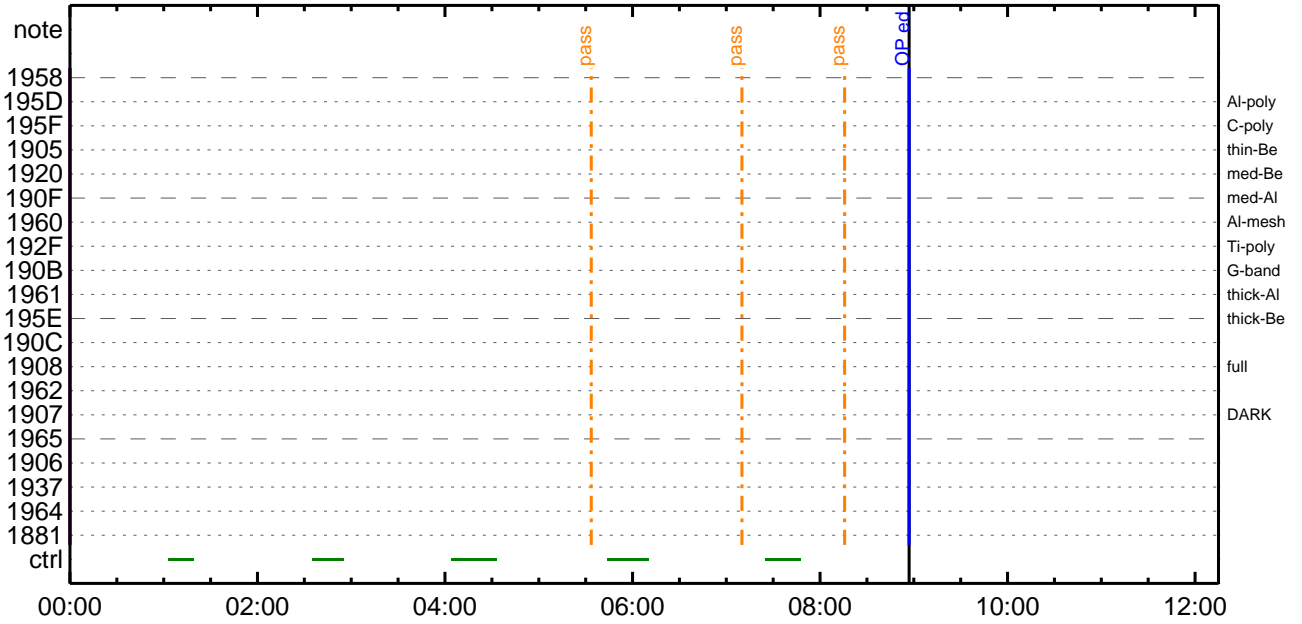
CMDI #0205 2013/01/28



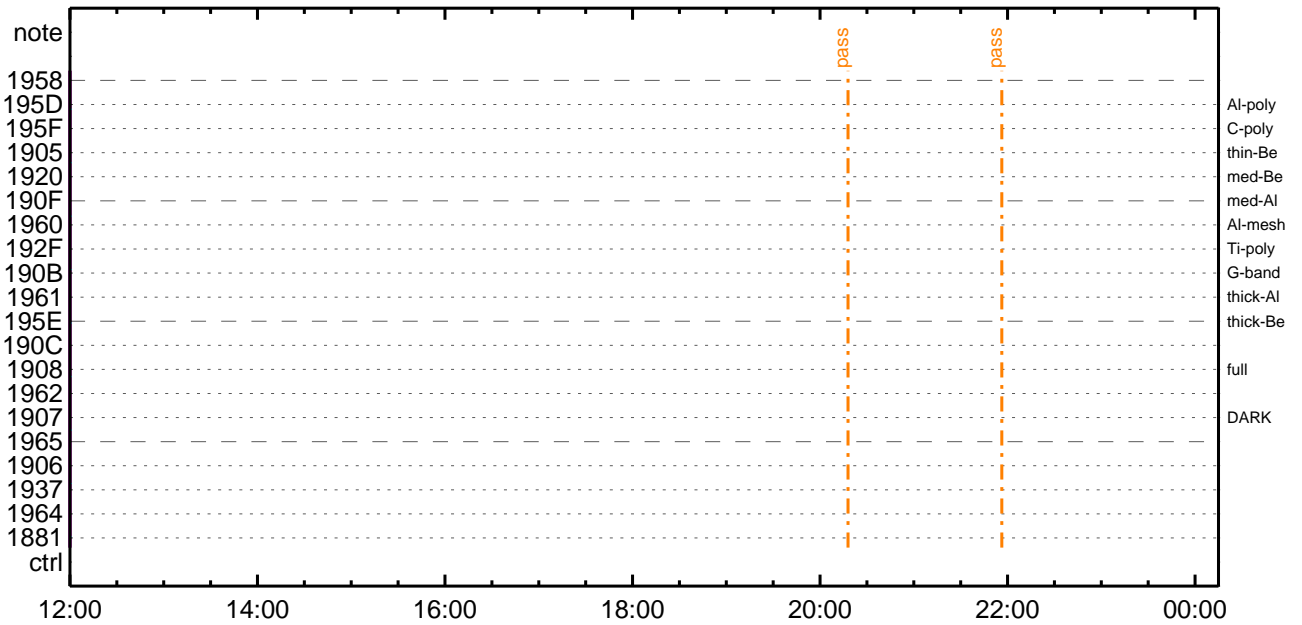
CMDI #0205 2013/01/28



CMDI #0205 2013/01/29



CMDI #0205 2013/01/29




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0096 C.          0303; 03SET0EDUMP01E01YNY1000|030E;E
0097 C.
0098 C. TTY3YBY6YH000AD1 (UT)
0099 +. TI 2013-01-24 12:15:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2013-01-24 12:15:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2013-01-24 12:15:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2013-01-24 12:19:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          00[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. 0E2%01A0%0i0N01YAY$YAY-1a0U
0116 C.          00[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          00[HK1_TI_CMD_NUM]          EQ          4
0118 C.          00[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          00[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TTY3YBY6YH000AD1
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          00[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          00[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          00[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          00[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          00[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          00[HK1_PKT_FORM_NO]          EQ          7
0136 C.          00[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          00[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          00[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          00[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YAY6YX%2^I>003IC$
0142 C.          00[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBL01E10E10K003IC$
0145 C.
0146 C. DHUYA;YBYE;E%Y%,Yi;YBYE;E00IA01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          00[HK1_PKT_FORM_NO]          EQ          2
0150 C.          00[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          00[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          00[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2013-01-24 12:19:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2013-01-24 12:19:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2013-01-24 12:19:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 C.          [ ] [HK1_TI_CMD_NUM]        EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2013-01-24 12:19:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]        EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 03A010E0Y0EAD010EDCBC.02E *****
0192 C. (%A01Y0YAYEYBYEYAY0E0E%0004A>U010E)
0193 C. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C. Reset the ICU and perform code loading from EEPROM 0 to RAM
0130 C. The reset command must be send twice, no pause - Critical command
0131 +. DC 07-FC EIS_ICU_SOFT_RESET
0132 BC (f5)
0133 + DC 07-FC EIS_ICU_SOFT_RESET
0134 BC (f5)
0135 . C. Confirm that the ICU is in BOOT mode, EIS_MODE = BOOT
0136 C. Copy ICU software from EEPROM 0 to ICU RAM
0137 +. DC 07-FC EIS_COPY_ICU_SW0
0138 BC (2b 00)
0139 C. Confirm EIS_CMD_BC1/_BC2 = 0x 2B 00
0140 C.
0141 C. Change EIS mode to STBY
0142 +. DC 07-FC EIS_MODE_STBY
0143 BC (21 01)
0144 C. Wait for 10 sec / Confirm EIS_MODE = STBY
0145 C. Confirm ICU_SW_MAIN_ID=0x02, ICU_SW_SUB_ID=0x03
0146 C.
0147 +. DC 07-F0 MDP_STS_EIS_ERR_CLR
0148 BC (f0)
0149 C. Confirm MDP_STS_EIS_ERR = OK
0150 +. DC 07-FC EIS_ICU_MON_DIS
0151 BC (25 02)
0152 C. Confirm EIS_ICU_MON_FLG = DIS
0153 C.
0154 . C. ### STS_CHK OFF ###
0155 +. DC 07-FC EIS_DUMP_HKTBL
0156 BC (0f 07 00 00 00 02 80)
0157 C. Error happens in comparison at ISAS EGSE.
0158 . C. Register dumped data to ISAS EGSE as default HK memory data
0159 C. Upload RAM-sub ID= 881 (EIS_HKTBL)
0160 . S. RAM ram-881:EIS_HKTBL
0161 ( )
0162 C.
0163 +. DC 07-FC EIS_DUMP_HKTBL
0164 BC (0f 07 00 00 00 02 80)
0165 . C. OK
0166 C. Error must not happen.
0167 . C. ### STS_CHK ON ###
0168 C.
0169 +. DC 07-FC EIS_ICU_MON_ENA
0170 BC (25 01)
0171 C. Confirm EIS_ICU_MON_FLG = ENA
0172 C.
0173 . C. Load ICU MHC parameters
0174 +. DC 07-FC EIS_SET_MHC_OPEPAR
0175 BC (87 02 58 00 01 e0 70 00)
0176 BC (00 80 fc 41)
0177 BC (33 14 6d 00 96 02 bc 06)
0178 BC (f4 01 27)
0179 C. Confirm parameters 0x 00 96 with EIS Java-QL in ISAS 2F operation room
0180 C.
0181 . C. Enable mode change command
0182 +. DC 07-FC EIS_MODE_CHG_ENA
0183 BC (20)
0184 C. Confirm EIS_MODE_CHG_FLG = ENA
0185 C.
0186 . C. ***** MDP 'úÃîï»ö¼Ý«ÉÄð¹«èDCBC•x²è *****
0187 C. (%ã°îÿÖYÁYÈYËYÉYáYçYè«É¼«%Á»Û¹«è)
0188 . S. DC-BC dcbc-402:DCBC
0189 (MDP_known_event)
0190 C.
0191 C.
0192 . C. ***** ¥ÐŸ¹•İ Daily±çİÑ«É´Ø¹«èDCBC•x²è *****
0193 . S. DC-BC dcbc-153:DCBC

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```
0194 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0195 C.
0196 C.
0197 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0198 C.
0199 . C. ***** LOS *****
0200 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-417 2013-01-24 13:17:06 228 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁð•µ°È»Í×ÁÇòíY¿Y×Yí;¼YÉ;ÈÈè%µ•ííÈ;ÈøÈ¼°ÇÒø•ø¿í¹¿øÍ;çÁ®, ùø¹òèøÈøÇÁ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-268:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-01-24 12:19:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 08 08)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a c0 c0 10 10)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 40 c0 10 10)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 40 40 10 10)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d c0 40 10 10)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0e 85 83 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

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0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 + DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 05)
0108 + DC 07-F0 MDP_XRT_FL_PROG_SET
0109 BC (c5 10)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 + DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 + TI 2013-01-24 12:19:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C. Change EIS mode to MANU
0124 . C. Change mode to Manual
0125 + DC 07-FC EIS_MODE_MANU
0126 BC (21 02)
0127 C. Confirm EIS_MODE = MANU
0128 . C.
0129 + DC 07-FC EIS_CAM_IDLE
0130 BC (41)
0131 C. Confirm EIS_CAM_VLD_FLG = VLD
0132 C.
0133 C. MHC in PROM mode
0134 + DC 07-FC EIS_MHC_WATDOG_ENA
0135 BC (76 c0 a9 00 02 00 01)
0136 C.
0137 . C. LOAD MHC code from EEPROM bank 3d to MHC RAM
0138 + DC 07-FC EIS_LOAD_MHC_SOFT3
0139 BC (2c 03)
0140 . C. It takes 2 min to complete.
0141 C.
0142 + DC 07-FC EIS_DUMP_MHCRAM
0143 BC (0a 07 00 00 00 80 00)
0144 . C. It takes 2 min to complete.
0145 C.
0146 + DC 07-FC EIS_MHC_MODE_RAM
0147 BC (6b 88 11 00 02 ff ff)
0148 C. 10sec interval is required after EIS_MHC_MODE_RAM.
0149 + DC 07-FC EIS_MHC_ABORT
0150 BC (51 e8 81)
0151 C.
0152 C. Confirm EIS_MHC_VLD_FLG = VLD, EIS_MHC_MEM_MODE = RAM
0153 C.
0154 + DC 07-FC EIS_CLR_ICU_ERR
0155 BC (23)
0156 C. Confirm EIS_MHC_IF_ERR= 0x0000
0157 C.
0158 C. Confirm EIS_CAM_IF_ERR= 0x0000
0159 C.
0160 + DC 07-FC EIS_MHC_WATDOG_ENA
0161 BC (76 c0 a9 00 02 00 01)
0162 C.
0163 C. Config MHC heaters - Default setting -
0164 C.
0165 + DC 07-FC EIS_UPLOAD_MHCPAR
0166 BC (6f c0 95 00 04 21 2e 24)
0167 BC (92)
0168 + DC 07-FC EIS_UPLOAD_MHCPAR
0169 BC (6f c0 95 00 04 c9 af 36)
0170 BC (db)
0171 + DC 07-FC EIS_UPLOAD_MHCPAR
0172 BC (6f c0 95 00 04 a9 30 24)
0173 BC (92)
0174 + DC 07-FC EIS_UPLOAD_MHCPAR
0175 BC (6f c0 95 00 04 41 b1 36)
0176 BC (db)
0177 + DC 07-FC EIS_UPLOAD_MHCPAR
0178 BC (6f c0 95 00 04 81 b2 12)
0179 BC (49)
0180 + DC 07-FC EIS_UPLOAD_MHCPAR
0181 BC (6f c0 95 00 04 69 33 24)
0182 BC (92)
0183 + DC 07-FC EIS_UPLOAD_MHCPAR
0184 BC (6f c0 95 00 04 e1 b4 12)
0185 BC (49)
0186 + DC 07-FC EIS_UPLOAD_MHCPAR
0187 BC (6f c0 95 00 04 09 35 1b)
0188 BC (6d)
0189 + DC 07-FC EIS_UPLOAD_MHCPAR
0190 BC (6f c0 95 00 04 c9 36 09)
0191 BC (24)
0192 + DC 07-FC EIS_UPLOAD_MHCPAR
0193 BC (6f c0 95 00 04 21 b7 09)

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0194 BC (24)
0195 + DC 07-FC EIS_UPLOAD_MHCPAR
0196 BC (6f c0 95 00 04 21 b8 24)
0197 BC (92)
0198 + DC 07-FC EIS_UPLOAD_MHCPAR
0199 BC (6f c0 95 00 04 c9 39 00)
0200 BC (00)
0201 C. Set heaters duty cycle
0202 C.
0203 +. DC 07-FC EIS_UPLOAD_MHCPAR
0204 BC (6f c0 95 00 04 09 3a 03)
0205 BC (e8)
0206 C. Dump MHC parameter table
0207 +. DC 07-FC EIS_DUMP_MHCPAR
0208 BC (0c 07 02 00 00 00 cc)
0209 +. DC 07-FC EIS_OPE_HTR_ALL_ON
0210 BC (6a 28 8d 00 02 0f ff)
0211 C. Confirm EIS_OPE_HTR##_PWR = sometimes ON
0212 C. The next daily plan MUST start with mhc_init, default_htrs1-6, default_htrs7-12, and trace_dump
0213 C.
0214 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂð¹æDCBC•x²è *****
0215 C. (¼ã°îÿÓÿÄÿËÿÌÿÍÿÎÿÏÿÐÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿØÿ¹ÿºÿ»ÿ¼ÿ½ÿ¾ÿ¿ÿÀÿÁÿÂÿÃÿÄÿÅÿ
0216 . S. DC-BC dcbc-402:DCBC
0217 (MDP_known_event)
0218 C.
0219 C.
0220 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑæ´Ø¹æDCBC•x²è *****
0221 . S. DC-BC dcbc-153:DCBC
0222 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0223 C.
0224 C.
0225 . C. ;ãLOSÿÄÿËÿËÿÄÿ-¼Ä»Û;ä
0226 C.
0227 . C. ***** LOS *****
0228 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-418 2013-01-24 13:17:06 119 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄY$YÄY-¼Ä»Ü;ä
0005 C.
0006 C. YÄYB;¼Y³YF¥ÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;È¿¿aAb•µ°E»ÍxÁÇcÍYçYÄY×Yí;¼YÉj;ÈÈ%µ•ííÉ;ÈBÈ¼°ÇÖa•a¿¼l¹çaÍ;çÄ®,ùa¹aèBpaÇÄ+¿®a•aÈaãa³aÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C. Change EIS mode to MANU
0015 . C. Change mode to Manual
0016 +. DC 07-FC EIS_MODE_MANU
0017 BC (21 02)
0018 C. Confirm EIS_MODE = MANU
0019 . C.
0020 +. DC 07-FC EIS_CAM_IDLE
0021 BC (41)
0022 C. Confirm EIS_CAM_VLD_FLG = VLD
0023 C.
0024 C. MHC in PROM mode
0025 +. DC 07-FC EIS_MHC_WATDOG_ENA
0026 BC (76 c0 a9 00 02 00 01)
0027 C.
0028 . C. LOAD MHC code from EEPROM bank 3d to MHC RAM
0029 +. DC 07-FC EIS_LOAD_MHC_SOFT3
0030 BC (2c 03)
0031 . C. It takes 2 min to complete.
0032 C.
0033 +. DC 07-FC EIS_DUMP_MHCRAM
0034 BC (0a 07 00 00 00 80 00)
0035 . C. It takes 2 min to complete.
0036 C.
0037 +. DC 07-FC EIS_MHC_MODE_RAM
0038 BC (6b 88 11 00 02 ff ff)
0039 C. 10sec interval is required after EIS_MHC_MODE_RAM.
0040 +. DC 07-FC EIS_MHC_ABORT
0041 BC (51 e8 81)
0042 C.
0043 C. Confirm EIS_MHC_VLD_FLG = VLD, EIS_MHC_MEM_MODE = RAM
0044 C.
0045 +. DC 07-FC EIS_CLR_ICU_ERR
0046 BC (23)
0047 C. Confirm EIS_MHC_IF_ERR= 0x0000
0048 C.
0049 C. Confirm EIS_CAM_IF_ERR= 0x0000
0050 C.
0051 +. DC 07-FC EIS_MHC_WATDOG_ENA
0052 BC (76 c0 a9 00 02 00 01)
0053 C.
0054 C. Config MHC heaters - Default setting -
0055 C.
0056 +. DC 07-FC EIS_UPLOAD_MHCPAR
0057 BC (6f c0 95 00 04 21 2e 24)
0058 BC (92)
0059 + DC 07-FC EIS_UPLOAD_MHCPAR
0060 BC (6f c0 95 00 04 c9 af 36)
0061 BC (db)
0062 + DC 07-FC EIS_UPLOAD_MHCPAR
0063 BC (6f c0 95 00 04 a9 30 24)
0064 BC (92)
0065 + DC 07-FC EIS_UPLOAD_MHCPAR
0066 BC (6f c0 95 00 04 41 b1 36)
0067 BC (db)
0068 + DC 07-FC EIS_UPLOAD_MHCPAR
0069 BC (6f c0 95 00 04 81 b2 12)
0070 BC (49)
0071 + DC 07-FC EIS_UPLOAD_MHCPAR
0072 BC (6f c0 95 00 04 69 33 24)
0073 BC (92)
0074 + DC 07-FC EIS_UPLOAD_MHCPAR
0075 BC (6f c0 95 00 04 e1 b4 12)
0076 BC (49)
0077 + DC 07-FC EIS_UPLOAD_MHCPAR
0078 BC (6f c0 95 00 04 09 35 1b)
0079 BC (6d)
0080 + DC 07-FC EIS_UPLOAD_MHCPAR
0081 BC (6f c0 95 00 04 c9 36 09)
0082 BC (24)
0083 + DC 07-FC EIS_UPLOAD_MHCPAR
0084 BC (6f c0 95 00 04 21 b7 09)
0085 BC (24)
0086 + DC 07-FC EIS_UPLOAD_MHCPAR
0087 BC (6f c0 95 00 04 21 b8 24)
0088 BC (92)
0089 + DC 07-FC EIS_UPLOAD_MHCPAR
0090 BC (6f c0 95 00 04 c9 39 00)
0091 BC (00)
0092 C. Set heaters duty cycle
0093 C.
0094 +. DC 07-FC EIS_UPLOAD_MHCPAR
0095 BC (6f c0 95 00 04 09 3a 03)
```

```

0096 BC (e8)
0097 C. Dump MHC parameter table
0098 +. DC 07-FC EIS_DUMP_MHCPAR
0099 BC (0c 07 02 00 00 00 cc)
0100 +. DC 07-FC EIS_OPE_HTR_ALL_ON
0101 BC (6a 28 8d 00 02 0f ff)
0102 C. Confirm EIS_OPE_HTR##_PWR = sometimes ON
0103 C. The next daily plan MUST start with mhc_init, default_htrs1-6, default_htrs7-12, and trace_dump
0104 C.
0105 . C. ***** MDP `úÃîñî»ò¼ŸñÊÂðñ¹ñèDCBC•x²è *****
0106 C. (¼á°îŸÓŸÃŸÈŸŒŸËŸáŸçŸèñÈ¼ñ¼Á»Ûñ¹ñè)
0107 . S. DC-BC dcbc-402:DCBC
0108 (MDP_known_event)
0109 C.
0110 C.
0111 . C. ***** ŸDŸ¹•Ï Daily±¿ÍÑñÈ´Øñ¹ñèDCBC•x²è *****
0112 . S. DC-BC dcbc-153:DCBC
0113 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0114 C.
0115 C.
0116 . C. ¡ãLOSŸÁŸSŸÃŸ¼Á»Ûñ¹ñè
0117 C.
0118 . C. ***** LOS *****
0119 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-420 2013-01-24 13:17:06 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÄY-¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YBYóYEÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOC : Reload orbital element (send every contact) *****
0010 C. Áí;È¿áÀß•µ°£»Í×ÁÇóÍYçYÄY×Yí;¼YE;ÈÈèµ•fÍÈ;ÈèÈ¼°ÇÒá•¿¼i¹çáí;çÀ®, ùá¹áèááçÁ+¿®á•àÈááá³áÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úÁÍáÍ»ò¼YáÈÁá¹áèDCBC•x²è *****
0016 C. (¼á°íYóYÁYEÏYÉYáYçYèáÈ¼áá¼Á»Üá¹áè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDY¹•İ Daily+¿ÍÑáÈ´Øá¹áèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁYŠYÄY-¼Á»Ü;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

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*** OP Sequence for XRT ***

```

2013/01/24 12:34:00.0 AOCS_ORe-point_Start_1_OG [0x097]
                          AOCU_NM                5 02-76 00 01 81 51 f3
2013/01/24 12:44:00.0 AOCS_ORe-point_Start_2_OG [0x098]
                          AOCU_NM                5 02-76 00 01 81 40 41
2013/01/24 12:54:00.0 AOCS_ORe-point_Start_3_OG [0x099]
                          AOCU_NM                5 02-76 00 01 81 2e 97
2013/01/24 13:04:00.0 AOCS_ORe-point_Start_4_OG [0x09a]
                          AOCU_NM                5 02-76 00 01 81 1c e5
2013/01/24 13:14:00.0 AOCS_ORe-point_Start_5_OG [0x09b]
                          AOCU_NM                5 02-76 00 01 81 0b 33
2013/01/24 13:24:00.0 AOCS_ORe-point_Start_6_OG [0x09c]
                          AOCU_NM                5 02-76 00 01 81 f9 9a
2013/01/24 13:34:00.0 AOCS_ORe-point_Start_7_OG [0x09d]
                          AOCU_NM                5 02-76 00 01 81 e7 e8
2013/01/24 13:44:00.0 AOCS_ORe-point_Start_8_OG [0x09e]
                          AOCU_NM                5 02-76 00 01 81 d6 36
2013/01/24 13:54:00.0 AOCS_ORe-point_Start_9_OG [0x09f]
                          AOCU_NM                5 02-76 00 01 81 c4 8c
2013/01/24 14:04:00.0 AOCS_ORe-point_Start_10_OG [0x0a0]
                          AOCU_NM                5 02-76 00 01 81 b2 da
2013/01/24 14:14:00.0 AOCS_ORe-point_Start_11_OG [0x0a1]
                          AOCU_NM                5 02-76 00 14 18 51 68
2013/01/24 14:24:00.0 AOCS_ORe-point_Start_12_OG [0x0a2]
                          AOCU_NM                5 02-76 00 13 02 43 8d
2013/01/24 14:55:00.0 AOCS_ORe-point_Start_13_OG [0x0a3]
                          AOCU_NM                5 02-76 00 11 81 33 8d
2013/01/24 15:05:00.0 AOCS_ORe-point_Start_14_OG [0x0a4]
                          AOCU_NM                5 02-76 00 10 9b 23 1a
2013/01/24 15:15:00.0 AOCS_ORe-point_Start_15_OG [0x0a5]
                          AOCU_NM                5 02-76 00 10 31 12 35
2013/01/24 15:25:00.0 AOCS_ORe-point_Start_16_OG [0x0a6]
                          AOCU_NM                5 02-76 00 10 00 00 fd
2013/01/24 15:35:00.0 AOCS_ORe-point_Start_17_OG [0x0a7]
                          AOCU_NM                5 02-76 00 10 31 f0 73
2013/01/24 15:45:00.0 AOCS_ORe-point_Start_18_OG [0x0a8]
                          AOCU_NM                5 02-76 00 10 9b df 8e
2013/01/24 15:55:00.0 AOCS_ORe-point_Start_19_OG [0x0a9]
                          AOCU_NM                5 02-76 00 11 81 cf 1b
2013/01/24 16:05:00.0 AOCS_ORe-point_Start_20_OG [0x0aa]
                          AOCU_NM                5 02-76 00 13 02 bf 1b
2013/01/24 16:42:00.0 AOCS_ORe-point_Start_21_OG [0x0ab]
                          AOCU_NM                5 02-76 00 15 99 af e8
2013/01/24 16:52:00.0 AOCS_ORe-point_Start_22_OG [0x0ac]
                          AOCU_NM                5 02-76 04 00 00 00 00
2013/01/24 18:17:30.0 AOCS_ORe-point_Start_23_OG [0x0ad]
                          AOCU_NM                5 02-76 00 00 00 00 00
2013/01/24 18:27:30.0 AOCS_ORe-point_Start_22_OG [0x0ac]
                          AOCU_NM                5 02-76 04 00 00 00 00
2013/01/25 00:00:00.0 XRT_CTRL_MANU_400_OG [0x190]
                          MDP_XRT_CTRL_MANU     1 07-F0 c1
2013/01/25 00:00:02.0 XRT_TCIB_XRT_S_HTR_A_DIS_431_OG [0x1af]
                          TCIB_XRT_S_HTR_A_DIS 0 04-C0
2013/01/25 06:03:24.0 XRT_CTRL_MANU_402_OG [0x192]
                          MDP_XRT_CTRL_MANU     1 07-F0 c1
2013/01/25 06:03:26.0 XRT_FOCUS_POSITION_403_OG [0x193]
                          XRT_FOCUS_POSITION     4 07-F8 22 ff aa 00
2013/01/25 06:03:30.0 AOCS_ORe-point_Start_23_OG [0x0ad]
                          AOCU_NM                5 02-76 00 00 00 00 00
2013/01/25 06:03:46.0 XRT_FLD_DIS_404_OG [0x194]
                          MDP_XRT_FLD_DIS       1 07-F0 d9
2013/01/25 06:03:48.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                          MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2013/01/25 06:03:50.0 XRT_ARS_DIS_406_OG [0x196]
                          MDP_XRT_ARS_DIS       1 07-F0 d5
2013/01/25 06:06:28.0 XRT_QT_PROG_SET_419_OG [0x1a3]
                          MDP_XRT_QT_PROG_SET    2 07-F0 c4 0d
2013/01/25 06:06:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                          MDP_XRT_CTRL_AUTO     1 07-F0 c0
2013/01/25 06:13:24.0 XRT_CTRL_MANU_443_OG [0x1bb]
                          MDP_XRT_CTRL_MANU     1 07-F0 c1
2013/01/25 06:13:30.0 AOCS_ORe-point_Start_24_OG [0x0ae]
                          AOCU_NM                5 02-76 00 2e f9 2e f9
2013/01/25 06:16:02.0 XRT_FOCUS_POSITION_441_OG [0x1b9]
                          XRT_FOCUS_POSITION     4 07-F8 22 ff aa 00
2013/01/25 06:16:22.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                          MDP_XRT_QT_PROG_SET    2 07-F0 c4 11
2013/01/25 06:16:24.0 XRT_FLD_DIS_404_OG [0x194]
                          MDP_XRT_FLD_DIS       1 07-F0 d9
2013/01/25 06:16:26.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                          MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2013/01/25 06:16:28.0 XRT_ARS_DIS_412_OG [0x19c]
                          MDP_XRT_ARS_DIS       1 07-F0 d5
2013/01/25 06:16:30.0 XRT_CTRL_AUTO_413_OG [0x19d]
                          MDP_XRT_CTRL_AUTO     1 07-F0 c0
2013/01/25 06:23:24.0 XRT_CTRL_MANU_443_OG [0x1bb]
                          MDP_XRT_CTRL_MANU     1 07-F0 c1
2013/01/25 06:23:30.0 AOCS_ORe-point_Start_25_OG [0x0af]
                          AOCU_NM                5 02-76 00 2e f9 d1 07
2013/01/25 06:26:02.0 XRT_FOCUS_POSITION_441_OG [0x1b9]
                          XRT_FOCUS_POSITION     4 07-F8 22 ff aa 00
2013/01/25 06:26:22.0 XRT_QT_PROG_SET_442_OG [0x1ba]

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2013/01/25	06:26:24.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/01/25	06:26:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/01/25	06:26:28.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	06:26:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	06:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	06:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	06:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	06:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	06:58:24.0	XRT_CTRL_MANU_443_OG [0x1bb]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	06:58:30.0	AOCS_Ore-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00 d1 07 d1 07	
2013/01/25	07:01:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/01/25	07:01:22.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2013/01/25	07:01:24.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/01/25	07:01:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/01/25	07:01:28.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	07:01:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	07:08:24.0	XRT_CTRL_MANU_443_OG [0x1bb]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	07:08:30.0	AOCS_Ore-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00 d1 07 2e f9	
2013/01/25	07:11:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/01/25	07:11:22.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2013/01/25	07:11:24.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/01/25	07:11:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/01/25	07:11:28.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	07:11:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	07:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	07:18:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/01/25	07:18:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	04 00 00 00 00	
2013/01/25	07:18:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/01/25	07:18:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/01/25	07:18:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/01/25	07:18:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	07:21:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	07:21:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2013/01/25	07:21:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/01/25	07:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	08:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	08:13:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	08:13:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	08:16:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	08:35:00.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	08:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	09:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	09:54:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	09:54:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	09:57:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	10:08:00.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	10:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2013/01/25	15:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	15:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	15:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	15:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	15:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	15:40:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/01/25	15:40:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/01/25	15:40:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/01/25	15:40:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/01/25	15:40:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	15:43:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	15:43:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2013/01/25	15:43:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/01/25	15:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	16:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	16:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	16:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	16:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	17:17:30.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	17:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/01/25	18:00:00.0	AOCS_Orе-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/01/25	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/01/25	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/01/25	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	18:02:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2013/01/25	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	18:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/01/25	18:10:00.0	AOCS_Orе-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	04 00 00 00 00	
2013/01/25	18:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/01/25	18:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/01/25	18:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/01/25	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/25	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	18:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2013/01/25	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/01/25	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	18:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	18:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	18:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	18:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	18:54:00.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	18:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	20:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	20:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	20:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					

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2013/01/25	20:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	20:31:30.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	20:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	21:45:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	21:45:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	21:45:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	21:48:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	22:07:30.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	22:08:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/25	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/25	23:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/25	23:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/25	23:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/25	23:35:30.0	XRT_Custom_434_OG [0x1b2]					
2013/01/25	23:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/26	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/26	00:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/26	00:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/26	00:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/26	01:06:00.0	XRT_Custom_434_OG [0x1b2]					
2013/01/26	01:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/26	02:22:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/26	02:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/26	02:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/26	02:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/26	02:42:30.0	XRT_Custom_434_OG [0x1b2]					
2013/01/26	02:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/26	03:50:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/26	03:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/26	03:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/26	03:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/26	04:20:00.0	XRT_Custom_434_OG [0x1b2]					
2013/01/26	04:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/26	05:31:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/26	05:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/01/26	05:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/01/26	05:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/01/26	05:57:30.0	XRT_Custom_434_OG [0x1b2]					
2013/01/26	05:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/26	06:07:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/26	06:07:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/01/26	06:07:30.0	AOCS_OrE-point_Start_23_OG [0x0ad]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2013/01/26	06:07:46.0	XRT_FLD_DIS_404_OG [0x194]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/01/26	06:07:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/01/26	06:07:50.0	XRT_ARS_DIS_406_OG [0x196]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/01/26	06:10:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2013/01/26	06:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/01/26	06:17:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/01/26	06:17:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/01/26	06:17:30.0	AOCS_OrE-point_Start_22_OG [0x0ac]					

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2013/01/26	06:17:46.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	04	00	00	00	00
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/26	06:17:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/26	06:17:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/26	06:17:52.0	XRT_ARS_DIS_437_OG [0x1b5]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/26	06:20:24.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/26	06:20:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/01/26	06:20:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2013/01/26	06:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/26	07:11:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/26	07:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/26	07:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/26	07:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/26	07:35:00.0	XRT_Custom_434_OG [0x1b2]								
2013/01/26	07:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/26	08:51:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/26	08:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/26	08:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/26	08:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/26	09:11:00.0	XRT_Custom_434_OG [0x1b2]								
2013/01/26	09:12:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/26	09:41:00.5	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/26	10:00:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]								
			AOCU_NM	5	02-76	00	ec	e6	ae	da
2013/01/26	11:04:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]								
			AOCU_NM	5	02-76	00	00	00	00	00