

XRT Timeline to be uploaded on 2013/01/29

Period: 2013/01/29 08:57:00 - 2013/02/02 10:20:00

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Normal mode

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XOB #1966: AR Morphology with PFB 384 FOV, thin-Be + multifilter context, 512x512 at 1016x1120, 120s-cad, full FOV 2x2 G-band w/ G-Band VLS Closed

Term	Pointing (x, y)	Comment
01/29 09:24:30 - 01/29 17:43:30	Track (-255.8, -91.7) @ 01/29 09:07:00	# OP start + 10min AR11663
01/29 18:20:00 - 01/30 05:53:54	Track (-173.1, -89.3) @ 01/29 18:17:00	# AR11663
01/30 06:07:00 - 01/30 17:56:54	Track (-64.9, -87.3) @ 01/30 06:04:00	# AR11663
01/30 18:10:00 - 01/31 05:59:54	Track (46.6, -86.6) @ 01/30 18:07:00	# AR11663

PROG= 03 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 69	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 11	1-time(s)	2.0sec
Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 512x512 (1016, 1120) DPCM 0 0 2.0sec
Seqn= 14	2-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 16.0s Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s Obs 1x1 512x512 (1016, 1120) Q=95 3 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 40	58-time(s)	120.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 512x512 (1016, 1120) Q=95 3 0 30.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 30.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 30.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(18)

Term	Pointing (x, y)	Comment
01/29 18:10:00 - 01/29 18:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
01/30 05:57:00 - 01/30 06:03:54	Fixed (0.0, 0.0)	synoptic, shifted -6.0 min
01/30 18:00:00 - 01/30 18:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min
01/31 06:03:05 - 01/31 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 13 1-time(s)

Subr= 1	1-time(s)	14.0sec
Seqn= 64	1-time(s)	4.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 6	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 70	1-time(s)	4.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 67	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 69	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 68	1-time(s)	2.0sec
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1967: AR Morphology with PFB 384 FOV, thin-Be + multifilter context, 512 FOV at 1112x984, 120s-cad, full FOV 2x2 G-band w/ G-Band VLS Closed

Term	Pointing (x, y)	Comment
01/31 06:13:00 - 01/31 10:24:00	Track (-28.7, 58.7) @ 01/31 06:10:00	# Small coronal hole near AR11663

PROG= 10 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 69	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 20	1-time(s)	2.0sec
Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 512x512 (1112, 984) DPCM 0 0 2.0sec
Seqn= 47	2-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1112, 984) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 512x512 (1112, 984) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 512x512 (1112, 984) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 512x512 (1112, 984) Q=95 3 0 2.0sec

Subr= 2	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1112, 984)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1112, 984)	Q=95	3	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1112, 984)	Q=95	3	0	2.0sec
	1-time(s) 2.0sec												
Seqn= 22	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1112, 984)	Q=95	3	0	30.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
58-time(s) 2.0sec													
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)		Comment										
01/29 09:24:30 - 01/29 17:43:30	Track (-255.8, -91.7)	^{01/29 09:07:00}	# OP start + 10min AR11663										
01/29 18:20:00 - 01/30 05:53:54	Track (-173.1, -89.3)	^{01/29 18:17:00}	# AR11663										
01/30 06:07:00 - 01/30 17:56:54	Track (-64.9, -87.3)	^{01/30 06:04:00}	# AR11663										
01/30 18:10:00 - 01/31 05:59:54	Track (46.6, -86.6)	^{01/30 18:07:00}	# AR11663										
01/31 06:13:00 - 01/31 10:24:00	Track (-28.7, 58.7)	^{01/31 06:10:00}	# Small coronal hole near AR11663										
PROG= 16 15-time(s)													
Subr= 1	45-time(s) 10.0sec												
	Seqn= 35	1-time(s) 2.0sec											
thin-Be/Open		med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s) 10.0sec												
	Seqn= 36	1-time(s) 2.0sec											
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 37	1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38	1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

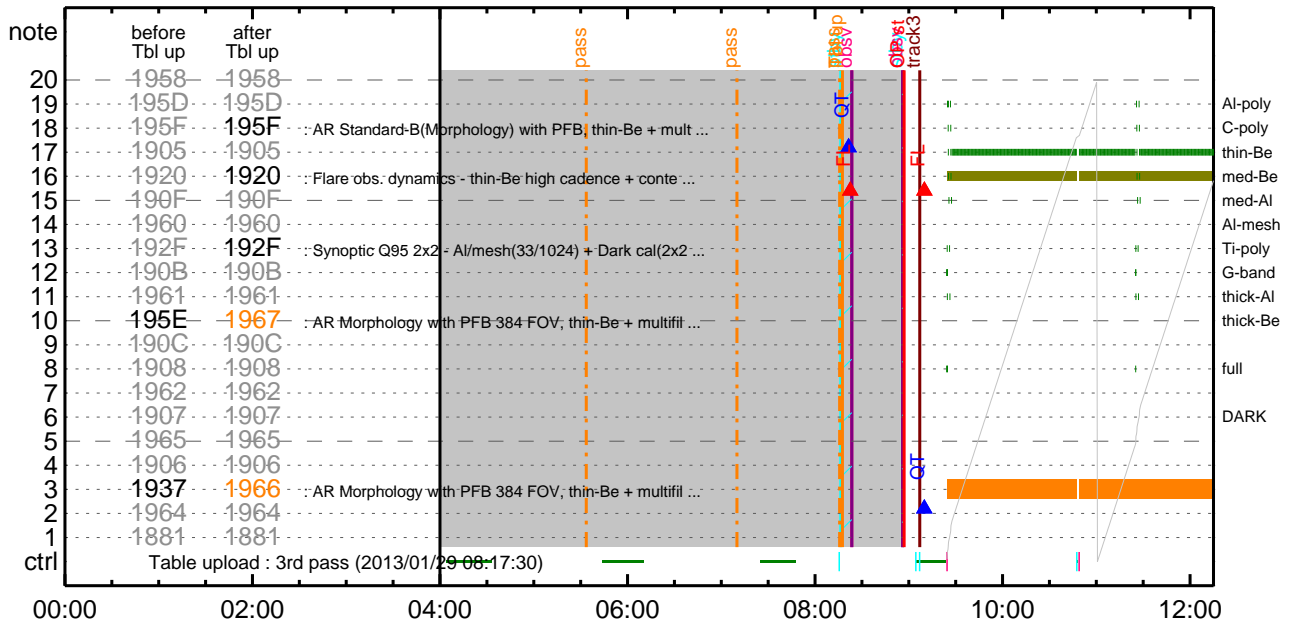
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Flare Detection

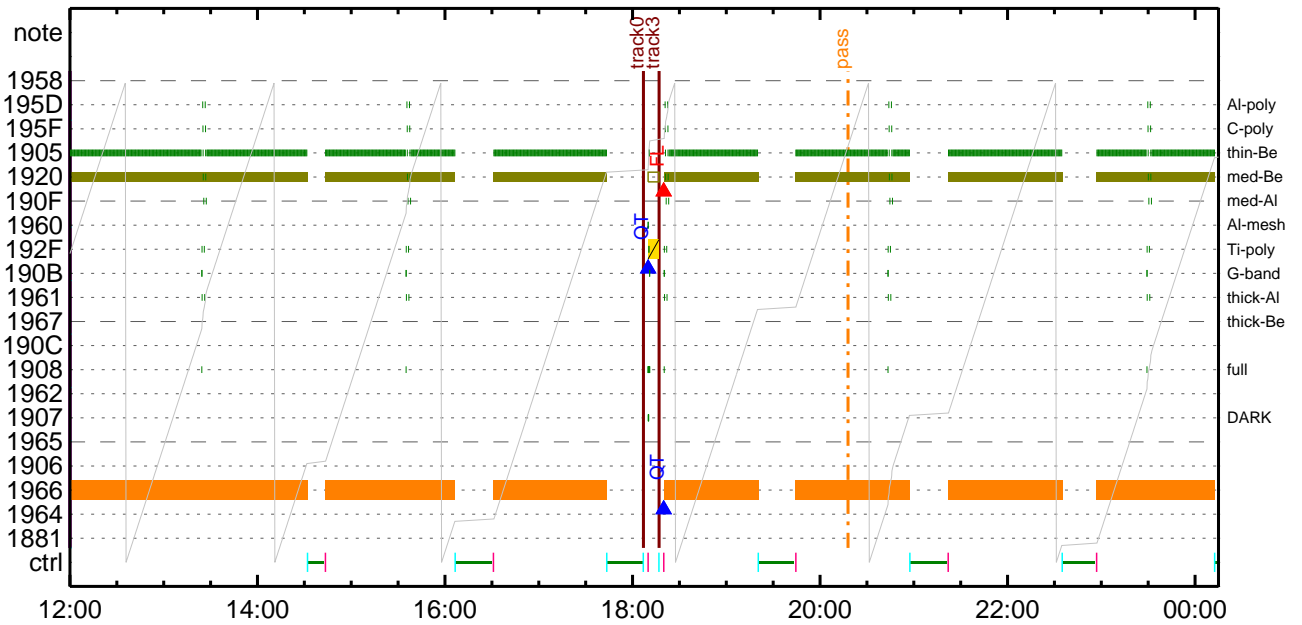
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FLD Patrol													
Term	Pointing (x, y)		Comment										
01/29 18:17:16 - 01/30 05:54:16	Track (-173.1, -89.3)	^{01/29 18:17:00}	# AR11663										
01/30 06:04:16 - 01/30 17:57:16	Track (-64.9, -87.3)	^{01/30 06:04:00}	# AR11663										
01/30 18:07:16 - 01/31 06:00:21	Track (46.6, -86.6)	^{01/30 18:07:00}	# AR11663										
01/31 06:10:16 - 02/02 10:20:00	Track (-28.7, 58.7)	^{01/31 06:10:00}	# Small coronal hole near AR11663										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8				Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

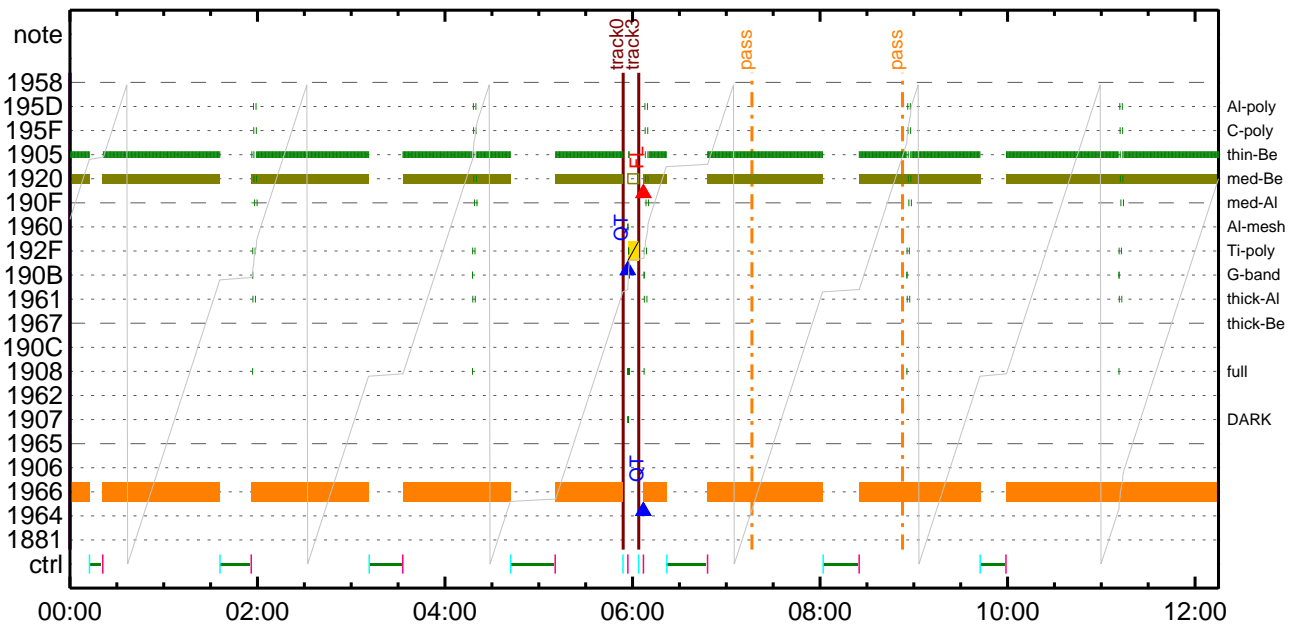
CMDI #0219 2013/01/29



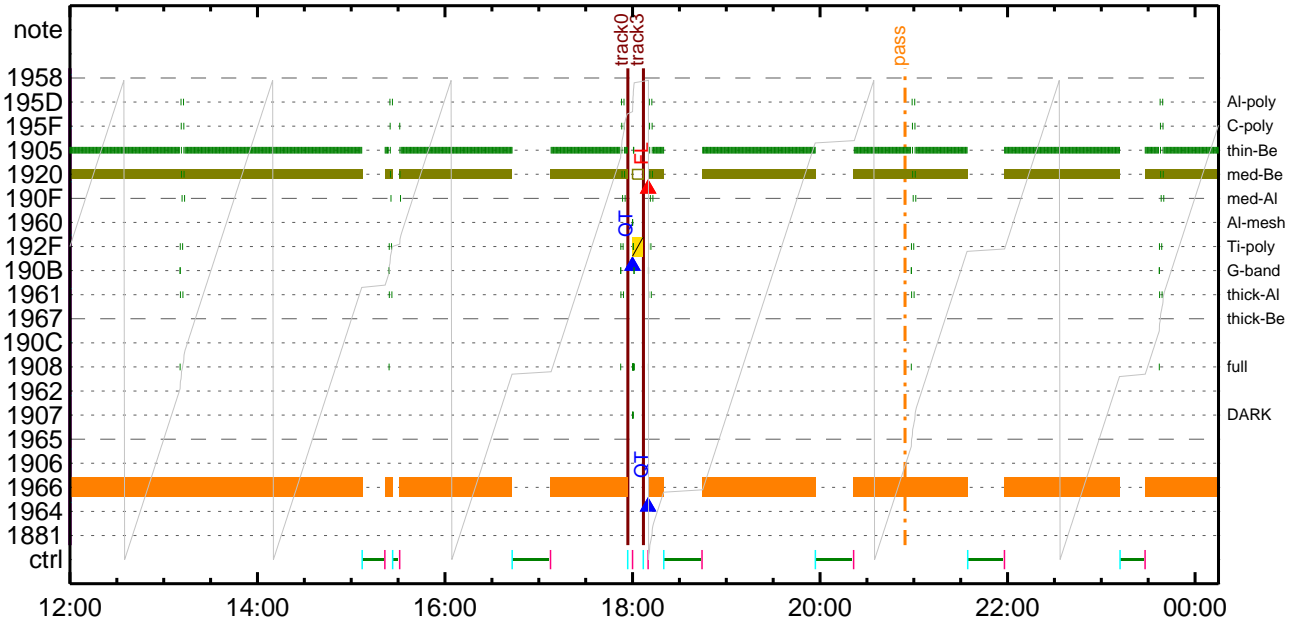
CMDI #0219 2013/01/29



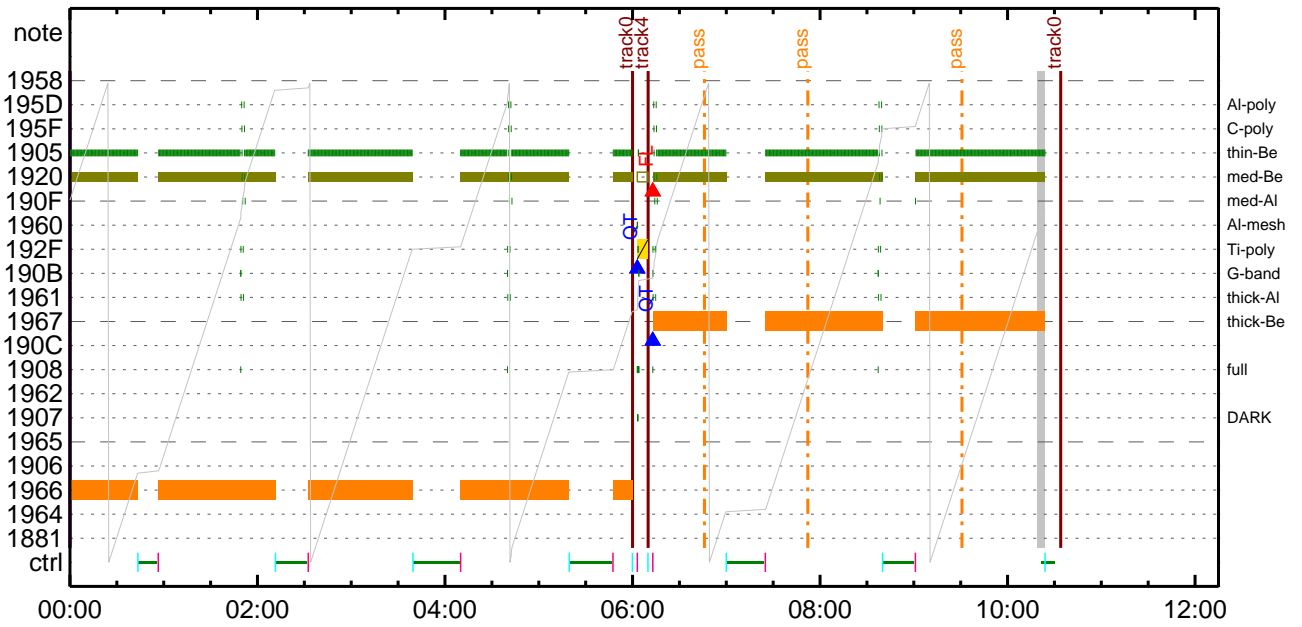
CMDI #0219 2013/01/30



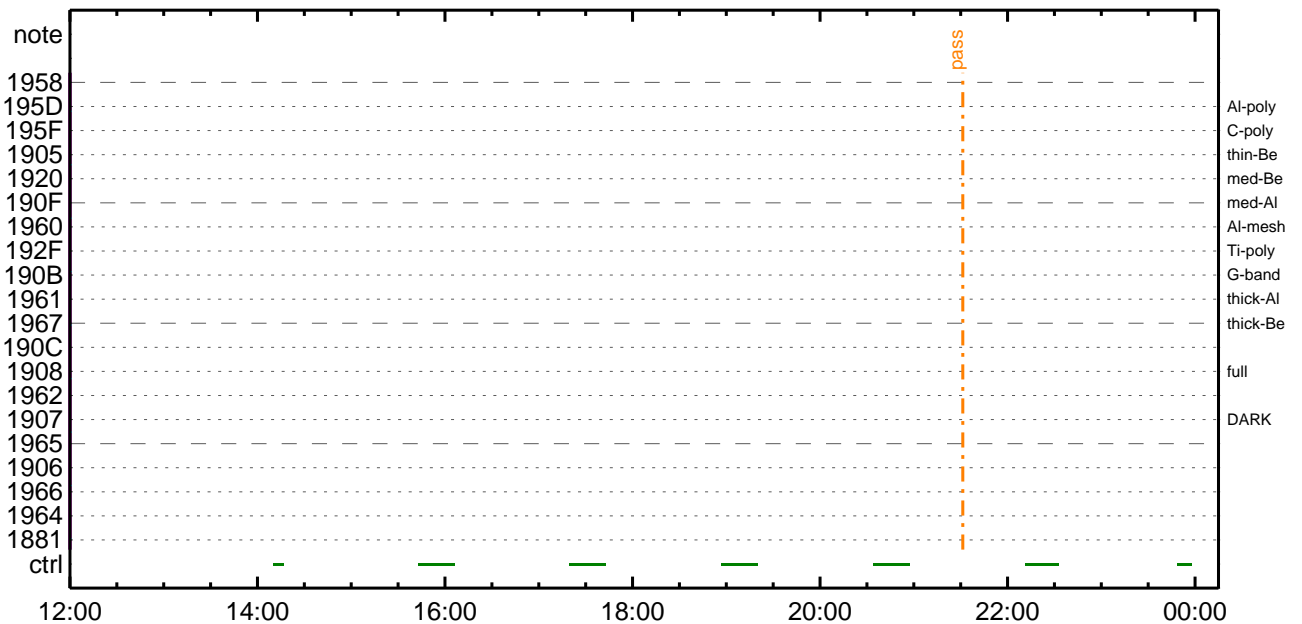
CMDI #0219 2013/01/30



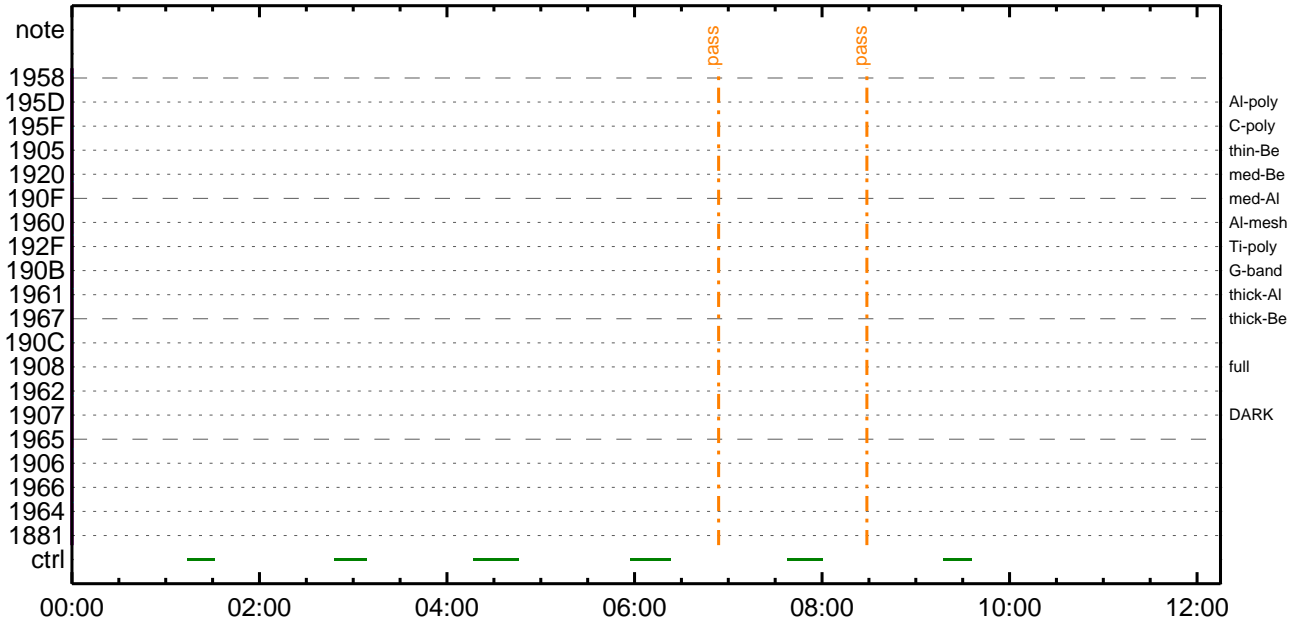
CMDI #0219 2013/01/31



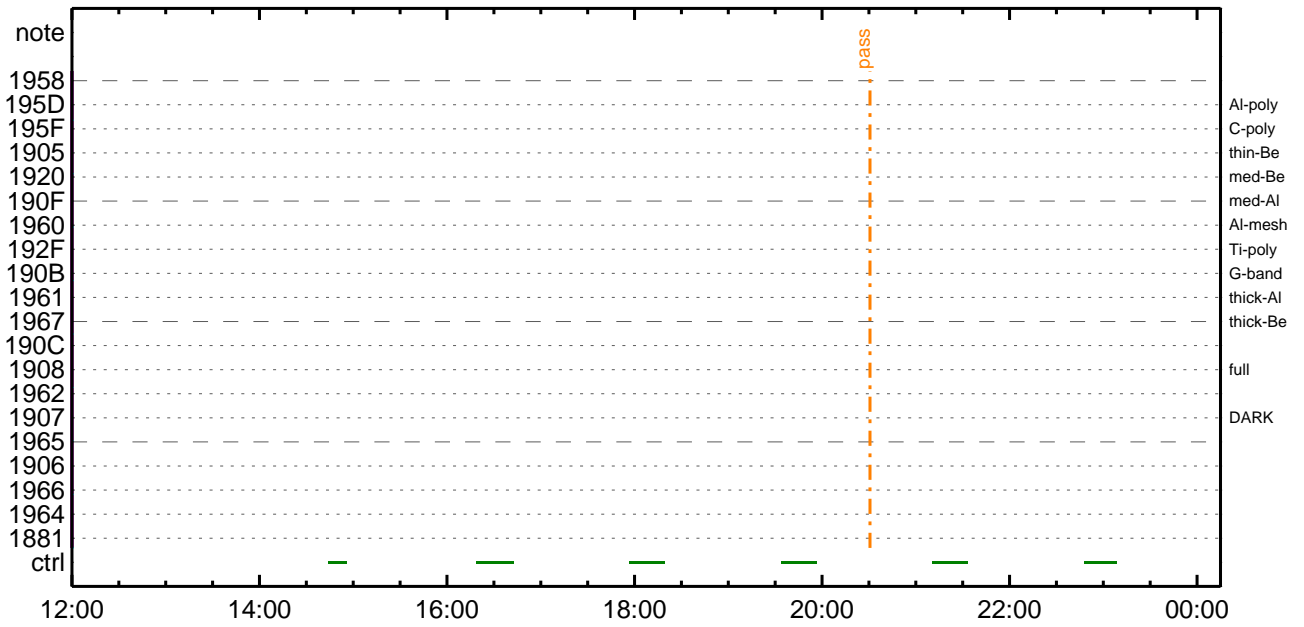
CMDI #0219 2013/01/31



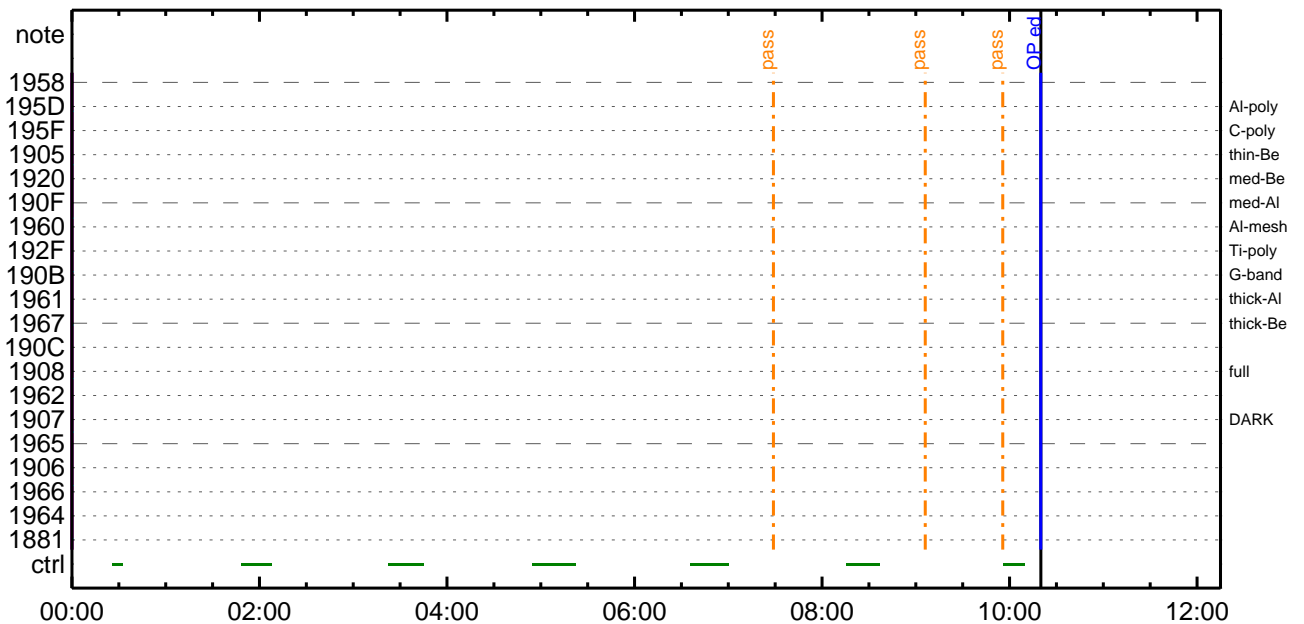
CMDI #0219 2013/02/01



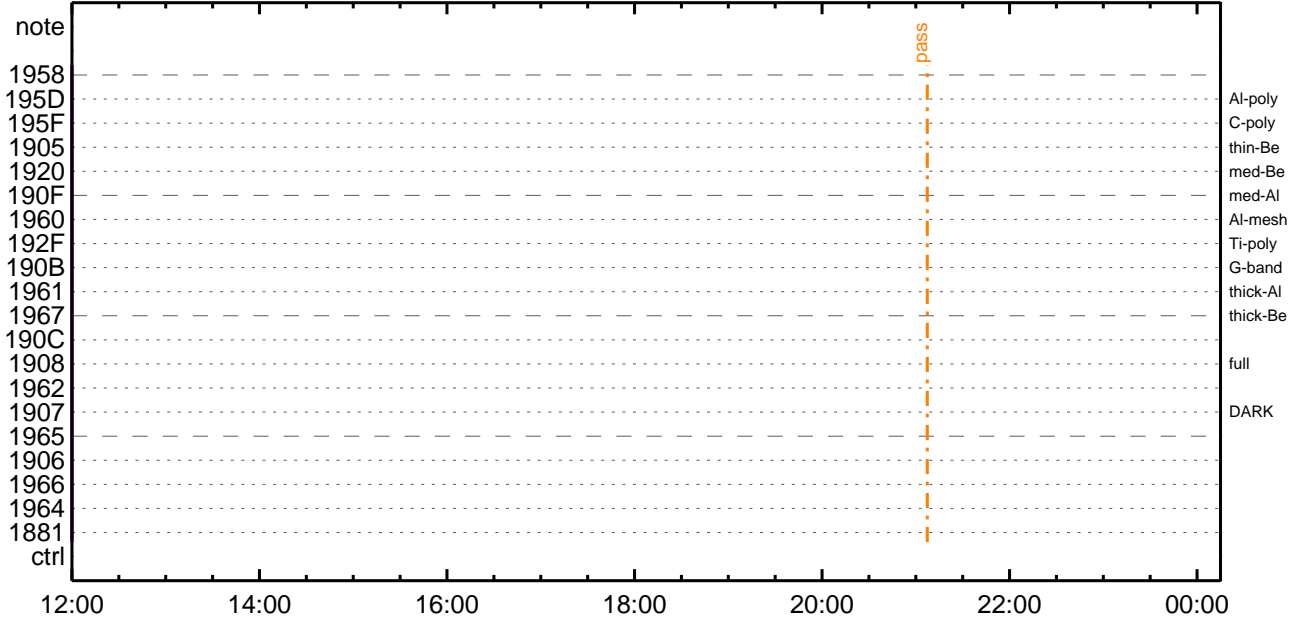
CMDI #0219 2013/02/01



CMDI #0219 2013/02/02



CMDI #0219 2013/02/02




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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 7f 8c 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 8b 7b 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 12)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 10)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2013-01-29 08:56:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop FG table >
0173 +. DC 07-F0 MDP_FG_CTRL_MANU
0174 BC (51)
0175 . C. -----
0176 C. MDP_FG_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload FG Observation Table>
0180 . S. RAM ram-261:MDP_OBS_F
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_F >
0184 +. DC 07-F0 MDP_DUMP_FGTBL
0185 BC (82 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_F verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 . C. < Upload DPL table >
0191 C.
0192 C. ¥ç¥Ã¥×¥í;¼¥É¼îÁ°¼ÈSTS_CHK¼ðOFF¼È¼¹¼è
0193 C.

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Jan 29, 13 13:26

XRT_OGLIST_0219.chk

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2013/01/29	18:17:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	03	00	00	00
2013/01/29	18:17:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/01/29	18:17:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/01/29	18:17:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/01/29	18:17:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/01/29	18:19:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/29	18:19:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2013/01/29	18:19:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2013/01/29	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/29	19:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/29	19:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/29	19:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/29	19:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/29	19:43:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/29	19:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/29	20:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/29	20:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/29	20:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/29	21:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/29	21:21:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/29	21:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/29	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/29	22:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/29	22:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/29	22:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/29	22:56:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/29	22:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/30	00:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/30	00:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/30	00:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/30	00:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/30	00:20:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	00:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/30	01:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/30	01:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/30	01:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/30	01:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/30	01:55:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	01:56:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/30	03:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/30	03:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/30	03:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/30	03:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/01/30	03:32:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	03:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/01/30	04:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/01/30	04:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/01/30	04:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/01/30	04:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2013/01/30	05:09:30.0	XRT_Custom_434_OG [0x1b2]			
2013/01/30	05:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	05:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/01/30	05:54:00.5	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/01/30	05:54:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/01/30	05:54:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/01/30	05:54:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/30	05:56:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2013/01/30	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	06:03:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/01/30	06:04:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/01/30	06:04:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/01/30	06:04:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/01/30	06:04:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/01/30	06:04:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/01/30	06:06:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/30	06:06:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2013/01/30	06:06:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/01/30	06:07:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	06:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	06:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/30	06:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/30	06:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/30	06:47:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/30	06:48:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	08:02:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	08:02:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/30	08:02:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/30	08:05:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/30	08:24:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/30	08:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	09:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	09:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/30	09:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/30	09:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/30	09:58:00.0	XRT_Custom_434_OG [0x1b2]			
2013/01/30	09:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	15:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	15:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/30	15:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/30	15:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/30	15:20:30.0	XRT_Custom_434_OG [0x1b2]			
2013/01/30	15:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/30	15:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/30	15:26:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/30	15:26:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/30	15:29:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2013/01/30	15:30:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	15:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	16:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	16:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/30	16:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/30	16:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/30	17:06:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	17:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	17:56:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/30	17:57:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/30	17:57:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/30	17:57:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/30	17:57:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/30	17:59:58.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/30	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	18:06:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/30	18:07:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2013/01/30	18:07:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/30	18:07:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/30	18:07:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/30	18:07:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/30	18:09:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/30	18:09:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2013/01/30	18:09:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/30	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	18:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	18:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/30	18:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/30	18:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/30	18:43:30.5	XRT_Custom_434_OG [0x1b2]							
2013/01/30	18:44:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	19:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/30	19:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/30	20:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/30	20:20:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	20:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	21:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/30	21:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/30	21:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/30	21:57:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	21:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/30	23:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/30	23:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/30	23:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/30	23:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2013/01/30	23:27:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/30	23:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/31	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/31	00:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/31	00:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/31	00:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/31	00:55:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/31	00:56:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/31	02:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/31	02:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/31	02:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/31	02:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/31	02:31:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/31	02:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/31	03:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/31	03:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/31	03:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/31	03:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/31	04:09:00.0	XRT_Custom_434_OG [0x1b2]							
2013/01/31	04:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/31	05:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/31	05:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/31	05:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/01/31	05:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/01/31	05:46:30.0	XRT_Custom_434_OG [0x1b2]							
2013/01/31	05:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/31	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/31	05:59:56.0	XRT_ROI_A_436_OG [0x1b4]							
		MDP_XRT_ROI_SET	6	07-F0	cd 05 91 72 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 20 20				
		MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 20 08				
		MDP_XRT_ROI_SET	6	07-F0	cd 0a 80 80 08 20				
		MDP_XRT_ROI_SET	6	07-F0	cd 0b 8b 7b 08 08				
		MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08				
2013/01/31	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/01/31	06:00:01.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/01/31	06:00:21.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/01/31	06:00:23.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/01/31	06:00:25.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/31	06:03:03.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/01/31	06:03:05.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/01/31	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/01/31	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/01/31	06:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/01/31	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/01/31	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/01/31	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/01/31	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/01/31	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/01/31	06:12:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2013/01/31	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/01/31	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2013/01/31	07:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/31	07:00:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/31	07:00:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/31	07:03:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/31	07:24:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/31	07:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_CTRL_AUTO_413_OG [0x19d]			
2013/01/31	08:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/31	08:40:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/01/31	08:40:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/01/31	08:43:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/01/31	09:00:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/01/31	09:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_CTRL_AUTO_413_OG [0x19d]			
2013/01/31	10:24:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/01/31	10:34:00.0	AOCS_OrE-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00