

# XRT Timeline to be uploaded on 2013/02/02

Period: 2013/02/02 10:20:00 - 2013/02/07 10:09:00

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Normal mode

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## XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
02/02 10:33:00 - 02/02 17:59:54	Track ( -802.4, 398.5) <sup>Ⓢ 02/02 10:30:00</sup>	# OP start + 10min - Returning AR 11652
02/03 06:22:30 - 02/03 17:32:30	Track ( -709.5, 414.5) <sup>Ⓢ 02/03 06:19:30</sup>	# Returning AR 11652
02/04 05:56:00 - 02/04 17:45:54	Track ( -566.6, 430.8) <sup>Ⓢ 02/04 05:53:00</sup>	#Returning AR 11652
<b>PROG= 18 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn=100 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Seqn= 65 4-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ med-Be/Open	med-Be/Open close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 94 70-time(s) 2.0sec		
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1926: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/1443) + Thin-Be(250

Term	Pointing (x, y)	Comment
02/02 18:03:00 - 02/02 20:09:54	Fixed ( 0.0, 0.0)	synoptic + SOT Flat-Field
02/03 06:12:30 - 02/03 06:19:24	Fixed ( 0.0, 0.0)	synoptic, shifted 9.5 min
02/03 18:01:00 - 02/03 18:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted -2.0 min
02/04 05:46:00 - 02/04 05:52:54	Fixed ( 0.0, 0.0)	synoptic, shifted -17.0 min
02/04 17:49:00 - 02/04 19:46:00	Fixed ( 0.0, 0.0)	synoptic, shifted -14.0 min + SOT Flat-Field
02/05 06:03:00 - 02/05 06:09:54	Fixed ( 0.0, 0.0)	synoptic
<b>PROG= 02 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 96 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 97 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 23 1-time(s) 2.0sec		
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1962: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (33ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
02/02 20:35:00 - 02/03 06:09:24	Track ( -760.5, 406.5) <sup>Ⓢ 02/02 20:10:00</sup>	# Returning AR 11652
02/03 18:11:00 - 02/04 05:42:54	Track ( -641.9, 423.1) <sup>Ⓢ 02/03 18:08:00</sup>	#Returning AR 11652
02/04 20:10:30 - 02/05 05:59:54	Track ( -467.4, 438.9) <sup>Ⓢ 02/04 19:56:00</sup>	#Returning AR 11652
02/05 06:15:00 - 02/05 10:06:00	Track ( -389.8, 443.9) <sup>Ⓢ 02/05 06:10:00</sup>	# Returning AR 11652
<b>PROG= 07 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn=100 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Seqn= 65 4-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 73 70-time(s) 120.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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### XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
02/02 10:33:00 - 02/02 17:59:54	Track (-802.4, 398.5) <sup>Ⓜ</sup> 02/02 10:30:00	# OP start + 10min - Returning AR 11652
02/02 20:35:00 - 02/03 06:09:24	Track (-760.5, 406.5) <sup>Ⓜ</sup> 02/02 20:10:00	# Returning AR 11652
02/03 06:22:30 - 02/03 17:32:30	Track (-709.5, 414.5) <sup>Ⓜ</sup> 02/03 06:19:30	# Returning AR 11652
02/03 18:11:00 - 02/04 05:42:54	Track (-641.9, 423.1) <sup>Ⓜ</sup> 02/03 18:08:00	#Returning AR 11652
02/04 05:56:00 - 02/04 17:45:54	Track (-566.6, 430.8) <sup>Ⓜ</sup> 02/04 05:53:00	#Returning AR 11652
02/04 20:10:30 - 02/05 05:59:54	Track (-467.4, 438.9) <sup>Ⓜ</sup> 02/04 19:56:00	#Returning AR 11652
02/05 06:15:00 - 02/05 10:06:00	Track (-389.8, 443.9) <sup>Ⓜ</sup> 02/05 06:10:00	# Returning AR 11652

### PROG= 16 15-time(s)

<b>Subr= 1 45-time(s) 10.0sec</b>												
<b>Seqn= 35 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>												
<b>Seqn= 36 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 37 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 38 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

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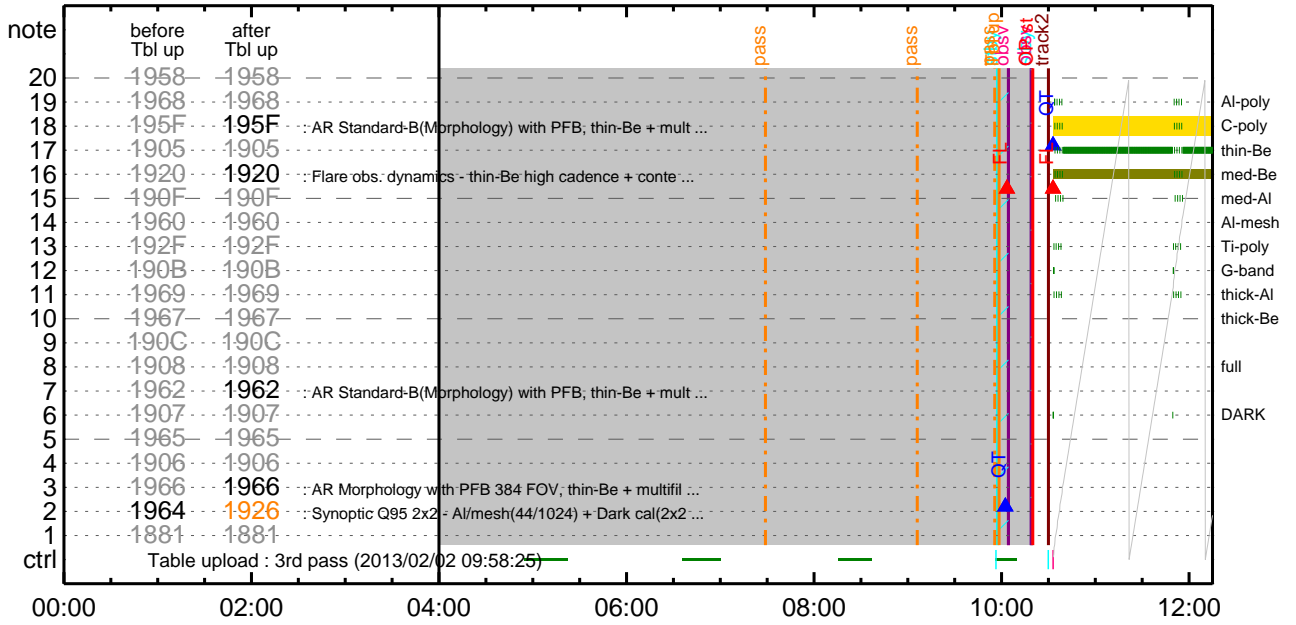
### Flare Detection

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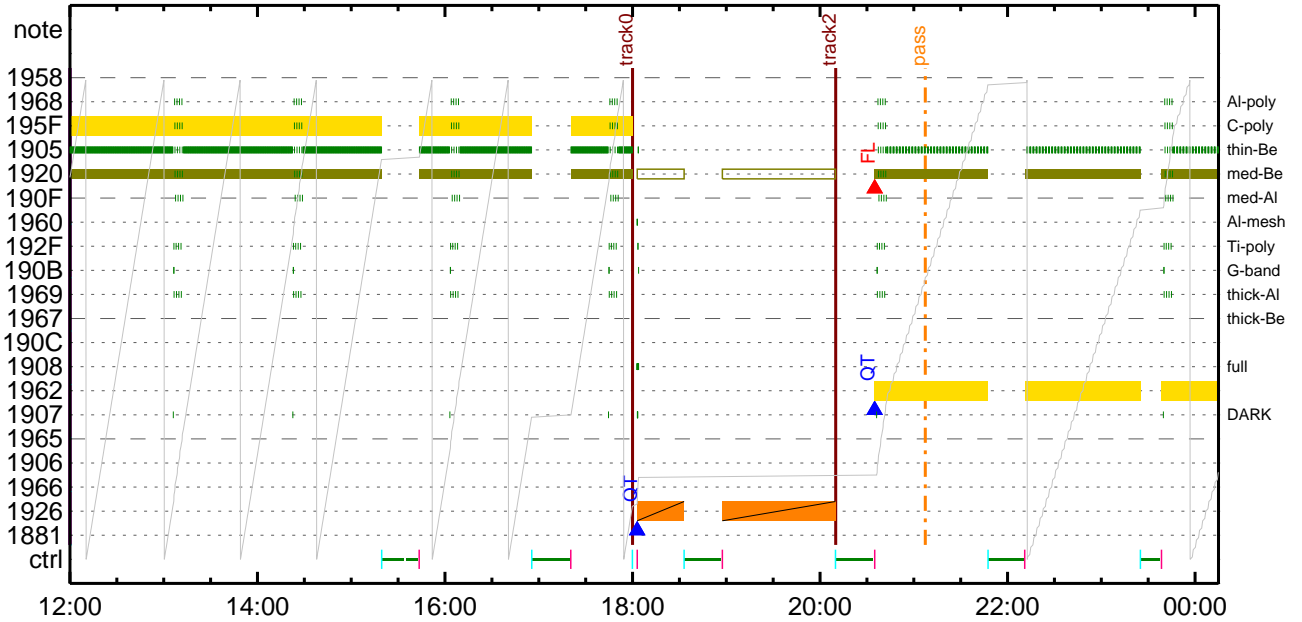
### FLD Patrol

Term	Pointing (x, y)	Comment										
02/02 20:10:16 - 02/03 06:09:46	Track (-760.5, 406.5) <sup>Ⓜ</sup> 02/02 20:10:00	# Returning AR 11652										
02/03 06:19:46 - 02/03 17:58:16	Track (-709.5, 414.5) <sup>Ⓜ</sup> 02/03 06:19:30	# Returning AR 11652										
02/03 18:08:16 - 02/04 05:43:16	Track (-641.9, 423.1) <sup>Ⓜ</sup> 02/03 18:08:00	#Returning AR 11652										
02/04 05:53:16 - 02/04 17:46:16	Track (-566.6, 430.8) <sup>Ⓜ</sup> 02/04 05:53:00	#Returning AR 11652										
02/04 19:56:16 - 02/05 06:00:16	Track (-467.4, 438.9) <sup>Ⓜ</sup> 02/04 19:56:00	#Returning AR 11652										
02/05 06:14:16 - 02/07 10:09:00	Track (-389.8, 443.9) <sup>Ⓜ</sup> 02/05 06:10:00	# Returning AR 11652										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

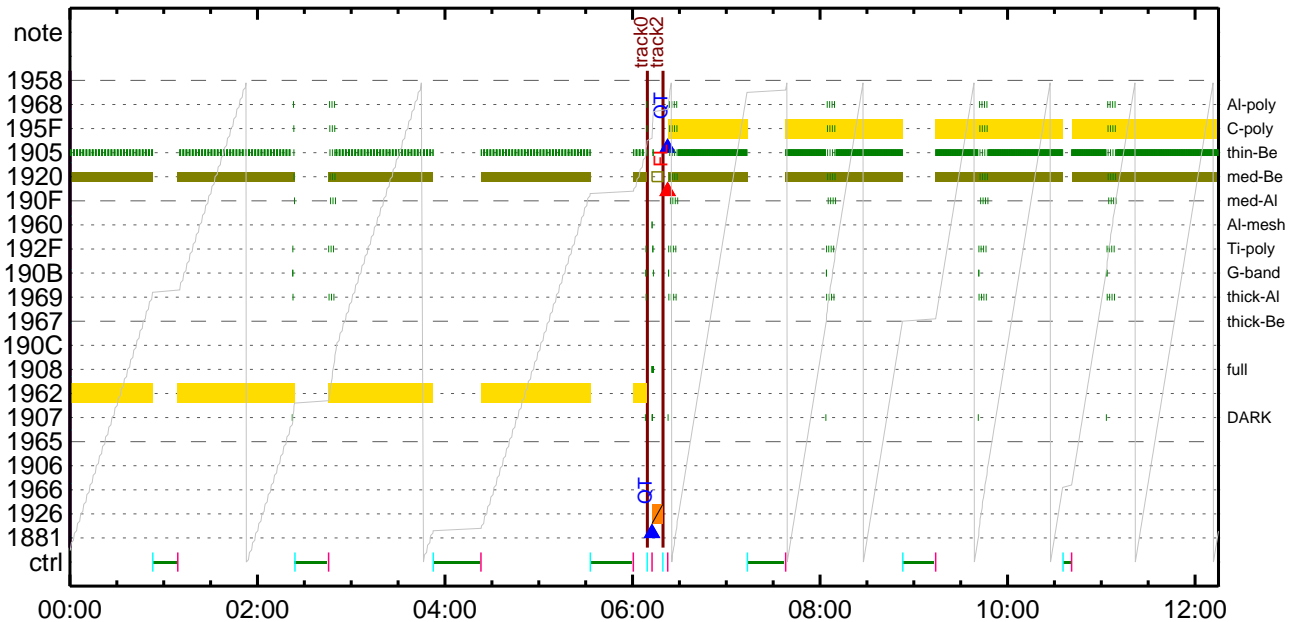
### CMDI #0228 2013/02/02



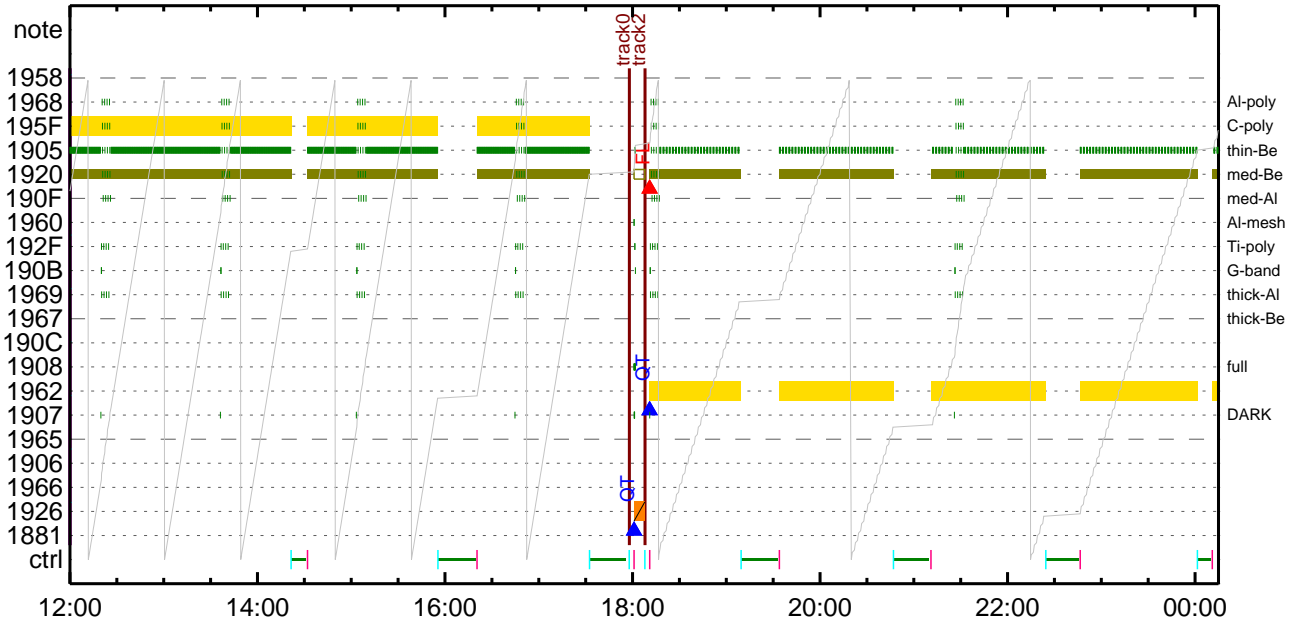
### CMDI #0228 2013/02/02



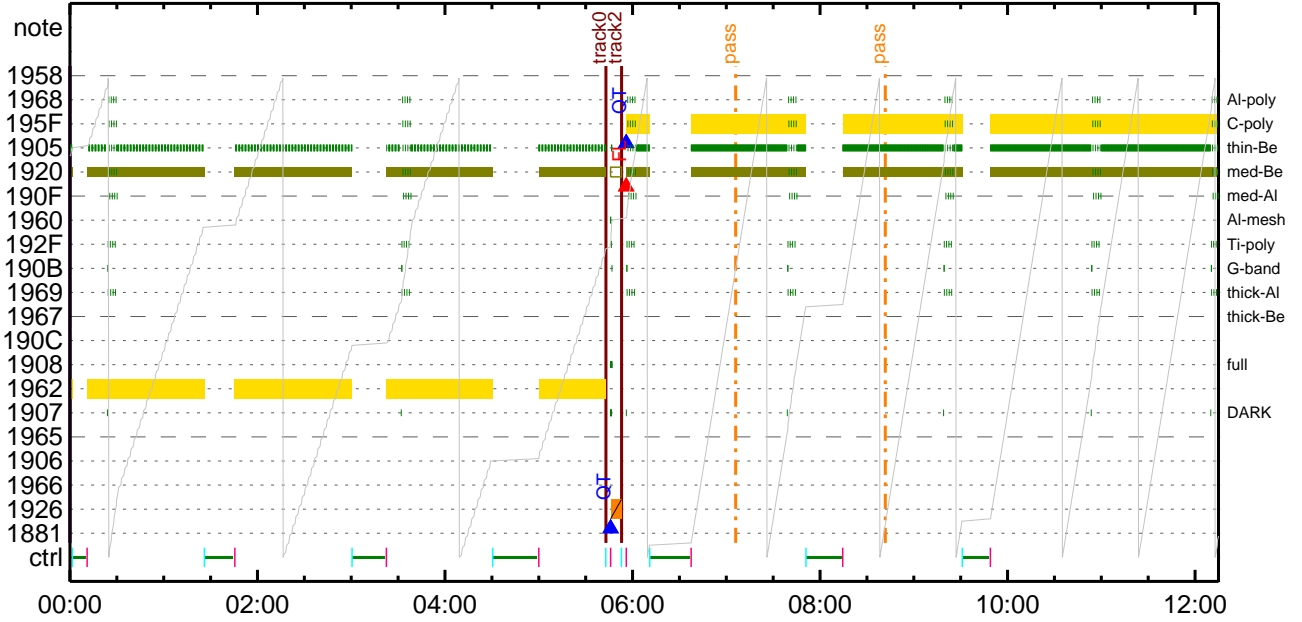
### CMDI #0228 2013/02/03



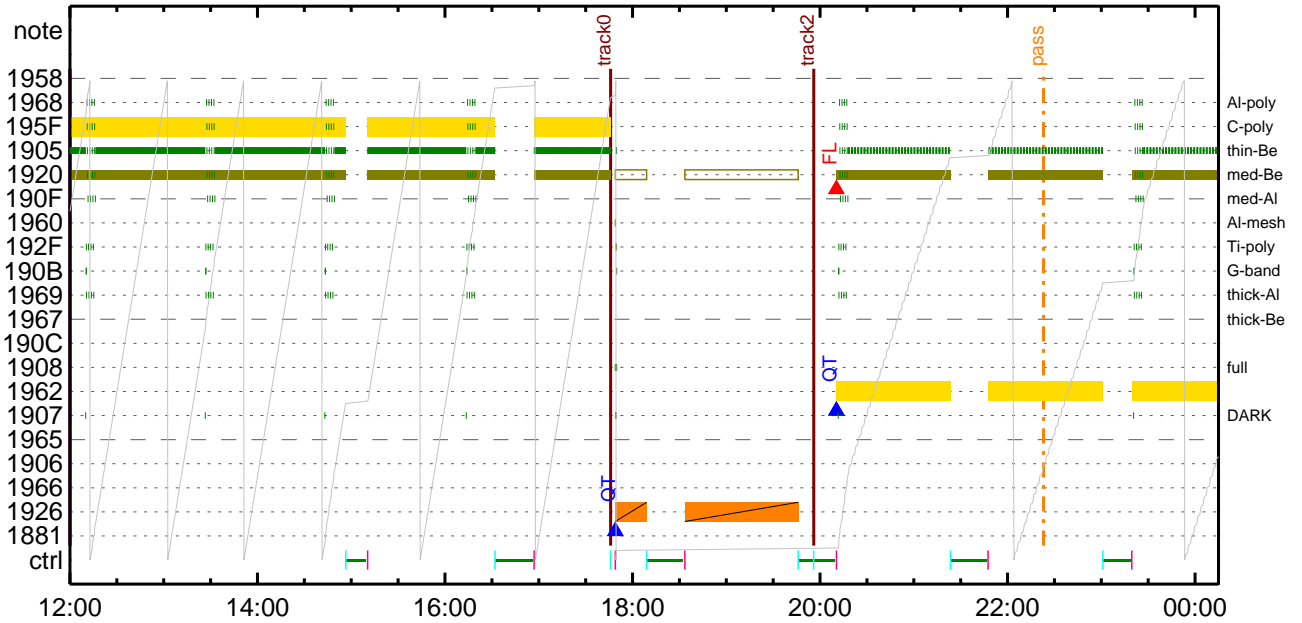
CMDI #0228 2013/02/03



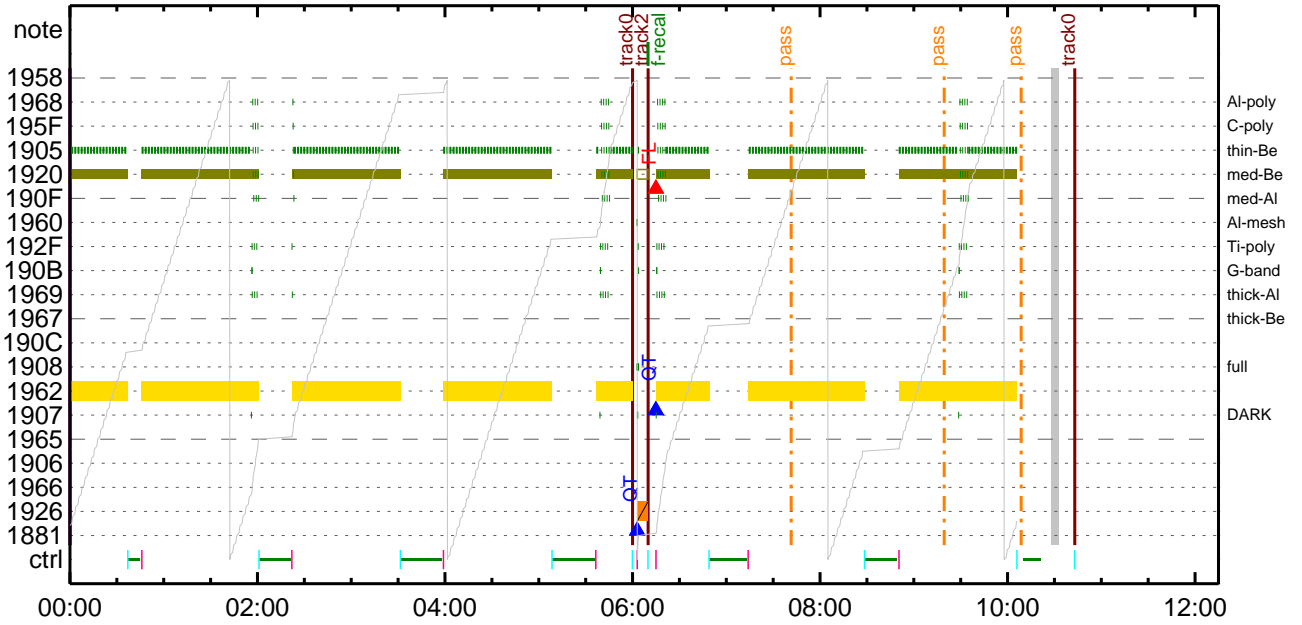
CMDI #0228 2013/02/04



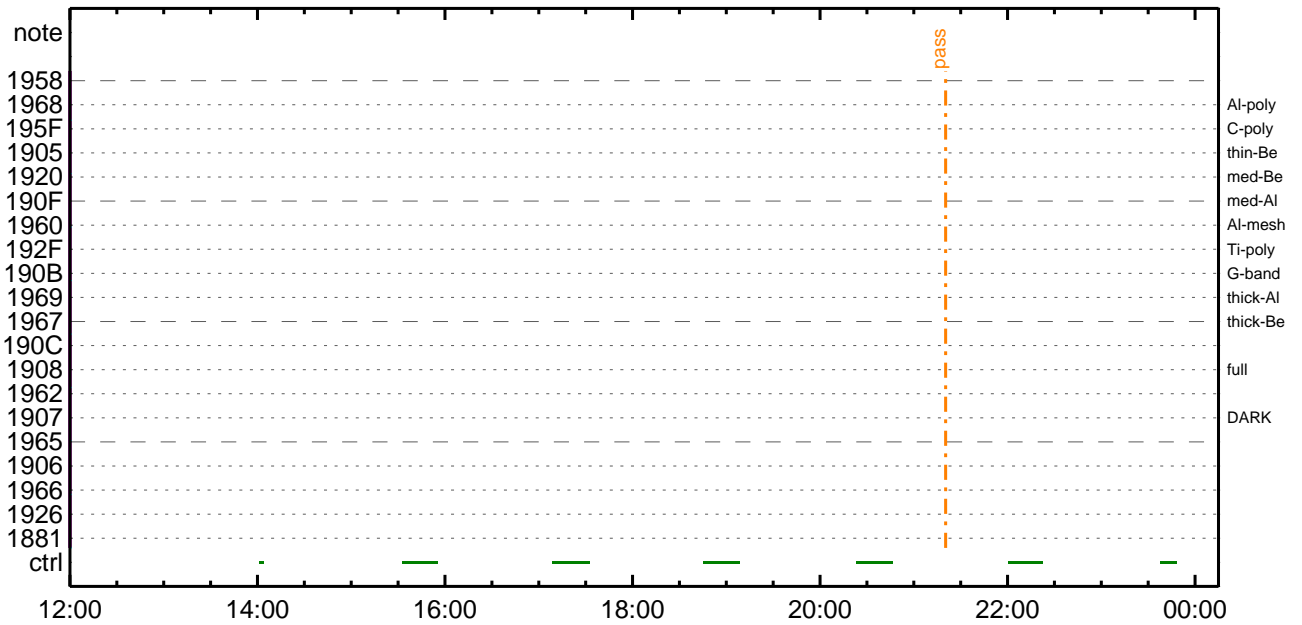
CMDI #0228 2013/02/04



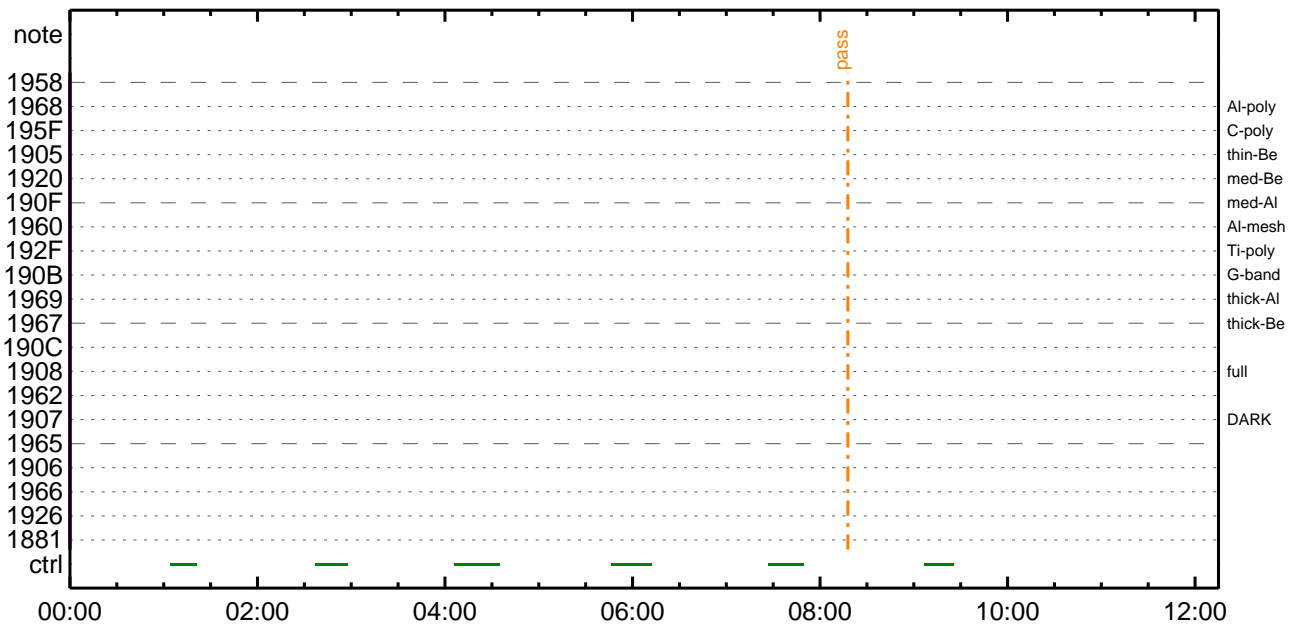
CMDI #0228 2013/02/05



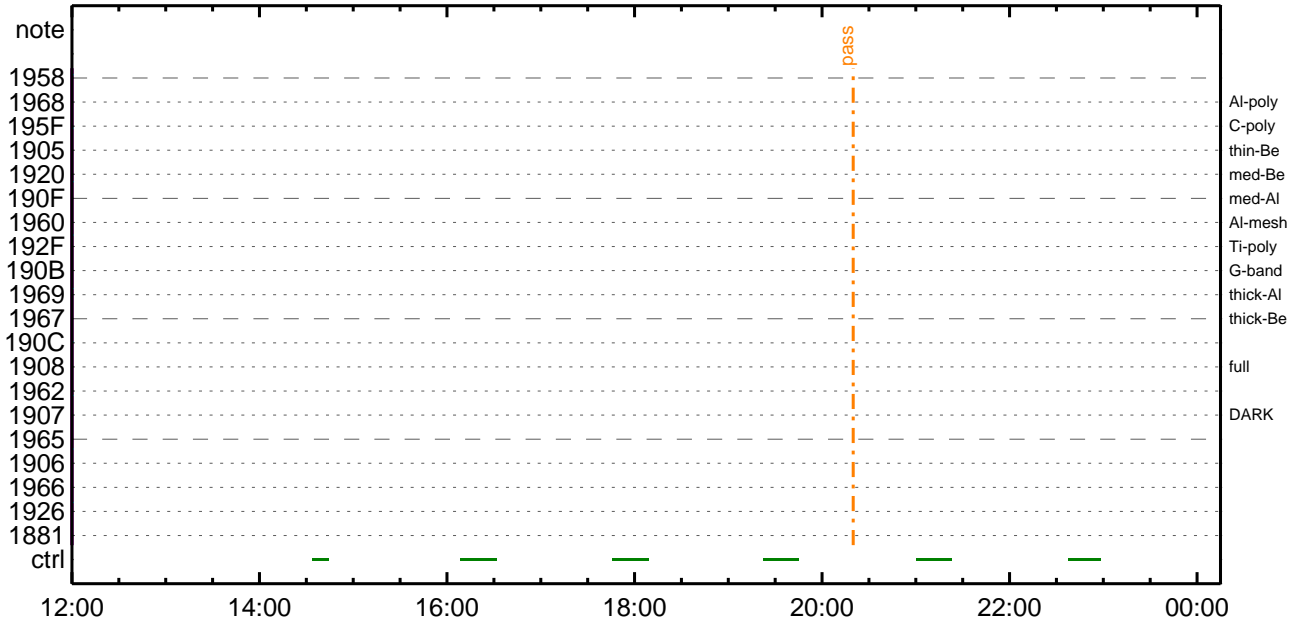
CMDI #0228 2013/02/05



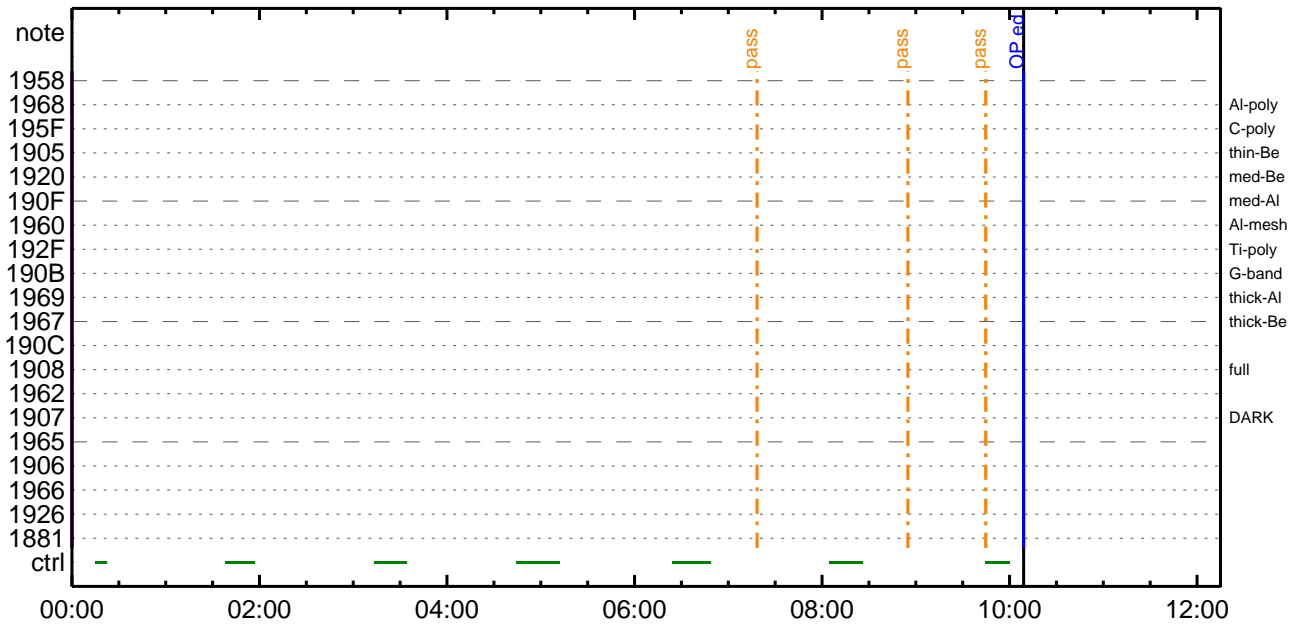
CMDI #0228 2013/02/06



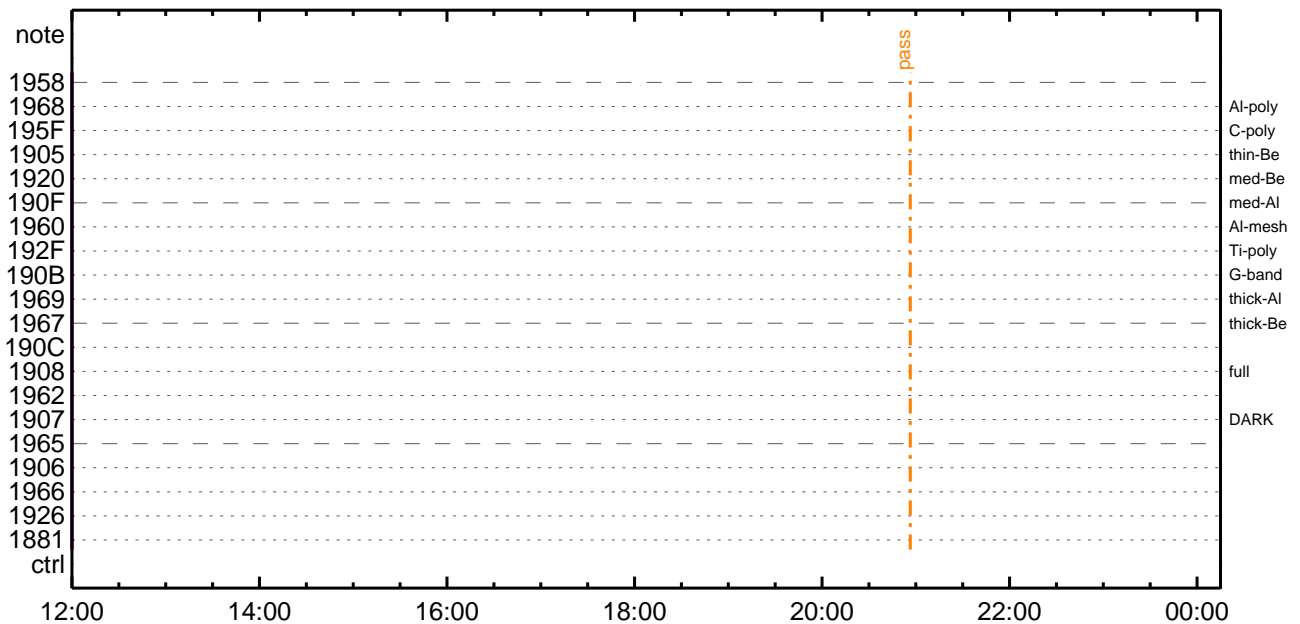
CMDI #0228 2013/02/06



CMDI #0228 2013/02/07



CMDI #0228 2013/02/07



(a) Spacecraft Operation Procedure (real-commands)

```
main-440 2013-02-02 11:38:57 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÀY$;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿ÒÁÒ•μ°Æ»Í×ÁÇÒÍYÇYÁY×Yí;¼YÉ;ÈÈÈμ•íÉ;ÈÈÈ¼°ÇÒÒ•α¿¼í¹ÇÒÍ; ÇÀ®, ùÒ¹ÒÈÒÒÇÁ+¿®Ò•ÒÈÒÒÒ³ÒÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿μ;ON
0016 C. *****
0017 C. Ç" °ÆÀ, Í×ÈYÒÁÒÌÒÒÒÇÒÍ»Þ'ÒÒÒ¹ÍÍ,Ò•; ÇÉÓÍ×ÒÈXÁÓÒÒÍ¹ÒÒÈÒÍÒÈÒÒÒ³ÒÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝÓYÉYíYÁY~¼ÔÁÒÒ-ÒÁÀÈÒ•α¿ÒÉ; Ç°È²¼ÒÍ°ÆÀ,¼È½ÇÒÒ¼Á¹ÒÒ¹ÒÉ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÌ¼í°ÆÀ,
0033 C. *****
0034 C. Ç" RESTART;ÈPT1;ÈÒ•α¿ÒÒ¼í¹ÇÒÍ; Ç°È²¼ÒÍ¼Á¹ÒÒ»Ò°; ÇDCBC-150ÒØ¿ÈÒà; f
0035 C.
0036 . C. ;ãPT1°ÆÀ,³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÁYÉÁÛÁÒ;ÈÁ•Á°²ÓÈÒ;È,ãÒÍ°ÆÀ,°Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ,Ò-¼«Æ°Áà»ÒÒ•α¿,á; Ç°È²¼ÒÒ¼Á¹ÒÒ¹ÒÉ; f
0055 C. YÇYÓYÁYÉÁÛÁÒÒÁÁ•Á°²ÓÈÒÒ-¾áÒÒ¼í¹ÇÒÍ'ÒÍ»Ò¹ÒÈÒÒÒÇÁÒÒÁ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÌ¼í°ÆÀ,
0059 C. *****
0060 C. Ç" RESTART;ÈPT2;ÈÒ•α¿ÒÒ¼í¹ÇÒÍ; Ç°È²¼ÒÍ¼Á¹ÒÒ»Ò°; ÇDCBC-151ÒØ¿ÈÒà; f
0061 C.
0062 . C. ;ãPT2°ÆÀ,³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÁYÉÁÛÁÒ;ÈÁ•Á°²ÓÈÒ;È,ãÒÍ°ÆÀ,°Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ,Áà»Ò;ÇXÁ+¿μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ,Áà»Ò;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-440:OP
0104 ( )
0105 S. OG og-440:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°è¥ÅYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥ÅYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼È¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥ÅYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼È¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥ÅYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼È¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °È²¼²î¼Ä´¶Á°²ÈÈ-²°Á÷¿@ (¼âµ-¥ÅYôYx½ªî»ò³îÇ§²°ÁÖÄæ²ç¼ª°²°è¼¹¹ç²ç²â) *****
0167 C. DHU¥â;¼YE;È¼Y½, ¥i;¼YE;Èòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼¹¹ç;ç°È²¼²î¼TI-CMDÁ÷¿@²î¼¹¹Ô²°²È²²²³²È;f
0180 C. ²²²¿;çSET²ÈDUMP²î¼±²°î¼Y¹²ç¹Ô²²³²È;f
0181 C.
0182 C. TIY³¥P¥ôYÈ²òðÁDî¿(UT)
0183 +. TI 2013-02-02 10:15:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-02-02 10:15:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-02-02 10:15:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.  
0195 +. TI 2013-02-02 10:19:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C.                   çç[HK1\_TI\_CMD\_NUM]                   EQ           1COUNTUP  
0198 C.  
0199 C. °Ê²¼□îÄë%îíñ□îŷÄŷ§ŷÄŷ¹âîÜ  
0200 C.                   çç[HK1\_TI\_CMD\_ENA/DIS]                   EQ           ENA  
0201 C.                   çç[HK1\_TI\_CMD\_NUM]                   EQ           4  
0202 C.                   çç[HK1\_NEXT\_EXEC\_PIM]                   EQ           DHU  
0203 C.                   çç[HK1\_NEXT\_EXEC\_DC]                   EQ           0xB3  
0204 C.  
0205 . C. \*\*\*\*\*  
0206 C. TIîî°èŷÄŷÖŷ×  
0207 C. \*\*\*\*\*  
0208 C.  
0209 C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC                   (03 ab 03 01 02)  
0212 C.                   çç[HK1\_DMP\_TOP\_ADRS\_1]                   EQ           07  
0213 C.                   çç[HK1\_DMP\_TOP\_ADRS\_0]                   EQ           2B  
0214 C.                   çç[HK1\_DMP\_BLOCK\_NUM]                   EQ           3  
0215 C.                   çç[HK1\_DMP\_REPEAT\_NUM]                   EQ           0  
0216 C.                   çç[HK1\_DMA\_DMP\_PIM]                   EQ           DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC                   (07 0b f8)  
0219 C.                   çç[HK1\_PKT\_FORM\_NO]                   EQ           7  
0220 C.                   çç[HK1\_PKT\_GEN\_TIME]                   EQ           0.25 s  
0221 C.                   çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ           32k  
0222 C.                   çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ           4M  
0223 C.                   çç[HK1\_DMP\_CHK\_FLG]                   EQ           EXEC  
0224 C.  
0225 . C. ŷÄŷÖŷ×½¹î»□ð³îç§  
0226 C.                   çç[HK1\_DMP\_CHK\_FLG]                   EQ           NON  
0227 C.  
0228 . C. RAM ID=TI\_TBL□îŷÄŷ¹ç.ë²îOK□ð³îç§  
0229 C.  
0230 . C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼.ŷî;¼ŷÉ;Ê□ðîä□  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC                   (02 0a f8)  
0233 C.                   çç[HK1\_PKT\_FORM\_NO]                   EQ           2  
0234 C.                   çç[HK1\_PKT\_GEN\_TIME]                   EQ           0.5S  
0235 C.                   çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ           32K  
0236 C.                   çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ           4M  
0237 C.  
0238 C. \*\*\*\*\*  
0239 C. SOT TI command set  
0240 C. \*\*\*\*\*  
0241 C. Execute, after the success of OP upload.  
0242 +. TI 2013-02-02 10:19:16.0  
0243 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0244 BC                   (41)  
0245 . C. -----  
0246 C.       HK1\_TI\_CMD\_NUM           = 1 CNTUP [ ]  
0247 C. -----  
0248 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0249 . C. Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0253 C. Execute, after the success of OP upload.  
0254 C. Set EIS TI-commands  
0255 +. TI 2013-02-02 10:19:30.0  
0256 DC 07-FC EIS\_MODE\_MANU  
0257 BC                   (21 02)  
0258 +. TI 2013-02-02 10:19:40.0  
0259 DC 07-FC EIS\_MODE\_CHG\_DIS  
0260 BC                   (22)  
0261 . C.                                   [    ] [HK1\_TI\_CMD\_NUM]           EQ           2 COUNTUP  
0262 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0263 C.  
0264 C.  
0265 C.  
0266 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2013-02-02 10:19:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC                   (c3)  
0271 . C.                                   [    ] [HK1\_TI\_CMD\_NUM]           EQ           1COUNTUP  
0272 C.  
0273 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 . C. \*\*\*\*\* MDP ´úÃî□î»ö¼ŷ□îÊÄ□î□îèDCBC.×²è \*\*\*\*\*  
0276 C. (¼á°îŷÖŷÄŷËŷŷËŷáŷçŷè□è%¼□□¼Ä»Ü□îè)  
0277 . S. DC-BC dcbc-402:DCBC  
0278 (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 . C. \*\*\*\*\* ŷÐŷ¹.î Daily±çîñ□è'Ø□î□èDCBC.×²è \*\*\*\*\*  
0282 . S. DC-BC dcbc-153:DCBC  
0283 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 . C. ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Ü;ã  
0287 C.  
0288 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

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main-441 2013-02-02 11:38:57 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Ü;ã
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Û¿µÃµ•µ°È»í×ÁÇ¿íYÇYÄY×Yí;¼YÉ;ËÈÈ¼µ•ííË;ÈµÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ;ÇÄ®, ù¹µãµµÇÄ+¿®µ•µËµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. Ç" °ÄÄ, í×ÈÝµãLOSµµÇ¿µÍ»´Öµð¹íí, µ•; ÇÉÖÍ×µÈXÄÖONµÍ¹ÖµÈµíµËµµ³µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÄY-¾ÖÄÖµ~°ÄÄêµ•µ¿µé; Ç°È²¼µí°ÈÄ, ¼ê¾Çµð¼Ä¹Öµ¹µé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÈÄ,
0033 C. *****
0034 C. Ç" RESTART;ËPT1;Ëµ•µ¿µ¿¼í¹ÇµÍ; Ç°È²¼µí°ÈÄ¹Öµ»µ°; ÇDCBC-150µØ¿Ëµà;f
0035 C.
0036 . C. ;ãPT1°ÈÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÄÜÁ;ËÄ•Ä°²ÓÈð;Ë, áµí°ÈÄ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÄ, µ~¼«È°Ää»ßµ•µ¿, á; Ç°È²¼µð¼Ä¹Öµ¹µé;f
0055 C. YÇYÓYÉYËÄÜÁ;ËÄ•Ä°²ÓÈðµ~¼áµ¿¼í¹ÇµÍ' °í»µ¹µãµµÇÄÖµÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÈÄ,
0059 C. *****
0060 C. Ç" RESTART;ËPT2;Ëµ•µ¿µ¿¼í¹ÇµÍ; Ç°È²¼µí°ÈÄ¹Öµ»µ°; ÇDCBC-151µØ¿Ëµà;f
0061 C.
0062 . C. ;ãPT2°ÈÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÄÜÁ;ËÄ•Ä°²ÓÈð;Ë, áµí°ÈÄ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÄ, Ää»ß; ÇXÄ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÄ, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF

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0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 03)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 10)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2013-02-02 10:19:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîî»ö¼ÝðËÂð¹æDCBC•x²è *****
0117 C. (%ã°îÿÓÿÄÿËÿÏÿËÿáÿçÿèæ¼æ¼Ä»Û¹æè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿDÿ¹•Ï Daily±¿ÎÑæ'Ø¹æèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿSÿÄÿ-¼Ä»Û;ã
0128 C.
0129 . C. ***** LOS *****
0130 C.

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Feb 02, 13 11:39

XRT\_OGLIST\_0228.chk

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\*\*\* OP Sequence for XRT \*\*\*

2013/02/02	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/02	10:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/02/02	10:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2013/02/02	10:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/02/02	10:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/02/02	10:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/02/02	10:30:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/02	10:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/02	10:32:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2013/02/02	10:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/02/02	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/02	15:19:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/02	15:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/02	15:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/02	15:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/02	15:42:30.0	XRT_Custom_434_OG [0x1b2]					
2013/02/02	15:43:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/02	16:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/02	16:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/02	16:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/02	16:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/02	17:19:30.0	XRT_Custom_434_OG [0x1b2]					
2013/02/02	17:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/02	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/02	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/02/02	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/02/02	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/02/02	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/02/02	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/02	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02		
2013/02/02	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/02	18:33:00.5	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/02	18:33:02.5	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/02	18:33:04.5	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/02	18:36:14.5	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/02	18:56:30.0	XRT_Custom_434_OG [0x1b2]					
2013/02/02	18:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/02	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/02	20:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/02/02	20:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2013/02/02	20:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/02/02	20:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/02/02	20:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/02/02	20:10:22.0	XRT_ARS_DIS_410_OG [0x19a]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/02	20:34:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/02	20:34:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2013/02/02	20:34:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		

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2013/02/02	20:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/02	21:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/02	21:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/02	21:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/02	21:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/02	22:10:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/02	22:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/02	23:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/02	23:25:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/02	23:25:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/02	23:28:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/02	23:37:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/02	23:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/03	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/03	00:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/03	00:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/03	00:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/03	01:08:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/03	01:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/03	02:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/03	02:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/03	02:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/03	02:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/03	02:44:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/03	02:45:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/03	03:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/03	03:52:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/03	03:52:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/03	03:55:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/03	04:22:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/03	04:23:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/03	05:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/03	05:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/03	05:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/03	05:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/03	05:59:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/03	06:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/03	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/03	06:09:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/02/03	06:09:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/02/03	06:09:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/02/03	06:09:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/02/03	06:09:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/03	06:12:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2013/02/03	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/03	06:19:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/03	06:19:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/02/03	06:19:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/02/03	06:19:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/02/03	06:19:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							

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2013/02/03	06:19:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/02/03	06:19:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/02/03	06:22:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	06:22:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2013/02/03	06:22:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/02/03	06:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	07:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	07:13:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	07:13:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/02/03	07:16:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/02/03	07:37:00.0	XRT_Custom_434_OG [0x1b2]					
2013/02/03	07:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	08:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	08:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	08:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/02/03	08:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/02/03	09:13:00.0	XRT_Custom_434_OG [0x1b2]					
2013/02/03	09:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	10:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	10:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	10:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/02/03	10:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/02/03	10:40:00.5	XRT_Custom_434_OG [0x1b2]					
2013/02/03	10:41:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	14:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	14:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	14:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/02/03	14:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/02/03	14:31:00.0	XRT_Custom_434_OG [0x1b2]					
2013/02/03	14:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	15:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	15:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/02/03	15:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/02/03	16:19:30.0	XRT_Custom_434_OG [0x1b2]					
2013/02/03	16:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	17:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	17:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/02/03	17:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/02/03	17:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/02/03	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/02/03	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/02/03	17:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/02/03	17:58:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/02/03	17:58:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/02/03	17:58:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/02/03	18:00:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2013/02/03	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/02/03	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]					



2013/02/03	18:07:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/03	18:08:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/02/03	18:08:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	02 00 00 00 00
2013/02/03	18:08:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/02/03	18:08:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/02/03	18:08:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/02/03	18:10:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/03	18:10:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/03	18:10:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2013/02/03	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/02/03	19:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/03	19:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/03	19:09:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/03	19:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/03	19:33:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/03	19:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2013/02/03	20:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/03	20:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/03	20:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/03	20:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/03	21:10:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/03	21:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2013/02/03	22:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/03	22:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/03	22:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/03	22:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/03	22:45:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/03	22:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2013/02/04	00:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	00:01:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	00:01:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	00:04:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	00:10:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	00:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2013/02/04	01:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	01:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	01:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	01:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	01:44:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	01:45:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2013/02/04	03:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	03:00:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	03:00:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	03:03:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	03:21:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	03:22:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	XRT_Custom_434_OG [0x1b2]			
2013/02/04	04:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	04:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	04:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2013/02/04	04:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	04:59:00.0	XRT_Custom_434_OG [0x1b2]			
2013/02/04	05:00:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	05:42:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	05:42:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/02/04	05:43:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/02/04	05:43:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/02/04	05:43:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/02/04	05:43:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/04	05:45:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/02/04	05:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	05:52:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/02/04	05:53:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/02/04	05:53:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/02/04	05:53:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/02/04	05:53:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/02/04	05:53:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/04	05:55:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	05:55:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2013/02/04	05:55:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/02/04	05:56:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	06:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	06:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	06:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	06:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	06:36:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/04	06:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	07:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	07:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	07:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	07:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	08:13:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/04	08:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	09:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	09:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	09:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	09:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	09:48:00.5	XRT_Custom_434_OG [0x1b2]			
2013/02/04	09:49:00.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	14:56:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	14:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	14:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/04	14:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/04	15:09:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/04	15:10:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/04	16:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/04	16:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/04	16:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2013/02/04	16:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/04	16:56:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/04	16:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/04	17:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/04	17:45:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/02/04	17:46:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/02/04	17:46:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/02/04	17:46:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/02/04	17:46:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/04	17:48:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2013/02/04	17:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/04	18:09:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/04	18:09:02.5	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/04	18:09:04.5	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/04	18:12:14.5	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/04	18:32:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/04	18:33:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/04	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/04	19:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/04	19:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/04	19:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/04	19:55:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/04	19:55:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/02/04	19:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/02/04	19:56:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/02/04	19:56:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/02/04	19:56:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/02/04	19:56:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/04	20:10:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/04	20:10:26.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2013/02/04	20:10:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/02/04	20:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/04	21:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/04	21:23:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/04	21:23:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/04	21:26:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/04	21:46:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/04	21:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/04	23:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/04	23:01:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/04	23:01:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/04	23:04:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/04	23:18:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/04	23:19:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	00:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	00:37:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	00:37:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/05	00:40:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/05	00:45:00.0	XRT_Custom_434_OG [0x1b2]							

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2013/02/05	00:46:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	02:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	02:01:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	02:01:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/05	02:04:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/05	02:21:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/05	02:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	03:31:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	03:31:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/05	03:34:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/05	03:58:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/05	03:59:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	05:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	05:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	05:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/05	05:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/05	05:35:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/05	05:36:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/02/05	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/02/05	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/02/05	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/02/05	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/05	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2013/02/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	06:09:56.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/02/05	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/02/05	06:13:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/02/05	06:14:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/02/05	06:14:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/02/05	06:14:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/02/05	06:14:22.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/05	06:14:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	06:14:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2013/02/05	06:14:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/02/05	06:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	06:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	06:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	06:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/05	06:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/05	07:13:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/05	07:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/05	08:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/05	08:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/05	08:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/05	08:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							

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2013/02/05	08:49:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/02/05	08:50:30.0	XRT_CTRL_AUTO_413_OG [0x19d]									
2013/02/05	10:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/02/05	10:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/02/05	10:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da					
2013/02/05	10:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/02/05	10:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/02/05	10:43:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
		AOCU_NM		5	02-76	00 00 00 00 00					