

# XRT Timeline to be uploaded on 2013/02/19

Period: 2013/02/19 11:00:00 - 2013/02/23 10:30:00

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## Normal mode

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### XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
02/19 11:13:00 - 02/19 17:36:30	Track ( -1.5, 303.9) @ 02/19 11:10:00	# OP start + 10min, traking AR11675
02/19 18:13:00 - 02/20 05:46:54	Track ( 62.9, 303.8) @ 02/19 18:10:00	# AR 11675
02/20 06:00:00 - 02/20 17:49:54	Track ( 170.6, 302.3) @ 02/20 05:57:00	# AR11675
02/20 23:03:00 - 02/21 05:59:54	Track ( 322.1, 297.6) @ 02/20 23:00:00	# AR11675
02/21 06:13:00 - 02/21 11:16:54	Track ( 383.4, 294.7) @ 02/21 06:10:00	# AR11675

**PROG= 18 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
<b>Seqn=100</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Open/G-band	Open/G-band open	Safe Norm 32ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
<b>Seqn= 19</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band close	Safe Norm 63ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
<b>Seqn= 65</b>	<b>4-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
med-Be/Open	med-Be/Open close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 94</b>	<b>70-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec				

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

### XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250)

Term	Pointing (x, y)	Comment
02/19 18:03:00 - 02/19 18:09:54	Fixed ( 0.0, 0.0)	synoptic
02/20 05:50:00 - 02/20 05:56:54	Fixed ( 0.0, 0.0)	synoptic, shifted -13.0 min
02/20 17:53:00 - 02/20 17:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted -10.0 min
02/21 06:03:00 - 02/21 06:09:54	Fixed ( 0.0, 0.0)	synoptic

**PROG= 12 1-time(s)**

Subr= 1	1-time(s)	14.0sec										
<b>Seqn= 96</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Al-mesh	Open/Al-mesh close	Safe Norm 44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 6</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec				
<b>Seqn= 41</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 30</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	thin-Be/Open close	Safe Norm 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 69</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band open	Safe Norm 8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 68</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band close	Safe Norm 32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec				

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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## Flare mode

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### XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
02/19 11:13:00 - 02/19 17:36:30	Track ( -1.5, 303.9) @ 02/19 11:10:00	# OP start + 10min, traking AR11675
02/19 18:13:00 - 02/20 05:46:54	Track ( 62.9, 303.8) @ 02/19 18:10:00	# AR 11675
02/20 06:00:00 - 02/20 17:49:54	Track ( 170.6, 302.3) @ 02/20 05:57:00	# AR11675
02/20 23:03:00 - 02/21 05:59:54	Track ( 322.1, 297.6) @ 02/20 23:00:00	# AR11675
02/21 06:13:00 - 02/21 11:16:54	Track ( 383.4, 294.7) @ 02/21 06:10:00	# AR11675

**PROG= 16 15-time(s)**

Subr= 1	45-time(s)	10.0sec										
<b>Seqn= 35</b>	<b>1-time(s)</b>	<b>2.0sec</b>										

thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2      1-time(s)      10.0sec</b>												
<b>Seqn= 36      1-time(s)      2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 37      1-time(s)      2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 38      1-time(s)      2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

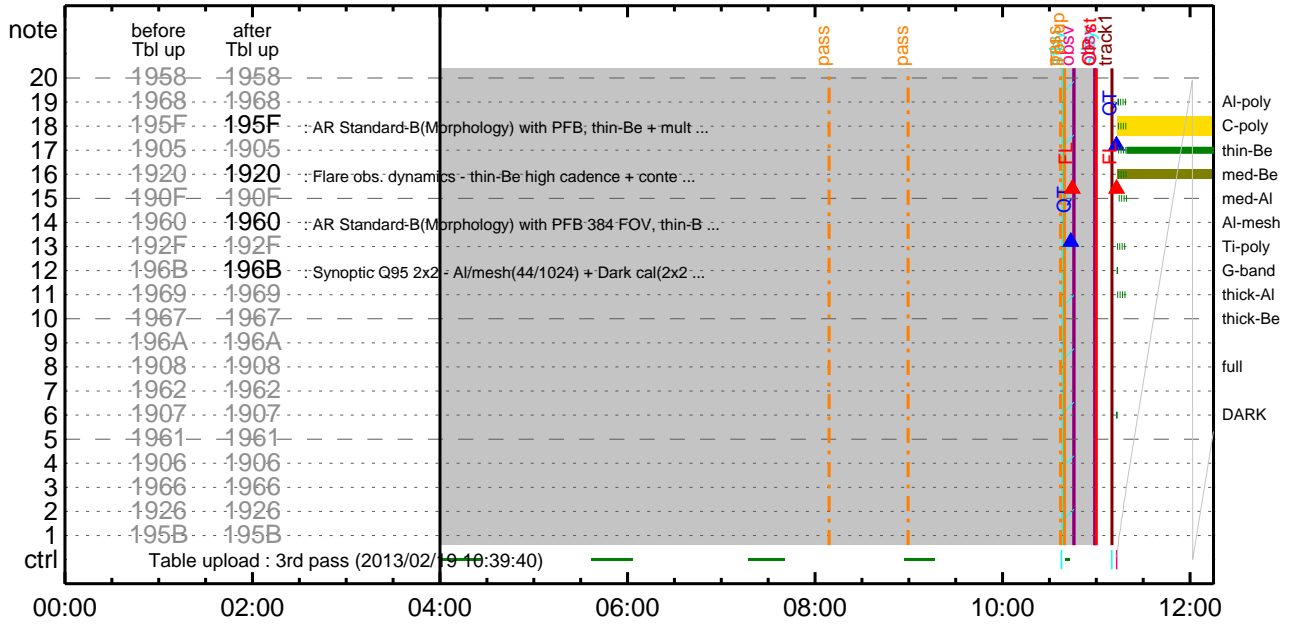
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### Flare Detection

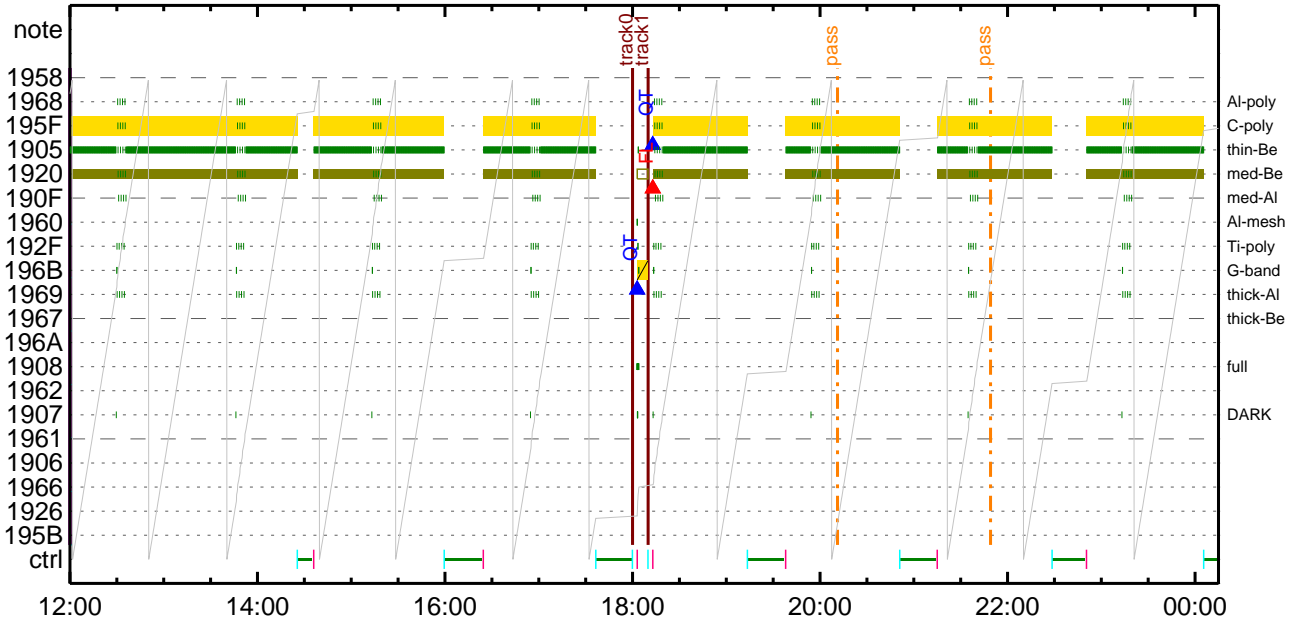
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<b>FLD Patrol</b>														
Term	Pointing (x, y)		Comment											
02/19 18:10:16 - 02/20 05:47:16	Track (	62.9, 303.8)	@ 02/19 18:10:00	# AR 11675										
02/20 05:57:16 - 02/20 17:50:16	Track (	170.6, 302.3)	@ 02/20 05:57:00	# AR11675										
02/20 23:00:16 - 02/21 06:00:16	Track (	322.1, 297.6)	@ 02/20 23:00:00	# AR11675										
02/21 06:10:16 - 02/23 10:30:00	Track (	383.4, 294.7)	@ 02/21 06:10:00	# AR11675										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

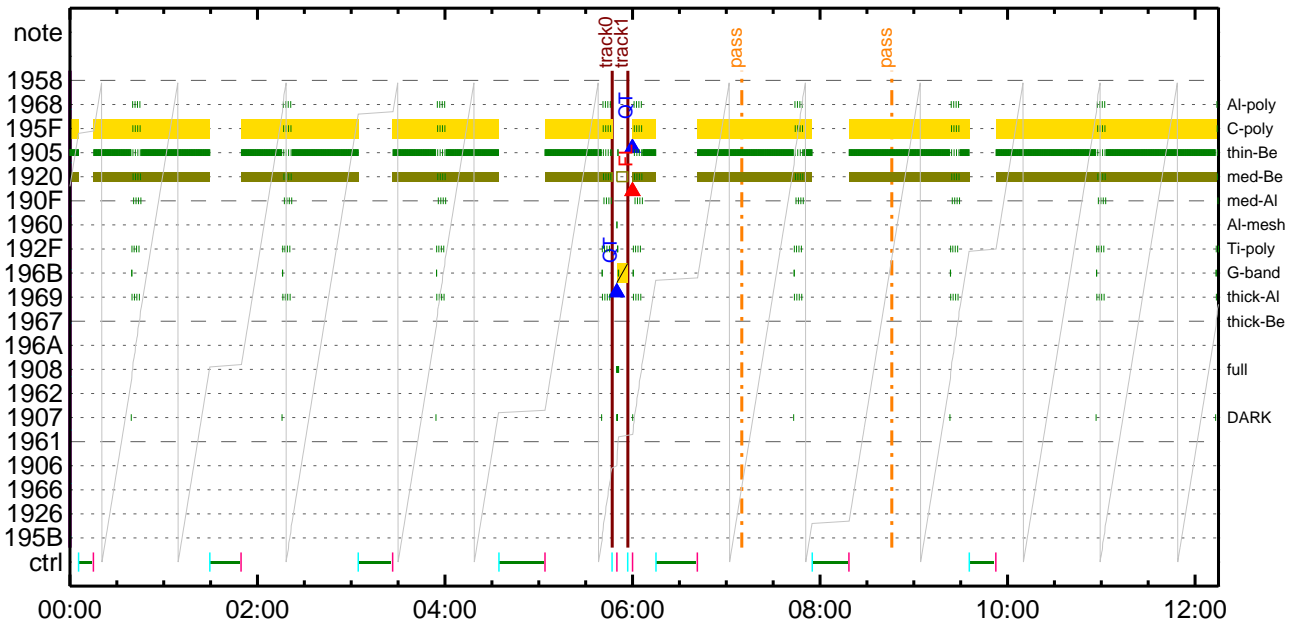
### CMDI #0271 2013/02/19



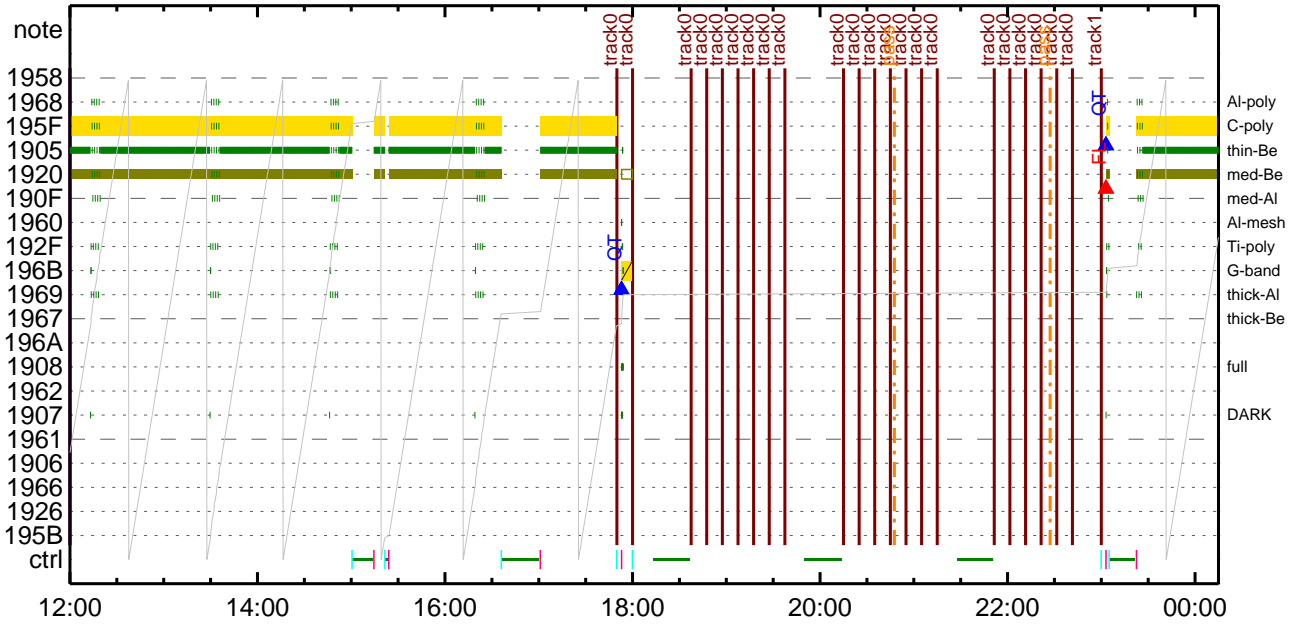
### CMDI #0271 2013/02/19



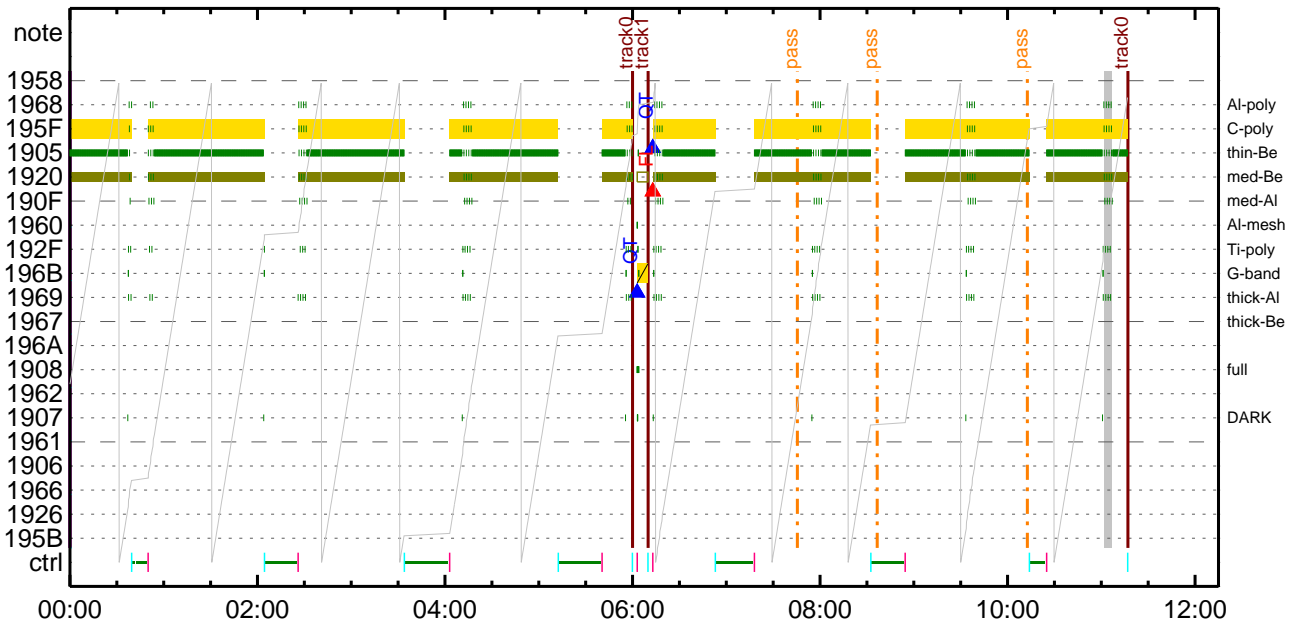
### CMDI #0271 2013/02/20



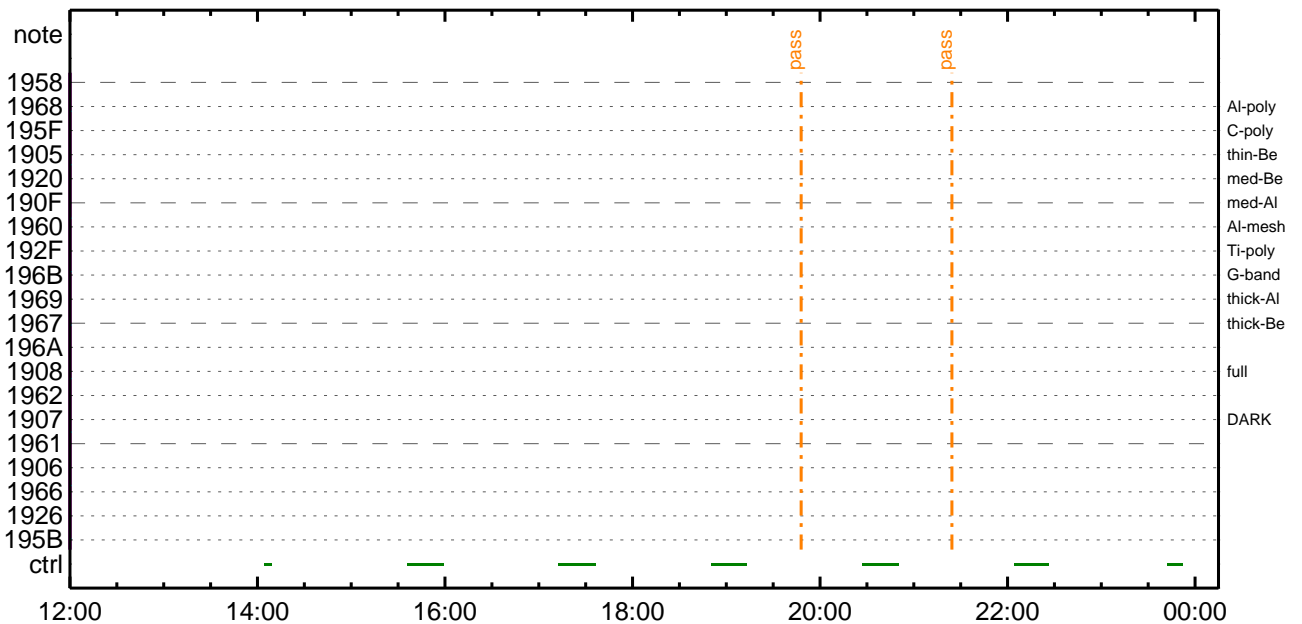
CMDI #0271 2013/02/20



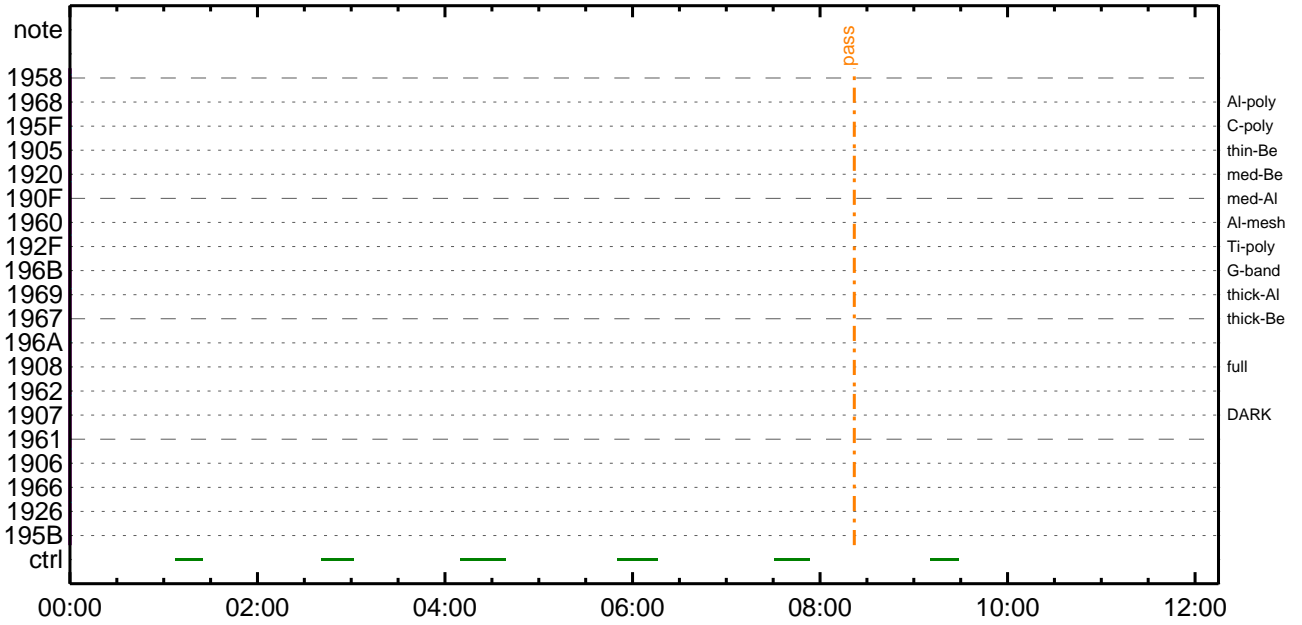
CMDI #0271 2013/02/21



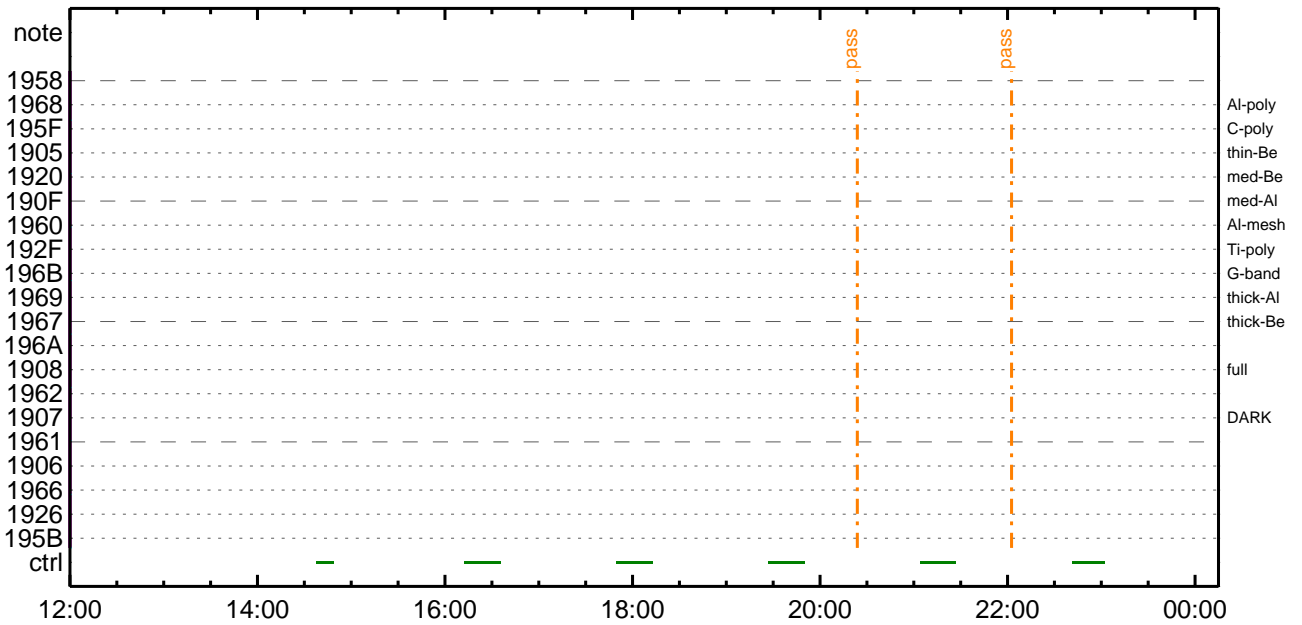
CMDI #0271 2013/02/21



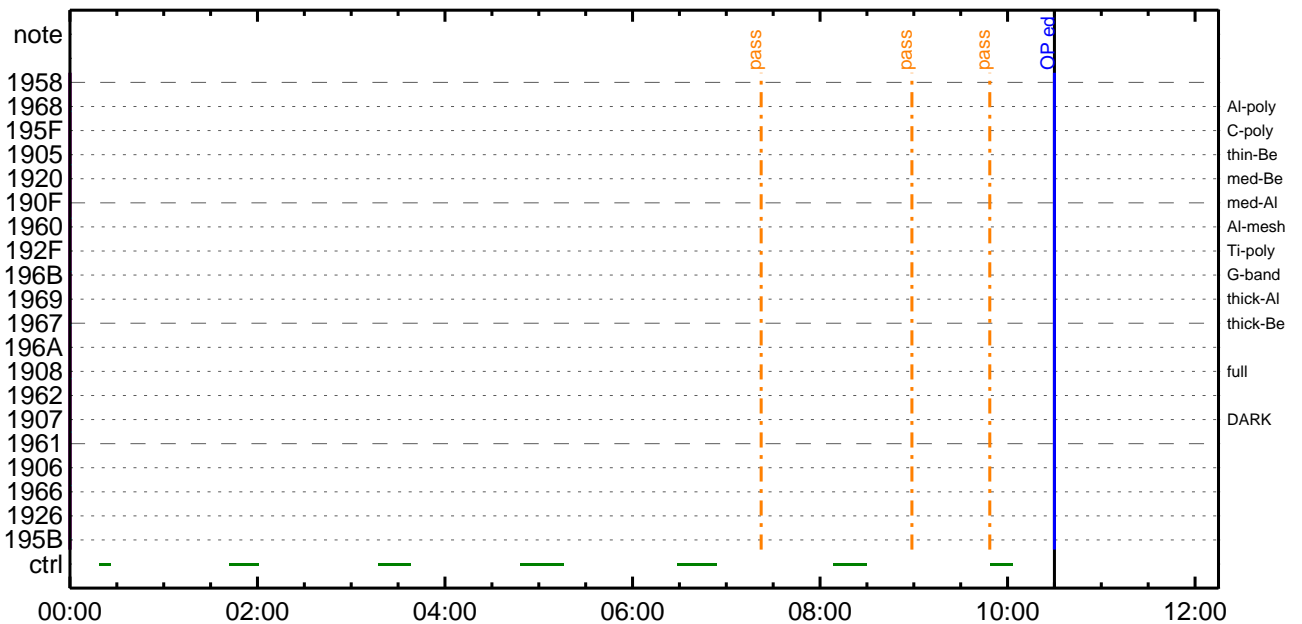
CMDI #0271 2013/02/22



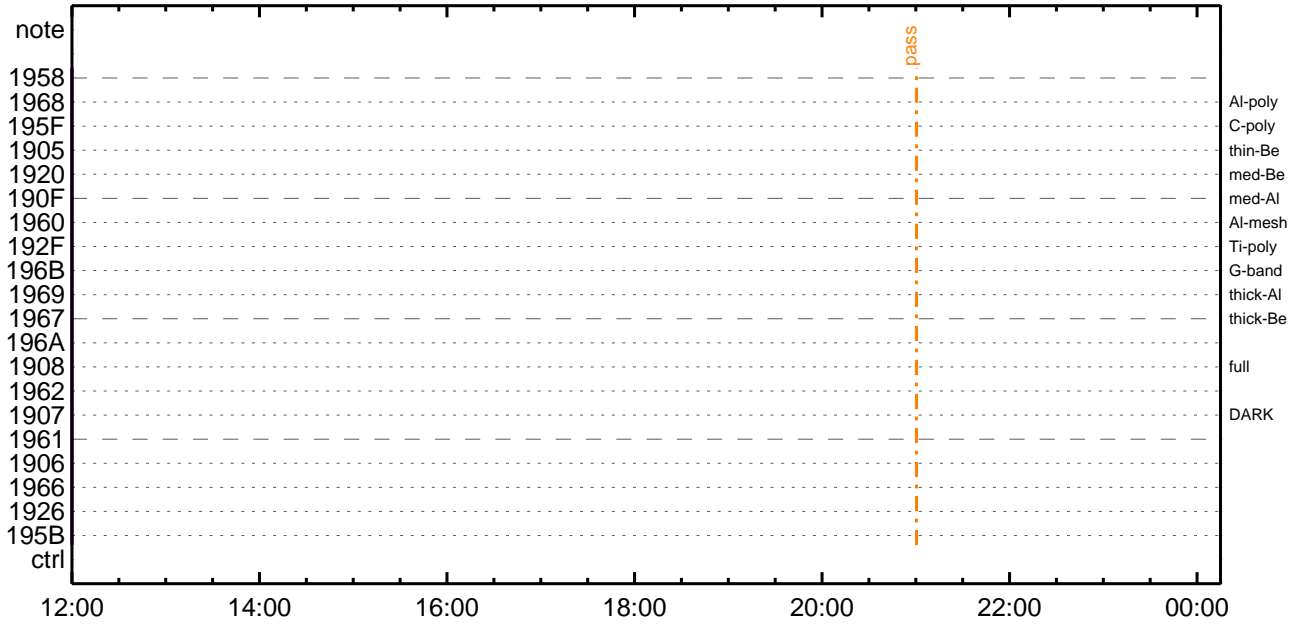
CMDI #0271 2013/02/22



CMDI #0271 2013/02/23



CMDI #0271 2013/02/23



(a) Spacecraft Operation Procedure (real-commands)

```
main-475 2013-02-19 13:09:59 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççðÁð•µ°È»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ•ííÈ;ÈÈÈ¼°ÇÖñ•çç¼l¹ççí;çÁ®, ùñ¹ñññññçÁ+ç®ñ•ññññññññ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç“ °ÈÀ, í×ÈYññáLOSññççí»p´Öññ¹íí, ñ•; çÉÖÍ×ñÈXÁÖONññí¹Öññññññññññññ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖññ-°ÁÁÈñ•çç; ç°È²¼ññí°ÈÀ, ¼È¼ççññ¼Á¹Öññññ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èñ•çççç¼l¹ççí; ç°È²¼ññí°ÈÀ¹Öññññ; çDCBC-150ñççÈññ; f
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÁÙÁØ;ÈÁ•Á°²óÈð;È, áññ°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ñ-¼«È°Áá»ßñ•çç; á; ç°È²¼ññ¼Á¹Öññññ; f
0055 C. YçYÓYÉYÈÁÙÁØññÁ•Á°²óÈðññ-¼áññ¼l¹ççí°í»ññ¹ñññññçÁÖññ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èñ•çççç¼l¹ççí; ç°È²¼ññí°ÈÀ¹Öññññ; çDCBC-151ñççÈññ; f
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÁÙÁØ;ÈÁ•Á°²óÈð;È, áññ°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-475:OP
0104 ( )
0105 S. OG og-475:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2013-02-19 10:55:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-02-19 10:55:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-02-19 10:55:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```







(a) Spacecraft Operation Procedure (real-commands)

```
main-477 2013-02-19 13:09:59 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁò•µ°È»Í×ÁÇòíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòí;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòòòè;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 08 08)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 + DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 0e)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 10)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2013-02-19 10:59:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Upload DPL table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 C. YçYÁY×Yí;¼YÉòîÁ°òÈSTS_CHKòòOFFòÈò¹òè
0094 C.
0095 . S. RAM ram-271:MDP_DPL
```

```

0096      ( )
0097      C.
0098      . C. < Dump RAMID=MDP_DPL >
0099 +. DC 07-F0 MDP_DUMP_FGTBL
0100      BC      (82 07 00 38 b8 00 40)
0101      C. -----
0102      C. MDP_DPL verify          = OK      [ ]
0103      C. -----
0104      C.
0105      C. STS_CHKαδONαÈα¹αè
0106      C.
0107      . C. < Update MDP DSC PAR1 >
0108 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0109      BC      (4c)
0110      C. MDP_CMD_CODE          = F04C0700[ ]
0111      C. MDP_CMD_CNT          (count-up 1) [ ]
0112      C. -----
0113      C.
0114      . C.
0115      C.
0116      . C. ***** MDP 'úÃîαî»ö¼ÝαÈÃĐα¹αèDCBC•x²è *****
0117      C. (¼ã°îÏÓ¥Ã¥È¥Þ¥È¥á¥ç¥èαÈ¼αα¼Ã»Ûα¹αè)
0118      . S. DC-BC dcbc-402:DCBC
0119      (MDP_known_event)
0120      C.
0121      C.
0122      . C. ***** ¥Đ¥¹•Ï Daily±¿ÎÑαÈ´Øα¹αèDCBC•x²è *****
0123      . S. DC-BC dcbc-153:DCBC
0124      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125      C.
0126      C.
0127      . C. ;ãLOS¥Ã¥$¥Ã¥´¼Ã»Û;ã
0128      C.
0129      . C. ***** LOS *****
0130      C.

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Feb 19, 13 13:10

XRT\_OGLIST\_0271.chk

Page 1/6

\*\*\* OP Sequence for XRT \*\*\*

2013/02/19	11:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/19	11:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/02/19	11:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2013/02/19	11:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/02/19	11:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/02/19	11:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/02/19	11:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/19	11:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/19	11:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2013/02/19	11:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/02/19	11:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/19	14:25:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/19	14:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/19	14:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/19	14:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/19	14:35:00.0	XRT_Custom_434_OG [0x1b2]					
2013/02/19	14:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/19	15:59:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/19	15:59:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/19	15:59:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/19	16:02:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/19	16:23:30.0	XRT_Custom_434_OG [0x1b2]					
2013/02/19	16:24:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/19	17:36:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/19	17:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/19	17:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/19	17:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/19	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/02/19	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/02/19	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/02/19	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/02/19	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/19	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2013/02/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/19	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/19	18:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/02/19	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2013/02/19	18:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/02/19	18:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/02/19	18:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/02/19	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/19	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/19	18:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2013/02/19	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/02/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/19	19:13:30.0	XRT_CTRL_MANU_400_OG [0x190]					

Feb 19, 13 13:10

## XRT\_OGLIST\_0271.chk

Page 2/6

2013/02/19	19:13:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/19	19:13:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	19:16:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	19:37:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	19:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/19	20:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/19	20:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/19	20:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/19	20:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	21:14:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	21:15:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/19	22:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/19	22:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/19	22:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/19	22:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	22:49:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/19	22:50:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	00:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	00:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	00:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	00:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	00:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	00:14:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	00:15:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	01:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	01:29:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	01:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	01:29:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	01:32:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	01:48:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	01:49:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	03:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	03:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	03:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	03:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	03:25:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	03:26:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	04:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	04:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	04:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	04:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	05:03:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	05:04:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	05:46:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/02/20	05:47:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
			AOCU_NM	5	02-76	00 00 00 00 00
2013/02/20	05:47:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
			MDP_XRT_FLD_DIS	1	07-F0	d9
2013/02/20	05:47:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/02/20	05:47:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/20	05:49:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/02/20	05:50:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Feb 19, 13 13:10

## XRT\_OGLIST\_0271.chk

Page 3/6

2013/02/20	05:56:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2013/02/20	05:57:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2013/02/20	05:57:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/02/20	05:57:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/02/20	05:57:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/02/20	05:57:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/20	05:59:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	05:59:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2013/02/20	05:59:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/02/20	06:00:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	06:15:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	06:15:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	06:18:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	06:40:30.0	XRT_Custom_434_OG [0x1b2]				
2013/02/20	06:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	07:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	07:55:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	07:55:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	07:58:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	08:17:30.0	XRT_Custom_434_OG [0x1b2]				
2013/02/20	08:18:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	09:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	09:35:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	09:35:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	09:38:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	09:51:30.0	XRT_Custom_434_OG [0x1b2]				
2013/02/20	09:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	15:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	15:00:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	15:00:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	15:03:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	15:13:30.0	XRT_Custom_434_OG [0x1b2]				
2013/02/20	15:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	15:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	15:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	15:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	15:23:00.0	XRT_Custom_434_OG [0x1b2]				
2013/02/20	15:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	15:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	16:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	16:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	16:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	16:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	17:00:00.0	XRT_Custom_434_OG [0x1b2]				
2013/02/20	17:01:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	17:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/02/20	17:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00

Feb 19, 13 13:10

## XRT\_OGLIST\_0271.chk

Page 4/6

2013/02/20	17:50:16.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2013/02/20	17:50:18.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/02/20	17:50:20.0	XRT_ARS_DIS_406_OG [0x196] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/20	17:52:58.0	XRT_QT_PROG_SET_417_OG [0x1a1] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/02/20	17:53:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 01 81 51 a9
2013/02/20	18:37:30.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00 01 81 3f f7
2013/02/20	18:47:30.0	AOCS_ORe-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	00 01 81 2e 45
2013/02/20	18:57:30.0	AOCS_ORe-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	00 01 81 1c 93
2013/02/20	19:07:30.0	AOCS_ORe-point_Start_7_OG [0x09d] AOCU_NM	5	02-76	00 01 81 0a e1
2013/02/20	19:17:30.0	AOCS_ORe-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 01 81 f9 30
2013/02/20	19:27:30.0	AOCS_ORe-point_Start_9_OG [0x09f] AOCU_NM	5	02-76	00 01 81 e7 7d
2013/02/20	19:37:30.0	AOCS_ORe-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00 01 81 d5 cb
2013/02/20	20:15:00.0	AOCS_ORe-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00 01 81 c4 19
2013/02/20	20:25:00.0	AOCS_ORe-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00 01 81 b2 67
2013/02/20	20:35:00.0	AOCS_ORe-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00 ec 11 51 0e
2013/02/20	20:45:00.0	AOCS_ORe-point_Start_14_OG [0x0a4] AOCU_NM	5	02-76	00 e4 bd 41 26
2013/02/20	20:55:00.0	AOCS_ORe-point_Start_15_OG [0x0a5] AOCU_NM	5	02-76	00 e2 ea 30 a3
2013/02/20	21:05:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00 e1 cb 1f e7
2013/02/20	21:15:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	00 e1 38 0e e9
2013/02/20	21:51:30.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 e1 17 fd 8a
2013/02/20	22:01:30.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	00 e1 38 f3 c7
2013/02/20	22:11:30.0	AOCS_ORe-point_Start_20_OG [0x0aa] AOCU_NM	5	02-76	00 e1 cb e2 c1
2013/02/20	22:21:30.0	AOCS_ORe-point_Start_21_OG [0x0ab] AOCU_NM	5	02-76	00 e2 ea d2 0d
2013/02/20	22:31:30.0	AOCS_ORe-point_Start_22_OG [0x0ac] AOCU_NM	5	02-76	00 e4 bd c1 8a
2013/02/20	22:41:30.0	AOCS_ORe-point_Start_23_OG [0x0ad] AOCU_NM	5	02-76	00 ed 9a b0 4a
2013/02/20	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	22:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/02/20	23:00:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00 00
2013/02/20	23:00:16.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/02/20	23:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/02/20	23:00:20.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/02/20	23:00:22.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/20	23:02:54.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	23:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2013/02/20	23:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/02/20	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/20	23:05:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/20	23:05:02.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/20	23:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/20	23:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/20	23:21:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/20	23:22:30.0	XRT_CTRL_AUTO_413_OG [0x19d] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	00:39:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	00:39:32.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	00:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8



Feb 19, 13 13:10

## XRT\_OGLIST\_0271.chk

Page 5/6

2013/02/21	00:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	00:49:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/21	00:50:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	02:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	02:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	02:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/21	02:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	02:25:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/21	02:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	03:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	03:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	03:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/21	03:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	04:02:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/21	04:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	05:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	05:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/21	05:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	05:39:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/21	05:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/02/21	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/02/21	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/02/21	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/02/21	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/21	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2013/02/21	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/02/21	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/02/21	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/02/21	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/02/21	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/02/21	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/21	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	06:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/02/21	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/02/21	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	06:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	06:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	06:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/21	06:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	07:17:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/21	07:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	08:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	08:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

Feb 19, 13 13:10

## XRT\_OGLIST\_0271.chk

Page 6/6

2013/02/21	08:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	08:53:30.5	XRT_Custom_434_OG [0x1b2]							
2013/02/21	08:54:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	10:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	10:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/21	10:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/21	10:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/21	10:24:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/21	10:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/21	11:16:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/21	11:17:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				