

XRT Timeline to be uploaded on 2013/02/21

Period: 2013/02/21 11:07:00 - 2013/02/26 10:26:00

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Normal mode

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XOB #1962: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (33ms) w/ G-Band VLS Cl

Term	Pointing (x, y)	Comment
02/21 11:20:00 - 02/21 17:53:24	Track (824.8, 199.2) @ 02/21 11:17:00	# OP start + 10min, AR 11678
02/21 18:06:30 - 02/22 06:26:24	Track (855.2, 192.4) @ 02/21 18:03:30	# Cont.
02/22 17:33:00 - 02/22 17:49:30	Fixed (920.0, 175.0)	* AR 11678, fixed pointing at W limb.
02/22 21:56:00 - 02/23 05:59:54	Fixed (920.0, 175.0)	* AR 11678.
02/23 06:13:00 - 02/23 10:39:54	Fixed (920.0, 175.0)	# Cont.

PROG= 07 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn=100	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Open/G-band	Open/G-band open	Safe Norm 32ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Seqn= 19	1-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm 63ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Seqn= 65	4-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
med-Be/Open	med-Be/Open close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Subr= 2	1-time(s)	2.0sec										
Seqn= 73	70-time(s)	120.0sec										
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec				

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250)

Term	Pointing (x, y)	Comment
02/21 17:56:30 - 02/21 18:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
02/22 06:29:30 - 02/22 06:36:24	Fixed (0.0, 0.0)	synoptic, shifted 26.5 min
02/22 18:16:00 - 02/22 18:22:54	Fixed (0.0, 0.0)	synoptic, shifted 13.0 min
02/23 06:03:00 - 02/23 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 12 1-time(s)

Subr= 1	1-time(s)	14.0sec										
Seqn= 96	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh close	Safe Norm 44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Seqn= 6	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec				
Seqn= 41	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Seqn= 30	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open close	Safe Norm 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Seqn= 69	1-time(s)	2.0sec										
Open/G-band	Open/G-band open	Safe Norm 8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Subr= 2	1-time(s)	2.0sec										
Seqn= 68	1-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm 32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec				

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1958: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33) - 1 loop

Term	Pointing (x, y)	Comment
02/22 19:55:30 - 02/22 20:15:00	Track (-15.1, -0.0) @ 02/22 18:23:00	# EIS sensitivity monitoring.

PROG= 20 1-time(s)

Subr= 1	1-time(s)	85.0sec										
Seqn= 82	1-time(s)	7.0sec										
Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Seqn= 6	1-time(s)	22.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 76			1-time(s)		8.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 77			1-time(s)		12.0sec							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 79			1-time(s)		10.0sec							
C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 75			1-time(s)		10.0sec							
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 78			1-time(s)		15.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2			1-time(s)		150.0sec							
Seqn= 34			1-time(s)		29.0sec							
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 83			1-time(s)		84.0sec							
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 69			2-time(s)		13.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 68			1-time(s)		10.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
02/21 11:20:00 - 02/21 17:53:24	Track (824.8, 199.2) @ 02/21 11:17:00	# OP start + 10min, AR 11678
02/21 18:06:30 - 02/22 06:26:24	Track (855.2, 192.4) @ 02/21 18:03:30	# Cont.
02/22 17:33:00 - 02/22 17:49:30	Fixed (920.0, 175.0)	* AR 11678, fixed pointing at W limb.
02/22 21:56:00 - 02/23 05:59:54	Fixed (920.0, 175.0)	* AR 11678.
02/23 06:13:00 - 02/23 10:39:54	Fixed (920.0, 175.0)	# Cont.

PROG= 16**15-time(s)**

Subr= 1			45-time(s)		10.0sec							
Seqn= 35			1-time(s)		2.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2			1-time(s)		10.0sec							
Seqn= 36			1-time(s)		2.0sec							
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 37			1-time(s)		2.0sec							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38			1-time(s)		2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

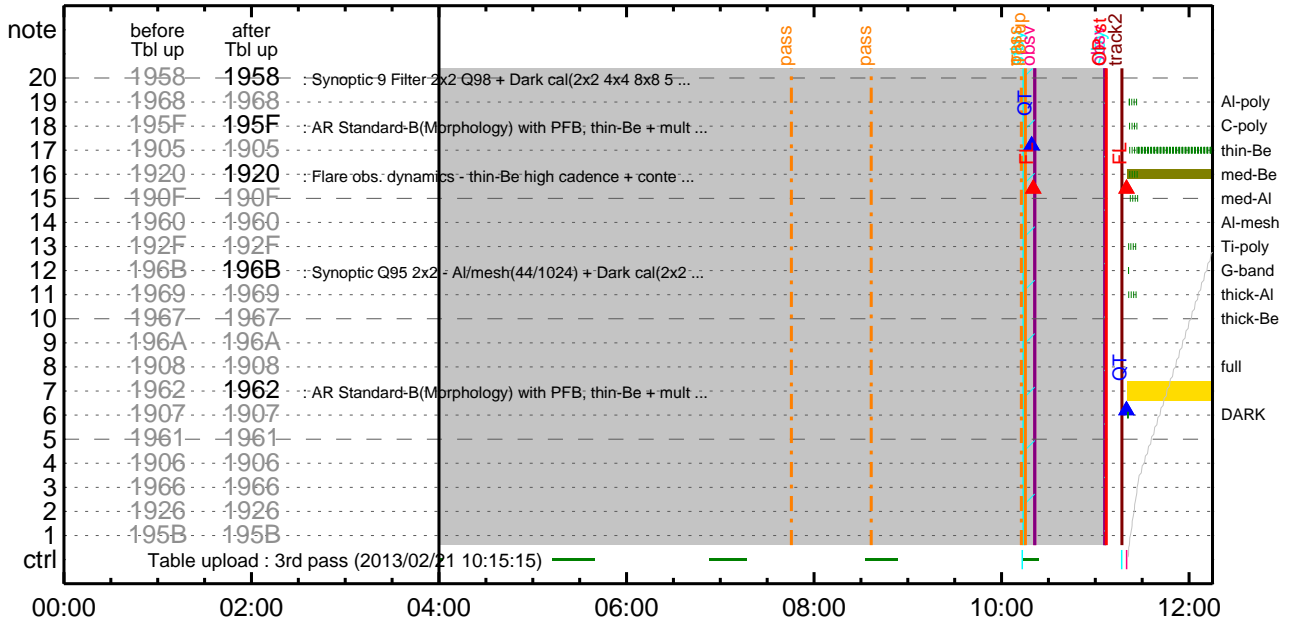
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Flare Detection

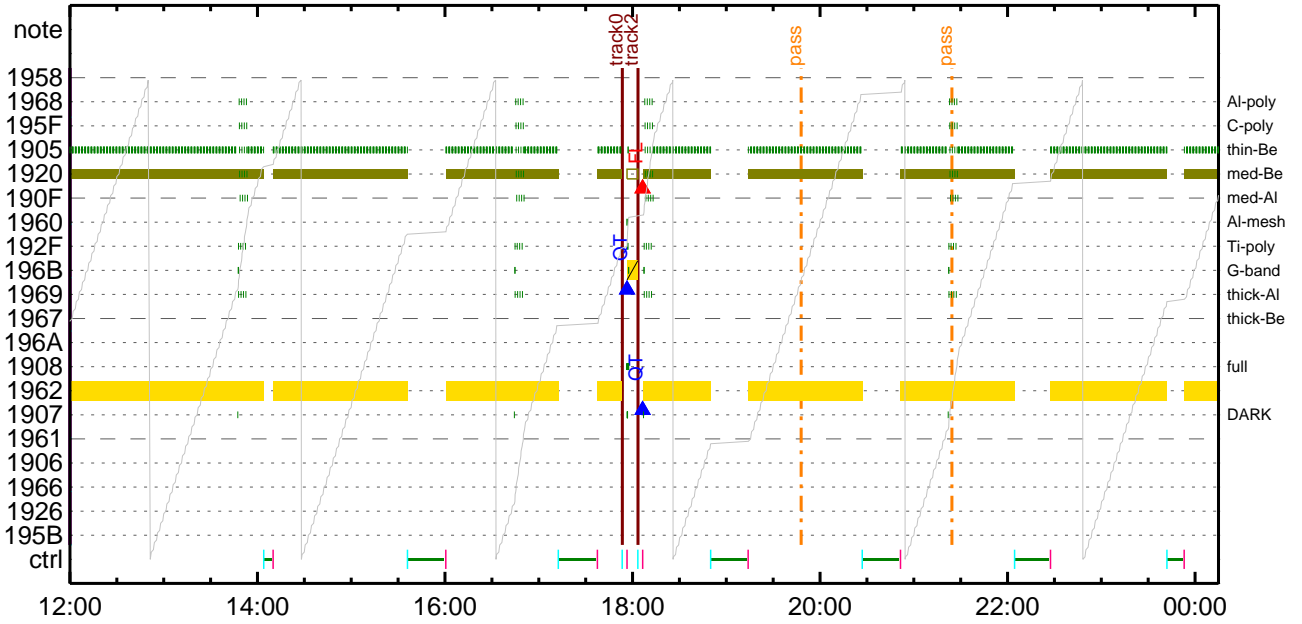
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Term	Pointing (x, y)	Comment									
02/21 18:03:46 - 02/22 06:26:46	Track (855.2, 192.4) @ 02/21 18:03:30	# Cont.									
02/22 17:30:16 - 02/22 18:13:16	Fixed (920.0, 175.0)	* AR 11678, fixed pointing at W limb.									
02/22 21:53:16 - 02/23 06:00:16	Fixed (920.0, 175.0)	* AR 11678.									
02/23 06:10:16 - 02/26 10:26:00	Fixed (920.0, 175.0)	# Cont.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

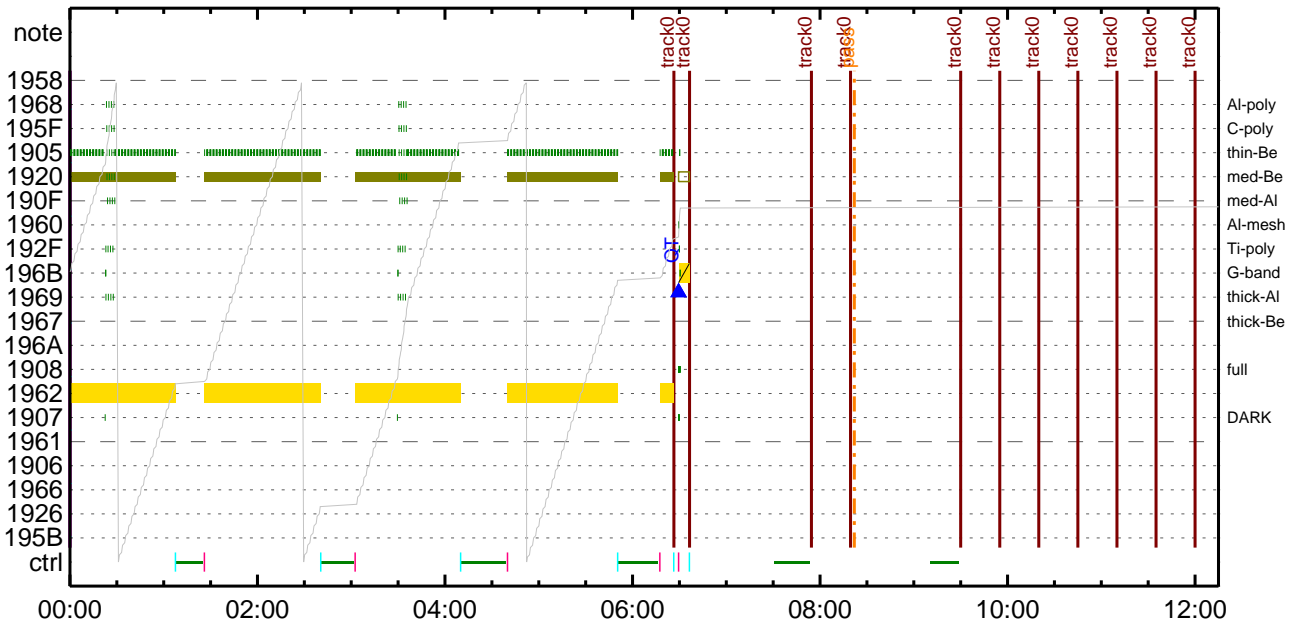
CMDI #0276 2013/02/21



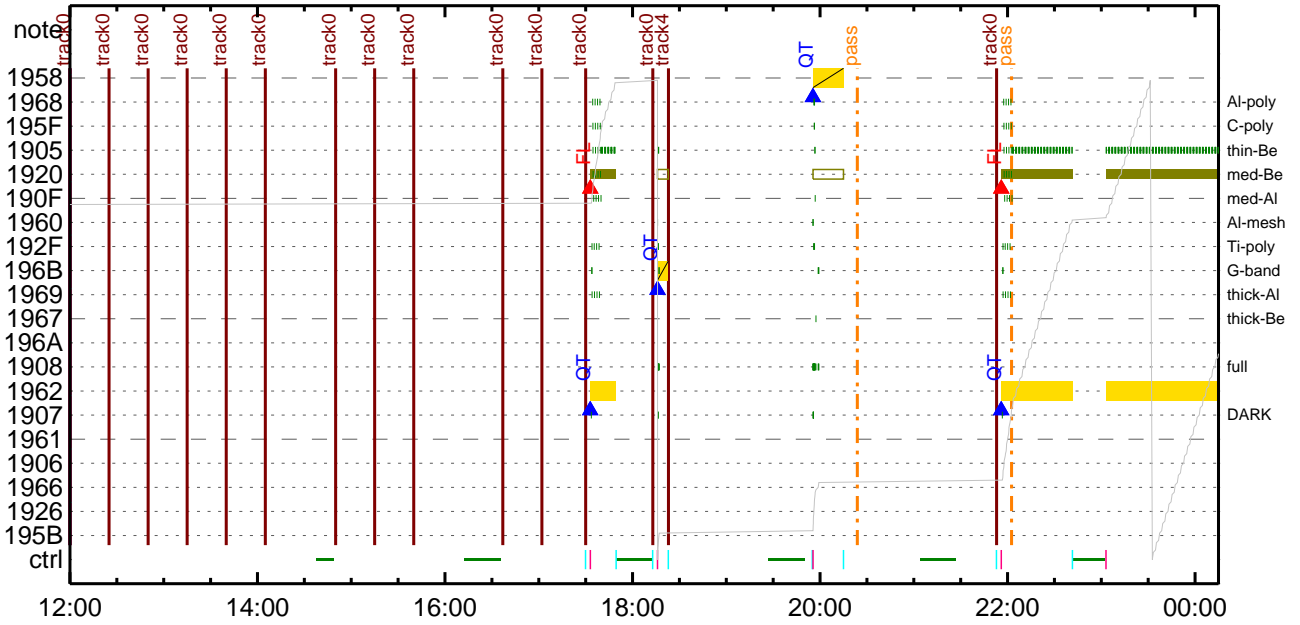
CMDI #0276 2013/02/21



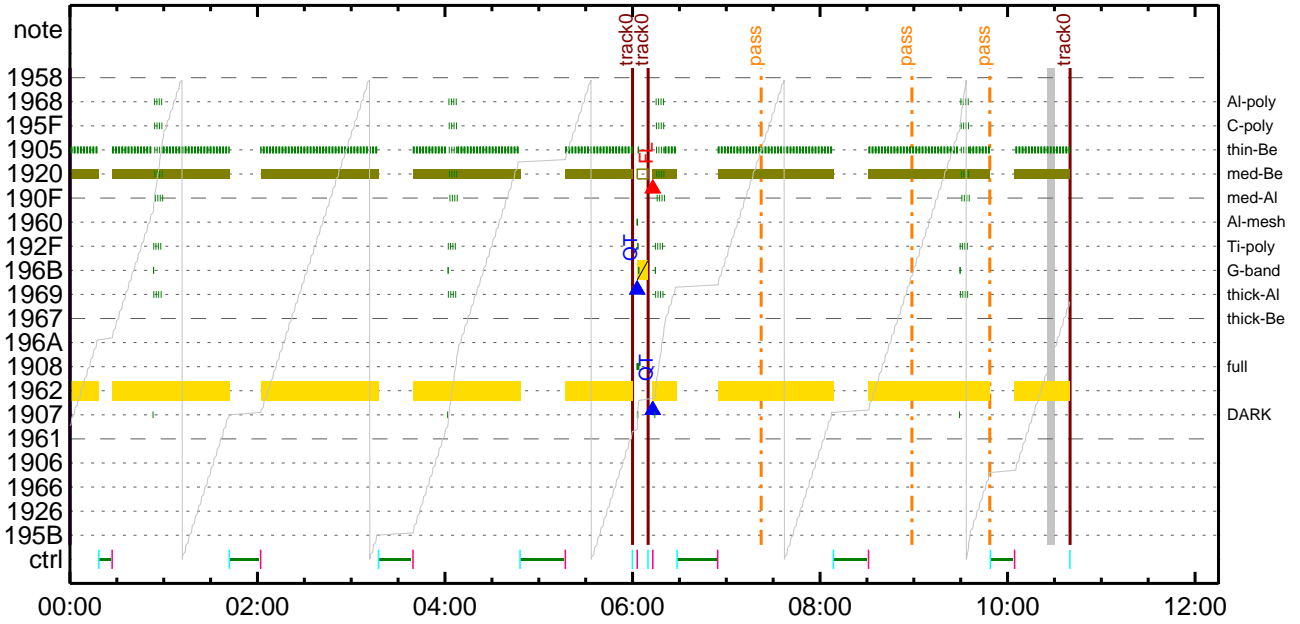
CMDI #0276 2013/02/22



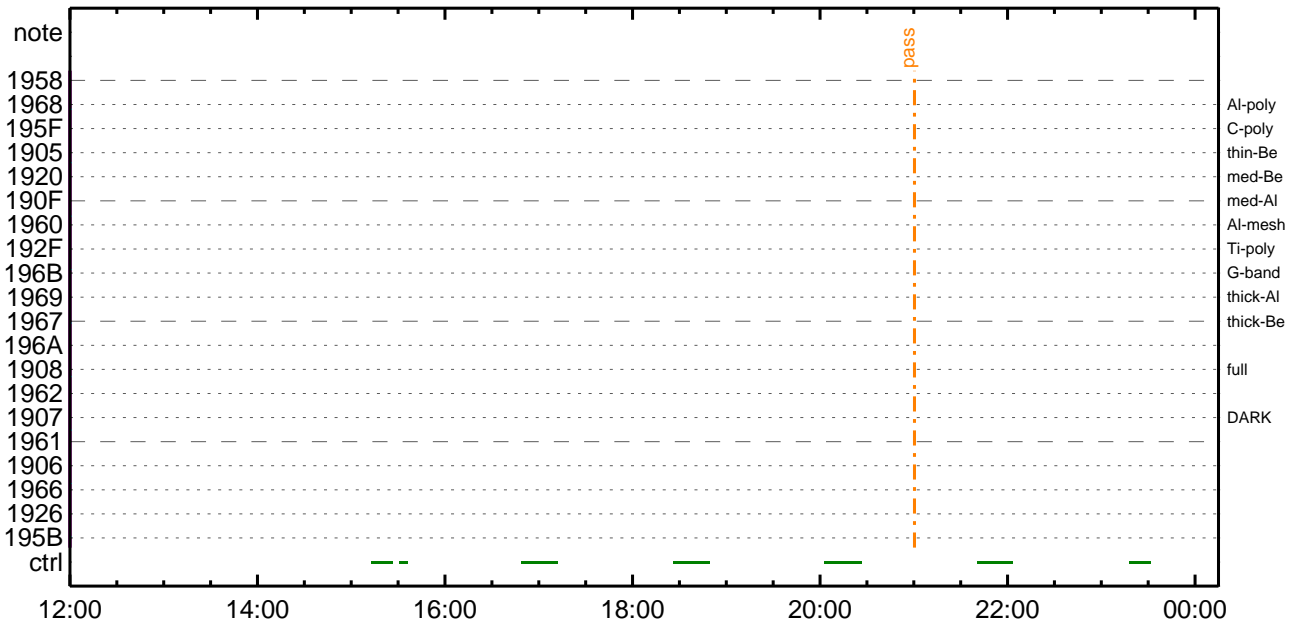
CMDI #0276 2013/02/22



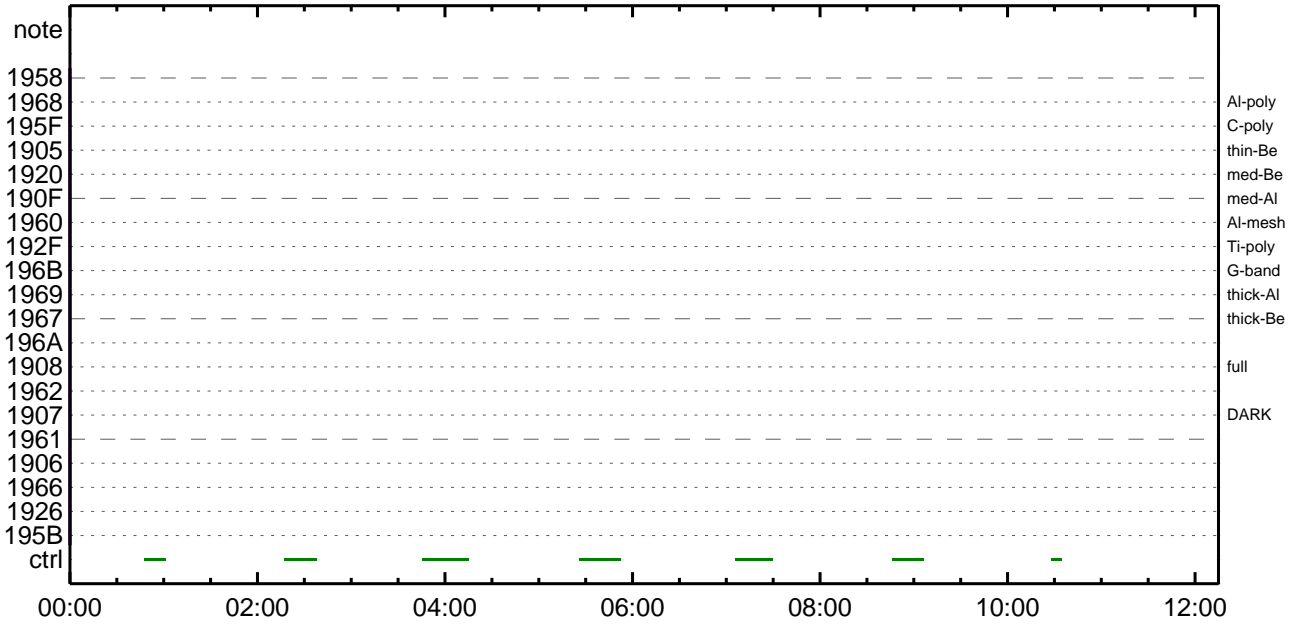
CMDI #0276 2013/02/23



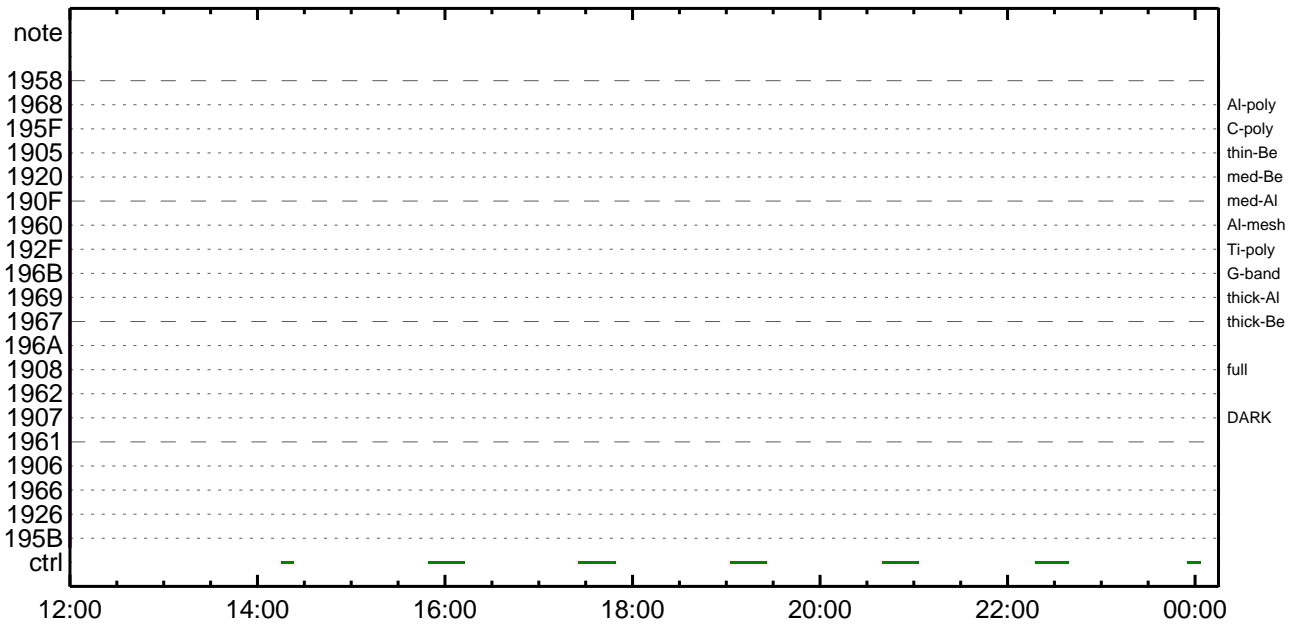
CMDI #0276 2013/02/23



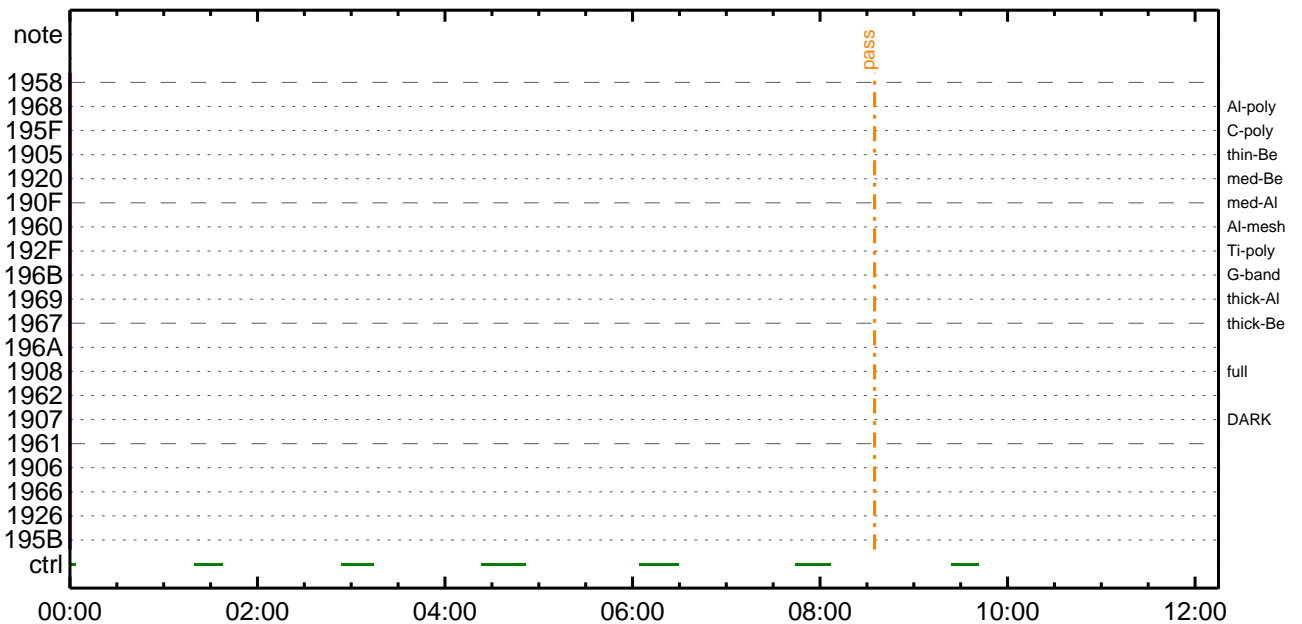
CMDI #0276 2013/02/24



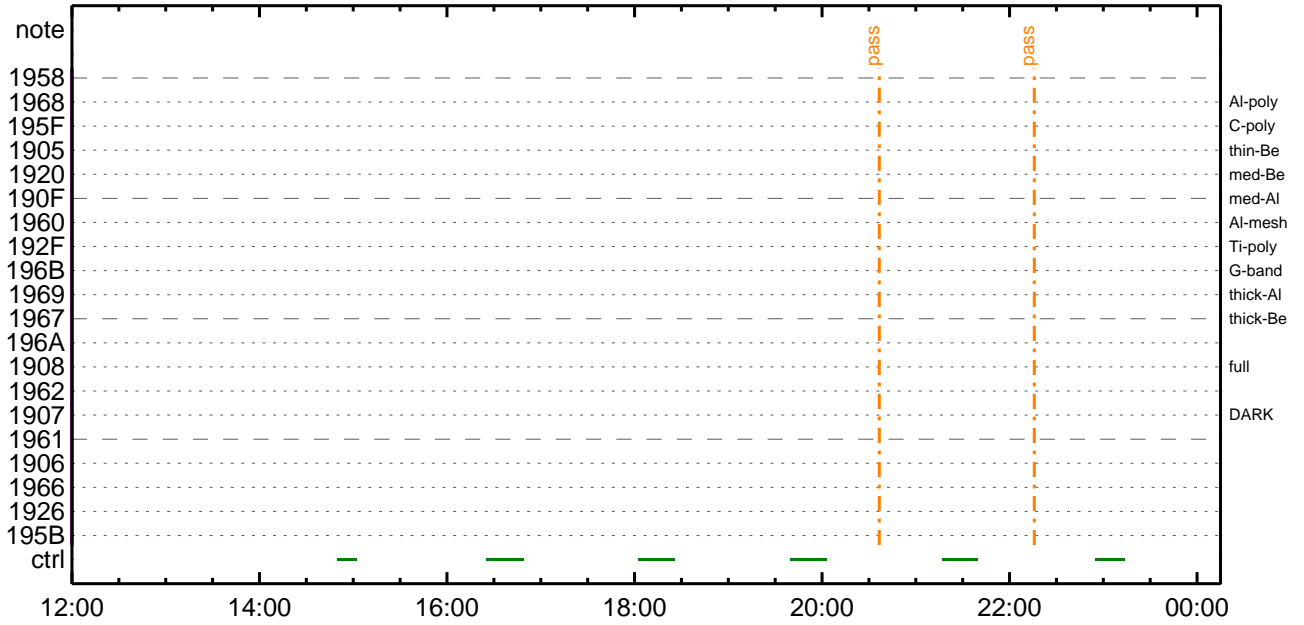
CMDI #0276 2013/02/24



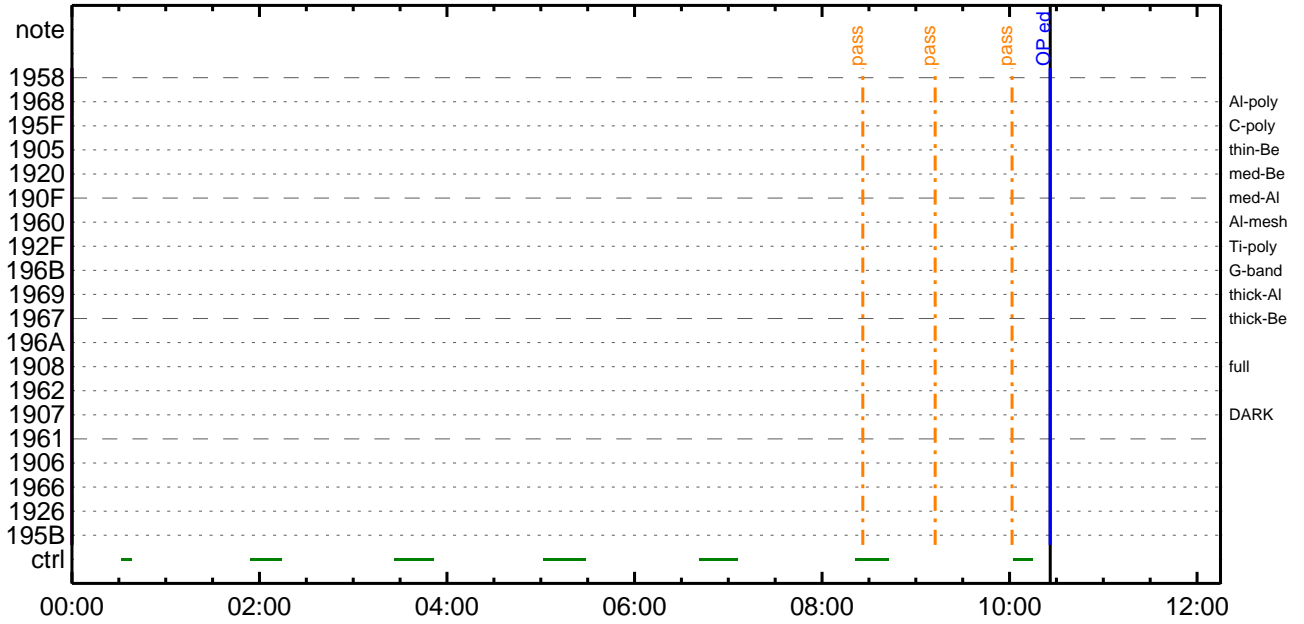
CMDI #0276 2013/02/25



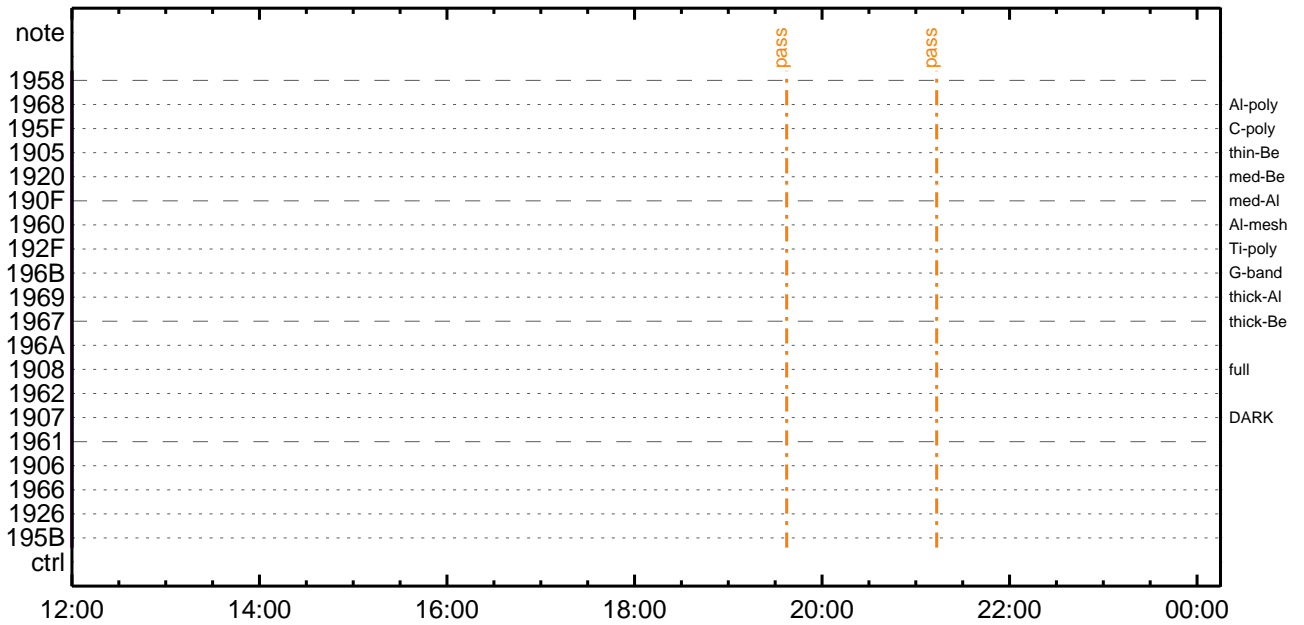
CMDI #0276 2013/02/25



CMDI #0276 2013/02/26



CMDI #0276 2013/02/26




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-480:OP
0104 ( )
0105 S. OG og-480:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî½ª¹ç•è² îOKªò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî½ª¹ç•è² îOKªò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî½ª¹ç•è² îOKªò³ îÇ§
0165 C.
0166 C. ***** °ê²¼ª î½ª¹ç•è² îOKªò³ îÇ§ *****
0167 C. DHUYâ;4YE;ê½ª¹ç•è² îOKªò³ îÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADª-Á÷ç@NGªî½ª¹ç•è² îOKªò³ îÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYóYêªò³ îÇ§ (UT)
0183 +. TI 2013-02-21 11:02:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-02-21 11:02:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-02-21 11:02:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

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main-481 2013-02-21 13:27:27 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄY$YÄY~¼Ä»Û;ä
0005 C.
0006 C. YÄY$;¼Y³YF¥ÖYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËçðÄß•µ°®»Í×ÄÇçÍYçYÄY×Yí;¼YÉ;ËË%µ•íÉ;ËßË¼°ÇÖç•ç¼l¹ççÍ;çÄ®,ùç¹çèçßçÄ+ç®ç•çËççç³çÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_ST$1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 1m9s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2013-02-21 11:06:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `üÄîçî»ó¼YçÈÄð¹çÈDCBC•x²è *****
0069 C. (¼ä°íYÖYÄYËYËYËYËYçYçYèçYçY¼ç¼Ä»Ûç¹çè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YD¥¹•Ï Daily±;jÎñçÈ´Øç¹çèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;äLOSÄY$YÄY~¼Ä»Û;ä
0080 C.
0081 . C. ***** LOS *****
0082 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-482 2013-02-21 13:27:27 128 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αΑβ•μ°Æ»Í×ÁÇαÍ¥çYÁY×¥í;¼¥É;ÈÈè%μ•ííÉ;ÈαÈ¼°ÇÇα•α¿¼í¹çαÍ;çÀ®, ùα¹αèαβαçÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_FLD_ENA
0055 BC (d8)
0056 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0057 BC (c8)
0058 + DC 07-F0 MDP_XRT_AEC_RESET
0059 BC (d0)
0060 + DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 + DC 07-F0 MDP_XRT_FLD_RESET
0063 BC (da)
0064 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0065 BC (c4 12)
0066 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0067 BC (c5 10)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C.
0071 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0072 C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074 BC (c2)
0075 +. TI 2013-02-21 11:06:02.0
0076 DC 07-F0 MDP_XRT_MODE_OBSV
0077 BC (c2)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C. ***** XRT END *****
0081 . C. *****
0082 C. SOT table upload
0083 C. *****
0084 . C. < Stop FG table >
0085 +. DC 07-F0 MDP_FG_CTRL_MANU
0086 BC (51)
0087 . C. -----
0088 C. MDP_FG_CTRL_MODE = MANU [ ]
0089 C. -----
0090 C.
0091 . C. <Upload FG Observation Table>
0092 . S. RAM ram-261:MDP_OBS_F
0093 ( )
0094 C.
0095 . C. < Dump RAMID=MDP_OBS_F >
```

```
0096 +. DC 07-F0 MDP_DUMP_FGTBL
0097 BC (82 07 00 00 00 38 b8)
0098 C. -----
0099 C. MDP_OBS_F verify = OK/NG [ ]
0100 C. -----
0101 C.
0102 C. *****
0103 C. SOT TI command set
0104 C. *****
0105 C. Execute, after the success of TBL upload.
0106 +. TI 2013-02-21 11:06:18.0
0107 DC 07-F0 MDP_SOT_MODE_OBSV
0108 BC (40)
0109 . C. -----
0110 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0111 C. -----
0112 C.
0113 C.
0114 . C. ***** MDP `úÃîâî»ô¼ŸαĒĀĐα¹αēDCBC•x²è *****
0115 C. (¼á°îŸÓŸĀŸĒŸĪŸĒŸĀŸçŸēαĒ¼α¼Ā»Ūα¹αē)
0116 . S. DC-BC dcbc-402:DCBC
0117 (MDP_known_event)
0118 C.
0119 C.
0120 . C. ***** ŸĐŸ¹•Ī Daily±¿ĪŃαĒĒ'Øα¹αēDCBC•x²è *****
0121 . S. DC-BC dcbc-153:DCBC
0122 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123 C.
0124 C.
0125 . C. ¡ãLOSŸĀŸŸŸĀŸ-¼Ā»Ū¡ã
0126 C.
0127 . C. ***** LOS *****
0128 C.
```

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*** OP Sequence for XRT ***

2013/02/21	11:16:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	11:16:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/02/21	11:17:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/02/21	11:17:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/02/21	11:17:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/02/21	11:17:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/02/21	11:17:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/21	11:19:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	11:19:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2013/02/21	11:19:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/02/21	11:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	14:04:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	14:04:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	14:04:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	14:07:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	14:09:00.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	14:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	15:36:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	15:36:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	15:39:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	15:59:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	16:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	17:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	17:12:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	17:12:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	17:15:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	17:36:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	17:37:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	17:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/02/21	17:53:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/02/21	17:53:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/02/21	17:53:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/02/21	17:53:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/21	17:56:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/02/21	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	18:03:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/02/21	18:03:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/02/21	18:03:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/02/21	18:03:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/02/21	18:03:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/02/21	18:03:52.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/02/21	18:06:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	18:06:26.0	XRT_QT_PROG_SET_430_OG [0x1ae]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2013/02/21	18:06:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10

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2013/02/21	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	18:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	18:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	18:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	18:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	19:13:00.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	19:14:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	20:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	20:27:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	20:27:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	20:30:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	20:50:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	20:51:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	22:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	22:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	22:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	22:26:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	22:27:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/21	23:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/21	23:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/21	23:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/21	23:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/21	23:52:00.0	XRT_Custom_434_OG [0x1b2]			
2013/02/21	23:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/22	01:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/22	01:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/22	01:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/22	01:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/22	01:25:00.0	XRT_Custom_434_OG [0x1b2]			
2013/02/22	01:26:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/22	02:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/22	02:40:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/22	02:40:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/22	02:43:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/22	03:01:30.5	XRT_Custom_434_OG [0x1b2]			
2013/02/22	03:02:30.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/22	04:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/22	04:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/22	04:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/22	04:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/22	04:39:00.5	XRT_Custom_434_OG [0x1b2]			
2013/02/22	04:40:00.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/22	05:50:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/22	05:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/02/22	05:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/02/22	05:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/02/22	06:16:30.0	XRT_Custom_434_OG [0x1b2]			
2013/02/22	06:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/02/22	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/02/22	06:26:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			

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2013/02/22	06:26:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2013/02/22	06:26:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/02/22	06:26:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/02/22	06:26:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/02/22	06:29:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2013/02/22	06:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/02/22	06:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/02/22	06:36:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	54	00	01
2013/02/22	07:54:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	4c	9b	01
2013/02/22	08:19:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	43	be	01
2013/02/22	09:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	3a	d9	01
2013/02/22	09:55:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	31	f3	01
2013/02/22	10:20:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	29	0e	01
2013/02/22	10:45:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	20	31	01
2013/02/22	11:10:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	17	4b	01
2013/02/22	11:35:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	0e	66	01
2013/02/22	12:00:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	05	81	01
2013/02/22	12:25:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	fd	82	01
2013/02/22	12:50:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	f4	9c	01
2013/02/22	13:15:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	eb	bf	01
2013/02/22	13:40:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	e2	da	01
2013/02/22	14:05:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	d9	f4	01
2013/02/22	14:50:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	d1	0f	01
2013/02/22	15:15:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	c8	32	01
2013/02/22	15:40:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	bf	4c	01
2013/02/22	16:37:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	b6	67	01
2013/02/22	17:02:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	ad	82	01
2013/02/22	17:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/02/22	17:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/02/22	17:30:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	f0	73	ae
2013/02/22	17:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/02/22	17:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/02/22	17:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/02/22	17:30:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/02/22	17:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/02/22	17:32:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2013/02/22	17:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2013/02/22	17:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/02/22	17:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/02/22	17:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/02/22	17:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/02/22	17:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/02/22	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/02/22	18:12:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/02/22	18:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2013/02/22	18:13:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			

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2013/02/22	18:13:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/02/22	18:13:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/22	18:15:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2013/02/22	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/22	18:22:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/22	18:23:00.0	AOCS_OrE-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/02/22	19:55:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/22	19:55:02.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/02/22	19:55:22.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/02/22	19:55:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/02/22	19:55:26.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/22	19:55:28.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2013/02/22	19:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/22	20:15:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/22	21:52:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/22	21:52:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/02/22	21:53:00.0	AOCS_OrE-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 f0 73 ae 36				
2013/02/22	21:53:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/02/22	21:53:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/02/22	21:53:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/02/22	21:53:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/02/22	21:55:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/22	21:55:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2013/02/22	21:55:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/02/22	21:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/22	22:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/22	22:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/22	22:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/22	22:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/22	23:02:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/22	23:03:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/23	00:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/23	00:18:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/23	00:18:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/23	00:21:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/23	00:26:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/23	00:27:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/23	01:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/23	01:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/23	01:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/23	01:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/23	02:01:00.0	XRT_Custom_434_OG [0x1b2]							
2013/02/23	02:02:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/02/23	03:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/02/23	03:17:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/02/23	03:17:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/02/23	03:20:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/02/23	03:38:30.0	XRT_Custom_434_OG [0x1b2]							
2013/02/23	03:39:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							

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2013/02/23	04:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	04:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	04:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	04:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/23	05:16:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/23	05:17:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/02/23	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2013/02/23	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/02/23	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/02/23	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/23	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2013/02/23	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/02/23	06:10:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 f0 73 ae 36		
2013/02/23	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/02/23	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/02/23	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/02/23	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/02/23	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	06:12:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2013/02/23	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/02/23	06:13:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	06:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	06:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	06:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/23	06:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/23	06:53:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	06:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	08:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	08:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	08:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/23	08:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/23	08:30:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	08:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	09:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	09:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/02/23	09:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/02/23	09:49:04.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/23	09:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/02/23	10:03:30.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/02/23	10:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	10:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/02/23	10:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		