

XRT Timeline to be uploaded on 2013/03/02

Period: 2013/03/02 10:10:00 - 2013/03/07 09:27:00

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Normal mode

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XOB #1970: Full-disk Full-Res AI/Mesh (256/4096), Ti/Poly (512/8192), Thick/Al (46s)- 1 loop													
Term	Pointing (x, y)		Comment										
03/02 10:55:00 - 03/02 11:07:54	Fixed (0.0, 0.0)		HOP130 (3/15)										
PROG= 17 1-time(s)													
┌ Subr= 1 1-time(s) 300.0sec													
├ Seqn= 44 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	45.2s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
├ Seqn= 43 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
├ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	0.5sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Subr= 2 1-time(s) 2.0sec													
├ Seqn= 13 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196E: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 240s-cad, shorter G-band (32ms) w/ G-Band VLS CI												
Term	Pointing (x, y)		Comment									
03/02 14:03:00 - 03/02 15:29:54	Track (304.5, -109.9) ^{Ⓜ 03/02 14:00:00}		Coronal cells									
03/02 15:33:00 - 03/02 17:51:30	Track (-149.1, -109.9) ^{Ⓜ 03/02 15:30:00}		Coronal cells									
03/02 18:28:00 - 03/03 06:00:24	Track (-683.4, -206.8) ^{Ⓜ 03/02 18:25:00}		AR 11683 tracking									
03/03 06:13:30 - 03/03 09:51:00	Track (-609.0, -197.7) ^{Ⓜ 03/03 06:10:30}		AR 11683 tracking									
03/03 18:13:00 - 03/04 05:28:00	Track (-525.2, -189.5) ^{Ⓜ 03/03 18:10:00}		AR 11683 tracking									
03/04 06:17:30 - 03/04 10:30:00	Track (-433.8, -182.5) ^{Ⓜ 03/04 06:14:30}		AR 11683 tracking									
03/04 18:20:00 - 03/05 05:37:54	Track (-336.9, -176.8) ^{Ⓜ 03/04 18:17:00}		AR 11683 tracking									
03/05 05:52:30 - 03/05 09:26:00	Track (-239.9, -172.8) ^{Ⓜ 03/05 05:48:00}		AR 11683 tracking									

PROG= 02 Inf.-time(s)													
┌ Subr= 1 1-time(s) 2.0sec													
├ Seqn=100 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
├ Seqn= 19 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
├ Seqn= 65 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Subr= 2 1-time(s) 2.0sec													
├ Seqn= 94 14-time(s) 240.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(256/4096)												
Term	Pointing (x, y)		Comment									
03/02 18:18:00 - 03/02 18:24:54	Fixed (0.0, 0.0)		synoptic, shifted 15.0 min									
03/03 06:03:30 - 03/03 06:10:24	Fixed (0.0, 0.0)		synoptic, shifted 0.5 min									
03/03 18:03:00 - 03/03 18:09:54	Fixed (0.0, 0.0)		synoptic									
03/04 06:07:30 - 03/04 06:14:24	Fixed (0.0, 0.0)		synoptic, shifted 4.5 min									
03/04 18:10:00 - 03/04 18:16:54	Fixed (0.0, 0.0)		synoptic, shifted 7.0 min									
03/05 05:41:00 - 03/05 05:47:54	Fixed (0.0, 0.0)		synoptic, shifted -22.0 min									

PROG= 12 1-time(s)													
┌ Subr= 1 1-time(s) 14.0sec													
├ Seqn= 96 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
├ Seqn= 6 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
├ Seqn= 41 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 68 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms

Term	Pointing (x, y)	Comment										
03/03 10:08:00 - 03/03 17:59:54	Fixed (-15.0, -937.0)	HOP81 day2										
03/04 10:38:00 - 03/04 16:29:54	Fixed (-15.0, -937.0)	HOP206										
PROG= 09 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 61 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 62 1-time(s) 30.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec												
Seqn= 60 1-time(s) 60.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms)

Term	Pointing (x, y)	Comment										
03/04 16:33:00 - 03/04 17:27:30	Track (-351.5, -177.6) @ 03/04 16:30:00	AR 11683 tracking										
PROG= 03 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 61 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 27 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 28 150-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment										
03/02 14:03:00 - 03/02 15:29:54	Track (304.5, -109.9) @ 03/02 14:00:00	Coronal cells										
03/02 15:33:00 - 03/02 17:51:30	Track (-149.1, -109.9) @ 03/02 15:30:00	Coronal cells										
03/02 18:28:00 - 03/03 06:00:24	Track (-683.4, -206.8) @ 03/02 18:25:00	AR 11683 tracking										
03/03 06:13:30 - 03/03 09:51:00	Track (-609.0, -197.7) @ 03/03 06:10:30	AR 11683 tracking										
03/03 10:08:00 - 03/03 17:59:54	Fixed (-15.0, -937.0)	HOP81 day2										
03/03 18:13:00 - 03/04 05:28:00	Track (-525.2, -189.5) @ 03/03 18:10:00	AR 11683 tracking										
03/04 06:17:30 - 03/04 10:30:00	Track (-433.8, -182.5) @ 03/04 06:14:30	AR 11683 tracking										
03/04 10:38:00 - 03/04 16:29:54	Fixed (-15.0, -937.0)	HOP206										
03/04 16:33:00 - 03/04 17:27:30	Track (-351.5, -177.6) @ 03/04 16:30:00	AR 11683 tracking										
03/04 18:20:00 - 03/05 05:37:54	Track (-336.9, -176.8) @ 03/04 18:17:00	AR 11683 tracking										
03/05 05:52:30 - 03/05 09:26:00	Track (-239.9, -172.8) @ 03/05 05:48:00	AR 11683 tracking										
PROG= 16 15-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 35 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 36 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 37		1-time(s)		2.0sec												
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
Seqn= 38		1-time(s)		2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

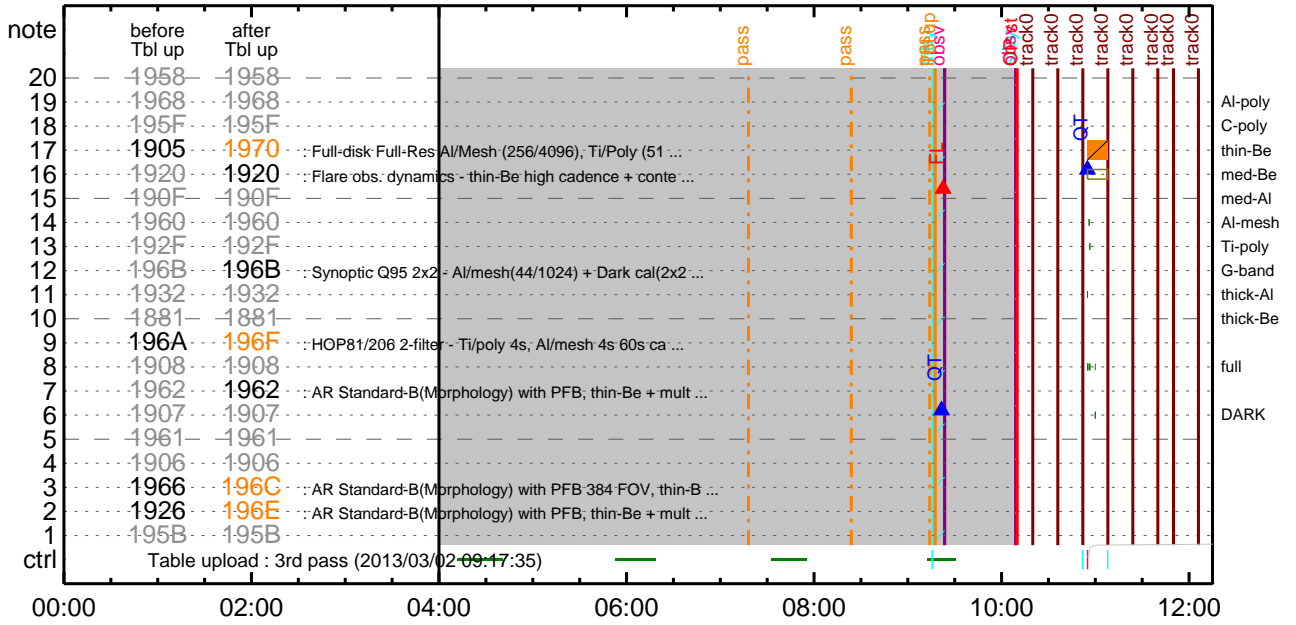
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Flare Detection

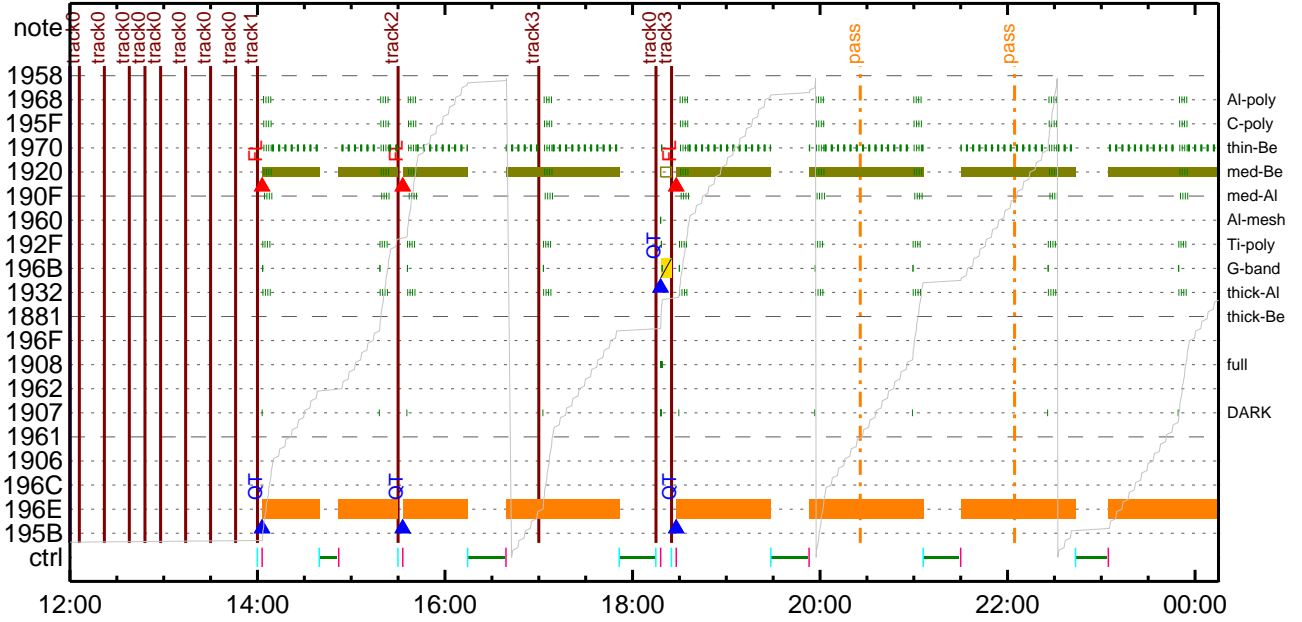
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FLD Patrol													
Term		Pointing (x, y)				Comment							
03/02 14:00:16 - 03/02 18:15:16	Track (304.5, -109.9)	⑥ 03/02 14:00:00				Coronal cells							
03/02 18:25:16 - 03/03 06:00:46	Track (-683.4, -206.8)	⑥ 03/02 18:25:00				AR 11683 tracking							
03/03 06:10:46 - 03/03 18:00:16	Track (-609.0, -197.7)	⑥ 03/03 06:10:30				AR 11683 tracking							
03/03 18:10:16 - 03/04 06:04:46	Track (-525.2, -189.5)	⑥ 03/03 18:10:00				AR 11683 tracking							
03/04 06:14:46 - 03/04 18:07:16	Track (-433.8, -182.5)	⑥ 03/04 06:14:30				AR 11683 tracking							
03/04 18:17:16 - 03/05 05:38:16	Track (-336.9, -176.8)	⑥ 03/04 18:17:00				AR 11683 tracking							
03/05 05:52:16 - 03/07 09:27:00	Track (-239.9, -172.8)	⑥ 03/05 05:48:00				AR 11683 tracking							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

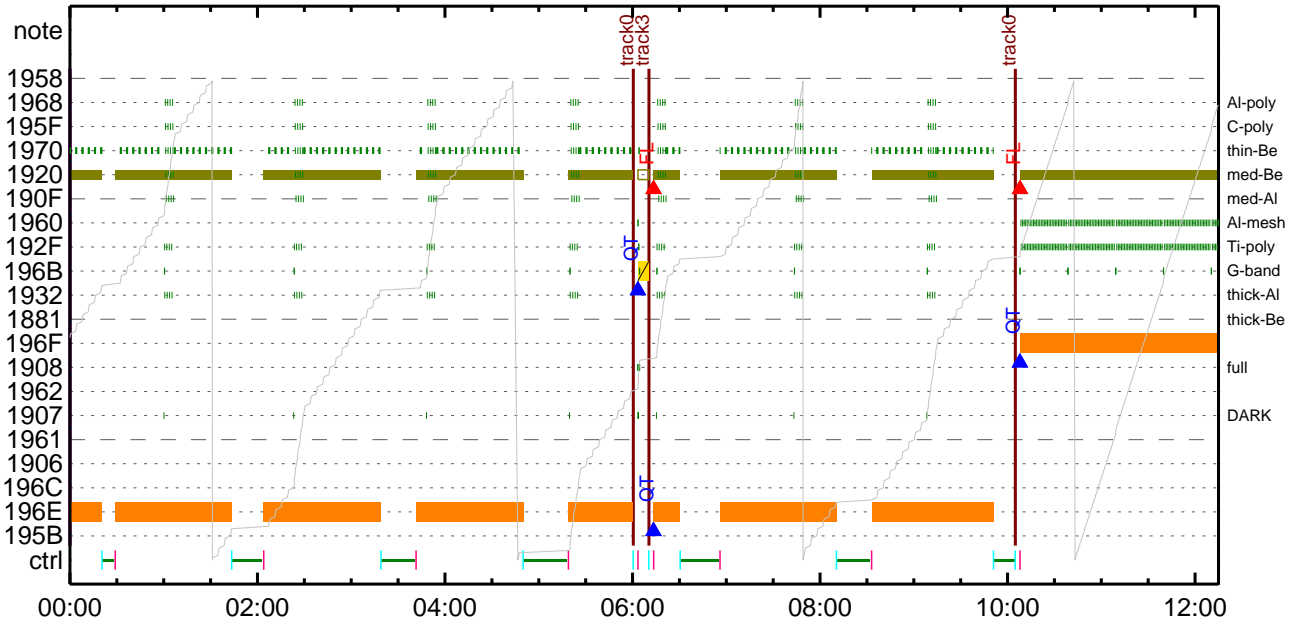
CMDI #0302 2013/03/02



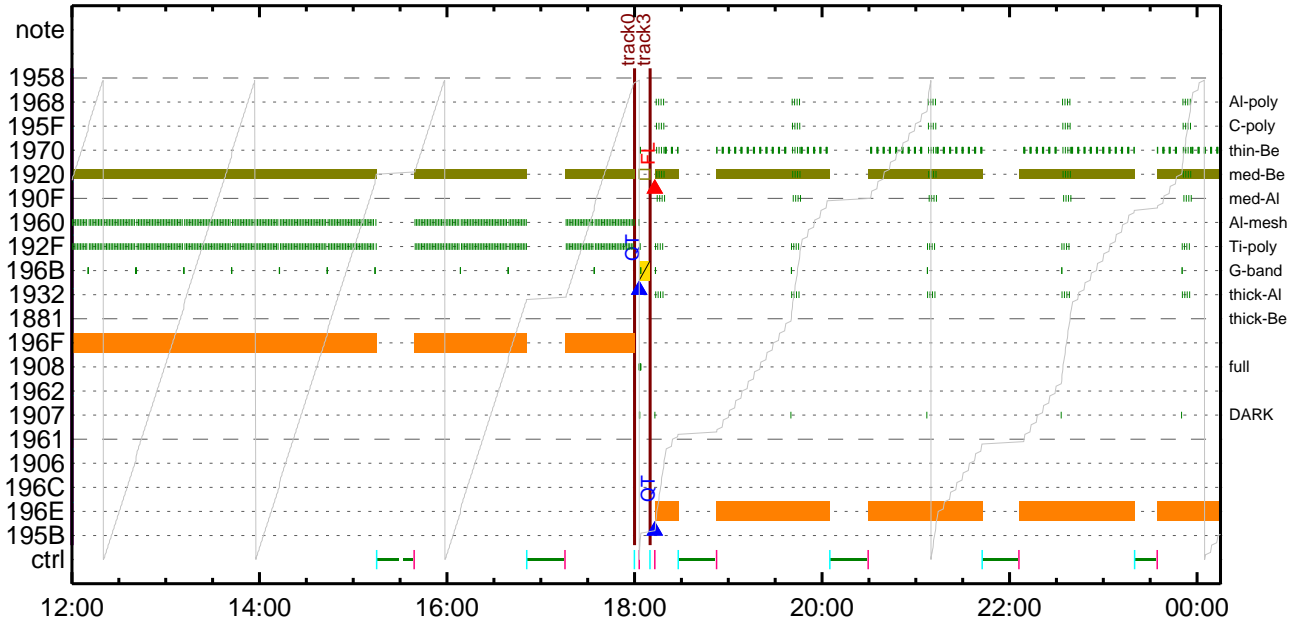
CMDI #0302 2013/03/02



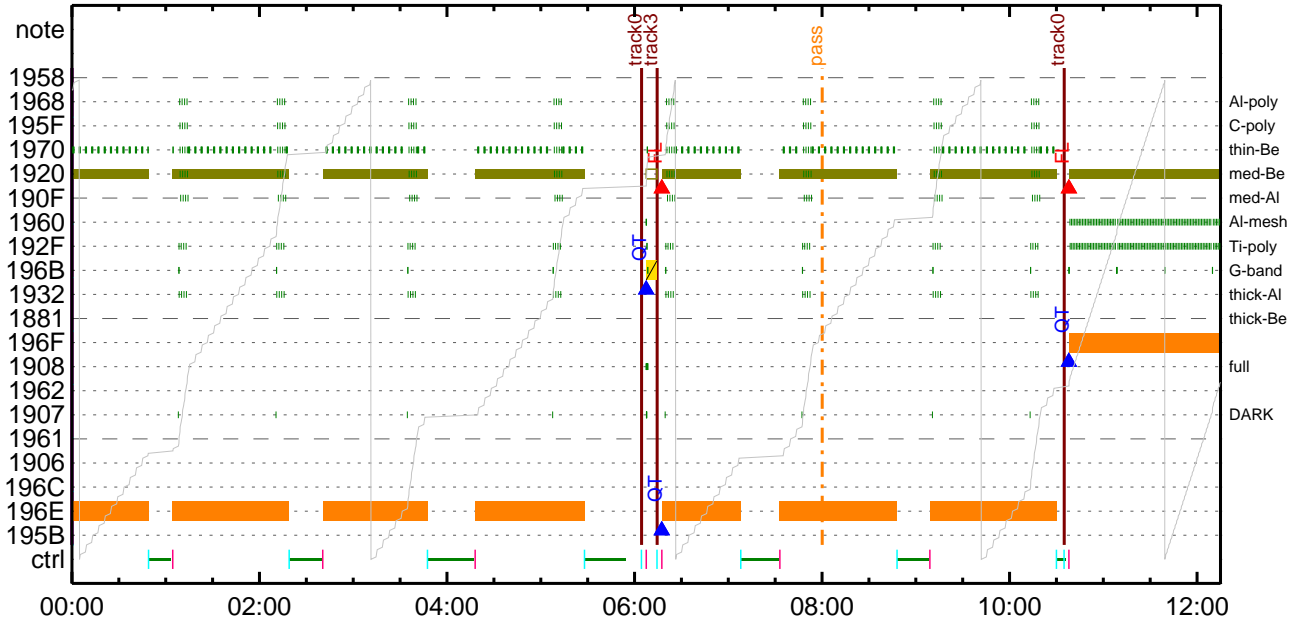
CMDI #0302 2013/03/03



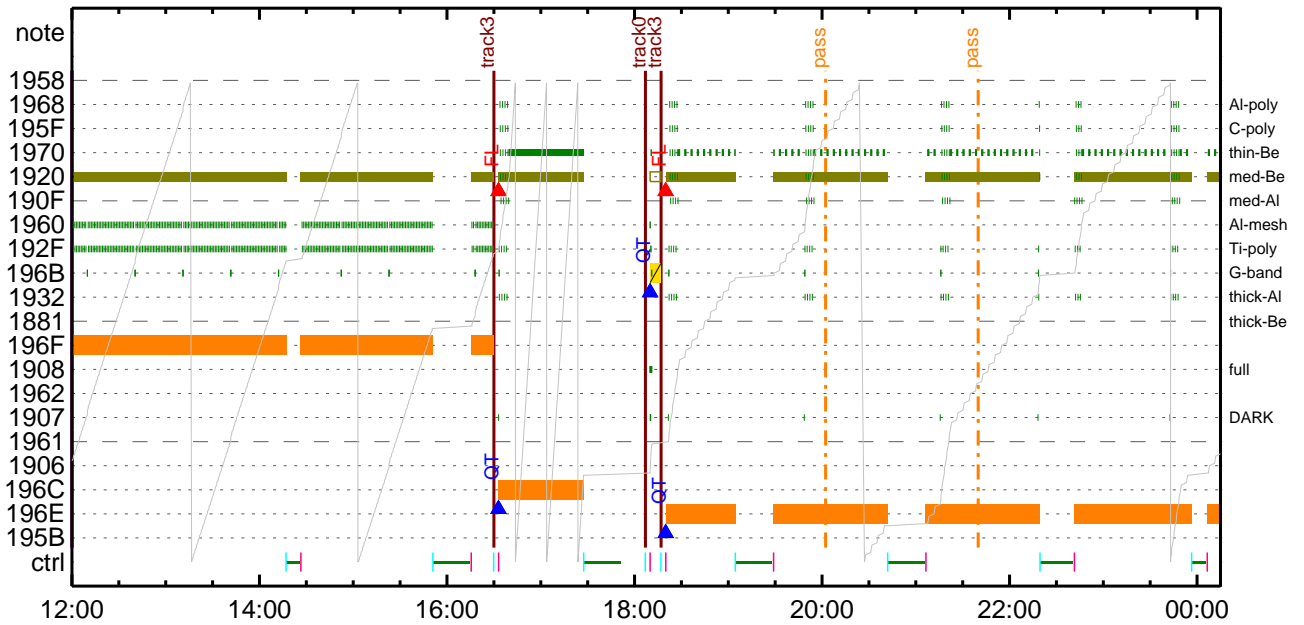
CMDI #0302 2013/03/03



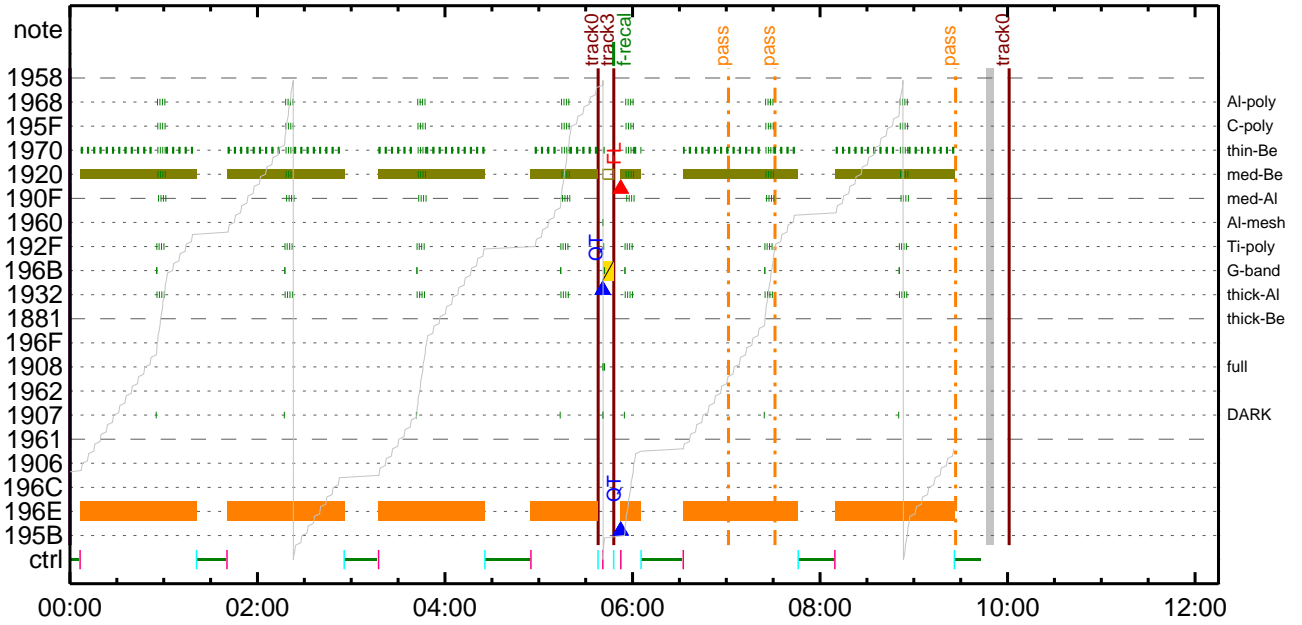
CMDI #0302 2013/03/04



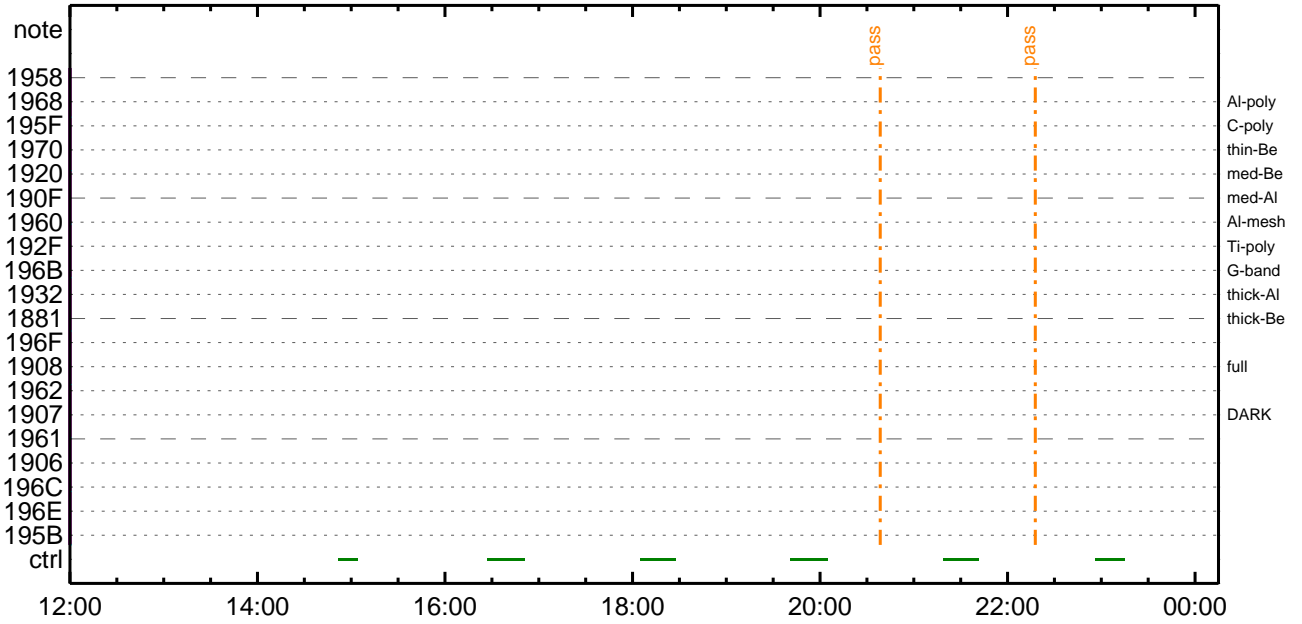
CMDI #0302 2013/03/04



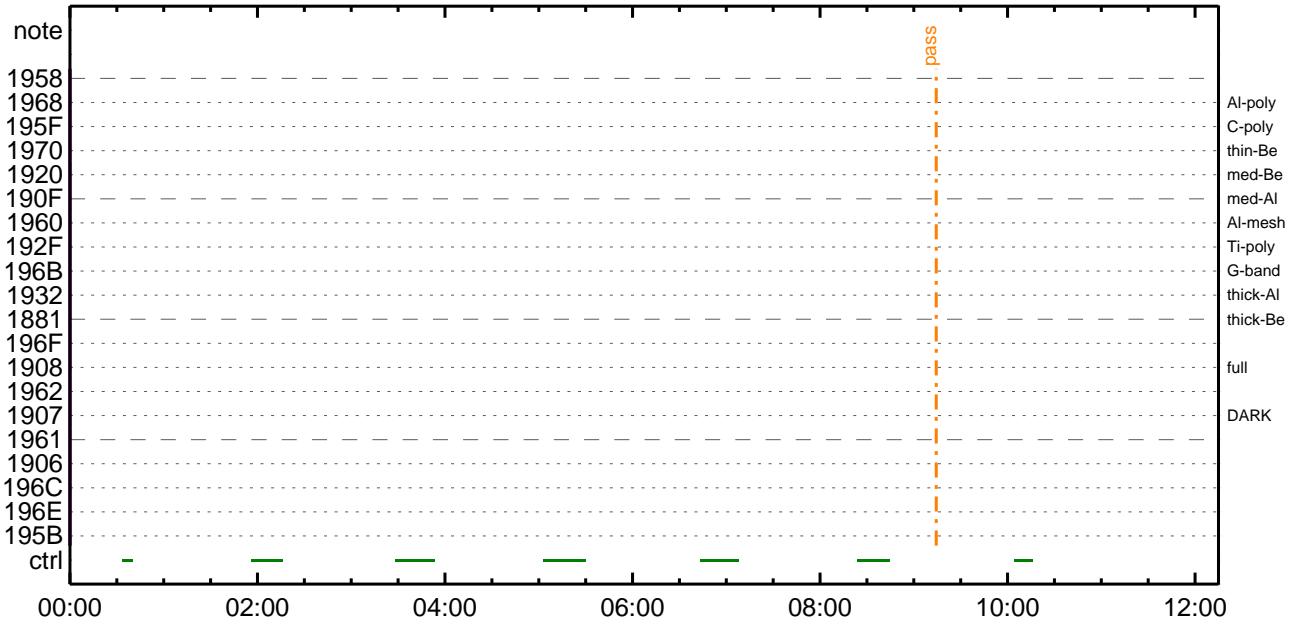
CMDI #0302 2013/03/05



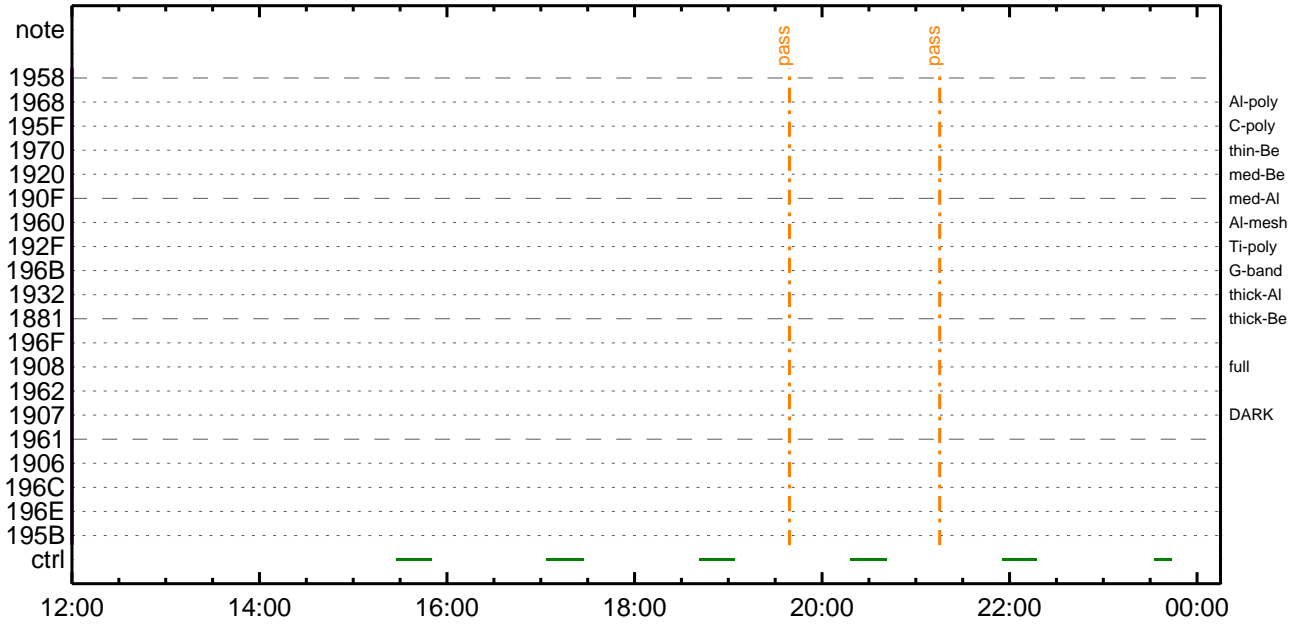
CMDI #0302 2013/03/05



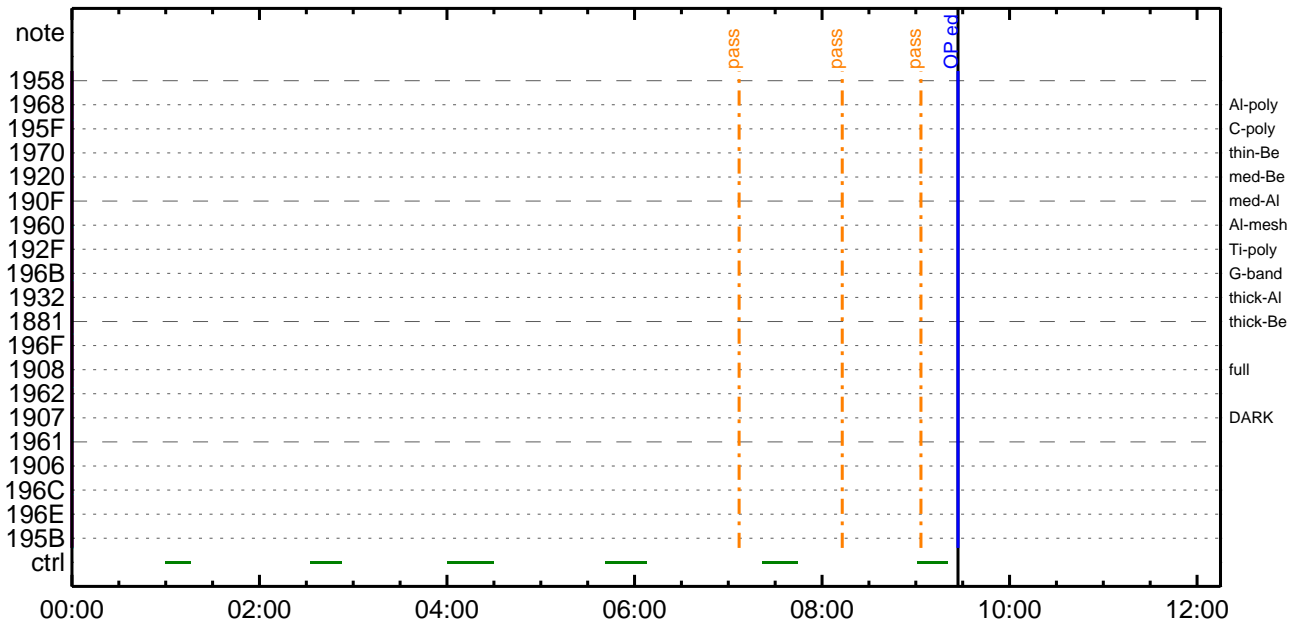
CMDI #0302 2013/03/06



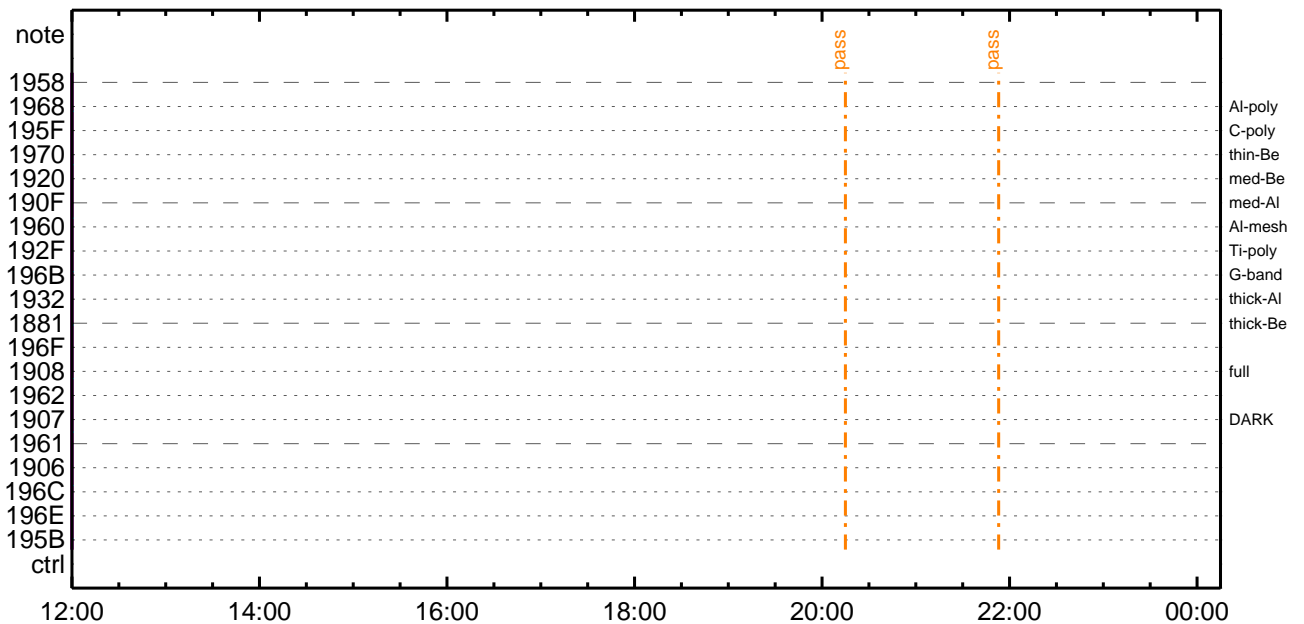
CMDI #0302 2013/03/06



CMDI #0302 2013/03/07



CMDI #0302 2013/03/07



(a) Spacecraft Operation Procedure (real-commands)

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main-497 2013-03-02 12:53:50 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÅY$YÅY-¼Ä»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YFÿÿÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Āī;Ēǝ¿ǝĀǝ•μ°Æ»İ×ÁÇǝİŸçYÅY×Yİ;¼YÉ;ĒĒē½μ•īīĒ;ĒǝĒ¼°Çǝ•ǝ¿¼l¹çǝİ;çĀ®, ùǝ¹ǝĒǝǝçĀ+¿®ǝ•ǝĒǝǝǝǝĒ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYİ;¼YÉ;|YÅYÿY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYİ;¼YÉ;ã
0019 . S. OP op-497:OP
0020 ( )
0021 . S. OG og-497:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPîî°èYÅYÿY×;ã
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYÿY×¼ª İ»ǝð³ İÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGǝİ¼Ē¹ç•ē²İOKǝð³ İÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYÿY×¼ª İ»ǝð³ İÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGǝİ¼Ē¹ç•ē²İOKǝð³ İÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYÿY×¼ª İ»ǝð³ İÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPǝİ¼Ē¹ç•ē²İOKǝð³ İÇ§
0081 C.
0082 . C. ***** òĒ²¼ǝİ¼Ā´¶Ā°ǝĒĒ-ǝ°Ā+¿® (¼ǝμ-YÅYÿY×¼ē¼çǝðĀĀœç¼ªǝ°ǝ¼¼ī¹çǝçǝā) *****
0083 C. DHUYâ;¼YÉ;Ē¼Y¼; Yİ;¼YÉ;Ēǝðİãǝ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADǝ-Ā+¿®NGǝİ¼¹ç;ç°Ē²¼ǝİTI-CMDĀ+¿®ǝİ¼Ā¹Ōǝ•ǝĒǝǝǝǝĒ;f
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0096 C.                0303; SET0EDUMP0I0i0Y0N0Y0a0C0O0a|030E;E
0097 C.
0098 C. TI03030302 10:05:00.0
0099 +. TI 2013-03-02 10:05:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2013-03-02 10:05:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2013-03-02 10:05:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2013-03-02 10:09:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C.                0E20I0A0E0i0N0I0Y0A0Y0S0Y0A0Y-1a0i0U
0116 C.                00[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.                00[HK1_TI_CMD_NUM]          EQ      4
0118 C.                00[HK1_NEXT_EXEC_PIM]       EQ      DHU
0119 C.                00[HK1_NEXT_EXEC_DC]        EQ      0xB3
0120 C.
0121 C. *****
0122 C. TI03030302 10:09:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]       EQ      07
0129 C.                00[HK1_DMP_TOP_ADRS_0]       EQ      2B
0130 C.                00[HK1_DMP_BLOCK_NUM]        EQ      3
0131 C.                00[HK1_DMP_REPEAT_NUM]       EQ      0
0132 C.                00[HK1_DMA_DMP_PIM]         EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]          EQ      7
0136 C.                00[HK1_PKT_GEN_TIME]         EQ      0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]       EQ      32k
0138 C.                00[HK1_X_TLM_BIT_RATE]      EQ      4M
0139 C.                00[HK1_DMP_CHK_FLG]         EQ      EXEC
0140 C.
0141 C.                0Y0A0Y0Y0X0Y0I0»0D0I0C0S
0142 C.                00[HK1_DMP_CHK_FLG]         EQ      NON
0143 C.
0144 C. RAM ID=TI_TBL0I0E0I0C0»0E0I0K0D0I0C0S
0145 C.
0146 C. DHU0A0;0Y0E0;0E0Y0Y0;0Y0i0;0Y0E0;0E0D0I0A0I0
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]          EQ      2
0150 C.                00[HK1_PKT_GEN_TIME]         EQ      0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]       EQ      32K
0152 C.                00[HK1_X_TLM_BIT_RATE]      EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2013-03-02 10:09:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2013-03-02 10:09:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2013-03-02 10:09:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM]         EQ      2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2013-03-02 10:09:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM]         EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0A0I0I0»0Y0E0E0A0D0I0E0DC0BC0»0E0 *****
0192 C. (0A0I0Y0Y0Y0E0Y0P0Y0E0Y0A0Y0C0Y0E0E0Y0»0A0»0I0E0)
0193 C. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 47s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-03-02 10:09:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÄîâî»ö¼ÝðËÄð¹ñëDCBC•x²è *****
0153 C. (%ã°ï¥Ó¥Ä¥Ë¥Ï¥Ë¥Ä¥¿¥èñ¾¼¼Ä»Û¹ñé)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¥Ð¥¹•ï Daily±¿íÑñÉ'Ø¹ñëDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOS¥Ä¥§¥Ä¥¹¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-499 2013-03-02 12:53:50 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÄò•µ°È»Í×ÁÇòíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòí;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-264:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-03-02 10:09:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 85 83 08 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
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0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 07)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 10)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2013-03-02 10:09:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂð¹ñèDCBC•x²è *****
0117 C. (¼ã°îÿÓÿÄÿÈÿÞÿËÿÁÿÇÿÈÿ¼ñ¼Ä»Û¹ñè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎñÈ´Ø¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿ§ÿÄÿ-¼Ä»Û;ä
0128 C.
0129 . C. ***** LOS *****
0130 C.
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*** OP Sequence for XRT ***

2013/03/02	10:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2013/03/02	10:36:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2013/03/02	10:51:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	10:51:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/03/02	10:52:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/03/02	10:52:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/03/02	10:52:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/03/02	10:52:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/02	10:54:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2013/03/02	10:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	11:07:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	11:08:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2013/03/02	11:24:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2013/03/02	11:40:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2013/03/02	11:50:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2013/03/02	12:06:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2013/03/02	12:22:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2013/03/02	12:38:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2013/03/02	12:48:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2013/03/02	12:58:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2013/03/02	13:14:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2013/03/02	13:30:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2013/03/02	13:46:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2013/03/02	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	13:59:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/03/02	14:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/03/02	14:00:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/02	14:00:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/02	14:00:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/02	14:00:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/02	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	14:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/03/02	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2013/03/02	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	14:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	14:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	14:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/02	14:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/02	14:51:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/02	14:52:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	15:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/03/02	15:30:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	02	00	00	00	00
2013/03/02	15:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/02	15:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/02	15:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2013/03/02	15:30:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/02	15:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	15:32:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/03/02	15:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2013/03/02	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	16:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	16:14:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	16:14:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/02	16:17:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/02	16:38:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/02	16:39:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	17:00:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/03/02	17:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	17:51:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	17:51:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/02	17:54:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/02	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	18:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/03/02	18:15:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/03/02	18:15:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/03/02	18:15:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/03/02	18:15:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/02	18:17:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2013/03/02	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	18:24:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/03/02	18:25:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/03/02	18:25:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/02	18:25:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/02	18:25:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/02	18:25:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/02	18:27:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	18:27:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/03/02	18:27:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2013/03/02	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	19:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	19:28:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	19:28:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/02	19:31:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/02	19:52:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/02	19:53:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	21:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/02	21:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	21:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/02	21:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/02	21:29:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/02	21:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/02	22:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2013/03/02	22:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/02	22:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/02	22:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/02	23:03:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/02	23:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	00:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	00:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	00:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	00:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	00:28:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/03	00:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	01:43:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	01:43:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	01:46:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	02:03:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/03	02:04:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	03:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	03:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	03:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	03:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	03:40:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/03	03:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	04:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	04:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	04:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	04:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	05:18:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/03	05:19:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	06:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/03/03	06:00:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/03/03	06:00:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/03/03	06:00:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/03/03	06:00:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/03	06:03:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2013/03/03	06:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	06:10:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/03	06:10:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2013/03/03	06:10:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/03	06:10:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/03	06:10:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/03	06:10:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/03	06:13:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	06:13:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2013/03/03	06:13:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/03	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	06:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2013/03/03	18:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	03	00	00	00	00
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/03	18:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/03	18:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/03	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/03	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	18:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/03/03	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2013/03/03	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	18:28:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	18:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	18:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	18:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	18:51:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/03	18:52:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	20:05:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	20:05:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	20:05:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	20:08:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	20:28:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/03	20:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	21:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	21:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	21:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	22:05:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/03	22:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/03	23:20:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/03	23:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/03	23:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/03	23:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/03	23:33:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/03	23:34:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/04	00:49:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/04	00:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/04	00:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/04	00:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/04	01:03:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/04	01:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/04	02:19:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/04	02:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/04	02:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/04	02:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/04	02:39:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/04	02:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/04	03:47:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/04	03:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/04	03:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/04	03:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/04	04:17:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/04	04:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								

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2013/03/04	05:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	05:28:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/04	05:28:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/04	05:31:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	06:04:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/03/04	06:04:26.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/03/04	06:04:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00
			AOCU_NM	5	02-76	00	00	00	00
2013/03/04	06:04:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/04	06:04:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/04	06:04:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2013/03/04	06:07:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/04	06:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	06:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/03/04	06:14:26.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/03/04	06:14:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	03	00	00	00
			AOCU_NM	5	02-76	03	00	00	00
2013/03/04	06:14:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/03/04	06:14:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/03/04	06:14:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/04	06:14:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/04	06:17:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2013/03/04	06:17:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
2013/03/04	06:17:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/04	06:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	07:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/04	07:08:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/04	07:08:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/04	07:11:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_CUSTOM_434	1	07-F0	e8			
2013/03/04	07:32:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CUSTOM_434	1	07-F0	e8			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/04	07:33:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	08:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/04	08:48:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/04	08:48:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/04	08:51:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_CUSTOM_434	1	07-F0	e8			
2013/03/04	09:08:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_CUSTOM_434	1	07-F0	e8			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/04	09:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	10:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/04	10:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/04	10:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/04	10:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/04	10:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/03/04	10:34:56.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	00	53	4b	01
2013/03/04	10:35:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	53	4b	01
			AOCU_NM	5	02-76	00	53	4b	01
2013/03/04	10:35:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/03/04	10:35:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/03/04	10:35:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/04	10:35:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/04	10:37:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			

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2013/03/04	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/04	19:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/04	19:04:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/04	19:04:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/04	19:07:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/04	19:28:00.0	XRT_Custom_434_OG [0x1b2]					
2013/03/04	19:29:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/04	20:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/04	20:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/04	20:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/04	20:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/04	21:05:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/04	21:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/04	22:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/04	22:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/04	22:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/04	22:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/04	22:40:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/04	22:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/04	23:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/04	23:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/04	23:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/04	23:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	00:05:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/05	00:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	01:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	01:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	01:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/05	01:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	01:39:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/05	01:40:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	02:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	02:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	02:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/05	02:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	03:16:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/05	03:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	04:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	04:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/05	04:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	04:54:00.0	XRT_Custom_434_OG [0x1b2]					
2013/03/05	04:55:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	05:37:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	05:37:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2013/03/05	05:38:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2013/03/05	05:38:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/05	05:38:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/05	05:38:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/05	05:40:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]					

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2013/03/05	05:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	05:47:56.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]	XRT_FOCUS_RECAL	2	07-F8	78	00
2013/03/05	05:48:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	03	00 00 00 00
2013/03/05	05:51:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/03/05	05:52:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/05	05:52:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/05	05:52:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/05	05:52:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/05	05:52:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	05:52:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2013/03/05	05:52:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/05	05:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	06:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	06:05:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	06:05:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/05	06:08:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	06:31:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/05	06:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	07:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	07:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	07:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/05	07:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	08:08:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/05	08:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/05	09:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/05	09:26:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/05	09:26:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/05	09:29:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/05	10:01:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00