XRT Timeline to be uploaded on 2013/03/05

Period: 2013/03/05 09:51:00 - 2013/03/09 10:41:00

* * * * * **Normal mode** * * * * *

XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms

Term	Pointing (x, y)	Comment
03/05 10:04:00 - 03/05 17:40:54	Fixed (-15.0, 883.0)	# OP start + 10min/ HOP81-day3
03/06 08:45:30 - 03/06 16:39:54	Fixed (-15.0, -957.0)	HOP81-day1

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 61 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 62 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 3 30-time(s) 2.0sec

Seqn= 60 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(25

Term	Pointing (x, y)	Comment
03/05 17:44:00 - 03/05 17:50:54	Fixed (0.0, 0.0)	synoptic, shifted -19.0 min
03/06 06:02:30 - 03/06 06:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min
03/06 16:43:00 - 03/06 16:49:30	Fixed (0.0, 0.0)	synoptic (-> XRT bakeout)

PROG= 12 1-time(s)

Subr= 1 1-time(s) 14.0sec

Seqn= 96 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 41 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 69 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 68 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

XOB #196D: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (32ms) w/ G-Band VLS Cl

Term	Pointing (x, y)	Comment
03/05 17:54:00 - 03/05 19:41:00	Track (-692.8, 241.8) @ 03/05 17:51:00	previous AR 11654 tracking

PROG= 04 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn=100 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Seqn= 65 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 94 30-time(s) 120.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
03/05 20:13:00 - 03/06 05:59:24	Track (-71.8, -169.0) @ 03/05 20:10:00	AR 11683 tarcking

PROG= 18 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn=100 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Seqn= 65 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 94 70-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms

Term	Pointing (x, y)	Comment
03/06 06:12:30 - 03/06 08:23:30	Track (16.7, -168.6) @ 03/06 06:09:30	AR 11683 tracking

PROG= 03 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 24 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 61 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec

Seqn= 27 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 28 150-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

* * * * * **Flare mode** * * * * *

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
03/05 10:04:00 - 03/05 17:40:54	Fixed (-15.0, 883.0)	# OP start + 10min/ HOP81-day3
03/05 17:54:00 - 03/05 19:41:00	Track (-692.8, 241.8) @ 03/05 17:51:00	previous AR 11654 tracking
03/05 20:13:00 - 03/06 05:59:24	Track (-71.8, -169.0) @ 03/05 20:10:00	AR 11683 tarcking
03/06 06:12:30 - 03/06 08:23:30	Track (16.7, -168.6) @ 03/06 06:09:30	AR 11683 tracking
03/06 08:45:30 - 03/06 16:39:54	Fixed (-15.0, -957.0)	HOP81-day1

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec

Seqn= 35 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 10.0sec

Seqn= 36 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 37 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 38 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

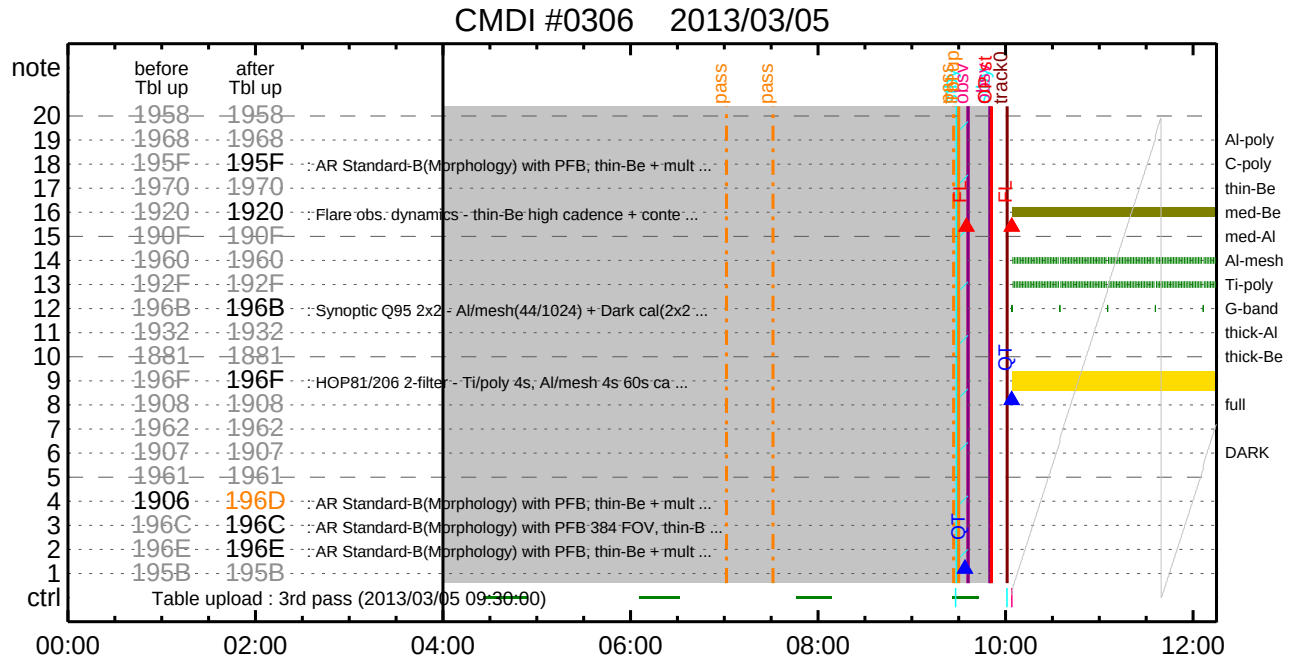
NOT USED

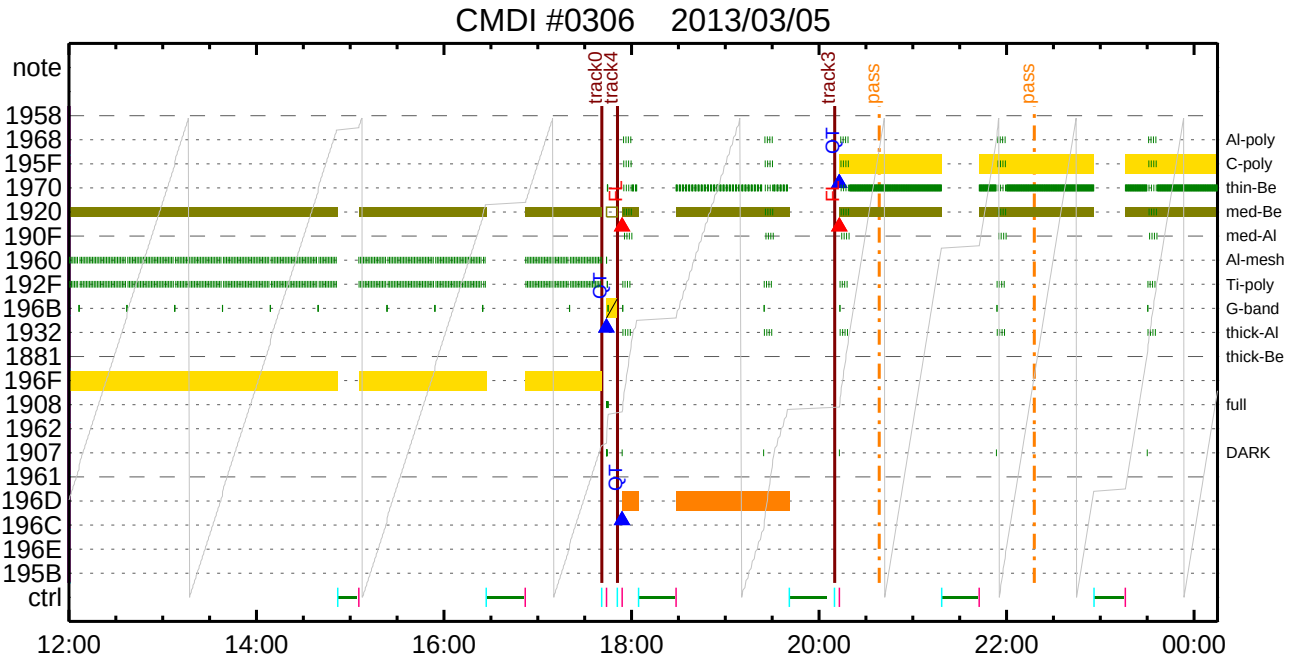
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

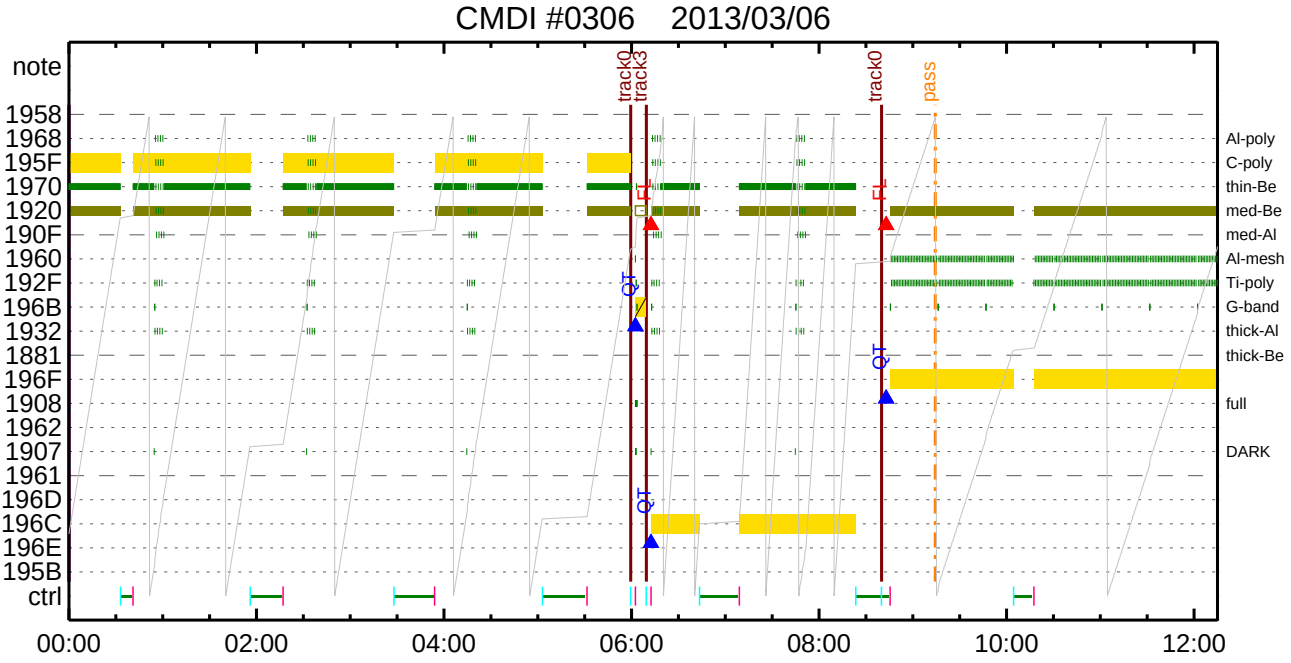
FLD Patrol

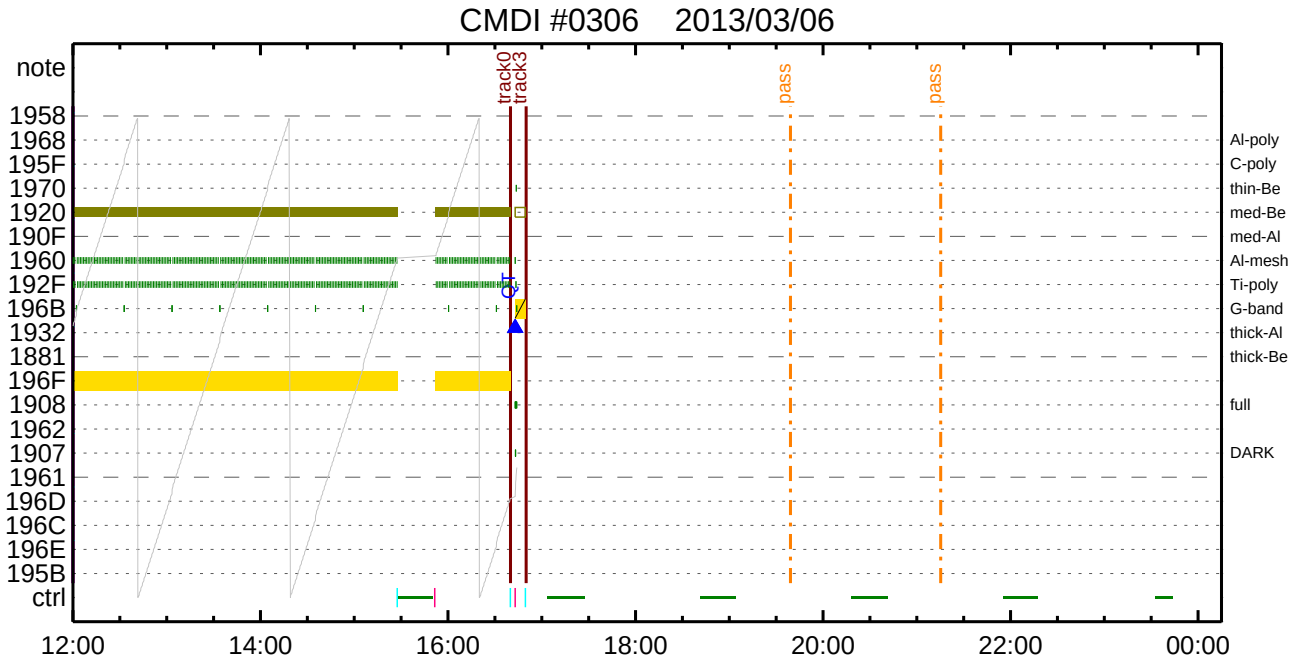
Term	Pointing (x, y)	Comment
03/05 17:51:16 - 03/06 05:59:46	Track (-692.8, 241.8) @ 03/05 17:51:00	previous AR 11654 tracking
03/06 06:09:46 - 03/06 16:40:16	Track (16.7, -168.6) @ 03/06 06:09:30	AR 11683 tracking

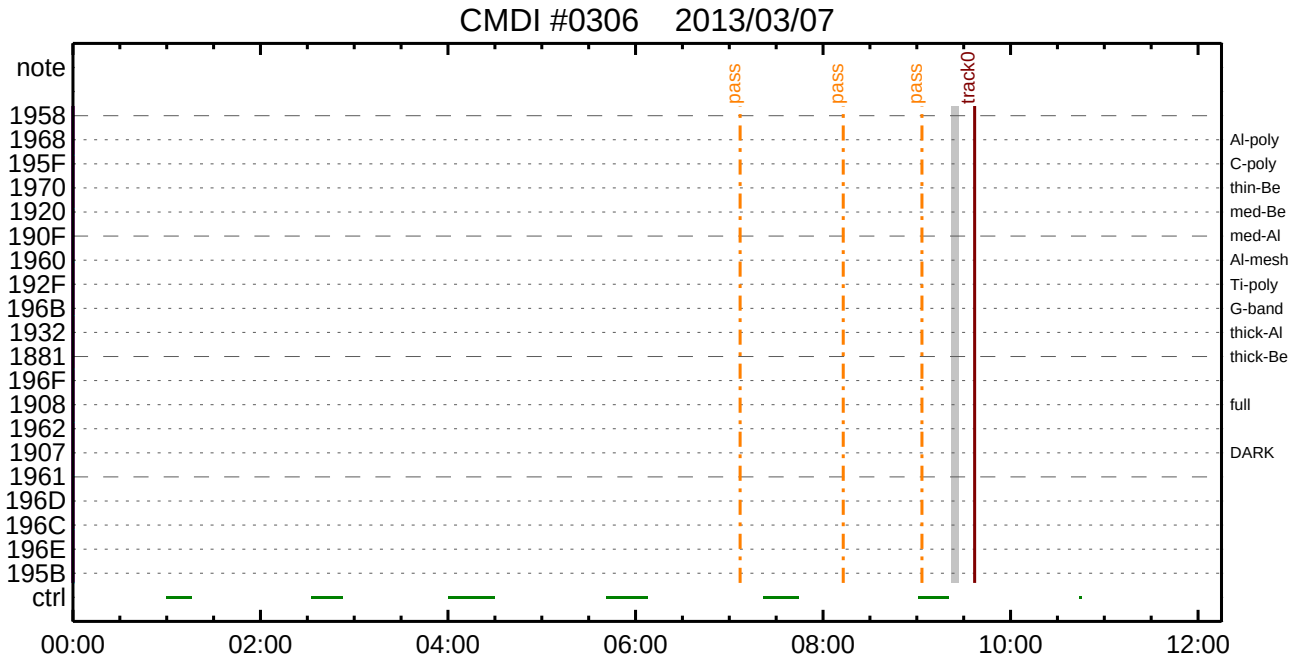
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

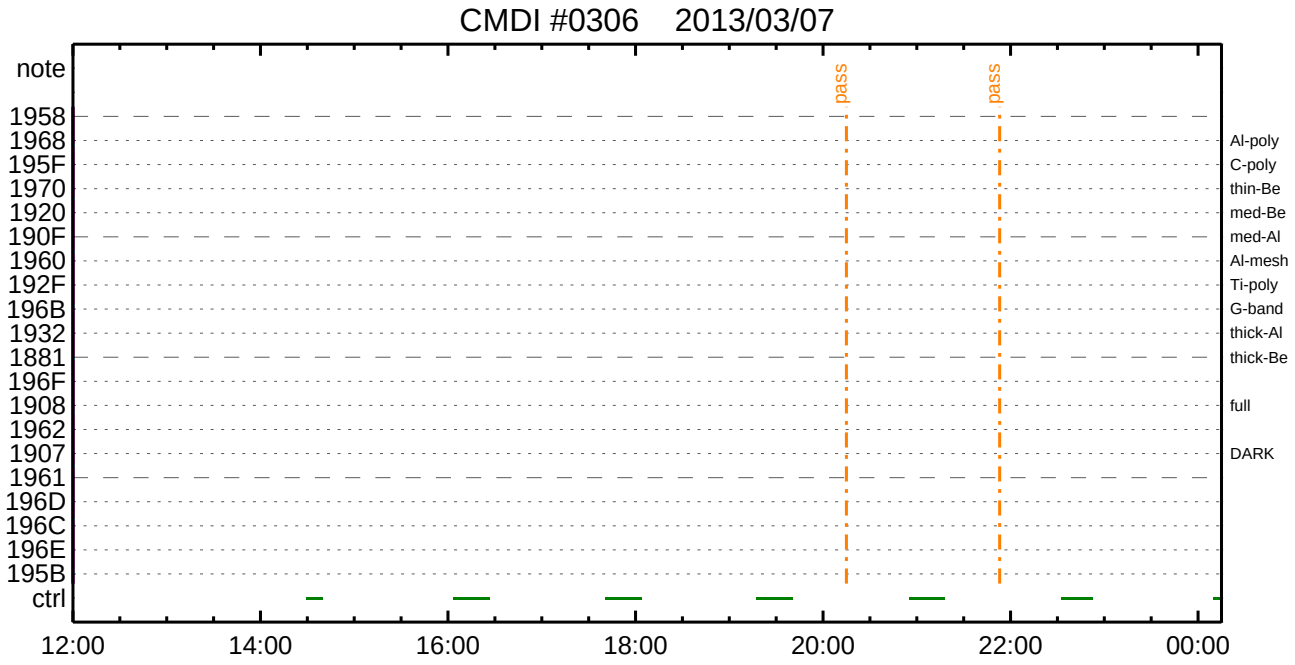

CMDI #0306 2013/03/05
note
before Tbl up
after Tbl up
20 1958 1958
19 1968 1968
18 195F 195F : AR Standard-B(Morphology) with PFB, thin-Be + mult ...
17 1970 1970
16 1920 1920 : Flare obs. dynamics - thin-Be high cadence + conte ...
15 190F 190F
14 1960 1960
13 192F 192F
12 196B 196B : Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 ...
11 1932 1932
10 1881 1881
9 196F 196F : HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s ca ...
8 1908 1908
7 1962 1962
6 1907 1907
5 1961 1961
4 1906 196D : AR Standard-B(Morphology) with PFB, thin-Be + mult ...
3 196C 196C : AR Standard-B(Morphology) with PFB 384 FOV, thin-B ...
2 196E 196E : AR Standard-B(Morphology) with PFB, thin-Be + mult ...
1 195B 195B
ctrl Table upload : 3rd pass (2013/03/05 09:30:00)
pass
pass
track0
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

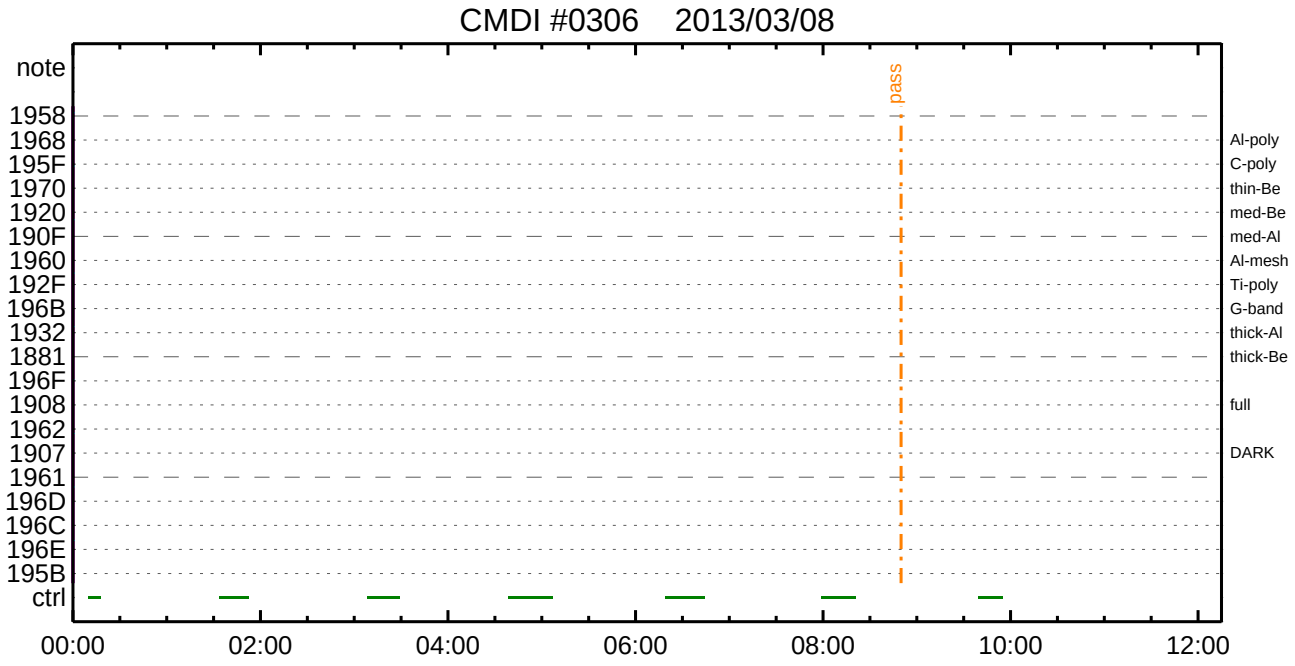

CMDI #0306 2013/03/05
note
track0
track4
track3
pass
pass
FL
QT
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

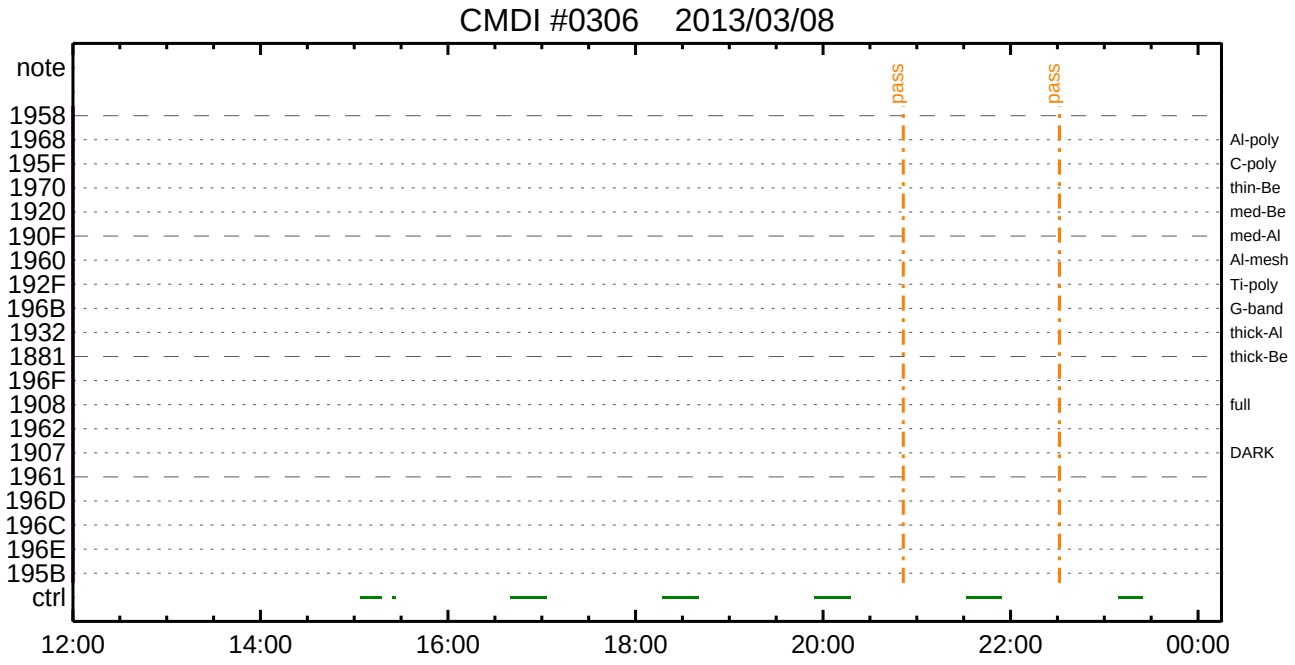

CMDI #0306 2013/03/06
note
track0
track3
track0
pass
FL
QT
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

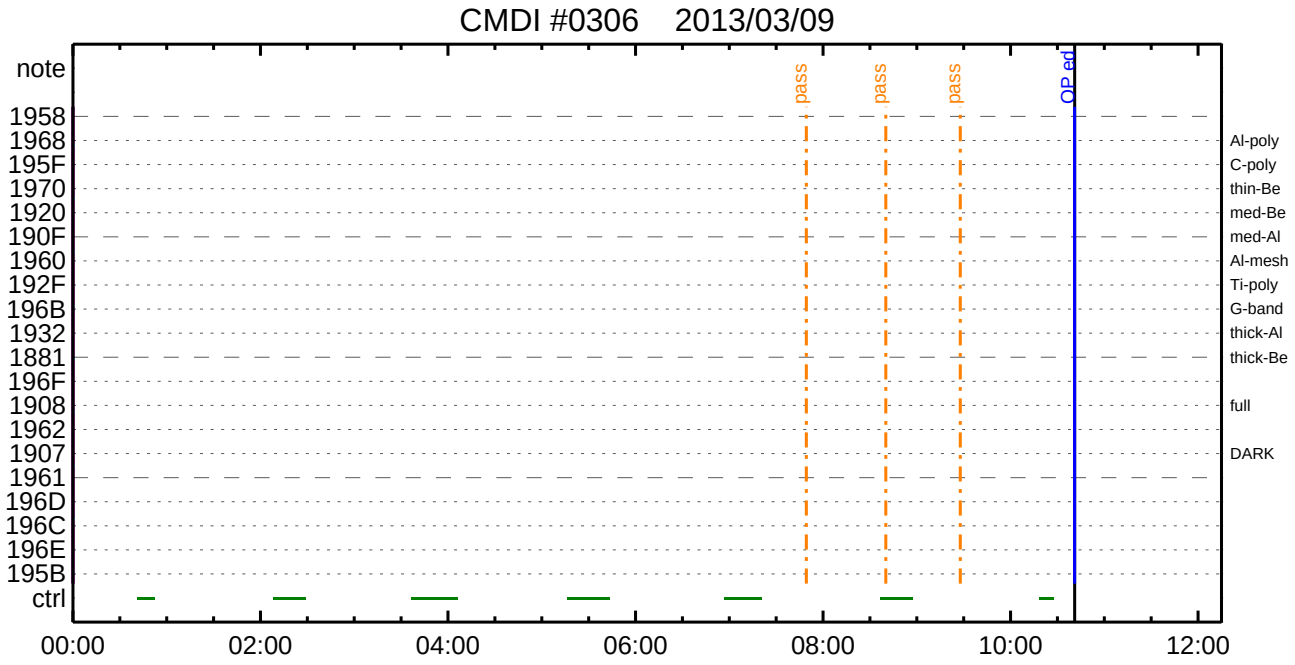

CMDI #0306 2013/03/06
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
track0
track3
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

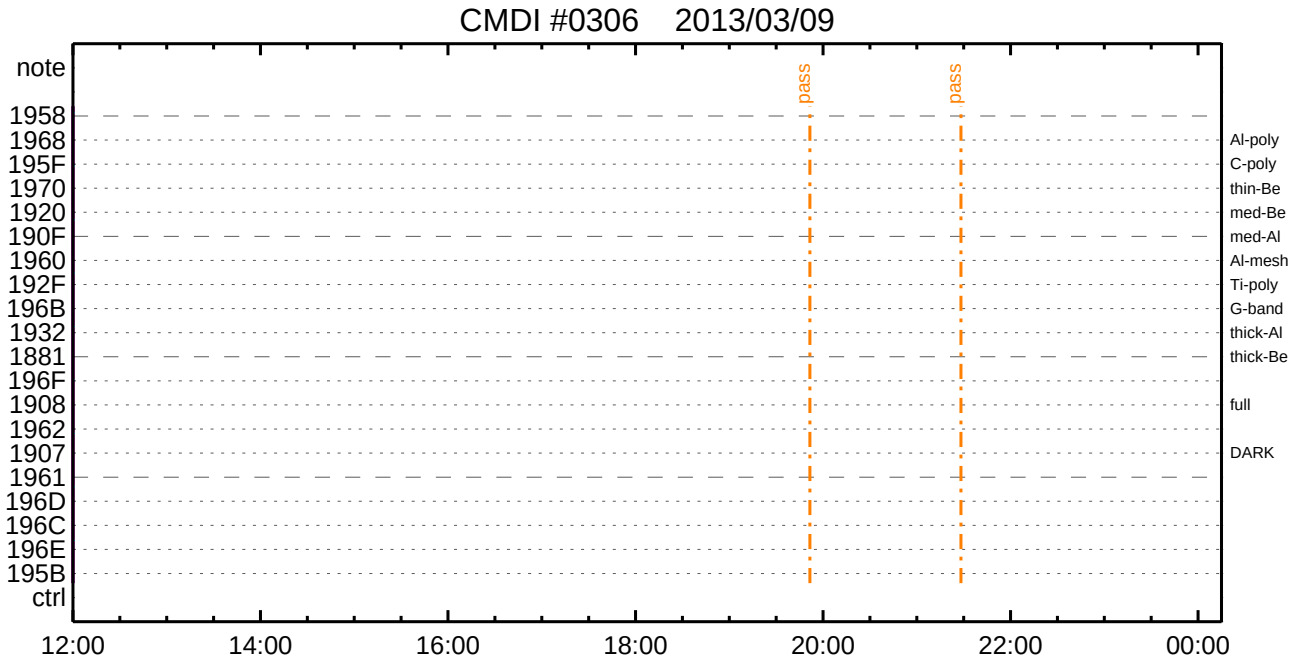

CMDI #0306 2013/03/07
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
pass
pass
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0306 2013/03/07
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0306 2013/03/08
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0306 2013/03/08
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0306 2013/03/09
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0306 2013/03/09
note
1958
1968
195F
1970
1920
190F
1960
192F
196B
1932
1881
196F
1908
1962
1907
1961
196D
196C
196E
195B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-501 2013-03-05 11:16:43 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-501:OP
0104     ()
0105   . S. OG    og-501:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2013-03-05 09:46:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2013-03-05 09:46:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2013-03-05 09:46:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2013-03-05 09:50:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2013-03-05 09:50:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2013-03-05 09:50:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ***********
0256     C. Execute, after the success of OP upload.
0257   +. TI 2013-03-05 09:50:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ***********
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-502 2013-03-05 11:16:43 91 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017    . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034    . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045    . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 1816318.8 +- 1.0 (s) [ ]
0052     C.
0053    . C.
0054    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057    . C. Verify EIS in MANUAL mode
0058    . C. Estimated OBSTBL upload time is 15s
0059     C. ********************************
0060     C. EIS START OBSTBL LOAD
0061     C. ********************************
0062    . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2013-03-05 09:50:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072    . C.                    [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. ********************************
0074     C. EIS END OBSTBL LOAD
0075     C. ********************************
0076     C.
0077    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079    . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084    . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090    . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-503 2013-03-05 11:16:43 130 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Upload DPL table >
0018   +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020   . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0025     C.
0026   . S. RAM   ram-271:MDP_DPL
0027     ()
0028     C.
0029   . C. < Dump RAMID=MDP_DPL >
0030   +. DC 07-F0 MDP_DUMP_FGTBL
0031     BC       (82 07 00 38 b8 00 40)
0032     C. ------------------------------------
0033     C.  MDP_DPL verify          = OK    [ ]
0034     C. ------------------------------------
0035     C.
0036     C. STS_CHK¤òON¤Ë¤¹¤ë
0037     C.
0038   . C. < Update MDP DSC PAR1 >
0039   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0040     BC       (4c)
0041     C.  MDP_CMD_CODE          = F04C0700[ ]
0042     C.  MDP_CMD_CNT        (count-up 1) [ ]
0043     C. ------------------------------------
0044     C.
0045   . C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +  DC 07-F0 MDP_XRT_MODE_STBY
0052     BC       (c3)
0053   . C. ----------- Success Verify ?             OK / NG____
0054     C.
0055     C. XRT Obs. Table Upload
0056   . S. RAM   ram-291:MDP_OBS_X
0057     ()
0058     C.
0059   +. DC 07-F0 MDP_DUMP_XRTTBL
0060     BC       (84 07 00 00 00 3a d4)
0061   . C. ----------- Comparison Check ?                      OK / ERR ____
0062     C.
0063     C.
0064   +. DC 07-F0 MDP_XRT_ROI_SET
0065     BC       (cd 01 b1 b1 04 04)
0066   +  DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 02 b1 b1 08 08)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 03 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 04 b1 b1 06 06)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 05 85 83 06 06)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 06 85 83 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 07 85 83 08 08)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 08 80 80 20 20)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 09 80 80 20 08)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 0a 80 80 08 20)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0f 80 80 06 06)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 10 80 80 08 08)
0088   +  DC 07-F0 MDP_XRT_FLD_ENA
0089     BC       (d8)
0090   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091     BC       (c8)
0092   +  DC 07-F0 MDP_XRT_AEC_RESET
0093     BC       (d0)
0094   +  DC 07-F0 MDP_XRT_ARS_DIS
0095     BC       (d5)
```

```
0096  +  DC 07-F0 MDP_XRT_FLD_RESET
0097     BC       (da)
0098  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099     BC       (c4 02)
0100  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101     BC       (c5 10)
0102   . C. ---------- Success Verify ?            OK / NG ____
0103     C.
0104     C.
0105   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0106     C.
0107  +. DC 07-F0 MDP_XRT_MODE_OBSV
0108     BC       (c2)
0109  +. TI 2013-03-05 09:50:02.0
0110     DC 07-F0 MDP_XRT_MODE_OBSV
0111     BC       (c2)
0112   . C. ---------- Success Verify ?            OK / NG ____
0113     C.
0114     C. ************ XRT END ***********
0115     C.
0116   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0117     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0118   . S. DC-BC dcbc-402:DCBC
0119     (MDP_known_event)
0120     C.
0121     C.
0122   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0123   . S. DC-BC dcbc-153:DCBC
0124     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125     C.
0126     C.
0127   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0128     C.
0129   . C. *********** LOS ***********
0130     C.
```

```
*** OP Sequence for XRT ***

2013/03/05  10:00:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  10:00:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/03/05  10:01:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 b1 82 01 58
2013/03/05  10:01:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2013/03/05  10:01:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/03/05  10:01:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2013/03/05  10:01:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/05  10:03:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  10:03:56.0  XRT_QT_PROG_SET_445_OG [0x1bd]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2013/03/05  10:03:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2013/03/05  10:04:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  14:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  14:52:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  14:52:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/05  14:55:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/05  15:04:30.0  XRT_Custom_434_OG [0x1b2]
2013/03/05  15:05:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  16:27:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  16:27:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  16:27:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/05  16:30:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/05  16:51:00.0  XRT_Custom_434_OG [0x1b2]
2013/03/05  16:52:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  17:40:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  17:40:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/03/05  17:41:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2013/03/05  17:41:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2013/03/05  17:41:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/03/05  17:41:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/05  17:43:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2013/03/05  17:44:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  17:50:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  17:50:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/03/05  17:51:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 04 00 00 00 00
2013/03/05  17:51:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2013/03/05  17:51:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/03/05  17:51:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2013/03/05  17:51:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/05  17:53:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  17:53:56.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/03/05  17:53:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2013/03/05  17:54:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  18:04:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  18:04:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  18:04:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/05  18:07:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/05  18:27:30.0  XRT_Custom_434_OG [0x1b2]
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2013/03/05  18:28:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  19:41:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  19:41:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  19:41:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/05  19:44:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/05  20:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  20:09:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/03/05  20:10:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 03 00 00 00 00
2013/03/05  20:10:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2013/03/05  20:10:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/03/05  20:10:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2013/03/05  20:10:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/05  20:12:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  20:12:56.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2013/03/05  20:12:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2013/03/05  20:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  21:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  21:18:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  21:18:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/05  21:21:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/05  21:41:30.0  XRT_Custom_434_OG [0x1b2]
2013/03/05  21:42:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/05  22:56:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/05  22:56:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/05  22:56:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/05  22:59:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/05  23:15:00.0  XRT_Custom_434_OG [0x1b2]
2013/03/05  23:16:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  00:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  00:33:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  00:33:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  00:36:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  00:40:00.5  XRT_Custom_434_OG [0x1b2]
2013/03/06  00:41:00.5  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  01:56:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  01:56:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  01:56:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  01:59:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  02:16:00.0  XRT_Custom_434_OG [0x1b2]
2013/03/06  02:17:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  03:28:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  03:28:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  03:28:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  03:31:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  03:53:00.0  XRT_Custom_434_OG [0x1b2]
2013/03/06  03:54:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  05:03:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  05:03:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  05:03:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
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2013/03/06  05:06:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  05:30:30.0  XRT_Custom_434_OG [0x1b2]
2013/03/06  05:31:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  05:59:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  05:59:26.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/03/06  05:59:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2013/03/06  05:59:46.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2013/03/06  05:59:48.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/03/06  05:59:50.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/06  06:02:28.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2013/03/06  06:02:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  06:09:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  06:09:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/03/06  06:09:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 03 00 00 00 00
2013/03/06  06:09:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2013/03/06  06:09:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/03/06  06:09:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2013/03/06  06:09:52.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/06  06:12:24.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  06:12:26.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2013/03/06  06:12:28.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2013/03/06  06:12:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  06:43:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  06:43:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  06:43:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  06:46:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  07:08:00.0  XRT_Custom_434_OG [0x1b2]
2013/03/06  07:09:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  08:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  08:23:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  08:23:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  08:26:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  08:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  08:39:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/03/06  08:40:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 55 0e 01 58
2013/03/06  08:40:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2013/03/06  08:40:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/03/06  08:40:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2013/03/06  08:40:22.0  XRT_ARS_DIS_437_OG [0x1b5]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/06  08:42:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  08:42:56.0  XRT_QT_PROG_SET_445_OG [0x1bd]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2013/03/06  08:42:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2013/03/06  08:44:30.5  XRT_Custom_434_OG [0x1b2]
2013/03/06  08:45:30.5  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  10:04:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  10:04:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  10:04:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  10:07:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
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2013/03/06  10:16:30.0  XRT_Custom_434_OG [0x1b2]
2013/03/06  10:17:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  15:27:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  15:27:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/03/06  15:27:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/03/06  15:30:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/03/06  15:50:30.0  XRT_Custom_434_OG [0x1b2]
2013/03/06  15:51:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  16:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  16:39:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/03/06  16:40:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2013/03/06  16:40:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2013/03/06  16:40:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/03/06  16:40:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/03/06  16:42:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2013/03/06  16:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/03/06  16:49:30.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/03/06  16:49:32.0  XRT_TCIB_XRT_S_HTR_A_ENA_426_OG [0x1aa]
                                TCIB_XRT_S_HTR_A_ENA 0 04-BC
2013/03/06  16:50:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 03 00 00 00 00
2013/03/07  09:37:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
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