

XRT Timeline to be uploaded on 2013/03/07

Period: 2013/03/07 09:27:00 - 2013/03/12 11:23:00

* * * * *

Normal mode

* * * * *

XOB #1905: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long-2 - w leak image												
Term		Pointing (x, y)					Comment					
03/08 23:28:00 - 03/08 23:34:54		Fixed (-528.4, -528.4)					XRT quadrant pointing					
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 1 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 7 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 15 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1906: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long-w leak image												
Term		Pointing (x, y)					Comment					
03/08 23:38:00 - 03/08 23:44:54		Fixed (528.4, -528.4)					XRT quadrant pointing					
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 2 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 7 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 15 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1907: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long-w leak image												
Term		Pointing (x, y)					Comment					
03/08 23:48:00 - 03/08 23:54:54		Fixed (528.4, 528.4)					XRT quadrant pointing					
PROG= 06 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 3 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 7 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 15 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 8 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1908: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long-w leak image												
Term		Pointing (x, y)					Comment					
03/08 23:58:00 - 03/09 00:04:54		Fixed (-528.4, 528.4)					XRT quadrant pointing					
PROG= 08 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 4 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 7		2-time(s)	2.0sec									
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3		1-time(s)	2.0sec									
Seqn= 15		1-time(s)	2.0sec									
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms)												
Term	Pointing (x, y)							Comment				
03/09 00:08:00 - 03/09 05:59:54	Track (542.4, -189.9) @ 03/09 00:05:00							Obs AR 11683				
03/09 06:13:00 - 03/09 08:36:30	Track (585.0, -194.0) @ 03/09 06:10:00							Obs AR 11683				

PROG= 03 Inf.-time(s)												
Subr= 1		1-time(s)	2.0sec									
Seqn= 24		1-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 61		1-time(s)	2.0sec									
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 27		4-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 28		150-time(s)	2.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(25ms)												
Term	Pointing (x, y)							Comment				
03/09 06:03:00 - 03/09 06:09:54	Fixed (0.0, 0.0)							synoptic				

PROG= 12 1-time(s)												
Subr= 1		1-time(s)	14.0sec									
Seqn= 96		1-time(s)	4.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6		1-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 41		1-time(s)	4.0sec									
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30		1-time(s)	2.0sec									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 69		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 68		1-time(s)	2.0sec									
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops												
---	--	--	--	--	--	--	--	--	--	--	--	--

Term	Pointing (x, y)							Comment				
03/09 00:08:00 - 03/09 05:59:54	Track (542.4, -189.9) @ 03/09 00:05:00							Obs AR 11683				
03/09 06:13:00 - 03/09 08:36:30	Track (585.0, -194.0) @ 03/09 06:10:00							Obs AR 11683				

PROG= 16 15-time(s)												
Subr= 1		45-time(s)	10.0sec									
Seqn= 35		1-time(s)	2.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2		1-time(s)		10.0sec											
Seqn= 36		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
Seqn= 37		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
Seqn= 38		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

* * * * *

Active Region Search

* * * * *

NOT USED

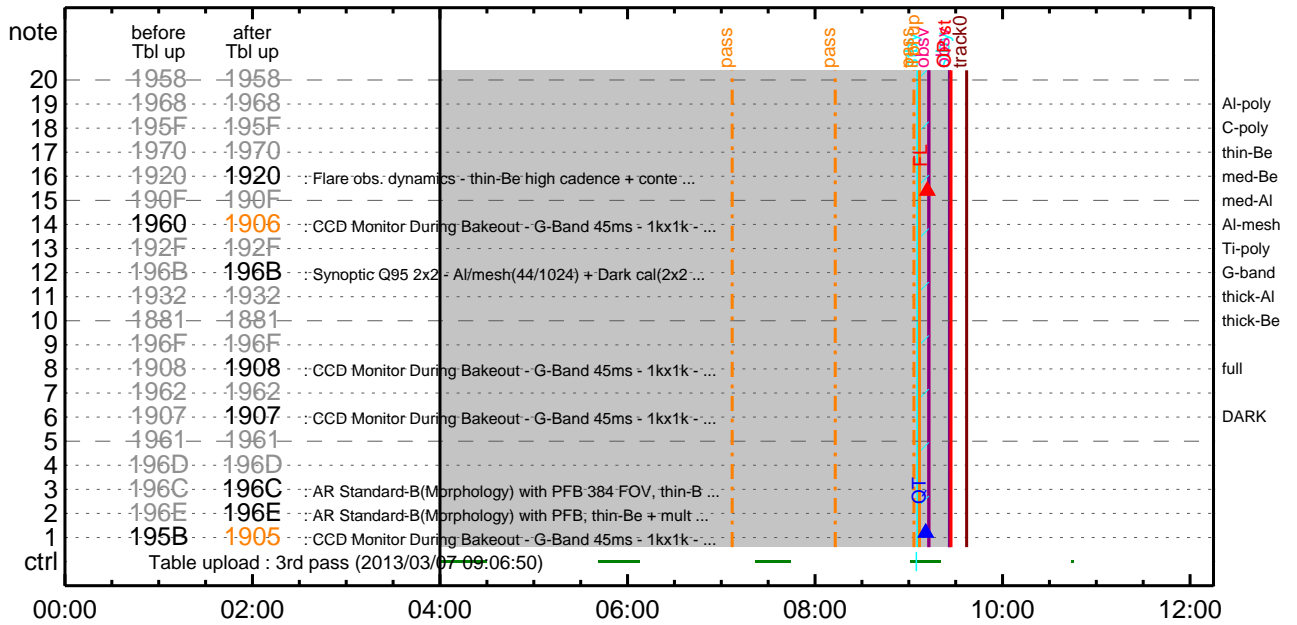
* * * * *

Flare Detection

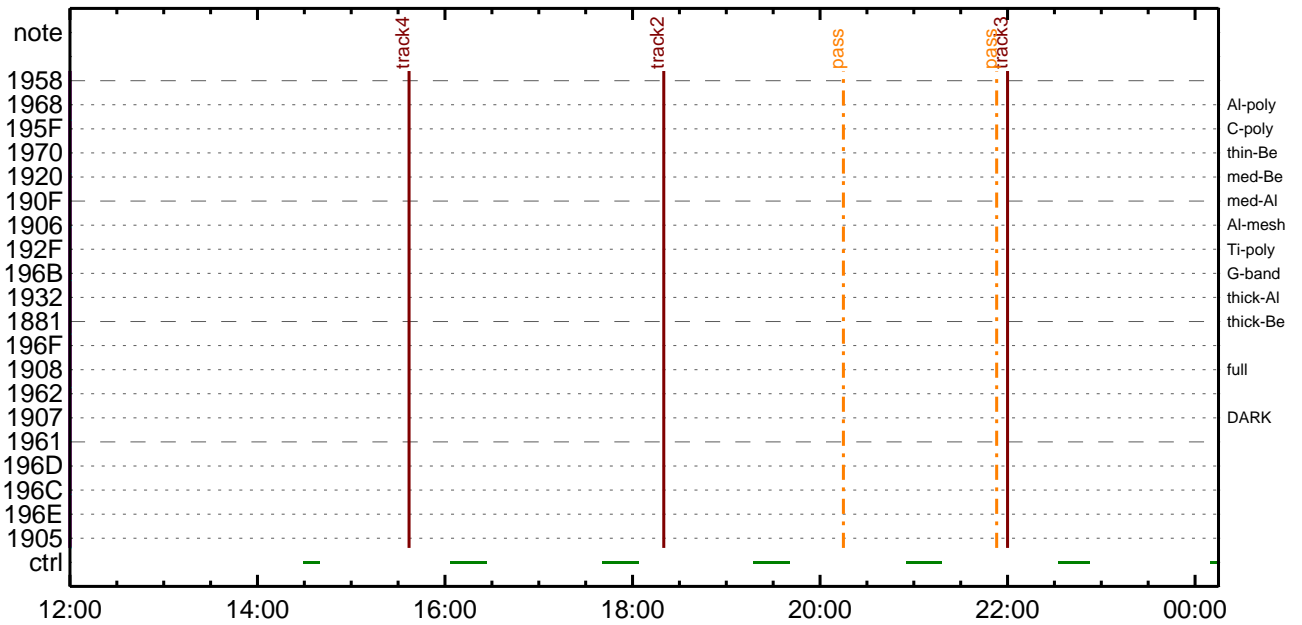
* * * * *

FLD Patrol													
Term		Pointing (x, y)				Comment							
03/09 00:05:16 - 03/09 06:00:16		Track (542.4, -189.9) @ 03/09 00:05:00				Obs AR 11683							
03/09 06:10:16 - 03/12 11:23:00		Track (585.0, -194.0) @ 03/09 06:10:00				Obs AR 11683							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

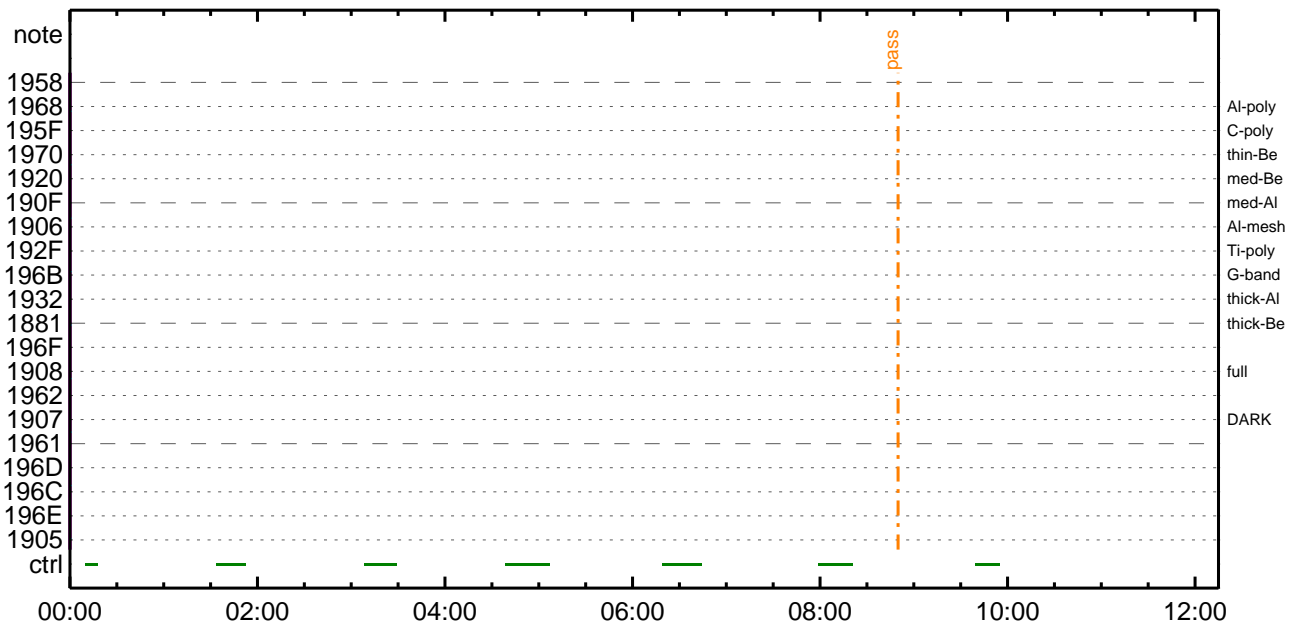
CMDI #0310 2013/03/07



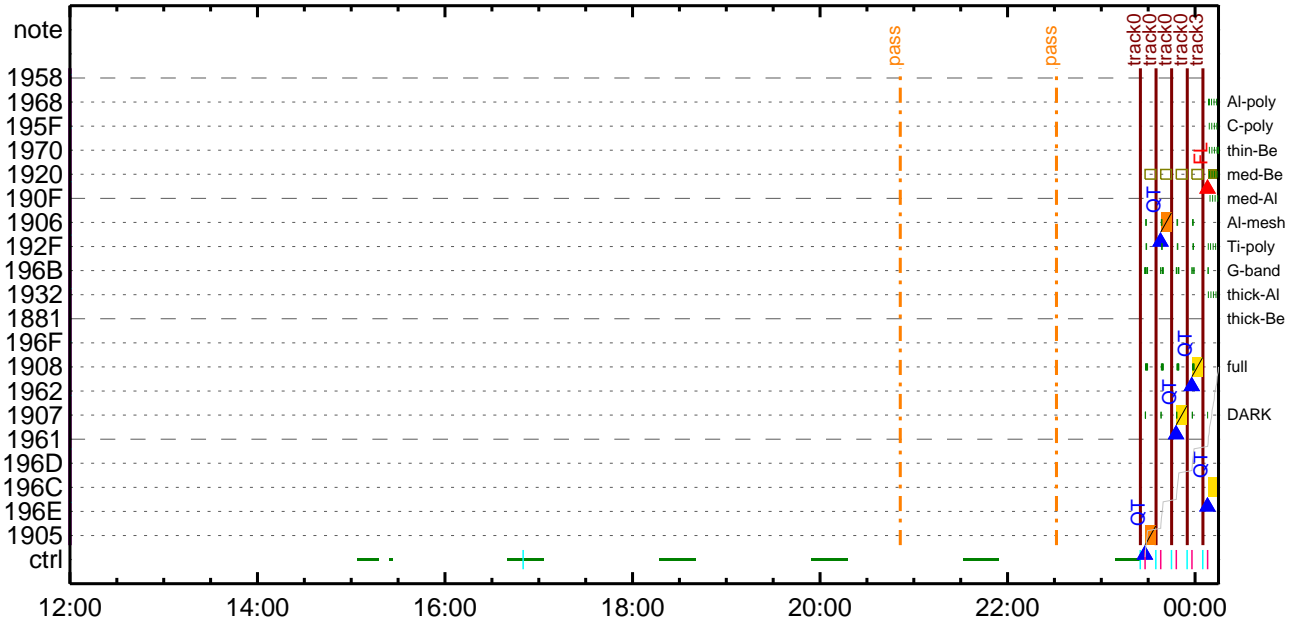
CMDI #0310 2013/03/07



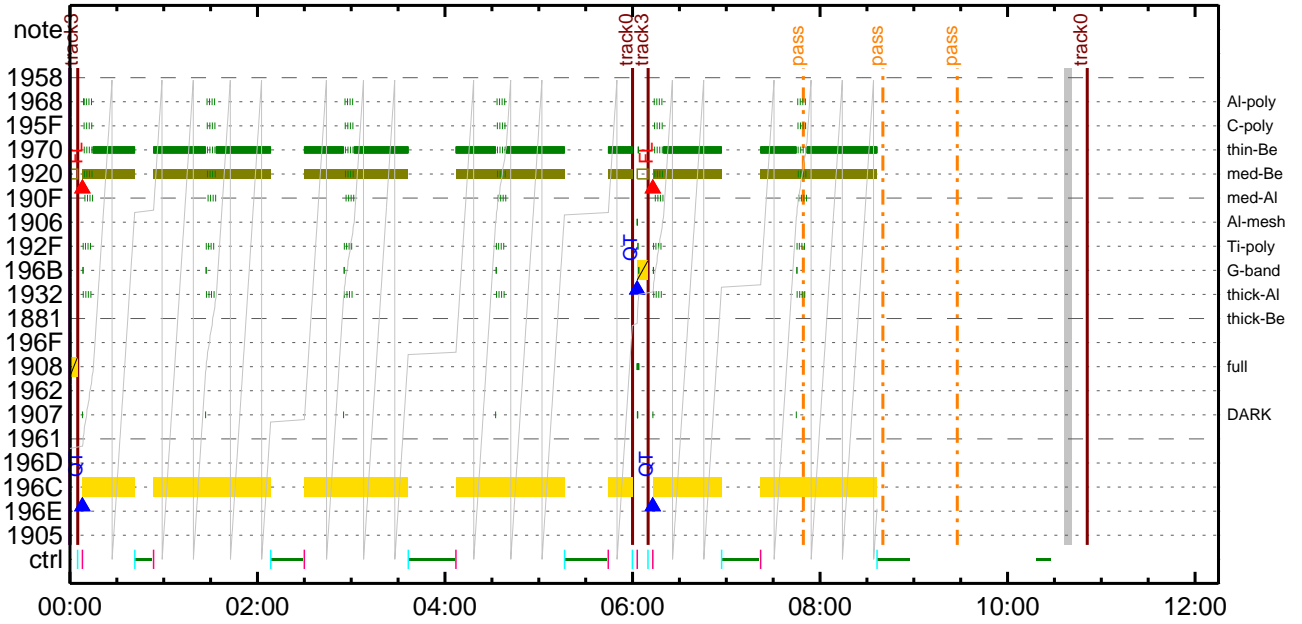
CMDI #0310 2013/03/08



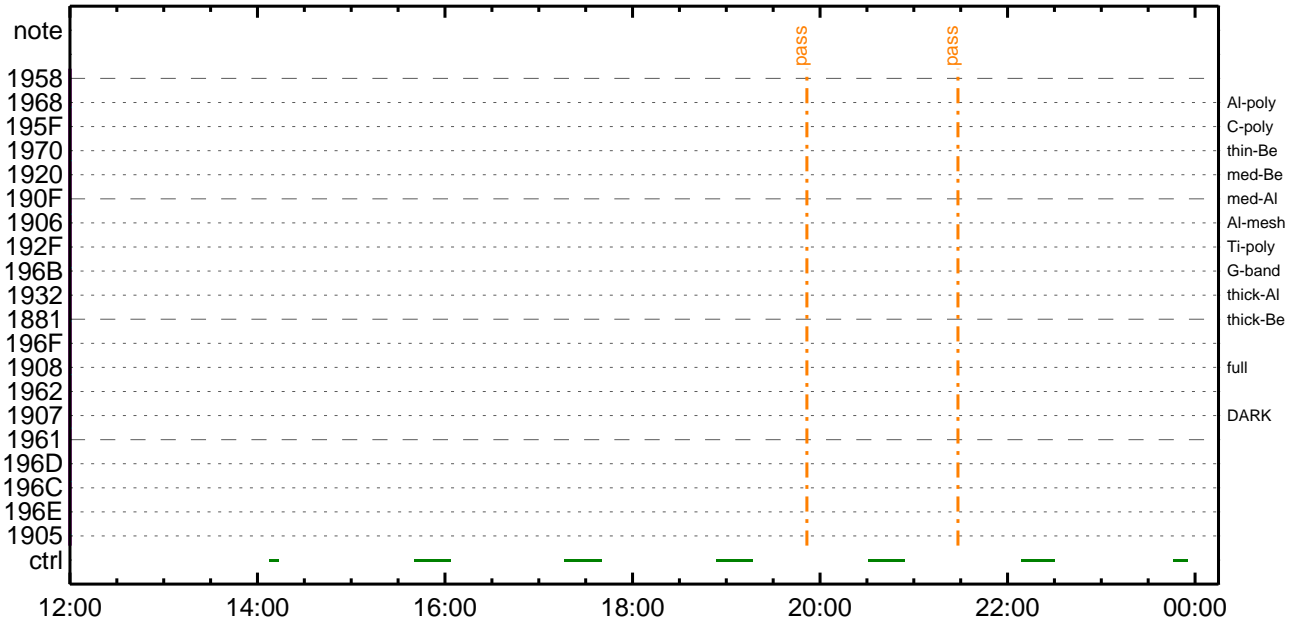
CMDI #0310 2013/03/08



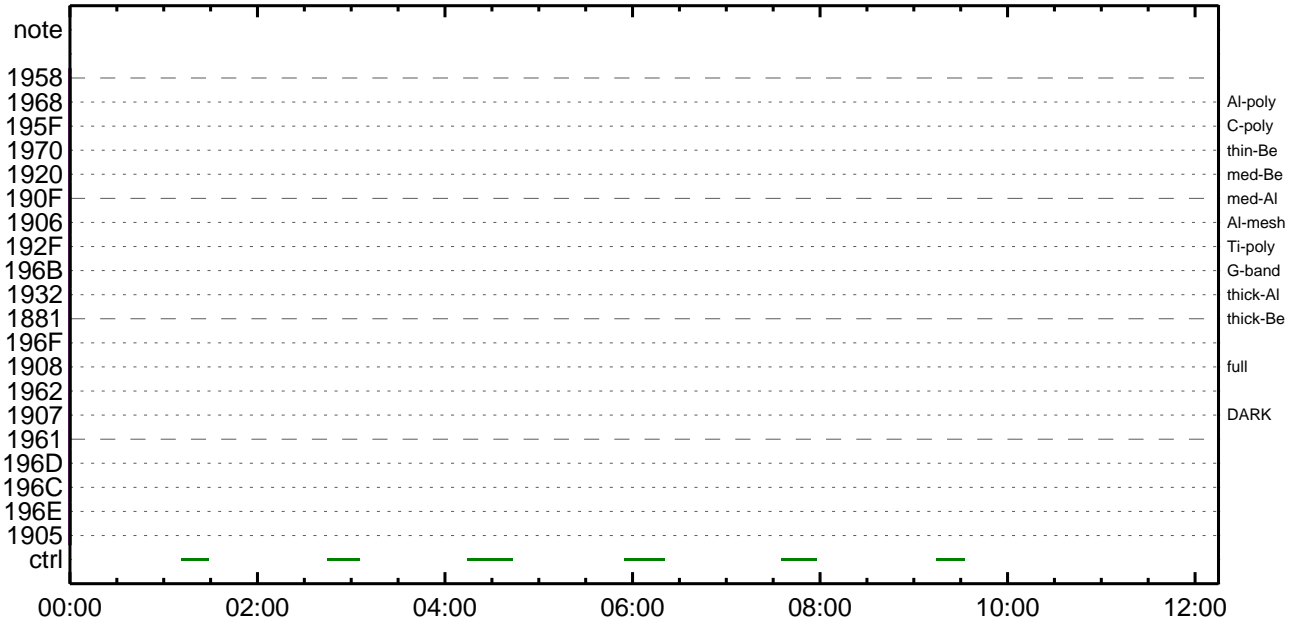
CMDI #0310 2013/03/09



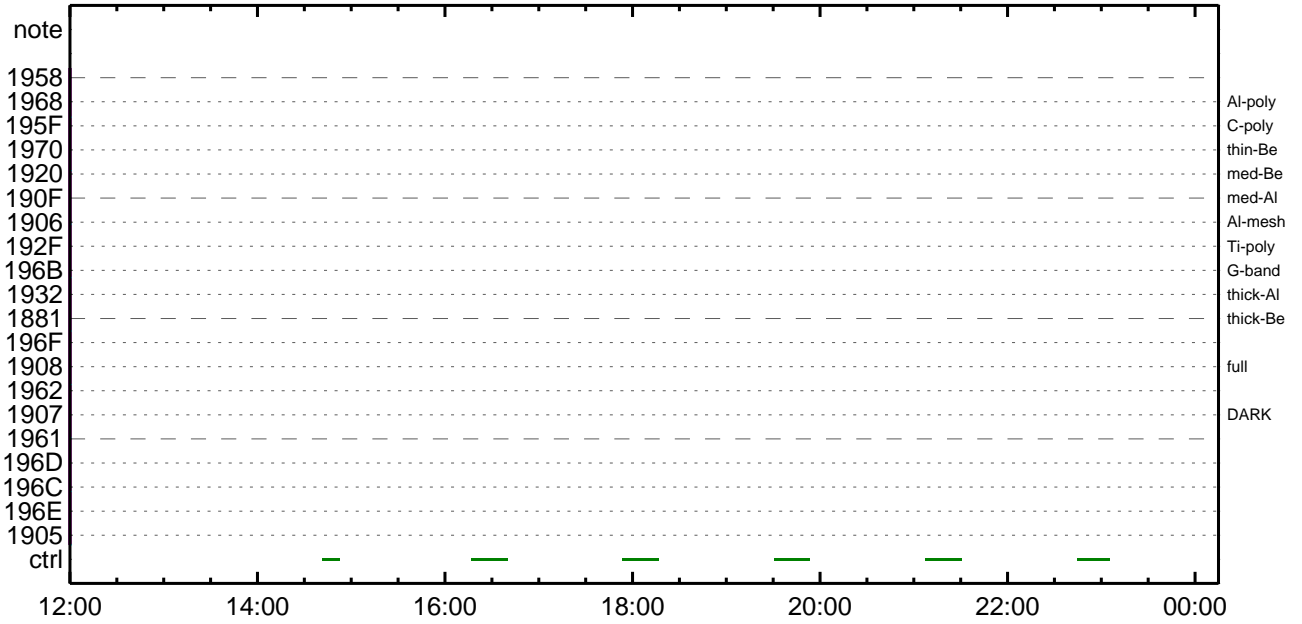
CMDI #0310 2013/03/09



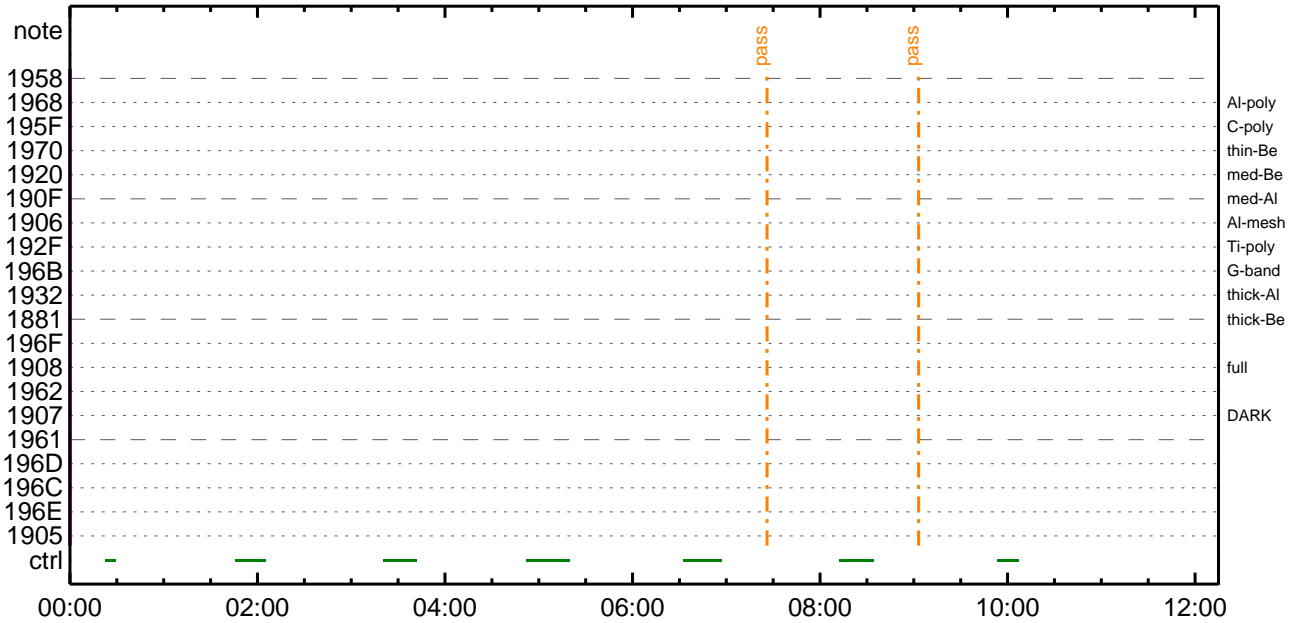
CMDI #0310 2013/03/10



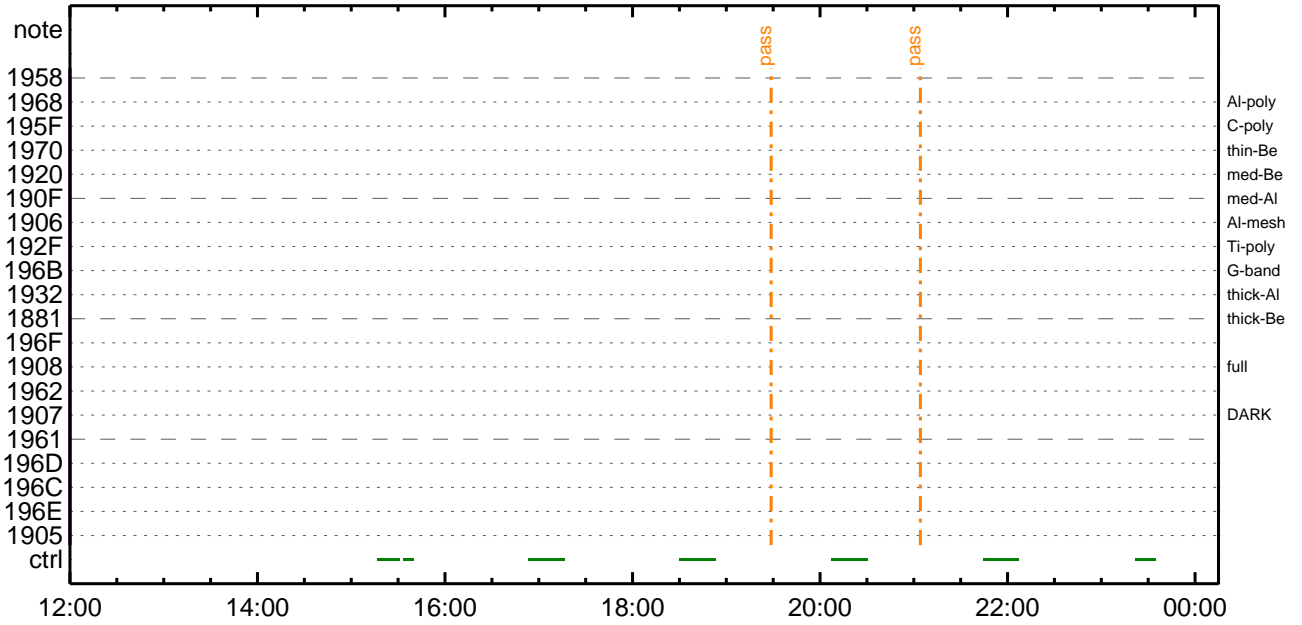
CMDI #0310 2013/03/10



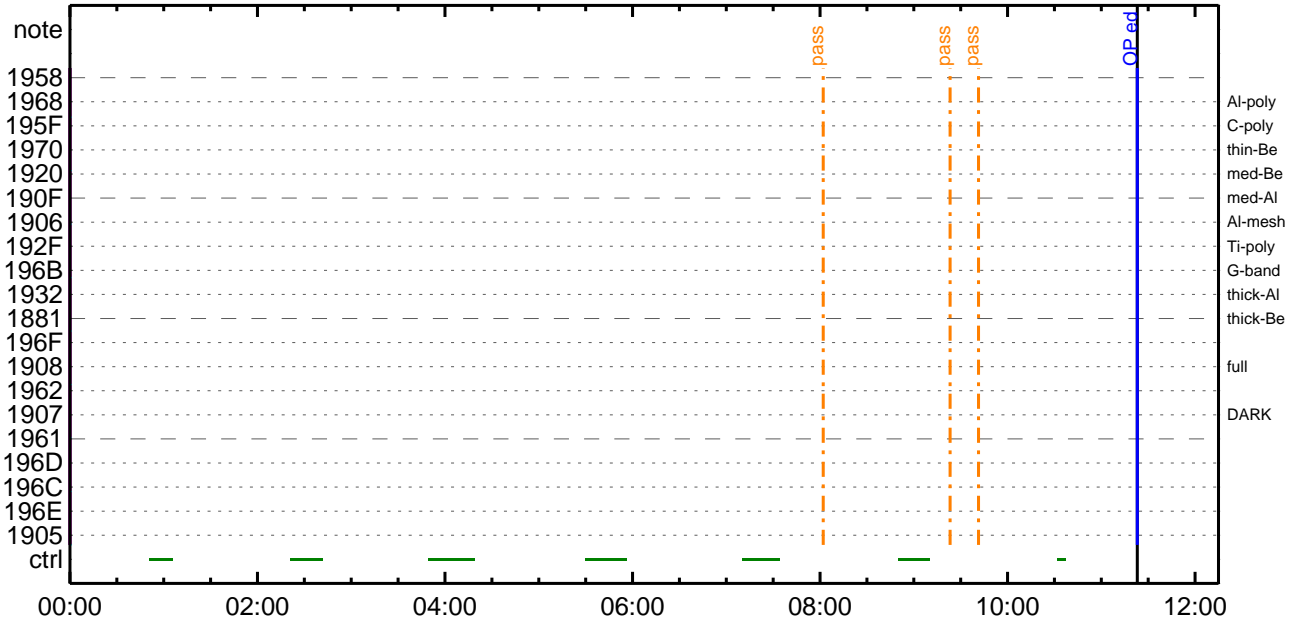
CMDI #0310 2013/03/11



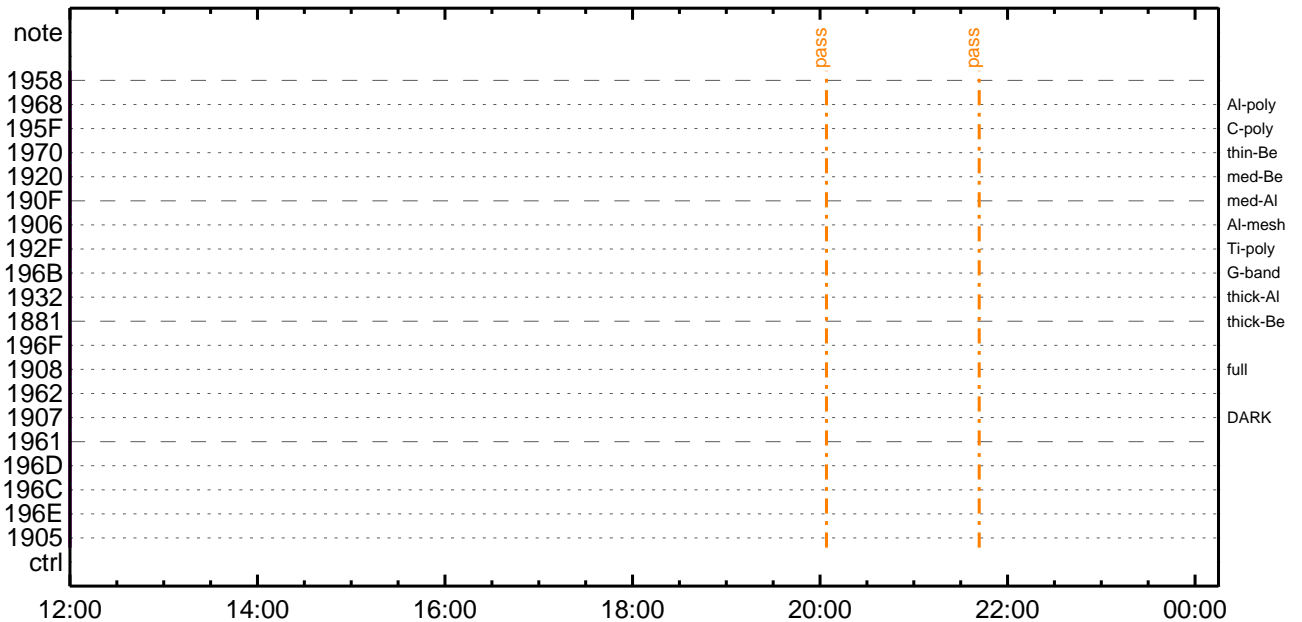
CMDI #0310 2013/03/11



CMDI #0310 2013/03/12



CMDI #0310 2013/03/12




```
0096 C.          01-03-07 09:22:00.0
0097 C.
0098 . C. TI 2013-03-07 09:22:00.0
0099 +. TI 2013-03-07 09:22:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2013-03-07 09:22:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2013-03-07 09:22:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2013-03-07 09:26:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.          *****
0116 C.          [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.          [HK1_TI_CMD_NUM] EQ 4
0118 C.          [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.          [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 . C. *****
0122 C. TI 2013-03-07 09:26:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.          [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.          [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.          [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.          [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          [HK1_PKT_FORM_NO] EQ 7
0136 C.          [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.          [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.          [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.          [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 . C. *****
0142 C.          [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 . C. RAM ID=TI_TBL(0x03AB00-0x03AEFF)
0145 C.
0146 . C. DHU 2013-03-07 09:26:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          [HK1_PKT_FORM_NO] EQ 2
0150 C.          [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.          [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.          [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2013-03-07 09:26:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2013-03-07 09:26:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2013-03-07 09:26:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2013-03-07 09:26:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 2013-03-07 09:26:00.0 *****
0192 C. (***** MDP 2013-03-07 09:26:00.0 *****)
0193 . S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 8s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-03-07 09:26:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîîî»ö¼ÝñÉÊĐñ¹ñèDCBC•x²è *****
0153 C. (%ã°îÝÓÝÁÝÉÝÞÝÈÝÁÝçÝèñ¼ñ¼Á»Üñ¹ñé)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÝĐÝ¹•İ Daily±çİÑñÉ´Øñ¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÝÁÝ§ÝÄÝ¹¼Á»Ü;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-507 2013-03-07 13:17:36 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁò•µ°È»Í×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÀ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-265:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-03-07 09:26:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 c0 c0 10 10)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 40 c0 10 10)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 40 40 10 10)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b c0 40 10 10)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 85 83 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d 80 80 20 08)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0e 80 80 08 20)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 02)
0108 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0109 BC (c5 10)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 +. DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 +. TI 2013-03-07 09:26:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C.
0124 . C. ***** MDP 'ûÃîñî»ö%ÿñÊÂñ¹ñèDCBC•x²è *****
0125 C. (%â°îÿÓÿÃÿÈÿPÿËÿãÿçÿèñE%¼ññ¼Ã»Ûñ¹ñè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹•î Daily±;îÑñÊ'Øñ¹ñèDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿËÿ-¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
```

Mar 07, 13 13:17

XRT_OGLIST_0310.chk

Page 1/3

*** OP Sequence for XRT ***

2013/03/07	09:37:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	53	4b	01	58
2013/03/07	15:37:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04	00	00	00	00
2013/03/07	18:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	00	00	00	00
2013/03/07	22:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/03/08	16:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/08	16:50:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_448_OG [0x1c0]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2013/03/08	23:24:54.0	XRT_CTRL_MANU_409_OG [0x199]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/08	23:25:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2013/03/08	23:27:32.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/03/08	23:27:52.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 01
2013/03/08	23:27:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/03/08	23:27:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/03/08	23:27:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/08	23:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/03/08	23:34:54.0	XRT_CTRL_MANU_409_OG [0x199]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/08	23:35:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2013/03/08	23:37:32.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/03/08	23:37:52.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0e
2013/03/08	23:37:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/03/08	23:37:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/03/08	23:37:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/08	23:38:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/03/08	23:44:54.0	XRT_CTRL_MANU_409_OG [0x199]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/08	23:45:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2013/03/08	23:47:32.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/03/08	23:47:52.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 06
2013/03/08	23:47:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/03/08	23:47:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/03/08	23:47:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/08	23:48:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/03/08	23:54:54.0	XRT_CTRL_MANU_409_OG [0x199]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/08	23:55:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2013/03/08	23:57:32.0	XRT_FOCUS_POSITION_436_OG [0x1b4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/03/08	23:57:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 08
2013/03/08	23:57:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/03/08	23:57:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/03/08	23:57:58.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/08	23:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/03/09	00:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/09	00:04:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2013/03/09	00:05:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/03/09	00:05:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/03/09	00:05:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/03/09	00:05:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2013/03/09	00:05:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/09	00:07:54.0	XRT_FLD_RESET_424_OG [0x1a8]							

Mar 07, 13 13:17

XRT_OGLIST_0310.chk

Page 2/3

2013/03/09	00:07:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2013/03/09	00:07:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2013/03/09	00:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	00:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	00:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	00:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	00:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	00:52:30.0	XRT_Custom_434_OG [0x1b2]						
2013/03/09	00:53:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	02:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	02:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	02:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	02:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	02:29:00.0	XRT_Custom_434_OG [0x1b2]						
2013/03/09	02:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	03:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	03:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	03:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	04:06:00.0	XRT_Custom_434_OG [0x1b2]						
2013/03/09	04:07:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	05:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	05:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	05:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	05:43:30.0	XRT_Custom_434_OG [0x1b2]						
2013/03/09	05:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2013/03/09	06:00:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	00 00 00 00	
2013/03/09	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/03/09	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/03/09	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/09	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2013/03/09	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/03/09	06:10:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00 00 00 00	
2013/03/09	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/03/09	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/03/09	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/03/09	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/09	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	06:12:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2013/03/09	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10	
2013/03/09	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	06:57:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	06:57:02.5	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		

Mar 07, 13 13:17

XRT_OGLIST_0310.chk

Page 3/3

2013/03/09	06:57:04.5	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/09	07:00:14.5	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/09	07:21:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/09	07:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/09	08:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	08:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	08:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/09	08:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/09	10:51:00.0	AOCS_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 00 00 00 00				