

XRT Timeline to be uploaded on 2013/03/09

Period: 2013/03/09 10:41:00 - 2013/03/14 11:01:00

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Normal mode

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XOB #1971: CH Boundary - Al/mesh 8s +Ti/Poly 16s - 384x384,1x1 60s cad													
Term		Pointing (x, y)					Comment						
03/09 10:54:00 - 03/09 13:59:54		Track (-106.0, -620.9) ^{© 03/09 10:51:00}					# OP start + 10min, Coronal Hole SP Map.						
PROG= 11 Inf.-time(s)													
└─ Subr= 1 1-time(s) 120.0sec													
└─ Seqn= 9 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1892: CME watch - 4x4 - AEC 2 - Ti/poly Al/Mesh													
Term		Pointing (x, y)					Comment						
03/09 14:03:00 - 03/09 17:57:24		Fixed (935.0, 0.0)					# EIS Off-Limb Spectral Atlas.						
PROG= 20 Inf.-time(s)													
└─ Subr= 1 1-time(s) 120.0sec													
└─ Seqn= 10 1-time(s) 4.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250/2048)												
Term		Pointing (x, y)					Comment					
03/09 18:00:30 - 03/09 18:07:24		Fixed (0.0, 0.0)					synoptic, shifted -2.5 min					
03/10 06:33:30 - 03/10 06:40:24		Fixed (0.0, 0.0)					synoptic, shifted 30.5 min					
03/10 18:20:00 - 03/10 18:26:54		Fixed (0.0, 0.0)					synoptic, shifted 17.0 min					
03/11 06:05:30 - 03/11 06:12:24		Fixed (0.0, 0.0)					synoptic, shifted 2.5 min					
03/11 18:03:00 - 03/11 18:09:54		Fixed (0.0, 0.0)					synoptic					
03/12 06:13:30 - 03/12 06:16:24		Fixed (0.0, 0.0)					synoptic, shifted 6.5 min					

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250/2048)													
PROG= 12 1-time(s)													
└─ Subr= 1 1-time(s) 14.0sec													
└─ Seqn= 96 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 41 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 30 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 69 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2													
Term		Pointing (x, y)					Comment						
03/09 18:22:30 - 03/09 20:07:24		Fixed (0.0, 945.0)					# Instrument Coalignment at North Pole						
PROG= 10 1-time(s)													
└─ Subr= 1 1-time(s) 360.0sec													
└─ Seqn= 16 24-time(s) 300.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2													
Term		Pointing (x, y)					Comment						
03/09 20:22:30 - 03/09 22:07:24		Fixed (-945.0, 0.0)					# Instrument Coalignment at East Limb						
PROG= 13 1-time(s)													
└─ Subr= 1 1-time(s) 360.0sec													
└─ Seqn= 17 15-time(s) 480.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1961: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be + multifilter context, 512x512 at 1064 1048, 60s-cad, shorter G-band (33ms) w/ G-Band

Term	Pointing (x, y)		Comment							
03/09 22:31:00 - 03/10 06:30:24	Track (-523.3, 282.9) @ 03/09 22:07:30		# Observe emerging AR in North East.							
PROG= 05 Inf.-time(s)										
Subr= 1 1-time(s) 2.0sec										
Seqn= 24 1-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0 0 2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0 0 2.0sec
Seqn= 61 1-time(s) 2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0 0 2.0sec
Seqn= 27 4-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
Subr= 2 1-time(s) 2.0sec										
Seqn= 99 70-time(s) 2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1 15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2 15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 3 15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #196D: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (32ms) w/ G-Band VLS C

Term	Pointing (x, y)		Comment							
03/10 06:43:30 - 03/10 09:14:30	Track (-456.1, 288.0) @ 03/10 06:40:30		# Emerging AR cont.							
03/10 15:32:30 - 03/10 17:53:30	Track (-383.5, 292.4) @ 03/10 15:30:00		# Emerging AR cont.							
03/10 18:30:00 - 03/11 06:02:24	Track (-358.5, 293.8) @ 03/10 18:27:00		# Emerging AR cont.							
03/11 06:15:30 - 03/11 17:59:54	Track (-256.5, 298.1) @ 03/11 06:12:30		# Emerging AR cont.							
03/11 18:13:00 - 03/12 06:06:24	Track (-149.3, 300.9) @ 03/11 18:10:00		# Emerging AR cont.							
03/12 06:19:30 - 03/12 11:32:54	Track (-38.8, 302.2) @ 03/12 06:16:30		# Emerging AR cont.							
PROG= 04 Inf.-time(s)										
Subr= 1 1-time(s) 2.0sec										
Seqn=100 1-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0 2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0 2.0sec
Seqn= 19 1-time(s) 2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0 2.0sec
Seqn= 65 4-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
Subr= 2 1-time(s) 2.0sec										
Seqn= 94 30-time(s) 120.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1 15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2 15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 3 15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms

Term	Pointing (x, y)		Comment							
03/10 09:34:00 - 03/10 15:29:24	Fixed (-15.0, -937.0)		# HOP-206 South Pole Panorama							
PROG= 09 Inf.-time(s)										
Subr= 1 1-time(s) 2.0sec										
Seqn= 61 2-time(s) 2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0 0 2.0sec
Subr= 2 1-time(s) 2.0sec										
Seqn= 62 1-time(s) 30.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
Subr= 3 30-time(s) 2.0sec										
Seqn= 60 1-time(s) 60.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
03/09 10:54:00 - 03/09 13:59:54	Track (-106.0, -620.9) ^{Ⓜ 03/09 10:51:00}	# OP start + 10min, Coronal Hole SP Map.
03/09 22:31:00 - 03/10 06:30:24	Track (-523.3, 282.9) ^{Ⓜ 03/09 22:07:30}	# Observe emerging AR in North East.
03/10 06:43:30 - 03/10 09:14:30	Track (-456.1, 288.0) ^{Ⓜ 03/10 06:40:30}	# Emerging AR cont.
03/10 09:34:00 - 03/10 15:29:24	Fixed (-15.0, -937.0)	# HOP-206 South Pole Panorama
03/10 15:32:30 - 03/10 17:53:30	Track (-383.5, 292.4) ^{Ⓜ 03/10 15:30:00}	# Emerging AR cont.
03/10 18:30:00 - 03/11 06:02:24	Track (-358.5, 293.8) ^{Ⓜ 03/10 18:27:00}	# Emerging AR cont.
03/11 06:15:30 - 03/11 17:59:54	Track (-256.5, 298.1) ^{Ⓜ 03/11 06:12:30}	# Emerging AR cont.
03/11 18:13:00 - 03/12 06:06:24	Track (-149.3, 300.9) ^{Ⓜ 03/11 18:10:00}	# Emerging AR cont.
03/12 06:19:30 - 03/12 11:32:54	Track (-38.8, 302.2) ^{Ⓜ 03/12 06:16:30}	# Emerging AR cont.

PROG= 16 15-time(s)

Subr=	1	45-time(s)	10.0sec										
Seqn=	35	1-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr=	2	1-time(s)	10.0sec										
Seqn=	36	1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn=	37	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=	38	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18E7: Flare obs. dynamics (DO NOT USE) - Ti_poly high cadence + context (thick-Al-384x384)-15 loops

Term	Pointing (x, y)	Comment
03/09 14:03:00 - 03/09 17:57:24	Fixed (935.0, 0.0)	# EIS Off-Limb Spectral Atlas.

PROG= 19 15-time(s)

Subr=	1	45-time(s)	10.0sec										
Seqn=	53	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Subr=	2	1-time(s)	10.0sec										
Seqn=	52	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Seqn=	55	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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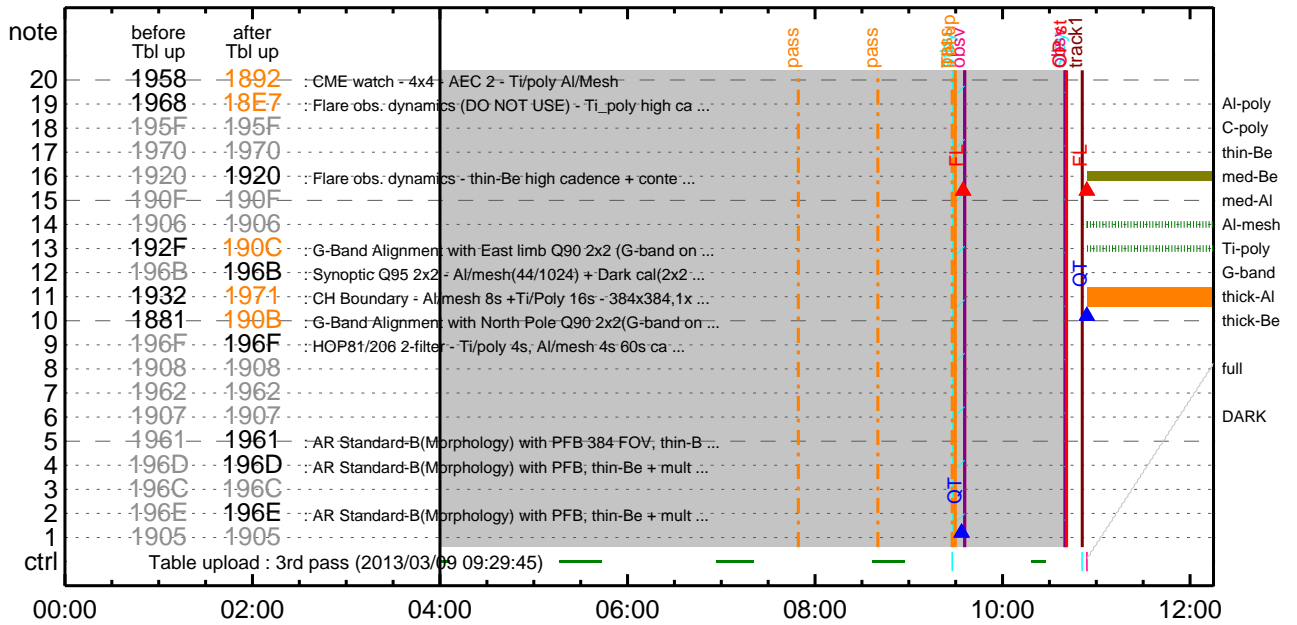
Flare Detection

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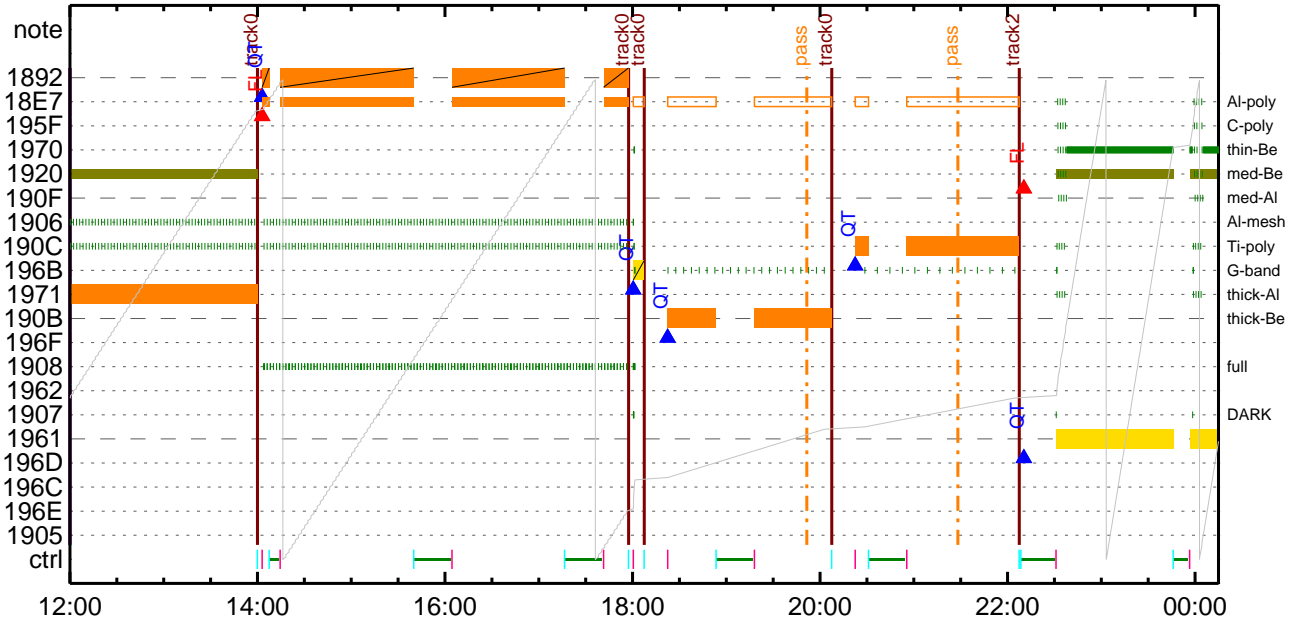
FLD Patrol

Term	Pointing (x, y)	Comment										
03/09 22:07:46 - 03/10 06:30:46	Track (-523.3, 282.9) ^{Ⓜ 03/09 22:07:30}	# Observe emerging AR in North East.										
03/10 06:40:46 - 03/10 18:17:16	Track (-456.1, 288.0) ^{Ⓜ 03/10 06:40:30}	# Emerging AR cont.										
03/10 18:27:16 - 03/11 06:02:46	Track (-358.5, 293.8) ^{Ⓜ 03/10 18:27:00}	# Emerging AR cont.										
03/11 06:12:46 - 03/11 18:00:16	Track (-256.5, 298.1) ^{Ⓜ 03/11 06:12:30}	# Emerging AR cont.										
03/11 18:10:16 - 03/12 06:10:46	Track (-149.3, 300.9) ^{Ⓜ 03/11 18:10:00}	# Emerging AR cont.										
03/12 06:16:46 - 03/14 11:01:00	Track (-38.8, 302.2) ^{Ⓜ 03/12 06:16:30}	# Emerging AR cont.										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

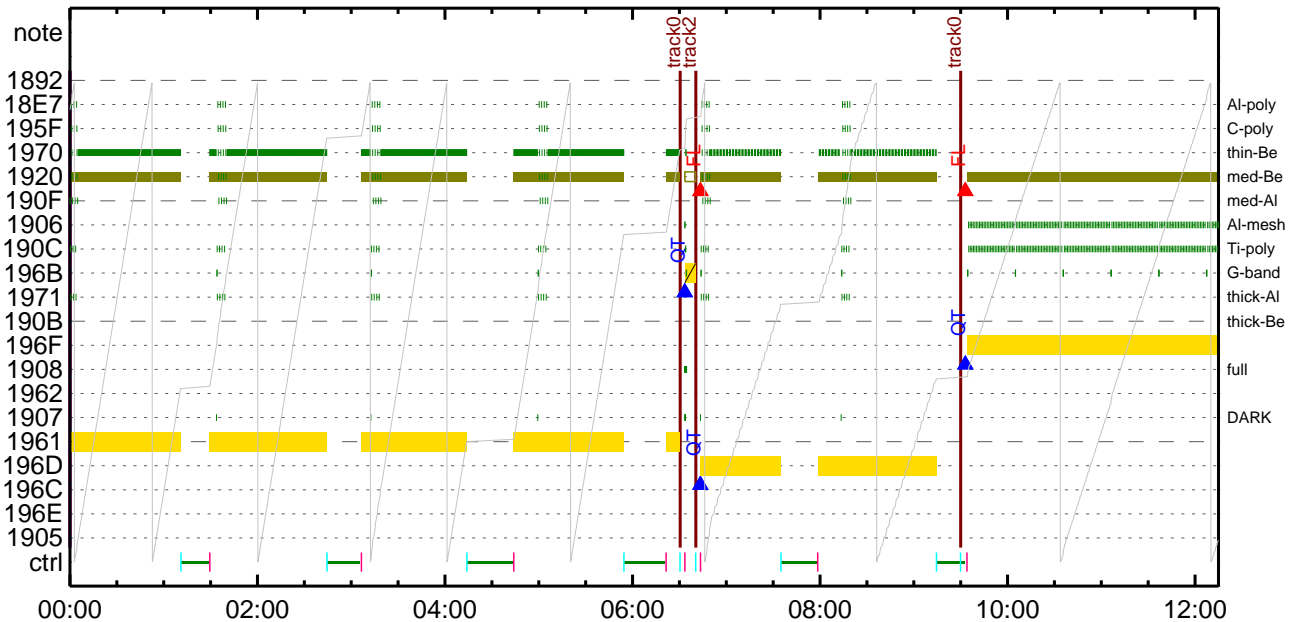
CMDI #0317 2013/03/09



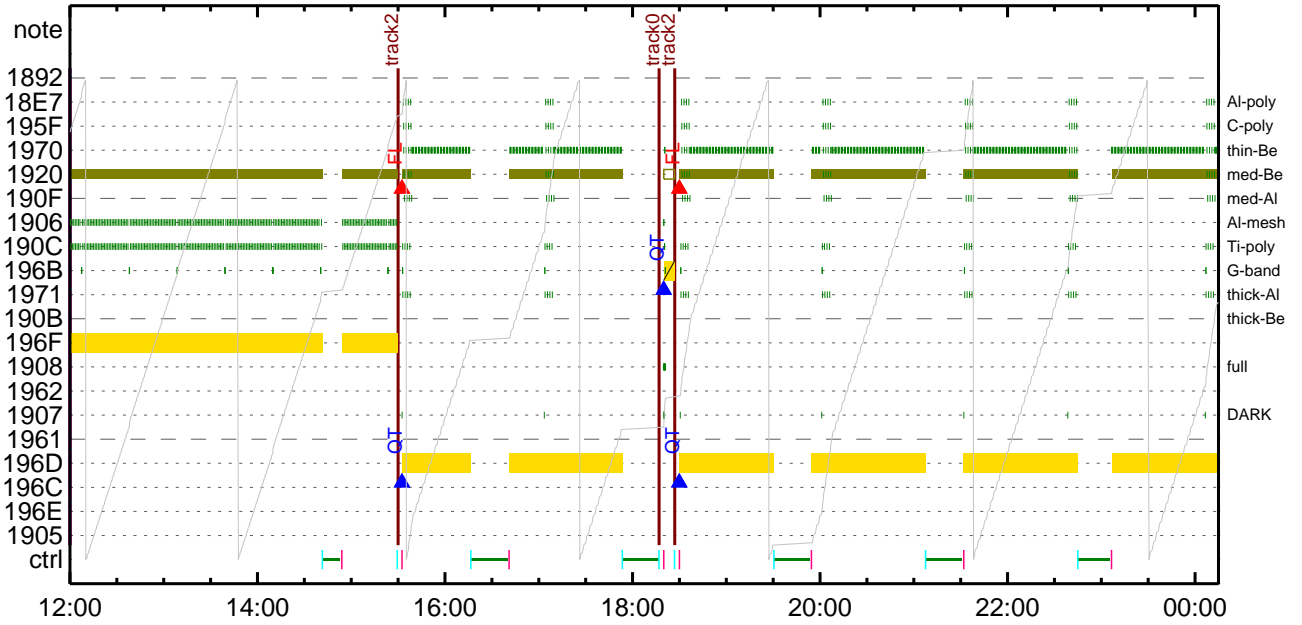
CMDI #0317 2013/03/09



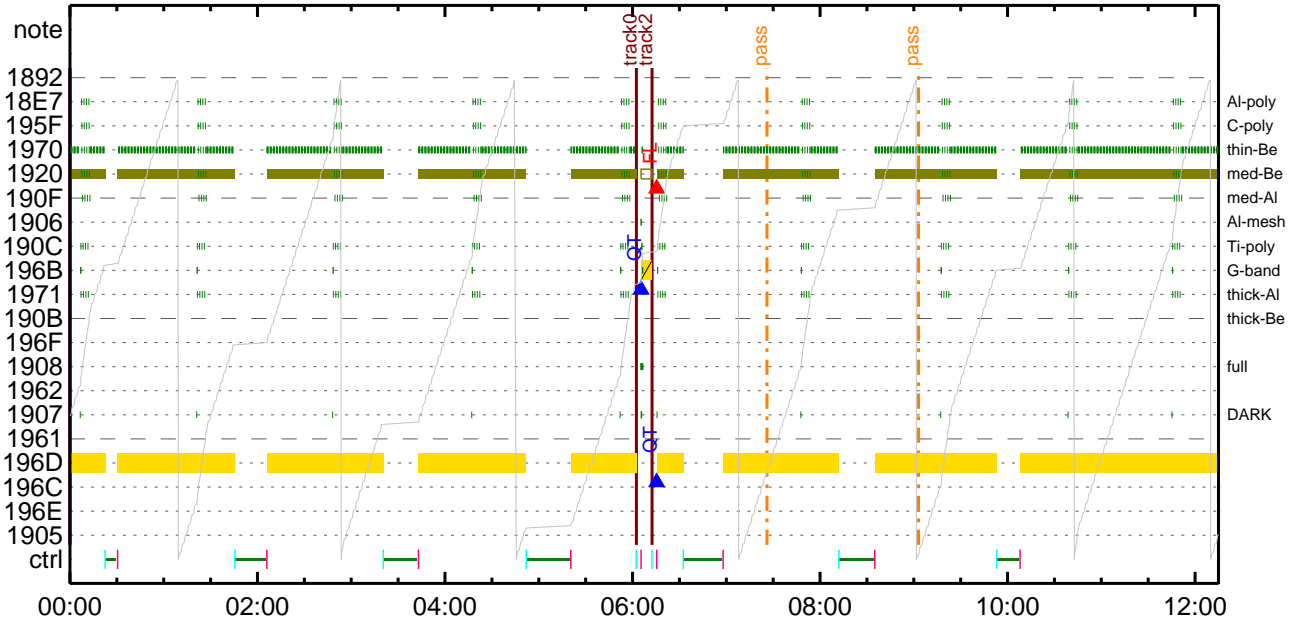
CMDI #0317 2013/03/10



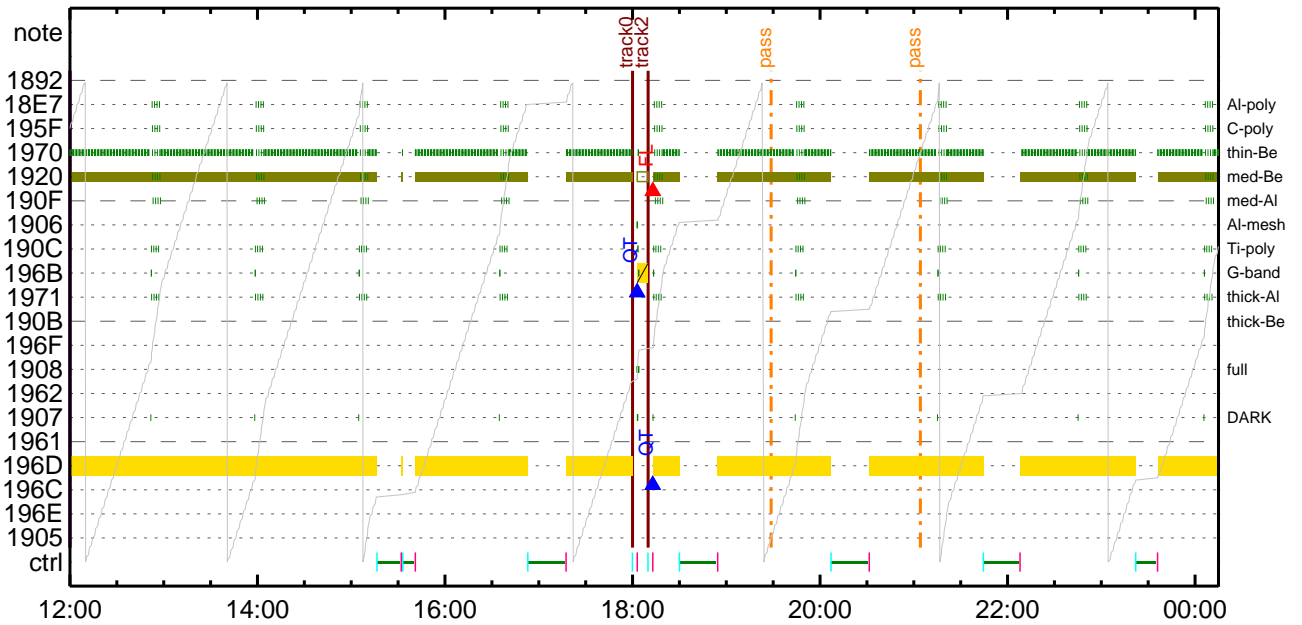
CMDI #0317 2013/03/10



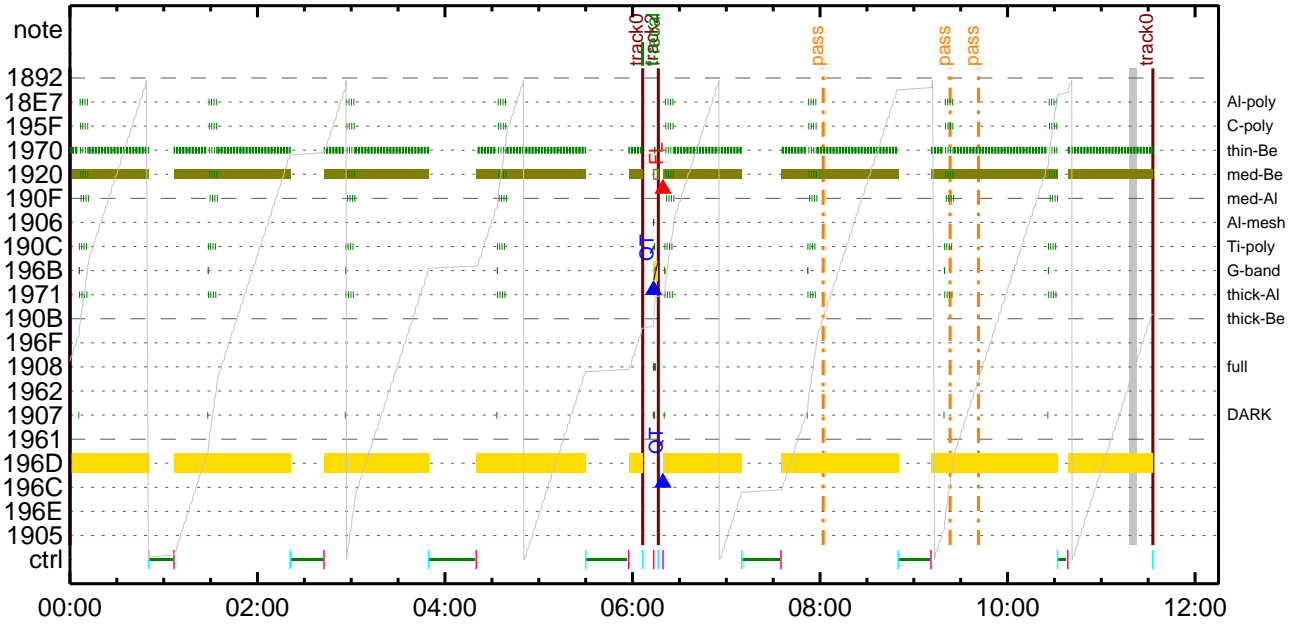
CMDI #0317 2013/03/11



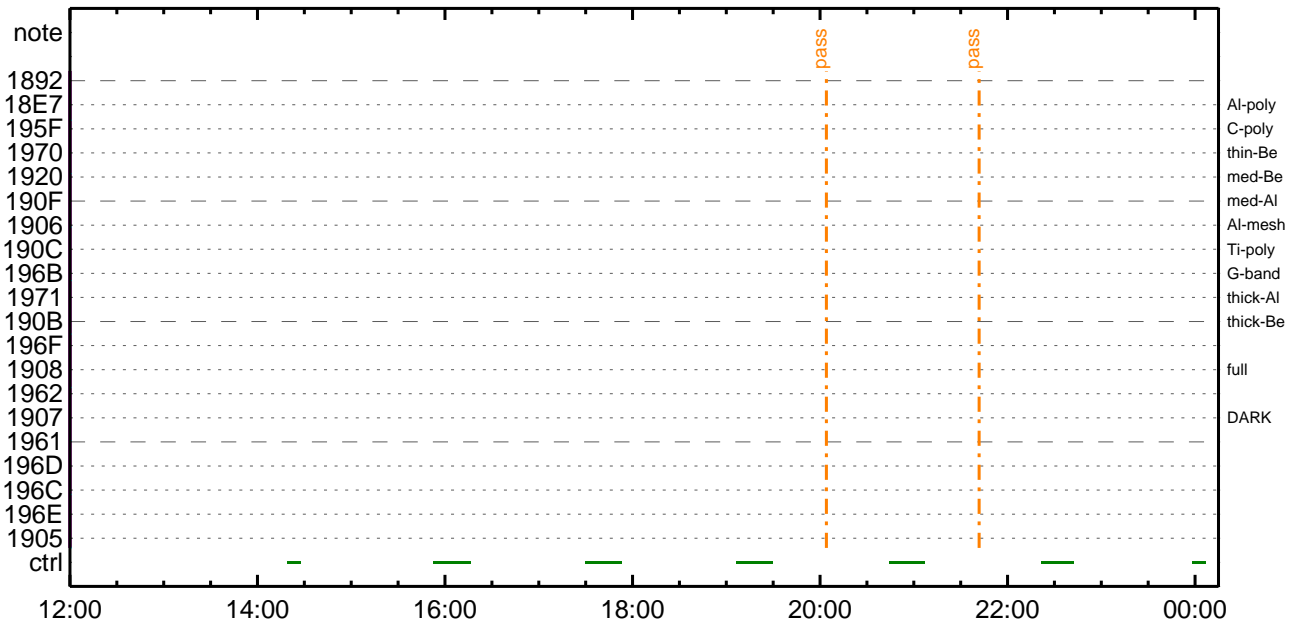
CMDI #0317 2013/03/11



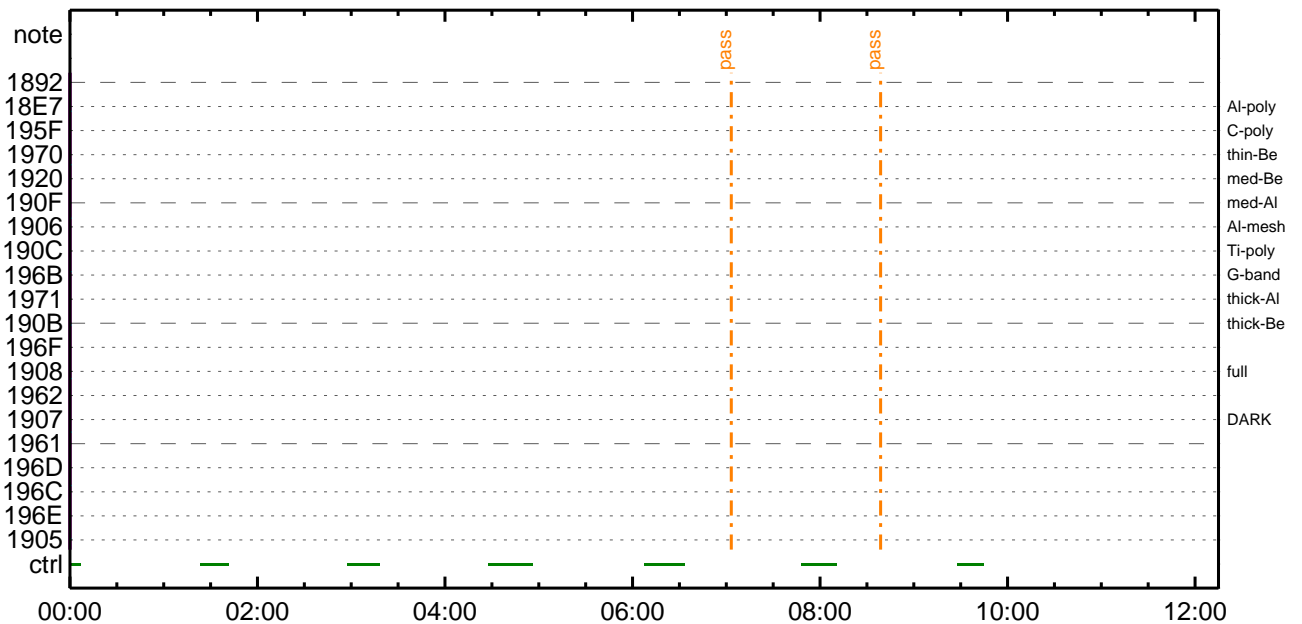
CMDI #0317 2013/03/12



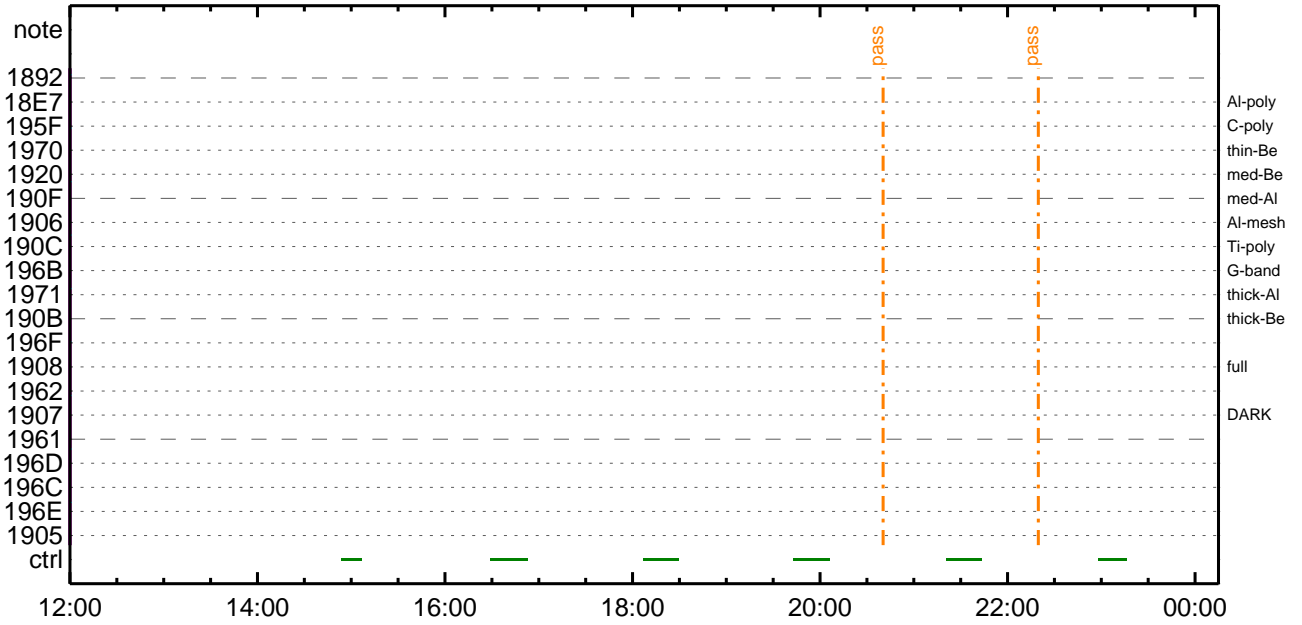
CMDI #0317 2013/03/12



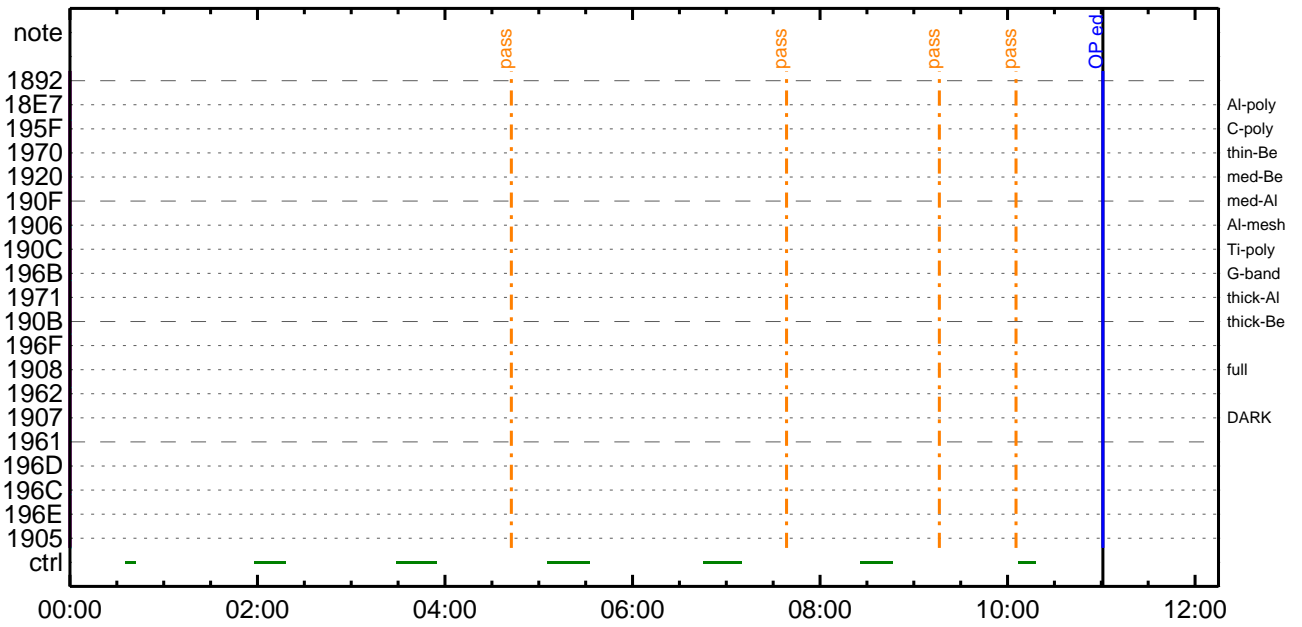
CMDI #0317 2013/03/13



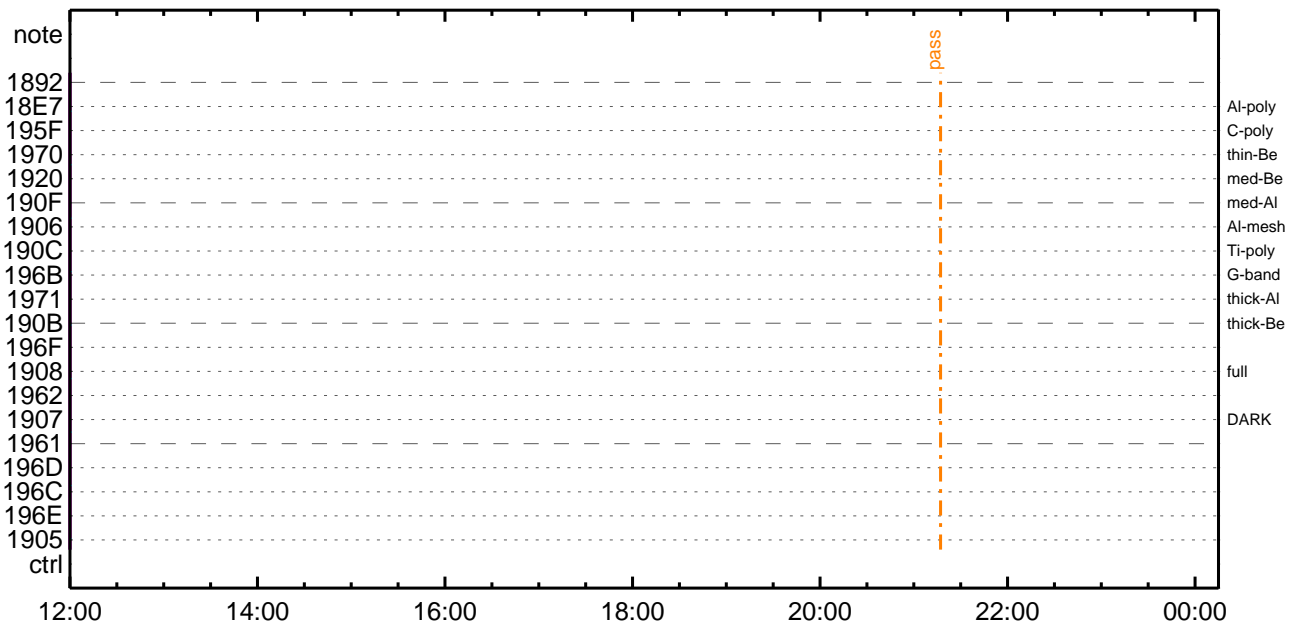
CMDI #0317 2013/03/13



CMDI #0317 2013/03/14



CMDI #0317 2013/03/14



(a) Spacecraft Operation Procedure (real-commands)

```

main-509 2013-03-09 13:38:55 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYAYSYAY^¼A»Û;ã
0005 C.
0006 C. YAYB;¼Y³YBYOYEA+z®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Aí;ËzãAã•µ°E»Í×ÁÇóÍYÇYAY×YÍ;¼YÉ;ËÈè½µ•íÍË;ËøE¼°ÇÖã•¾l¹ÇãÍ; ÇÀ®. ùã¹ãøãPãÇÁ+z®ã•øÈãã³ãÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+z®µ;ON
0016 C. *****
0017 C. Ç° °EÀ. Í×ËÝããLOSãPãÇãÍ»P´Öãð¹íí. ñ. ; ÇËÓÍ×ãËXÁÓONãÍ¹ÖãÈã¹ãÈãã³ãÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYAY^¾OÁÖã-°ÁÁèã•¾;æ; Ç°È²¼ãÍ°EÀ. ¼E½Çãð½Á¹Öã¹ãÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 AÍ¼i°EÀ.
0033 C. *****
0034 C. Ç° °EÀ. Í×ËÝããLOSãPãÇãÍ»P´Öãð¹íí. ñ. ; ÇËÓÍ×ãËXÁÓONãÍ¹ÖãÈã¹ãÈãã³ãÈ; f
0035 C.
0036 . C. ;ãPT1°EÀ. ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö. ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYAYEAYÁUÁO;ÊÁ•Á°²óÈð;Ë. ããí°EÀ. °E³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö. ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÀ. ñ-¼«E°Áã»ßã•¾;ã; Ç°È²¼ãð½Á¹Öã¹ãÈ; f
0055 C. YÇYÓYAYEAYÁUÁOãÁ•Á°²óÈðã-¶ãã¾l¹ÇãÍ°EÀ. ñã¹ãøãPãÇÁÖã; f
0056 C.
0057 . C. *****
0058 C. DR PT2 AÍ¼i°EÀ.
0059 C. *****
0060 C. Ç° °EÀ. Í×ËÝããLOSãPãÇãÍ»P´Öãð¹íí. ñ. ; ÇËÓÍ×ãËXÁÓONãÍ¹ÖãÈã¹ãÈãã³ãÈ; f
0061 C.
0062 . C. ;ãPT2°EÀ. ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö. ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYAYEAYÁUÁO;ÊÁ•Á°²óÈð;Ë. ããí°EÀ. °E³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö. ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÀ. Áã»ß; ÇXÁ+z®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÀ. Áã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+z®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF

```



```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-509:OP
0104 ( )
0105 S. OG og-509:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²ò³òE;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2013-03-09 10:36:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-03-09 10:36:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-03-09 10:36:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-510 2013-03-09 13:38:55 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁµ°Æ»Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÔµ•µ¿¼l¹çµÍ;çÀ®, ùµ¹µèµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 39s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2013-03-09 10:40:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁîîî»ó%YµÈÁµ¹µèDCBC•x²è *****
0069 C. (%á°íYÓYÁYÈYÞYÉYáYçYèµÈ%¼µ¼Á»Ûµ¹µè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÝ¹.İ Daily±¿ÍÑµÈ`Øµ¹µèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁYŞYÁY~¼Á»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 60 20 18)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c a0 80 18 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 85 83 08 08)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0f 80 80 06 06)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 10 80 80 08 08)
0146 + DC 07-F0 MDP_XRT_FLD_ENA
0147 BC (d8)
0148 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0149 BC (c8)
0150 + DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_FLD_RESET
0155 BC (da)
0156 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0157 BC (c4 02)
0158 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0159 BC (c5 10)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C.
0163 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0164 C.
0165 +. DC 07-F0 MDP_XRT_MODE_OBSV
0166 BC (c2)
0167 +. TI 2013-03-09 10:40:02.0
0168 DC 07-F0 MDP_XRT_MODE_OBSV
0169 BC (c2)
0170 . C. ----- Success Verify ? OK / NG ____
0171 C.
0172 C. ***** XRT END *****
0173 . C. *****
0174 C. SOT table upload
0175 C. *****
0176 . C. < Stop FG table >
0177 +. DC 07-F0 MDP_FG_CTRL_MANU
0178 BC (51)
0179 . C. -----
0180 C. MDP_FG_CTRL_MODE = MANU [ ]
0181 C. -----
0182 C.
0183 . C. <Upload FG Observation Table>
0184 . S. RAM ram-266:MDP_OBS_F
0185 ( )
0186 C.
0187 . C. < Dump RAMID=MDP_OBS_F >
0188 +. DC 07-F0 MDP_DUMP_FGTBL
0189 BC (82 07 00 00 00 38 b8)
0190 C. -----
0191 C. MDP_OBS_F verify = OK/NG [ ]
0192 C. -----
0193 C.

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```
0194 . C. < Stop SP table >
0195 +. DC 07-F0 MDP_SP_CTRL_MANU
0196 BC (61)
0197 C. -----
0198 C. MDP_SP_CTRL_MODE = MANU [ ]
0199 C. -----
0200 C.
0201 . C. <Upload SP Observation Table>
0202 . S. RAM ram-288:MDP_OBS_S
0203 ( )
0204 C.
0205 . C. < Dump RAMID=MDP_OBS_S >
0206 +. DC 07-F0 MDP_DUMP_SPTBL
0207 BC (83 07 00 00 00 38 b8)
0208 C. -----
0209 C. MDP_OBS_S verify = OK/NG [ ]
0210 C. -----
0211 C.
0212 C. *****
0213 C. SOT TI command set
0214 C. *****
0215 C. Execute, after the success of TBL upload.
0216 +. TI 2013-03-09 10:40:18.0
0217 DC 07-F0 MDP_SOT_MODE_OBSV
0218 BC (40)
0219 . C. -----
0220 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0221 C. -----
0222 C.
0223 C.
0224 . C. ***** MDP `uãîñî»ö%ýñëãðñ¹ñèDCBC•x²è *****
0225 C. (%á°îÿóÿãÿèÿþÿÿÿáÿçÿèñè%¼ñ¼ã»Ûñ¹ñè)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** ÿDÿ¹•Ï Daily±¿îññè´øñ¹ñèDCBC•x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ¡ãLOSÿÁÿ$ÿÃÿ-¼ã»Û;ã
0236 C.
0237 . C. ***** LOS *****
0238 C.
```

Mar 09, 13 13:39

XRT_OGLIST_0317.chk

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*** OP Sequence for XRT ***

2013/03/09	10:50:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	10:50:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/03/09	10:51:00.0	AOCS_Orе-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2013/03/09	10:51:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/03/09	10:51:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/03/09	10:51:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/03/09	10:51:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/09	10:53:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	10:53:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2013/03/09	10:53:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/03/09	10:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	13:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	14:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 ac e6		
2013/03/09	14:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/03/09	14:02:46.0	XRT_FLD_ENA_418_OG [0x1a2]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/03/09	14:02:48.0	XRT_FLRCTRL_ENA_401_OG [0x191]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/03/09	14:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/03/09	14:02:52.0	XRT_ARS_DIS_412_OG [0x19c]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/09	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	14:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2013/03/09	14:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/03/09	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	14:07:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	14:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	14:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	14:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	14:13:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/09	14:14:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	15:40:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	15:40:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	15:40:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	15:43:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	16:03:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/09	16:04:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	17:16:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	17:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/09	17:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/09	17:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/09	17:40:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/09	17:41:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/09	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/09	17:57:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/03/09	17:57:30.0	AOCS_Orе-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/03/09	17:57:46.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/03/09	17:57:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/03/09	17:57:50.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/09	18:00:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		

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2013/03/09	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/09	18:07:24.0	XRT_CTRL_MANU_416_OG [0x1a0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	18:07:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2013/03/09	18:21:58.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/09	18:22:18.0	XRT_FLD_DIS_439_OG [0x1b7]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/03/09	18:22:20.0	XRT_FLRCTRL_DIS_440_OG [0x1b8]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/03/09	18:22:22.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/09	18:22:24.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/09	18:22:26.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	18:22:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2013/03/09	18:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/09	18:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	18:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	18:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/09	18:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/09	19:17:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/09	19:18:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/09	20:07:24.0	XRT_CTRL_MANU_416_OG [0x1a0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	20:07:30.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2013/03/09	20:21:58.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/09	20:22:18.0	XRT_FLD_DIS_439_OG [0x1b7]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/03/09	20:22:20.0	XRT_FLRCTRL_DIS_440_OG [0x1b8]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/03/09	20:22:22.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/09	20:22:24.0	XRT_ARS_DIS_412_OG [0x19c]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/09	20:22:26.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	20:22:28.0	XRT_QT_PROG_SET_419_OG [0x1a3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/03/09	20:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/09	20:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	20:31:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	20:31:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/09	20:34:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/09	20:54:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/09	20:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/09	22:07:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	22:07:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/09	22:07:30.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/03/09	22:07:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/09	22:07:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/09	22:07:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/09	22:07:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/09	22:08:31.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/09	22:08:33.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	22:08:35.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/09	22:10:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/09	22:10:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2013/03/09	22:10:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/09	22:11:45.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/09	22:30:00.0	XRT_Custom_434_OG [0x1b2]							

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2013/03/09	22:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/09	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/09	23:46:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/09	23:46:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/09	23:49:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/09	23:55:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/09	23:56:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	01:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	01:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	01:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	01:28:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/10	01:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	02:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	02:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	02:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	02:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	03:05:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/10	03:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	04:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	04:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	04:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	04:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	04:43:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/10	04:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	05:54:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	05:54:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	05:57:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	06:20:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/10	06:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	06:30:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	06:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/10	06:30:30.0	AOCS_Orе-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/03/10	06:30:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/03/10	06:30:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/03/10	06:30:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/10	06:33:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/03/10	06:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	06:40:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	06:40:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/10	06:40:30.0	AOCS_Orе-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/03/10	06:40:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/10	06:40:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/10	06:40:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/10	06:40:52.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/10	06:43:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	06:43:26.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/03/10	06:43:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10

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2013/03/10	06:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/10	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	07:35:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	07:35:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/10	07:38:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/10	07:57:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/10	07:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/10	09:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	09:14:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	09:14:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/10	09:17:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/10	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	09:29:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/10	09:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 53 4b 01 58				
2013/03/10	09:30:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/10	09:30:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/10	09:30:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/10	09:30:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/10	09:32:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	09:32:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2013/03/10	09:32:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/10	09:33:00.5	XRT_Custom_434_OG [0x1b2]							
2013/03/10	09:34:00.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/10	14:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	14:41:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	14:41:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/10	14:44:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/10	14:53:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/10	14:54:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/10	15:29:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	15:29:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/10	15:29:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/10	15:29:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/10	15:29:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/10	15:29:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/10	15:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/03/10	15:32:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	15:32:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2013/03/10	15:32:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/10	15:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/10	16:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	16:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	16:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/10	16:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/10	16:40:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/10	16:41:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/10	17:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/10	17:53:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/10	17:53:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							

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2013/03/10	17:56:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	18:16:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/10	18:17:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2013/03/10	18:17:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/03/10	18:17:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/03/10	18:17:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/10	18:19:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/03/10	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	18:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	18:26:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/10	18:27:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	02 00 00 00 00
2013/03/10	18:27:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/10	18:27:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/10	18:27:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/10	18:27:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/10	18:29:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	18:29:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/03/10	18:29:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/10	18:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	19:30:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	19:30:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	19:33:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	19:53:30.0	XRT_Custom_434_OG [0x1b2]	XRT_CUSTOM_434_OG [0x1b2]			
2013/03/10	19:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	21:07:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	21:07:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	21:10:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	21:31:00.0	XRT_Custom_434_OG [0x1b2]	XRT_CUSTOM_434_OG [0x1b2]			
2013/03/10	21:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/10	22:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/10	22:45:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/10	22:45:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/10	22:48:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/10	23:05:30.0	XRT_Custom_434_OG [0x1b2]	XRT_CUSTOM_434_OG [0x1b2]			
2013/03/10	23:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	00:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	00:22:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	00:22:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	00:25:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	00:29:30.0	XRT_Custom_434_OG [0x1b2]	XRT_CUSTOM_434_OG [0x1b2]			
2013/03/11	00:30:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	01:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	01:45:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	01:45:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	01:48:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

2013/03/11	02:05:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/11	02:06:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	03:20:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	03:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/03/11	03:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/03/11	03:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/03/11	03:42:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/11	03:43:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	04:52:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	04:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/03/11	04:52:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/03/11	04:55:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/03/11	05:19:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/11	05:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	06:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2013/03/11	06:02:30.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2013/03/11	06:02:46.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2013/03/11	06:02:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2013/03/11	06:02:50.0	XRT_ARS_DIS_406_OG [0x196]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/03/11	06:05:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c					
2013/03/11	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	06:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2013/03/11	06:12:30.0	AOCS_OrE-point_Start_6_OG [0x09c]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2013/03/11	06:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/03/11	06:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/03/11	06:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/03/11	06:12:52.0	XRT_ARS_DIS_437_OG [0x1b5]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/03/11	06:15:24.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/03/11	06:15:26.0	XRT_QT_PROG_SET_446_OG [0x1be]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04					
2013/03/11	06:15:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10					
2013/03/11	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	06:32:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	06:32:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/03/11	06:32:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/03/11	06:35:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/03/11	06:57:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/11	06:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	08:12:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	08:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/03/11	08:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/03/11	08:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/03/11	08:34:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/11	08:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/03/11	09:53:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/03/11	09:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/03/11	09:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/03/11	09:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					

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2013/03/11	10:07:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	10:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	15:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	15:16:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	15:16:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	15:19:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	15:31:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	15:32:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	15:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	15:33:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	15:33:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	15:36:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	15:40:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	15:41:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	16:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	16:53:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	16:53:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	16:56:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	17:16:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	17:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/11	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/03/11	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/03/11	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/03/11	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/11	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/03/11	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	18:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/11	18:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/03/11	18:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/11	18:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/11	18:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/11	18:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/11	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	18:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/03/11	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/11	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	18:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	18:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	18:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	18:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	18:53:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	18:54:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	20:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	20:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	20:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	20:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

2013/03/11	20:30:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	20:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	21:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	21:44:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	21:44:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	21:47:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	22:07:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	22:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/11	23:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/11	23:22:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/11	23:22:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/11	23:25:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/11	23:35:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/11	23:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	00:50:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	00:50:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	00:50:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	00:53:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	01:05:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/12	01:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	02:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	02:21:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	02:21:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	02:24:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	02:41:30.5	XRT_Custom_434_OG [0x1b2]			
2013/03/12	02:42:30.5	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	03:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	03:49:32.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	03:49:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	03:52:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	04:19:01.0	XRT_Custom_434_OG [0x1b2]			
2013/03/12	04:20:01.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	05:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	05:30:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	05:30:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	05:33:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	05:56:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/12	05:57:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	06:06:26.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2013/03/12	06:06:30.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/03/12	06:10:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/12	06:10:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/03/12	06:10:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/03/12	06:10:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/12	06:13:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/03/12	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	06:16:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/12	06:16:30.0	AOCS_OrE-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	02 00 00 00 00

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2013/03/12	06:16:46.0	XRT_FLD_ENA_428_OG [0x1ac]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/12	06:16:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/12	06:16:50.0	XRT_AEC_RESET_423_OG [0x1a7]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/12	06:16:52.0	XRT_ARS_DIS_437_OG [0x1b5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/12	06:19:24.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	06:19:26.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/03/12	06:19:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/12	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	07:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	07:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	07:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	07:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	07:34:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/12	07:35:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	08:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	08:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	08:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	08:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	09:10:00.0	XRT_Custom_434_OG [0x1b2]			
2013/03/12	09:11:00.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	10:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	10:32:02.0	XRT_FLD_RESET_424_OG [0x1a8]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/12	10:32:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/12	10:35:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/12	10:37:30.0	XRT_Custom_434_OG [0x1b2]			
2013/03/12	10:38:30.0	XRT_CTRL_AUTO_413_OG [0x19d]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/12	11:32:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/12	11:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00