

XRT Timeline to be uploaded on 2013/03/16

Period: 2013/03/16 10:37:00 - 2013/03/21 09:21:00

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Normal mode

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XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms												
Term	Pointing (x, y)	Comment										
03/16 10:50:00 - 03/16 17:55:24	Fixed (-15.0, -937.0)	# OP start + 10min, HOP 206, polar monitoring, S pole.										
PROG= 09 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 61 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 62 1-time(s) 30.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Subr= 3 30-time(s) 2.0sec												
└─ Seqn= 60 1-time(s) 60.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(25)												
Term	Pointing (x, y)	Comment										
03/16 17:58:30 - 03/16 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min										
03/17 06:03:00 - 03/17 06:09:54	Fixed (0.0, 0.0)	synoptic										
03/17 18:02:30 - 03/17 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min										
03/18 05:31:30 - 03/18 05:38:24	Fixed (0.0, 0.0)	synoptic, shifted -31.5 min										
03/18 18:21:30 - 03/18 18:28:24	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min										
03/19 06:07:30 - 03/19 06:14:24	Fixed (0.0, 0.0)	synoptic, shifted 4.5 min										

PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 14.0sec												
└─ Seqn= 96 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 6 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 41 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 30 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 69 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 68 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1823: AR Al/Poly 384x384FOV at 1064 1048 30s cadence												
Term	Pointing (x, y)	Comment										
03/16 18:08:30 - 03/16 21:05:24	Fixed (745.0, -587.0)	# HOP 228, jets.										
03/18 05:41:30 - 03/18 08:34:15	Fixed (805.0, 463.0)	# HOP 228, jets.										

PROG= 15 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 84 1-time(s) 30.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 4.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 4.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 4.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 4.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1972: HOP201 Updated - Filter-Ratio with PFB, shorter thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048 (all), 60s cad, AEC2												
Term	Pointing (x, y)	Comment										
03/16 21:08:30 - 03/17 05:59:54	Fixed (880.0, 300.0)	* HOP 201, CME heating, near ARs 11693 and 11687 at/over W limb.										
03/17 18:12:30 - 03/18 04:16:00	Fixed (880.0, 300.0)	# HOP 201, and ARs at/over W limb.										
03/18 09:42:06 - 03/18 14:43:00	Fixed (880.0, 300.0)	* HOP 201, and ARs at/over W limb.										
03/18 21:35:36 - 03/19 03:21:30	Fixed (880.0, 300.0)	# Cont.										

PROG= 17 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 24 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec												

Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 45 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 57 25-time(s) 60.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196D: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (32ms) w/ G-Band VLS C

Term	Pointing (x, y)	Comment
03/17 06:13:00 - 03/17 13:59:54	Track (315.3, 247.0) ^{03/17 06:10:00}	# AR 11692.
PROG= 04 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn=100 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 65 4-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 94 30-time(s) 120.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
03/16 10:50:00 - 03/16 17:55:24	Fixed (-15.0, -937.0)	# OP start + 10min, HOP 206, polar monitoring, S pole.
03/16 18:08:30 - 03/16 21:05:24	Fixed (745.0, -587.0)	# HOP 228, jets.
03/16 21:08:30 - 03/17 05:59:54	Fixed (880.0, 300.0)	* HOP 201, CME heating, near ARs 11693 and 11687 at/over W limb.
03/17 06:13:00 - 03/17 13:59:54	Track (315.3, 247.0) ^{03/17 06:10:00}	# AR 11692.
03/17 18:12:30 - 03/18 04:16:00	Fixed (880.0, 300.0)	# HOP 201, and ARs at/over W limb.
03/18 05:41:30 - 03/18 08:34:15	Fixed (805.0, 463.0)	# HOP 228, jets.
03/18 09:42:06 - 03/18 14:43:00	Fixed (880.0, 300.0)	* HOP 201, and ARs at/over W limb.
03/18 21:35:36 - 03/19 03:21:30	Fixed (880.0, 300.0)	# Cont.
PROG= 16 15-time(s)		
Subr= 1 45-time(s) 10.0sec		
Seqn= 35 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
Seqn= 36 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 37 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 38 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

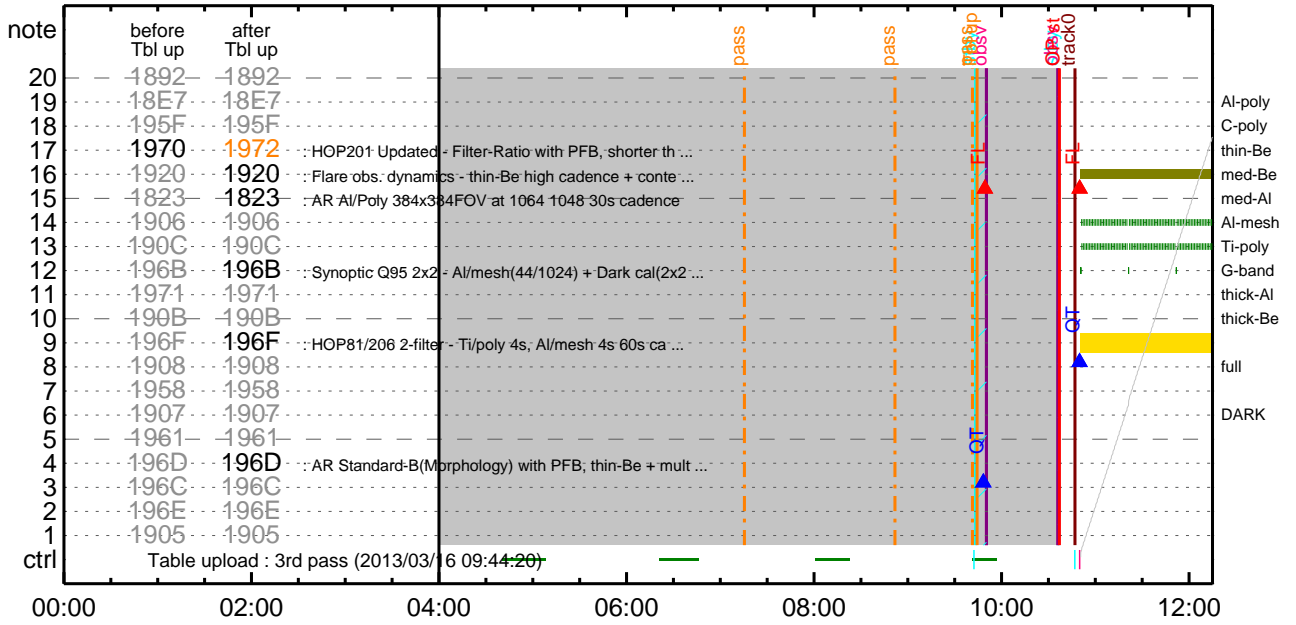
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Flare Detection

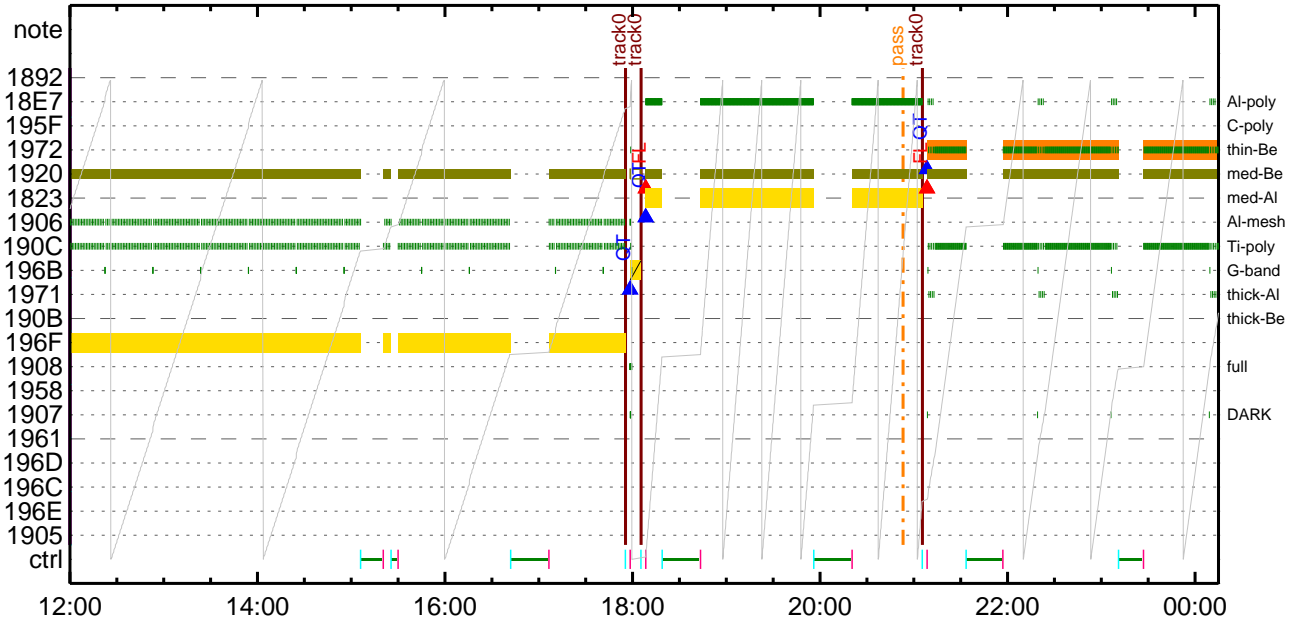
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FLD Patrol											
Term		Pointing (x, y)				Comment					
03/16 18:05:46 - 03/17 06:00:16		Fixed (745.0, -587.0)				# HOP 228, jets.					
03/17 06:10:16 - 03/17 17:59:46		Track (315.3, 247.0) @ 03/17 06:10:00				# AR 11692.					
03/17 18:09:46 - 03/18 05:28:46		Fixed (880.0, 300.0)				# HOP 201, and ARs at/over W limb.					
03/18 05:38:46 - 03/18 18:18:46		Fixed (805.0, 463.0)				# HOP 228, jets.					
03/18 21:32:52 - 03/19 06:04:46		Fixed (880.0, 300.0)				# Cont.					
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp. AEC Buffer Interval

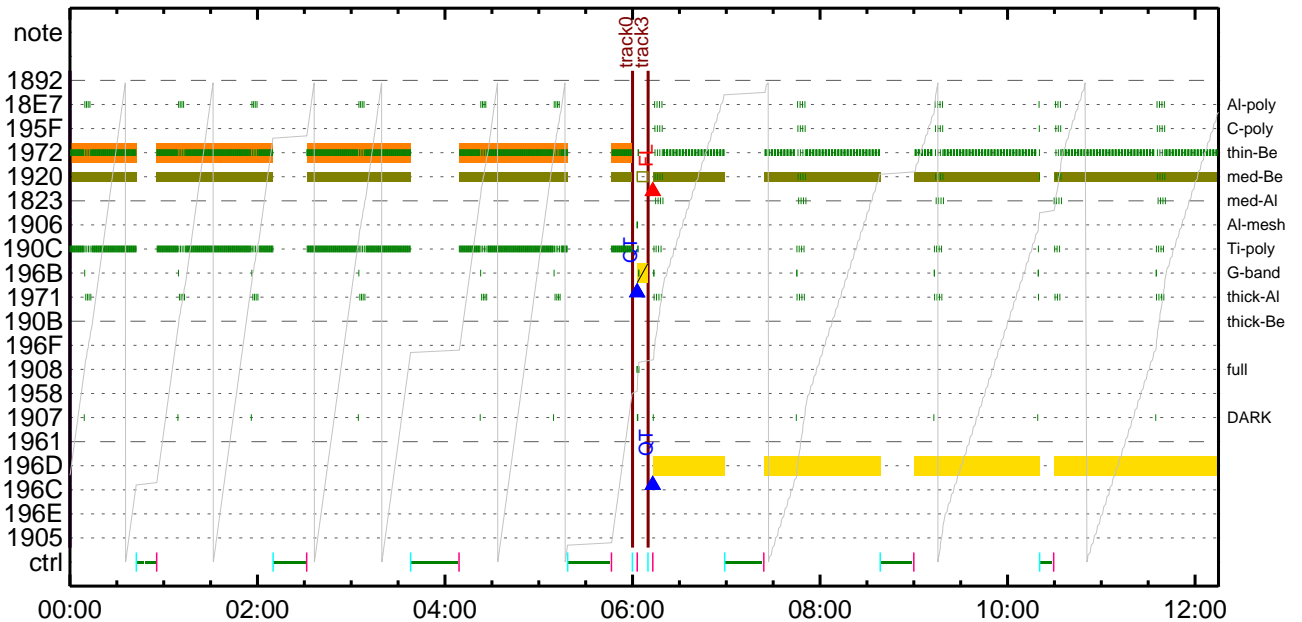
CMDI #0333 2013/03/16



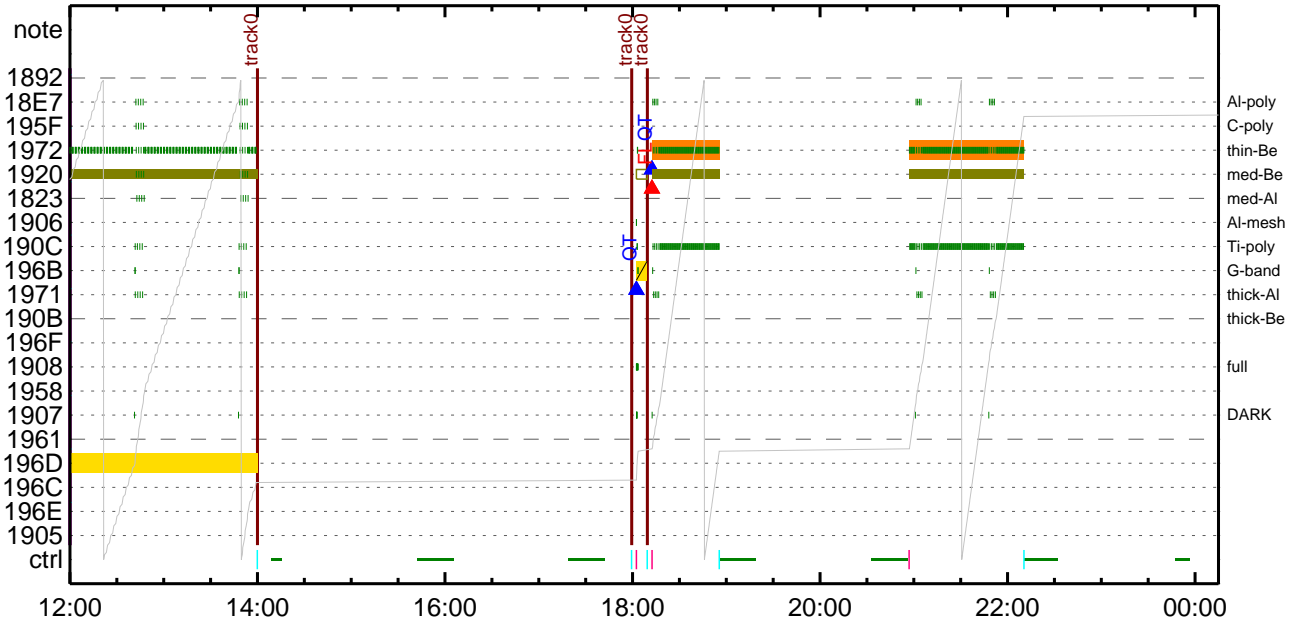
CMDI #0333 2013/03/16



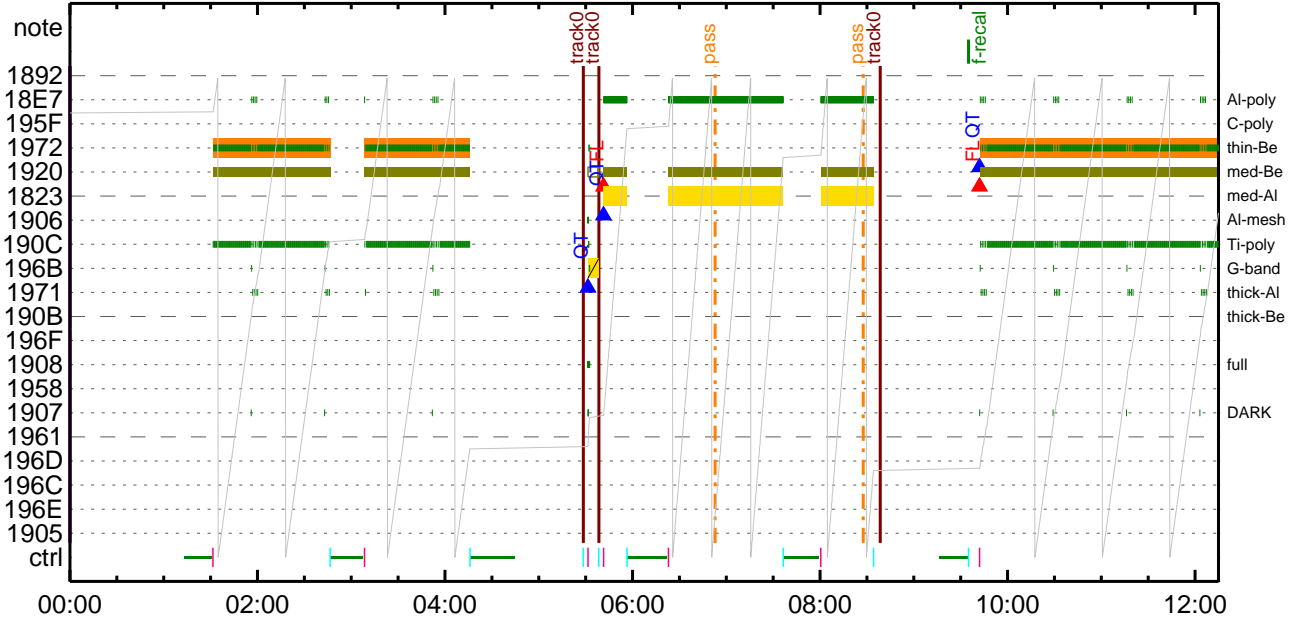
CMDI #0333 2013/03/17



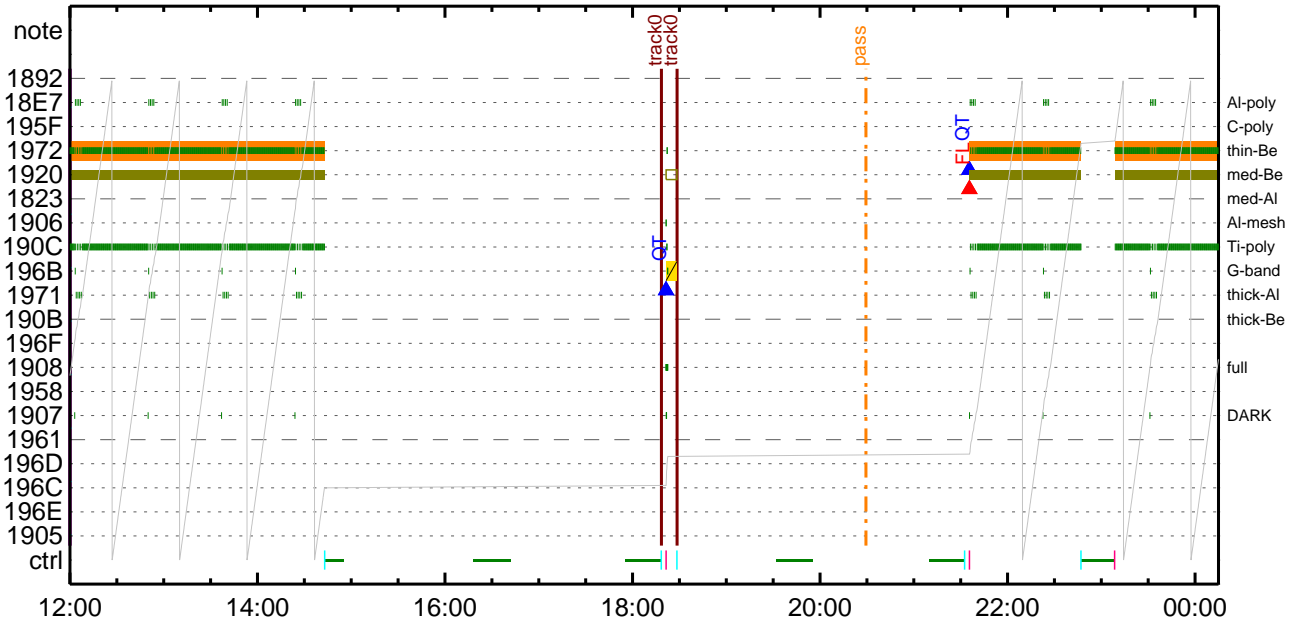
CMDI #0333 2013/03/17



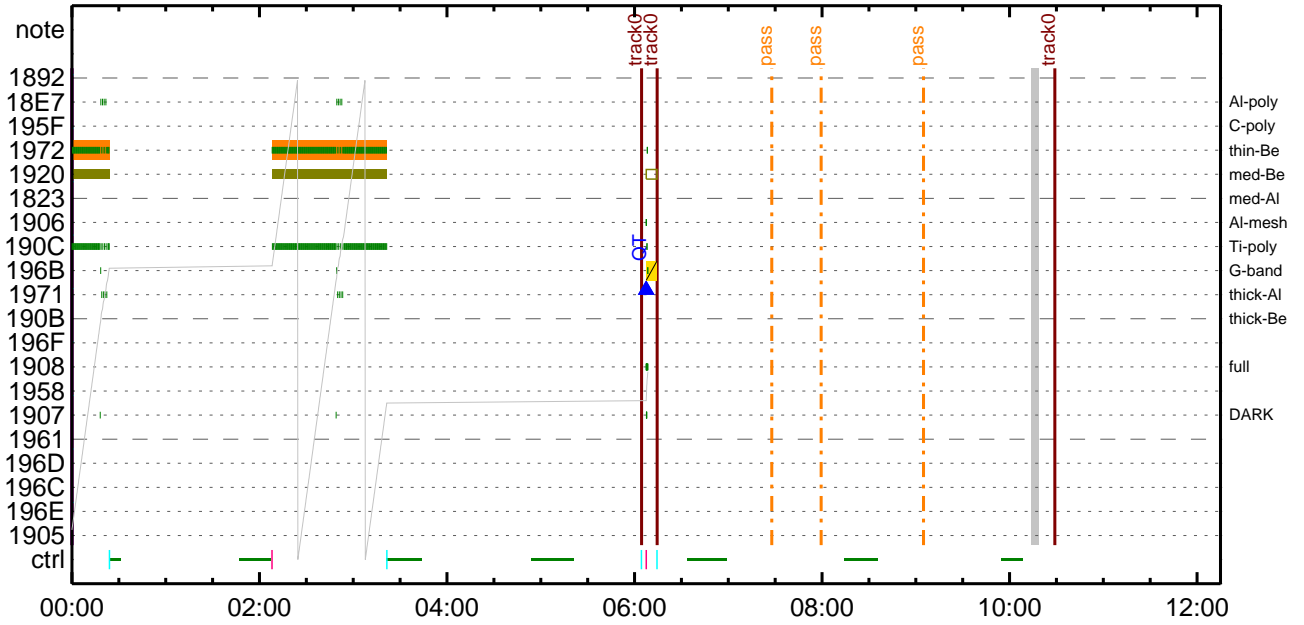
CMDI #0333 2013/03/18



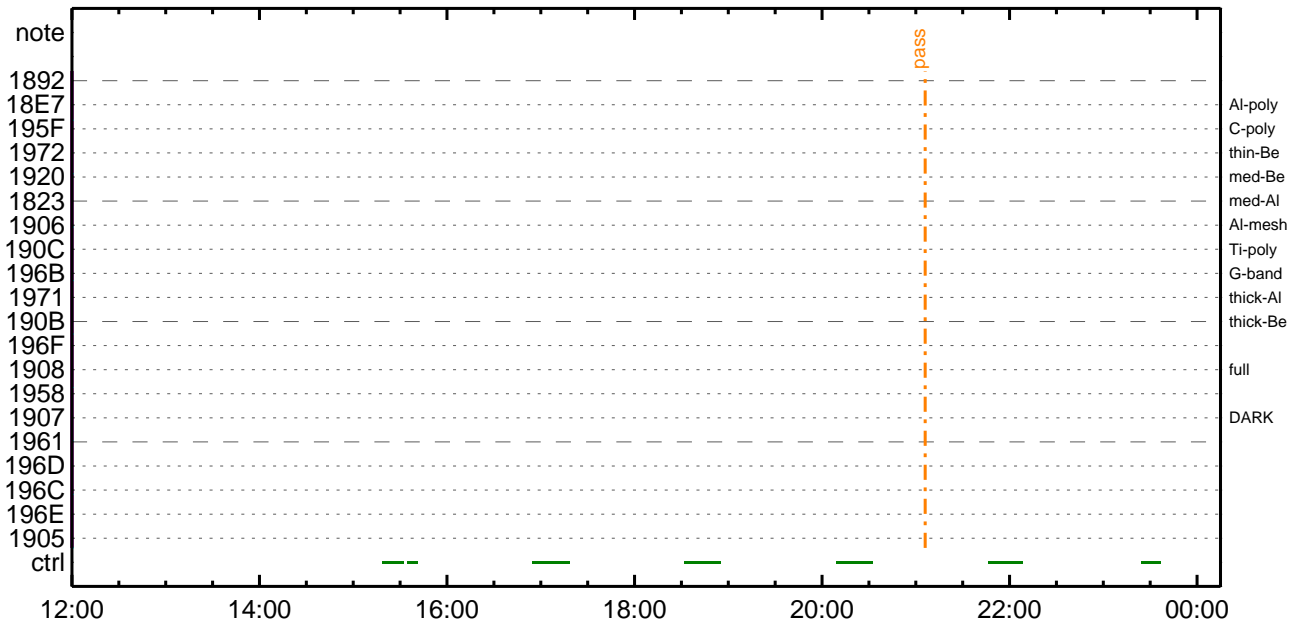
CMDI #0333 2013/03/18



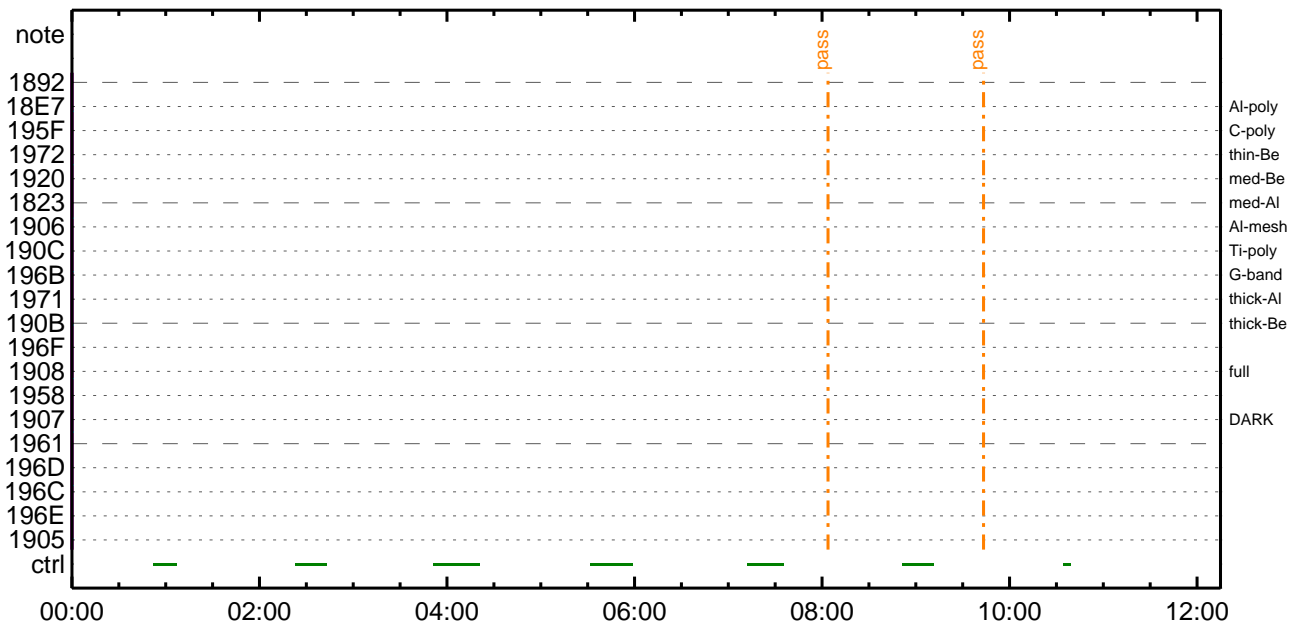
CMDI #0333 2013/03/19



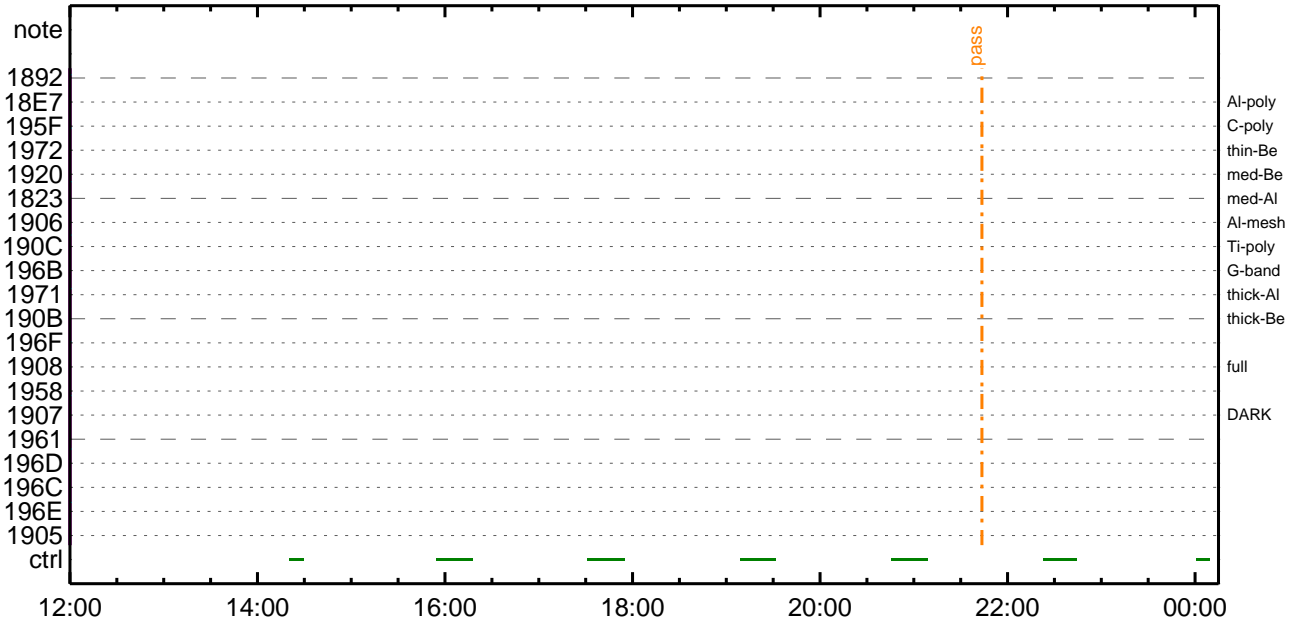
CMDI #0333 2013/03/19



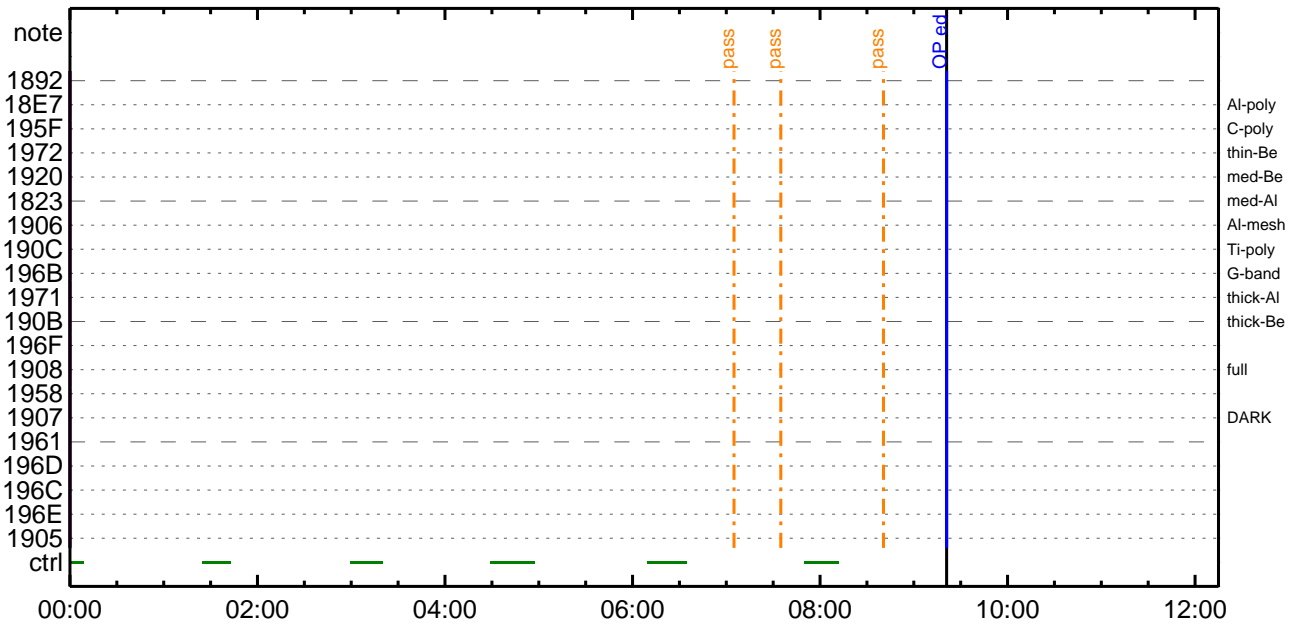
CMDI #0333 2013/03/20



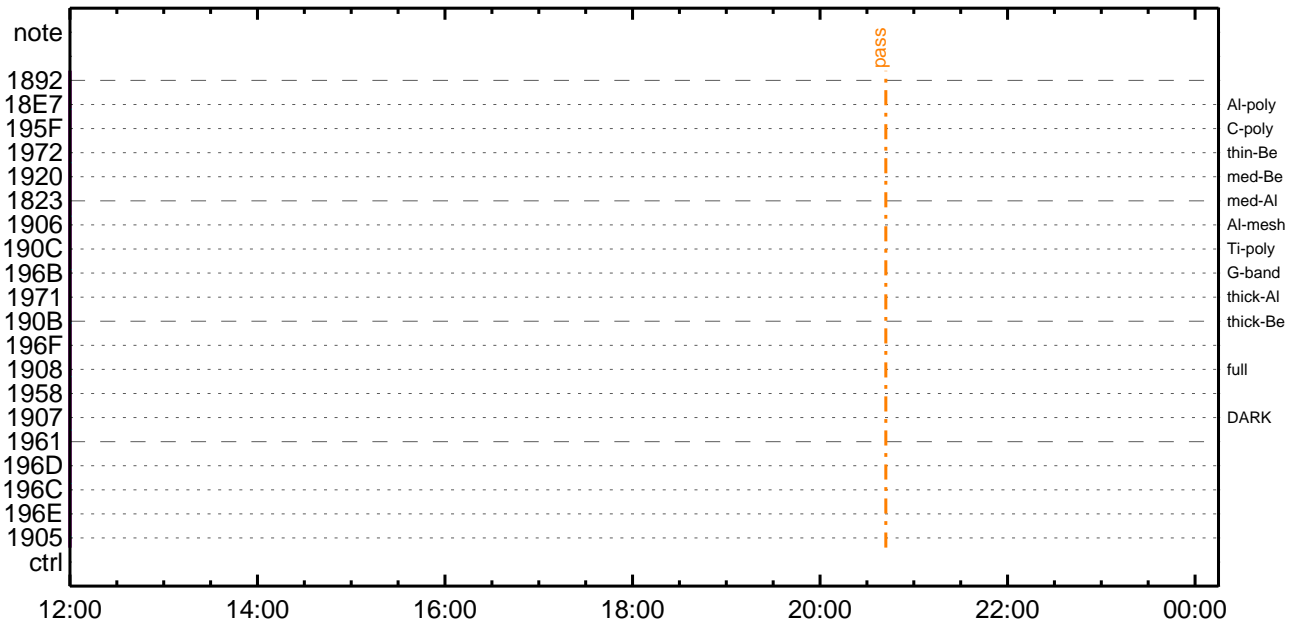
CMDI #0333 2013/03/20



CMDI #0333 2013/03/21



CMDI #0333 2013/03/21




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-524:OP
0104 ( )
0105 S. OG og-524:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EDUMPAîÆ±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2013-03-16 10:32:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-03-16 10:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-03-16 10:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-03-16 10:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0198 C.
0199 C.  °Ê¼∩ĀĒē%īīñ∩îŷĀŷ§ŷĀŷ-¹āiÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                    EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                 EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                  EQ      0xB3
0204 C.
0205 . C. *****
0206 C.  TIîî°èŷĀŷÔŷ×
0207 C. *****
0208 C.
0209 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                 EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]               EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                   EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                   EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                   EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                   EQ      EXEC
0224 C.
0225 . C.  ŷĀŷÔŷ×½ª î»∩ð³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                    EQ      NON
0227 C.
0228 . C.  RAM ID=TI_TBL∩îî¼ē¹ç•ē²îOK∩ð³îç§
0229 C.
0230 . C.  DHUŷā;¼ŷÉ;Ē¼ŷ¼. ŷi;¼ŷĒ;Ē∩ðĀ∩¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                   EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                   EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                EQ      4M
0237 C.
0238 C. *****
0239 C.  SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-03-16 10:36:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 . C. -----
0246 C.  HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-03-16 10:36:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-03-16 10:36:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 . C.          [ ] [HK1_TI_CMD_NUM]                    EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-03-16 10:36:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP `ūĀî∩Āī»ō¼ŷ∩ĒĀð∩¹∩ēDCBC•x²è *****
0276 C.  (¼ā°îŷŌŷĀŷĒŷŷŷĒŷĀŷçŷè∩Ē¼∩∩¼Ā»Ū∩¹∩è)
0277 . S. DC-BC dcbc-402:DCBC
0278      (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷÐŷ¹•ī Daily±çĒñ∩Ēē¹∩∩ēDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C.  ;āLOSŷĀŷ§ŷĀŷ-¼Ā»Û;ā
0287 C.
0288 . C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 55s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-03-16 10:36:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP `uãîïî»ö¼ÿñÉÂðñ¹ñèDCBC•x²è *****
0153 C. (%ã°îÿÓÿÅÿÉÿÏÿËÿÅÿçÿèñÉ¾¼ñ¼Å»Ûñ¹è)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¥D¥¹•İ Daily±¿İÑñÉ´øñ¹èDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLO$ÿÁÿ$ÿÅÿÿ¼Å»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-526 2013-03-16 13:52:08 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁα•μ°È»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈè%μ•ííÈ;ÈαÈ¼°ÇÓα•α¿¼í¹çαÍ;çÁ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 + DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 04)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 10)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2013-03-16 10:36:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop FG table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload FG Observation Table>
0094 . S. RAM ram-269:MDP_OBS_F
0095 ( )
```

```

0096 C.
0097 . C. < Dump RAMID=MDP_OBS_F >
0098 +. DC 07-F0 MDP_DUMP_FGTBL
0099 BC (82 07 00 00 00 38 b8)
0100 C. -----
0101 C. MDP_OBS_F verify = OK/NG [ ]
0102 C. -----
0103 C.
0104 . C. < Stop SP table >
0105 +. DC 07-F0 MDP_SP_CTRL_MANU
0106 BC (61)
0107 C. -----
0108 C. MDP_SP_CTRL_MODE = MANU [ ]
0109 C. -----
0110 C.
0111 . C. <Upload SP Observation Table>
0112 . S. RAM ram-281:MDP_OBS_S
0113 ( )
0114 C.
0115 . C. < Dump RAMID=MDP_OBS_S >
0116 +. DC 07-F0 MDP_DUMP_SPTBL
0117 BC (83 07 00 00 00 38 b8)
0118 C. -----
0119 C. MDP_OBS_S verify = OK/NG [ ]
0120 C. -----
0121 C.
0122 C. *****
0123 C. SOT TI command set
0124 C. *****
0125 C. Execute, after the success of TBL upload.
0126 +. TI 2013-03-16 10:36:18.0
0127 DC 07-F0 MDP_SOT_MODE_OBSV
0128 BC (40)
0129 . C. -----
0130 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0131 C. -----
0132 C.
0133 C.
0134 . C. ***** MDP 'uAÎaî»ö¼ÝaEÂÐa¹aëDCBC•x²è *****
0135 C. (%á°iYÓYÁYÉYÉYÉYáYçYèaE¼aa¼Á»Ûa¹aè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** YD¥¹•Ï Daily±¿ÎÑaE´Øa¹aëDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOS¥Á¥S¥Á¥-¼Á»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.

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*** OP Sequence for XRT ***

2013/03/16	10:46:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/16	10:46:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/03/16	10:47:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 53 4b 01 58		
2013/03/16	10:47:16.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/03/16	10:47:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/03/16	10:47:20.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/03/16	10:47:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/16	10:49:54.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/16	10:49:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2013/03/16	10:49:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2013/03/16	10:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/16	15:06:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/16	15:06:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/16	15:06:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/16	15:09:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/16	15:19:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/16	15:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/16	15:25:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/16	15:25:32.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/16	15:25:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/16	15:28:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/16	15:29:00.0	XRT_Custom_434_OG [0x1b2]					
2013/03/16	15:30:00.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/16	16:42:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/16	16:42:02.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/16	16:42:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/03/16	16:45:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/03/16	17:05:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/16	17:06:30.0	XRT_CTRL_AUTO_413_OG [0x19d]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/16	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/16	17:55:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/03/16	17:55:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/03/16	17:55:46.0	XRT_FLD_DIS_404_OG [0x194]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/03/16	17:55:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/03/16	17:55:50.0	XRT_ARS_DIS_406_OG [0x196]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/16	17:58:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2013/03/16	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/03/16	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/03/16	18:05:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/03/16	18:05:30.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 34 31 bd cb		
2013/03/16	18:05:46.0	XRT_FLD_ENA_428_OG [0x1ac]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/03/16	18:05:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/03/16	18:05:50.0	XRT_AEC_RESET_423_OG [0x1a7]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/03/16	18:05:52.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/03/16	18:08:24.0	XRT_FLD_RESET_424_OG [0x1a8]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/03/16	18:08:26.0	XRT_QT_PROG_SET_411_OG [0x19b]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2013/03/16	18:08:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		

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2013/03/16	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/16	18:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/16	18:19:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/16	18:19:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/16	18:22:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/16	18:42:30.5	XRT_Custom_434_OG [0x1b2]							
2013/03/16	18:43:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/16	19:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/16	19:56:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/16	19:56:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/16	19:59:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/16	20:19:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/16	20:20:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/16	21:05:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/16	21:05:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/16	21:05:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 e5 59 b1 cb				
2013/03/16	21:05:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/16	21:05:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/16	21:05:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/16	21:05:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/16	21:08:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/16	21:08:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2013/03/16	21:08:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/16	21:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/16	21:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/16	21:33:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/16	21:33:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/16	21:36:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/16	21:56:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/16	21:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/16	23:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/16	23:11:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/16	23:11:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/16	23:14:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/16	23:26:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/16	23:27:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	00:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	00:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	00:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	00:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	00:54:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/17	00:55:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	02:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	02:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	02:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	02:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	02:30:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/17	02:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	03:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	03:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]							

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2013/03/17	03:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	03:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	04:08:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/17	04:09:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	05:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	05:18:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	05:18:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	05:21:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	05:45:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/17	05:46:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/03/17	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2013/03/17	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/03/17	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/03/17	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/17	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2013/03/17	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	06:09:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/17	06:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	03 00 00 00 00				
2013/03/17	06:10:16.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/17	06:10:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/17	06:10:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/17	06:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/17	06:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	06:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2013/03/17	06:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/17	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	06:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	06:59:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	06:59:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	07:02:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	07:23:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/17	07:24:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	08:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	08:38:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	08:38:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	08:41:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	08:59:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/17	09:00:00.5	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	10:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	10:20:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/17	10:20:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/17	10:23:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/17	10:28:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/17	10:29:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/17	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/17	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]								

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2013/03/17	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/17	17:59:26.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2013/03/17	17:59:30.0	AOCS_OrE-point_Start_2_OG [0x098]								
		AOCU_NM		5	02-76	00	00	00	00	00
2013/03/17	17:59:46.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2013/03/17	17:59:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2013/03/17	17:59:50.0	XRT_ARS_DIS_406_OG [0x196]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/03/17	18:02:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2013/03/17	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/03/17	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/17	18:09:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]								
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2013/03/17	18:09:30.0	AOCS_OrE-point_Start_4_OG [0x09a]								
		AOCU_NM		5	02-76	00	e5	59	b1	cb
2013/03/17	18:09:46.0	XRT_FLD_ENA_428_OG [0x1ac]								
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2013/03/17	18:09:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2013/03/17	18:09:50.0	XRT_AEC_RESET_423_OG [0x1a7]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2013/03/17	18:09:52.0	XRT_ARS_DIS_437_OG [0x1b5]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/03/17	18:12:24.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/03/17	18:12:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2013/03/17	18:12:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	10			
2013/03/17	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/03/17	18:55:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/17	18:55:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/03/17	18:55:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/03/17	18:58:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/03/17	20:56:00.0	XRT_Custom_434_OG [0x1b2]								
2013/03/17	20:57:00.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/03/17	22:10:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/17	22:10:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/03/17	22:10:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/03/17	22:13:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/03/18	01:30:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/18	01:31:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/03/18	02:46:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/18	02:46:32.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/03/18	02:46:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/03/18	02:49:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/03/18	03:07:30.0	XRT_Custom_434_OG [0x1b2]								
2013/03/18	03:08:30.0	XRT_CTRL_AUTO_413_OG [0x19d]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/03/18	04:16:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/18	04:16:02.0	XRT_FLD_RESET_424_OG [0x1a8]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/03/18	04:16:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/03/18	04:19:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/03/18	05:28:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/03/18	05:28:26.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2013/03/18	05:28:30.0	AOCS_OrE-point_Start_2_OG [0x098]								
		AOCU_NM		5	02-76	00	00	00	00	00
2013/03/18	05:28:46.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2013/03/18	05:28:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2013/03/18	05:28:50.0	XRT_ARS_DIS_406_OG [0x196]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/03/18	05:31:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]								

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2013/03/18	05:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	05:38:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	05:38:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/03/18	05:38:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d6 da b8	73
2013/03/18	05:38:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/18	05:38:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/18	05:38:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/18	05:38:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/18	05:41:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	05:41:26.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2013/03/18	05:41:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/18	05:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	05:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	05:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	05:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/18	05:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/18	06:22:00.0	XRT_Custom_434_OG [0x1b2]					
2013/03/18	06:23:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	07:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	07:36:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	07:36:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/18	07:39:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/18	07:59:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/18	08:00:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	08:34:15.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	08:38:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 e5 59 b1	cb
2013/03/18	09:35:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	09:35:02.0	XRT_FOCUS_RECALIBRATE_425_OG [0x1a9]	XRT_FOCUS_RECAL	2	07-F8	78	00
2013/03/18	09:39:02.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/03/18	09:39:22.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/18	09:39:24.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/18	09:39:26.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/18	09:39:28.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/18	09:42:00.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	09:42:02.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/03/18	09:42:04.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/18	09:42:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	14:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	14:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	14:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/18	14:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/18	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	18:18:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2013/03/18	18:18:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2013/03/18	18:18:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/18	18:18:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/18	18:18:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	

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2013/03/18	18:21:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2013/03/18	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	18:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	18:28:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	e5 59 b1 cb
2013/03/18	21:32:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	21:32:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/03/18	21:32:52.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/18	21:32:54.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/18	21:32:56.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/18	21:32:58.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/18	21:35:30.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	21:35:32.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/03/18	21:35:34.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/18	21:35:36.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/18	22:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/18	22:47:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/18	22:47:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/18	22:50:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/18	23:07:30.0	XRT_Custom_434_OG [0x1b2]					
2013/03/18	23:08:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	00:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	00:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	00:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/19	00:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/19	02:07:00.0	XRT_Custom_434_OG [0x1b2]					
2013/03/19	02:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	03:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	03:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	03:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/19	03:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/19	06:04:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	06:04:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/03/19	06:04:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2013/03/19	06:04:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/19	06:04:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/19	06:04:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	06:07:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2013/03/19	06:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	06:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	06:14:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	e5 59 b1 cb
2013/03/19	10:29:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00