

# XRT Timeline to be uploaded on 2013/03/19

Period: 2013/03/19 10:19:00 - 2013/03/23 10:02:00

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## Normal mode

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XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms													
Term	Pointing (x, y)							Comment					
03/19 10:32:00 - 03/19 17:59:54	Fixed ( -15.0, -937.0)							# OP start + 10min, HOP-206 (South Pole Panorama Map)					
<b>PROG= 09 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 61 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs 1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec	
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 62 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
┌ Subr= 3 30-time(s) 2.0sec													
└ Seqn= 60 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(25)													
Term	Pointing (x, y)							Comment					
03/19 18:03:00 - 03/19 18:09:54	Fixed ( 0.0, 0.0)							synoptic					
03/20 06:11:00 - 03/20 06:17:24	Fixed ( 0.0, 0.0)							synoptic, shifted 8.0 min					
03/20 17:59:30 - 03/20 18:06:24	Fixed ( 0.0, 0.0)							synoptic, shifted -3.5 min					
03/21 05:44:30 - 03/21 05:51:24	Fixed ( 0.0, 0.0)							synoptic, shifted -18.5 min					
<b>PROG= 12 1-time(s)</b>													
┌ Subr= 1 1-time(s) 14.0sec													
└ Seqn= 96 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 6 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└ Seqn= 41 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 30 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 69 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1968: CME watch - 4x4 - AEC 2 - Ti/poly- 240s cad - 2x2 Gband - 1hr cad													
Term	Pointing (x, y)							Comment					
03/19 18:13:00 - 03/19 18:31:30	Fixed ( 936.0, 0.0)							# HOP-130 (EIS full Sun scan) 1/15					
03/19 19:04:00 - 03/19 19:16:54	Fixed ( 468.0, 0.0)							# 2/15					
03/19 19:36:00 - 03/19 19:48:54	Fixed ( -468.0, 0.0)							# 4/15					
03/19 19:52:00 - 03/19 20:09:00	Fixed ( -936.0, 0.0)							# 5/15					
03/19 20:41:30 - 03/19 20:48:24	Fixed ( 815.0, 470.0)							# 6/15					
03/19 20:51:30 - 03/19 21:04:24	Fixed ( 411.0, 847.0)							# 7/15					
03/19 21:07:30 - 03/19 21:20:24	Fixed ( 0.0, 941.0)							# 8/15					
03/19 21:23:30 - 03/19 21:36:24	Fixed ( -411.0, 847.0)							# 9/15					
03/19 21:39:30 - 03/19 21:46:30	Fixed ( -815.0, 470.0)							# 10/15					
03/19 22:17:30 - 03/19 22:24:24	Fixed ( 815.0, -470.0)							# 11/15					
03/19 22:27:30 - 03/19 22:40:24	Fixed ( 411.0, -847.0)							# 12/15					
03/19 22:43:30 - 03/19 22:56:24	Fixed ( 0.0, -941.0)							# 13/15					
03/19 22:59:30 - 03/19 23:12:24	Fixed ( -411.0, -847.0)							# 14/15					
03/19 23:15:30 - 03/19 23:24:00	Fixed ( -815.0, -471.0)							# 15/15					
<b>PROG= 01 Inf.-time(s)</b>													
┌ Subr= 1 15-time(s) 240.0sec													
└ Seqn= 12 1-time(s) 4.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec	
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 69 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1958: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33) - 1 loop**

Term	Pointing (x, y)	Comment
03/19 19:20:00 - 03/19 19:32:54	Fixed ( 0.0, 0.0)	# 3/15
<b>PROG= 07 1-time(s)</b>		
<b>Subr= 1 1-time(s) 85.0sec</b>		
<b>Seqn= 82 1-time(s) 7.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 6 1-time(s) 22.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 76 1-time(s) 8.0sec</b>		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 77 1-time(s) 12.0sec</b>		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 79 1-time(s) 10.0sec</b>		
C-poly/Open	C-poly/Open close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
C-poly/Open	C-poly/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 75 1-time(s) 10.0sec</b>		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 78 1-time(s) 15.0sec</b>		
thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 2 1-time(s) 150.0sec</b>		
<b>Seqn= 34 1-time(s) 29.0sec</b>		
med-Al/Open	med-Al/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 83 1-time(s) 84.0sec</b>		
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 69 2-time(s) 13.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 68 1-time(s) 10.0sec</b>		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1974: HOP201 Updated 2min cad - Filter-Ratio with PFB, shorter thin-Be, thick Al and Al/Poly context, 512x384 at 1064 1048 (all), AEC2**

Term	Pointing (x, y)	Comment
03/20 00:23:00 - 03/20 05:31:30	Fixed ( 880.0, 300.0)	# HOP-201 (CME watch).
03/20 18:09:30 - 03/21 04:29:00	Fixed ( 880.0, 300.0)	# HOP-201 (CEM watch)
03/21 05:54:30 - 03/21 07:50:00	Fixed ( 880.0, 300.0)	# HOP-201 (CME watch).
<b>PROG= 14 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 48 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 512x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 512x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 50 4-time(s) 2.0sec</b>		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 512x384 (1064, 1048) Q=95 2 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x384 (1064, 1048) Q=95 2 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 512x384 (1064, 1048) Q=95 2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 49 15-time(s) 120.0sec</b>		
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x384 (1064, 1048) Q=95 2 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x384 (1064, 1048) Q=95 2 0 15.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops**

Term	Pointing (x, y)	Comment
03/19 10:32:00 - 03/19 17:59:54	Fixed ( -15.0, -937.0)	# OP start + 10min, HOP-206 (South Pole Panorama Map)
03/19 18:13:00 - 03/19 18:31:30	Fixed ( 936.0, 0.0)	# HOP-130 (EIS full Sun scan) 1/15
03/19 19:04:00 - 03/19 19:16:54	Fixed ( 468.0, 0.0)	# 2/15
03/19 19:36:00 - 03/19 19:48:54	Fixed ( -468.0, 0.0)	# 4/15
03/19 19:52:00 - 03/19 20:09:00	Fixed ( -936.0, 0.0)	# 5/15
03/19 20:41:30 - 03/19 20:48:24	Fixed ( 815.0, 470.0)	# 6/15
03/19 20:51:30 - 03/19 21:04:24	Fixed ( 411.0, 847.0)	# 7/15
03/19 21:07:30 - 03/19 21:20:24	Fixed ( 0.0, 941.0)	# 8/15

03/19 21:23:30 - 03/19 21:36:24	Fixed ( -411.0, 847.0)	# 9/15
03/19 21:39:30 - 03/19 21:46:30	Fixed ( -815.0, 470.0)	# 10/15
03/19 22:17:30 - 03/19 22:24:24	Fixed ( 815.0, -470.0)	# 11/15
03/19 22:27:30 - 03/19 22:40:24	Fixed ( 411.0, -847.0)	# 12/15
03/19 22:43:30 - 03/19 22:56:24	Fixed ( 0.0, -941.0)	# 13/15
03/19 22:59:30 - 03/19 23:12:24	Fixed ( -411.0, -847.0)	# 14/15
03/19 23:15:30 - 03/19 23:24:00	Fixed ( -815.0, -471.0)	# 15/15
03/20 00:23:00 - 03/20 05:31:30	Fixed ( 880.0, 300.0)	# HOP-201 (CME watch).
03/20 18:09:30 - 03/21 04:29:00	Fixed ( 880.0, 300.0)	# HOP-201 (CEM watch)
03/21 05:54:30 - 03/21 07:50:00	Fixed ( 880.0, 300.0)	# HOP-201 (CME watch).

<b>PROG= 16 15-time(s)</b>												
Subr= 1	45-time(s)	10.0sec										
└─ Seqn= 35	1-time(s)	2.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	10.0sec										
└─ Seqn= 36	1-time(s)	2.0sec										
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 37	1-time(s)	2.0sec										
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 38	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

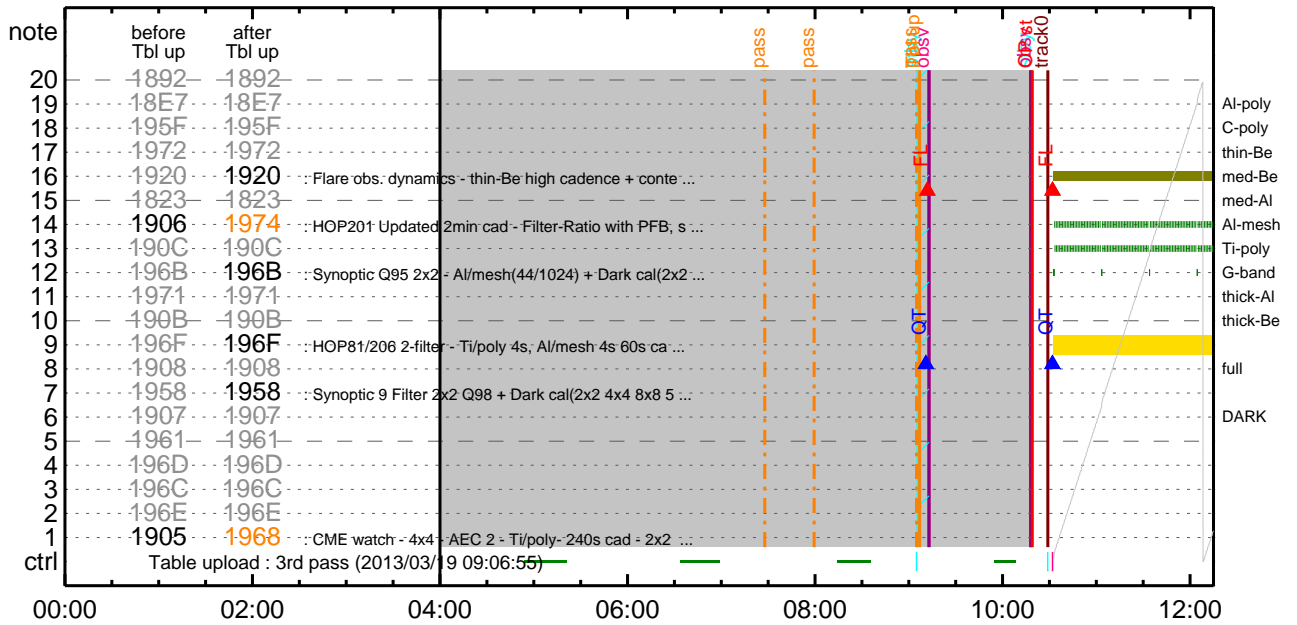
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### Flare Detection

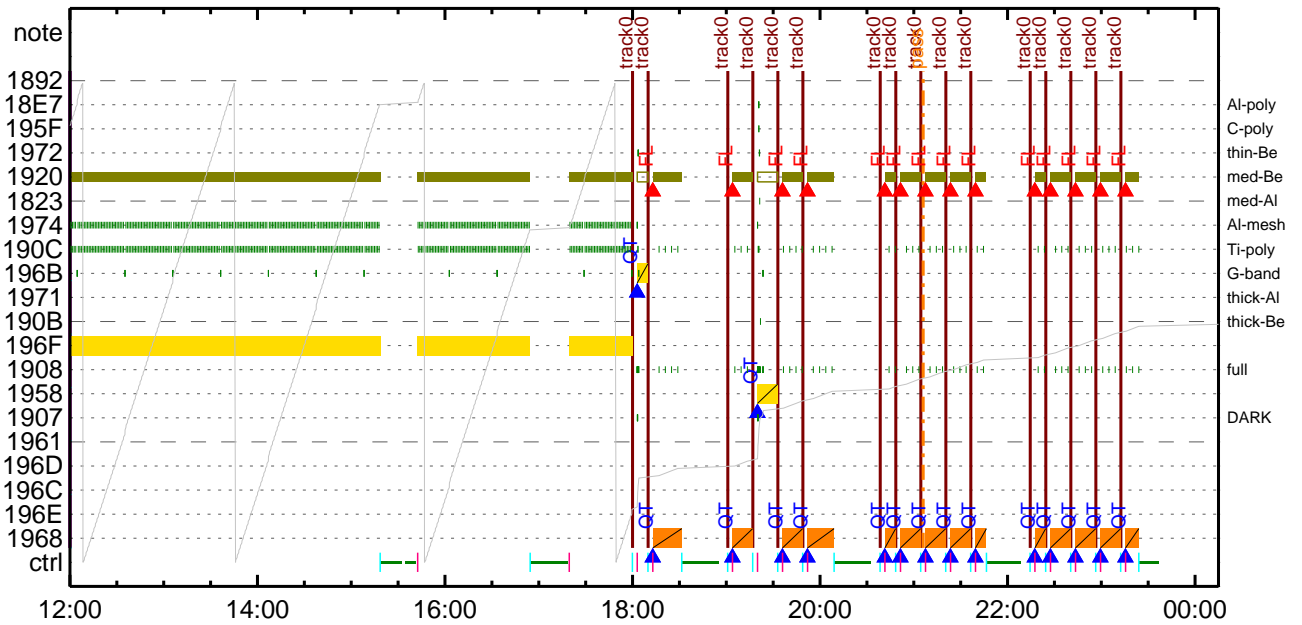
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<b>FLD Patrol</b>													
Term	Pointing (x, y)				Comment								
03/19 18:10:16 - 03/19 19:17:16	Fixed ( 936.0, 0.0)				# HOP-130 (EIS full Sun scan) 1/15								
03/19 19:33:16 - 03/20 06:08:16	Fixed ( -468.0, 0.0)				# 4/15								
03/20 18:06:46 - 03/21 05:41:46	Fixed ( 880.0, 300.0)				# HOP-201 (CEM watch)								
03/21 05:51:46 - 03/23 10:02:00	Fixed ( 880.0, 300.0)				# HOP-201 (CME watch).								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

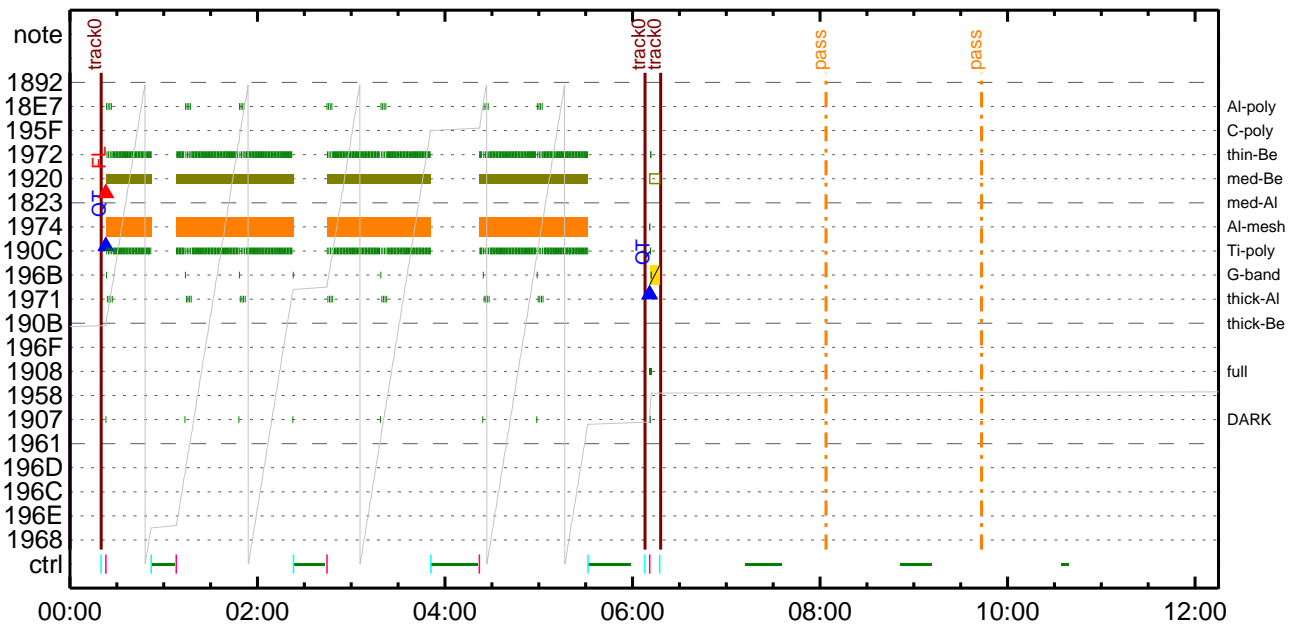
### CMDI #0337 2013/03/19



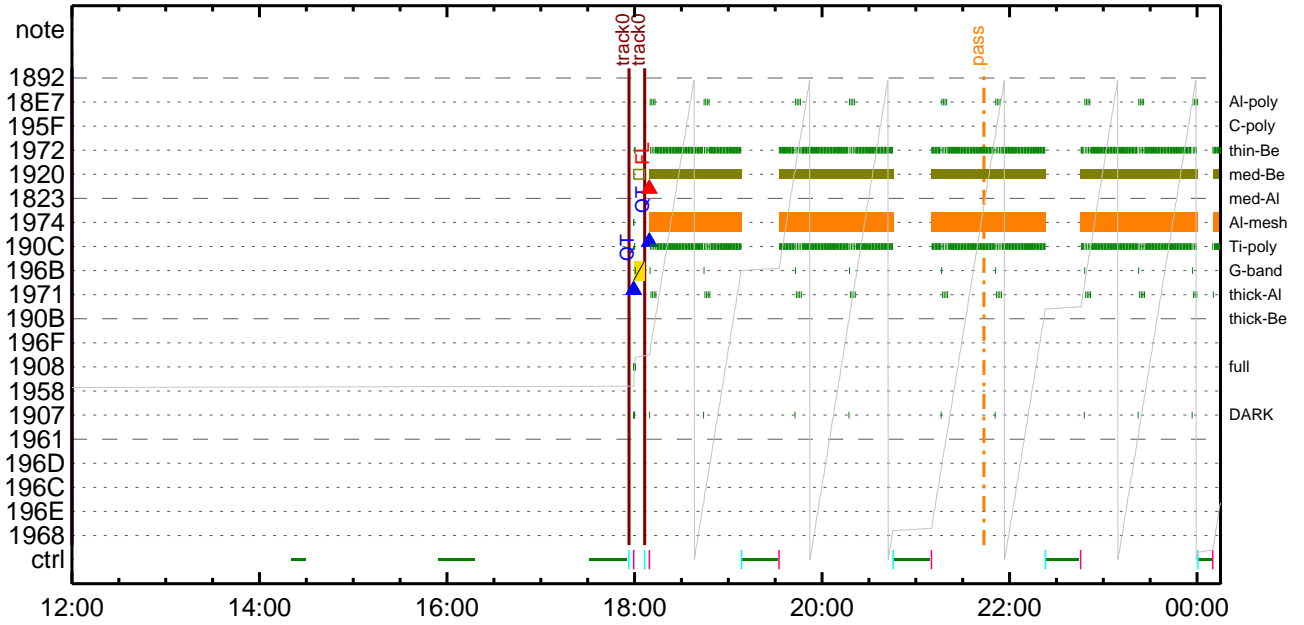
### CMDI #0337 2013/03/19



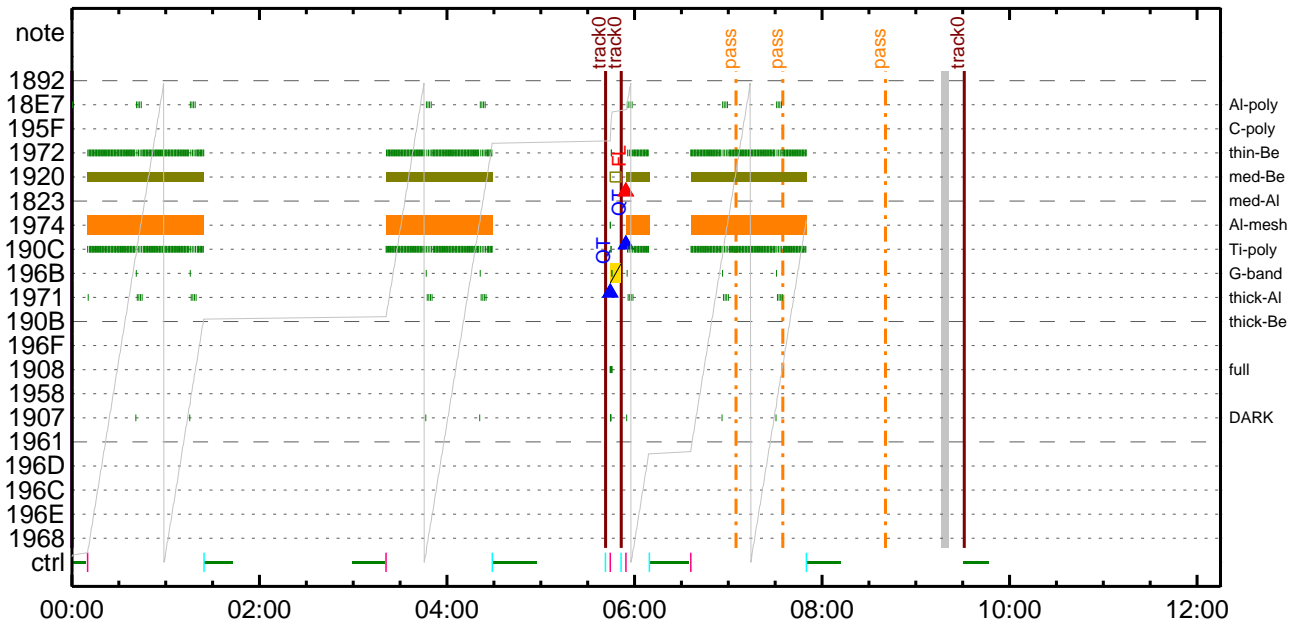
### CMDI #0337 2013/03/20



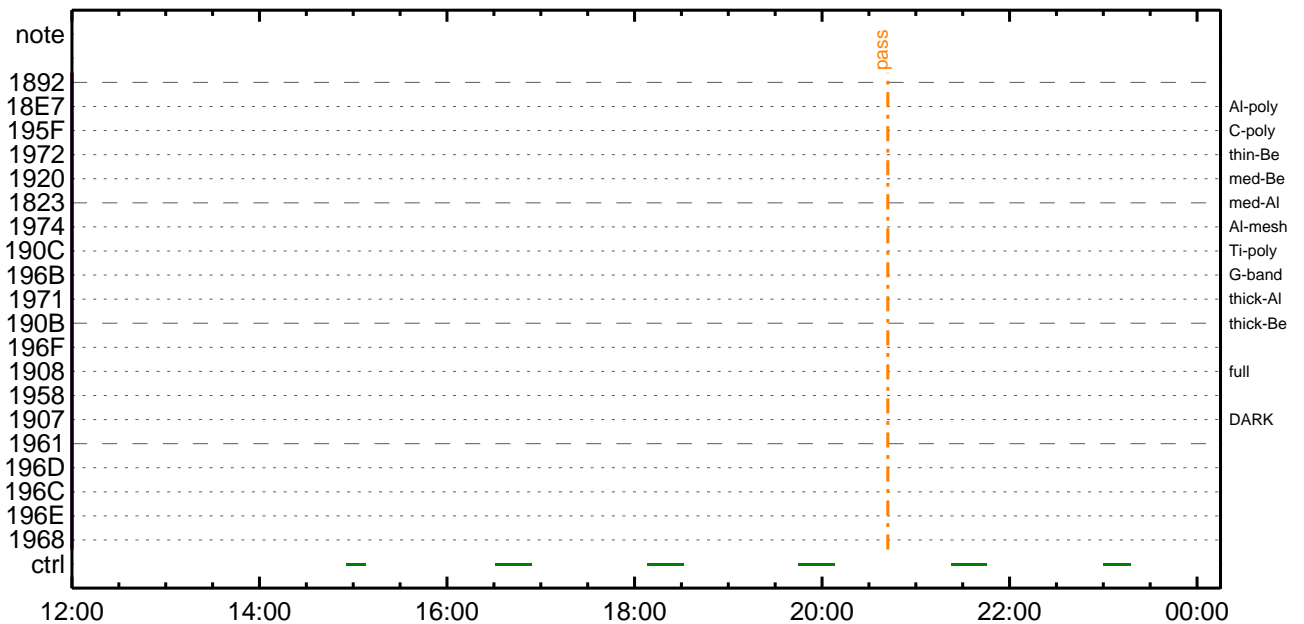
CMDI #0337 2013/03/20



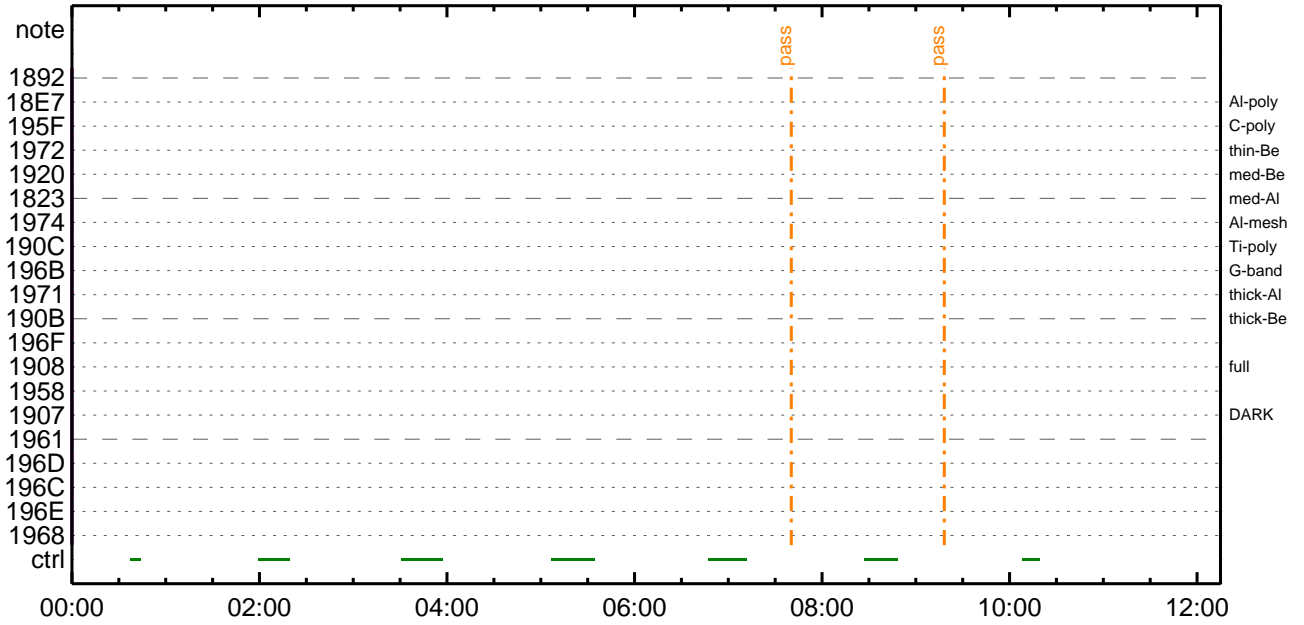
CMDI #0337 2013/03/21



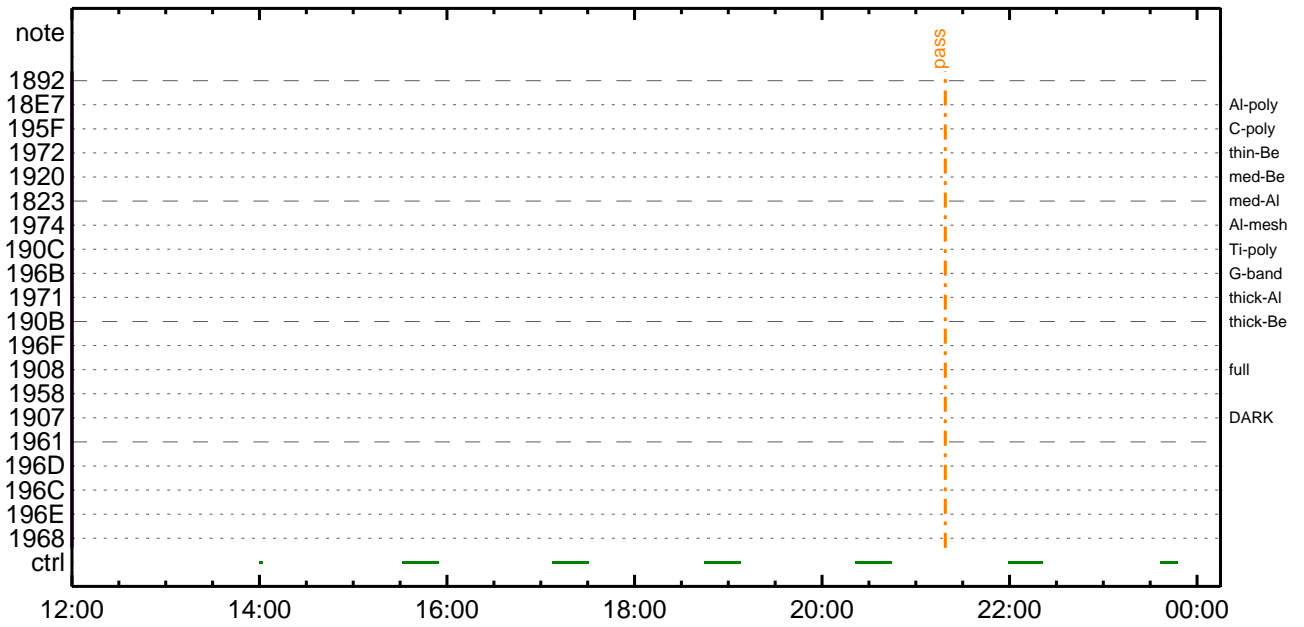
CMDI #0337 2013/03/21



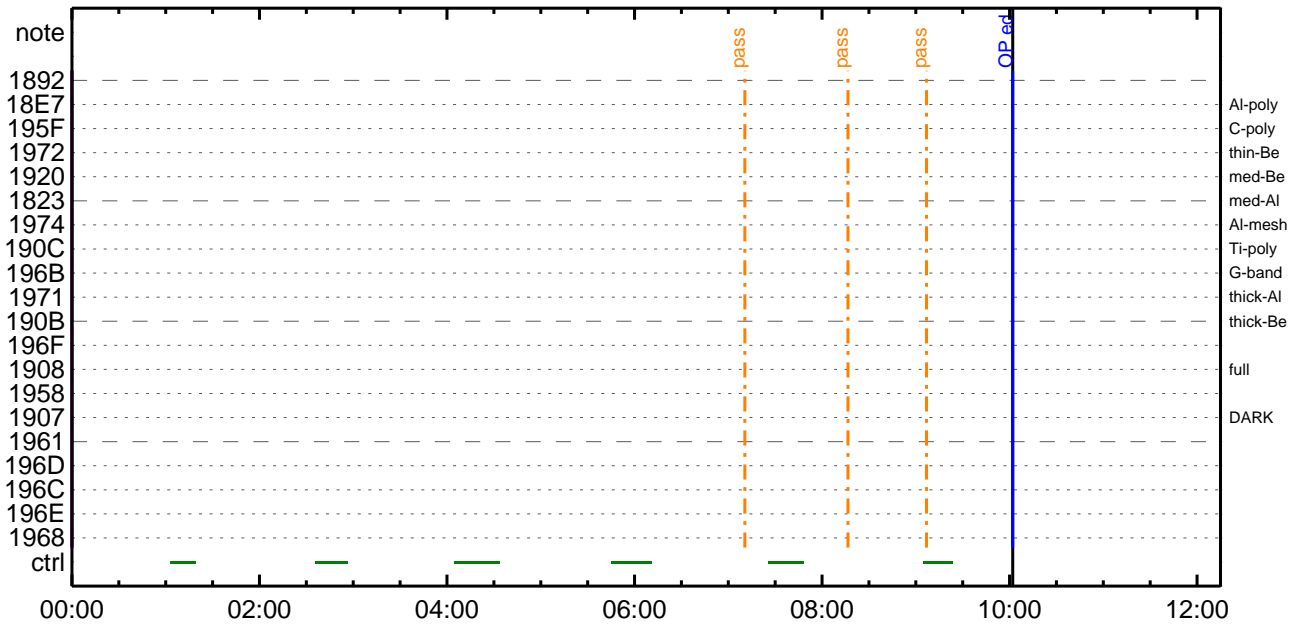
CMDI #0337 2013/03/22



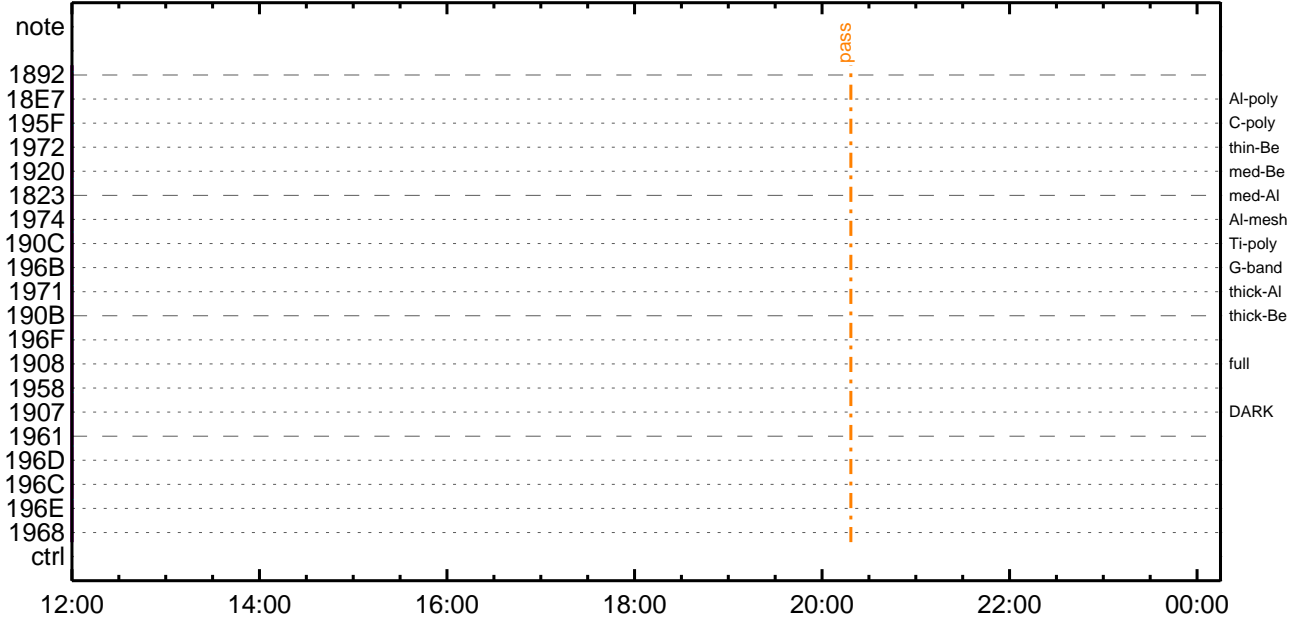
CMDI #0337 2013/03/22



CMDI #0337 2013/03/23



CMDI #0337 2013/03/23



(a) Spacecraft Operation Procedure (real-commands)

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main-528 2013-03-19 11:33:18 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY^-¼Á»Ü;ä
0005 C.
0006 C. YÀYŞ;¼Y³YŞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCSS : Reload orbital element (send every contact) *****
0010 C. ÁÍ;Èç²âÄ±·µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ·íÉ;È±È¼°ÇÖ±·±ç¼¹ççÍ;çÀ®, ù±²³±±±±çÁ+ç®±·±±±±±±È;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+çµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ÈY±âÀLOS±±ççÍ»'Ö±¹Íí, ±·; çÉÖÍ×±ÈXÁÖON±Í¹Ò±È±±±±±±È;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDŞYÓYÉYÍYÁY^-¾ÔÖ±-°ÁÄ±±·±ç±é; ç°Ê²¼±¹°ÆÀ, ¼±±ççç²¼±¹°±±±±;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;È±·±ççç¼¹ççÍ; ç°Ê²¼±¹°ÆÀ±±±±; çDCBC-150±±ç±È±±±;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYËÄÜÁÏ; ÈÄ·Ä°²óÈð; È, ä±¹°ÆÀ, °Æ³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ±-¼«Æ°Ä±»±±±±ç±, ä; ç°Ê²¼±¹°ÆÀ±±±±±±±±;ç
0055 C. YçYÓYÉYËÄÜÁÏ±±±±Ä·Ä°²óÈð±-¼±±±¼¹ççÍ' °Í»±±±±±±çÁÖ±±±;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;È±·±ççç¼¹ççÍ; ç°Ê²¼±¹°ÆÀ±±±±; çDCBC-151±±ç±È±±±;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYËÄÜÁÏ; ÈÄ·Ä°²óÈð; È, ä±¹°ÆÀ, °Æ³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Ä±»±; çXÁ+çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Ä±»±;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+çµ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|AYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-528:OP
0104 ( )
0105 S. OG og-528:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^eAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ;[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ;[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ;[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ;[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ;[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ;[HK1_PKT_FORM_NO] EQ 7
0120 C. ;[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ;[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ;[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. AYOXx1^i>od^IcS
0125 C. ;[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGafE^c.e^IOKod^IcS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ;[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ;[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ;[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ;[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ;[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ;[HK1_PKT_FORM_NO] EQ 7
0139 C. ;[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ;[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ;[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. AYOXx1^i>od^IcS
0144 C. ;[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGafE^c.e^IOKod^IcS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ;[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ;[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ;[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ;[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ;[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ;[HK1_PKT_FORM_NO] EQ 7
0158 C. ;[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ;[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ;[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. AYOXx1^i>od^IcS
0163 C. ;[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPafE^c.e^IOKod^IcS
0165 C.
0166 C. ***** oE^2qI^A^¶A^oEE^-oA^:z@ (%au-AYOXx1e^c.oadAOAæoc^a^"oE%i^1cqcã) *****
0167 C. DHUa;4YE;E^Y%,Yi;4YE;EodIã^1
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ;[HK1_PKT_FORM_NO] EQ 2
0171 C. ;[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ;[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;s OPOG UPLOAD-a^:z@NGafI^1c;^oE^2qoITi-CMDA^:z@oI^A^1Oa.oEoqo^3oE;f
0180 C. oEo;^cSEToEDUMPaIã±oI^N^1c^1Oa|o^3oE;f
0181 C.
0182 C. TIY^3YpYOXEodAD^I^z(UT)
0183 +. TI 2013-03-19 10:14:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ;[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-03-19 10:14:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ;[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-03-19 10:14:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ;[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 85 83 08 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 09)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 10)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2013-03-19 10:18:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop SP table >
0173 +. DC 07-F0 MDP_SP_CTRL_MANU
0174 BC (61)
0175 C. -----
0176 C. MDP_SP_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload SP Observation Table>
0180 . S. RAM ram-282:MDP_OBS_S
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_S >
0184 +. DC 07-F0 MDP_DUMP_SPTBL
0185 BC (83 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_S verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 . C. < Upload DPL table >
0191 +. DC 07-F0 MDP_FG_CTRL_MANU
0192 BC (51)
0193 . C. -----

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0194 C. MDP_FG_CTRL_MODE = MANU [ ]
0195 C. -----
0196 C.
0197 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°ñESTS_CHK°ðOFF°È°¹°è
0198 C.
0199 . S. RAM ram-271:MDP_DPL
0200 ( )
0201 C.
0202 . C. < Dump RAMID=MDP_DPL >
0203 +. DC 07-F0 MDP_DUMP_FGTBL
0204 BC (82 07 00 38 b8 00 40)
0205 C. -----
0206 C. MDP_DPL verify = OK [ ]
0207 C. -----
0208 C.
0209 C. STS_CHK°ðON°È°¹°è
0210 C.
0211 . C. < Update MDP DSC PAR1 >
0212 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0213 BC (4c)
0214 C. MDP_CMD_CODE = F04C0700[ ]
0215 C. MDP_CMD_CNT (count-up 1) [ ]
0216 C. -----
0217 C.
0218 . C.
0219 C. *****
0220 C. SOT TI command set
0221 C. *****
0222 C. Execute, after the success of TBL upload.
0223 +. TI 2013-03-19 10:18:18.0
0224 DC 07-F0 MDP_SOT_MODE_OBSV
0225 BC (40)
0226 C. -----
0227 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0228 C. -----
0229 C.
0230 C.
0231 . C. ***** MDP `ûÃîñî»ò¼¥ñÈÁ°¹°èDCBC•×²è *****
0232 C. (¼á°î¥ó¥Ã¥É¥¥¥¥¥¥¥¥¥¥¥ç¥è°È¼°¼¼Á»Û°¹°è)
0233 . S. DC-BC dcbc-402:DCBC
0234 (MDP_known_event)
0235 C.
0236 C.
0237 . C. ***** ¥Ð¥¹•İ Daily±;îÑ°È`Ø°¹°èDCBC•×²è *****
0238 . S. DC-BC dcbc-153:DCBC
0239 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0240 C.
0241 C.
0242 . C. ;ãLOS¥Á¥S¥Ã¥`¼Á»Û;ã
0243 C.
0244 . C. ***** LOS *****
0245 C.
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\*\*\* OP Sequence for XRT \*\*\*

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2013/03/19 10:28:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/03/19 10:28:56.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2013/03/19 10:29:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 53 4b 01 58
2013/03/19 10:29:16.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2013/03/19 10:29:18.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2013/03/19 10:29:20.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2013/03/19 10:29:22.0 XRT_ARS_DIS_437_OG [0x1b5]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/03/19 10:31:54.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/03/19 10:31:56.0 XRT_QT_PROG_SET_445_OG [0x1bd]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 09
2013/03/19 10:31:58.0 XRT_FL_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2013/03/19 10:32:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/03/19 15:18:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/03/19 15:18:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/03/19 15:18:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/03/19 15:21:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/03/19 15:41:30.0 XRT_Custom_434_OG [0x1b2]
2013/03/19 15:42:30.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/03/19 16:54:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/03/19 16:54:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/03/19 16:54:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/03/19 16:57:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/03/19 17:18:30.0 XRT_Custom_434_OG [0x1b2]
2013/03/19 17:19:30.0 XRT_CTRL_AUTO_413_OG [0x19d]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/03/19 17:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/03/19 17:59:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2013/03/19 18:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2013/03/19 18:00:16.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2013/03/19 18:00:18.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2013/03/19 18:00:20.0 XRT_ARS_DIS_406_OG [0x196]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/03/19 18:02:58.0 XRT_QT_PROG_SET_417_OG [0x1a1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2013/03/19 18:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/03/19 18:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/03/19 18:09:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2013/03/19 18:10:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 ac cd
2013/03/19 18:10:16.0 XRT_FLD_ENA_428_OG [0x1ac]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2013/03/19 18:10:18.0 XRT_FLRCTRL_ENA_429_OG [0x1ad]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2013/03/19 18:10:20.0 XRT_AEC_RESET_423_OG [0x1a7]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2013/03/19 18:10:22.0 XRT_ARS_DIS_437_OG [0x1b5]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/03/19 18:12:54.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/03/19 18:12:56.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2013/03/19 18:12:58.0 XRT_FL_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 10
2013/03/19 18:13:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/03/19 18:31:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/03/19 18:31:32.0 XRT_FLD_RESET_424_OG [0x1a8]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/03/19 18:31:34.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/03/19 18:34:44.0 XRT_PREFLR_STOP_433_OG [0x1b1]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/03/19 19:00:54.0 XRT_CTRL_MANU_402_OG [0x192]

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2013/03/19	19:00:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	19:01:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/19	19:01:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	00 00 00 d6 67	
2013/03/19	19:01:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/19	19:01:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/19	19:01:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	19:03:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	19:03:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	19:03:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2013/03/19	19:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/03/19	19:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	19:16:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	19:17:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/19	19:17:16.0	XRT_FLD_DIS_404_OG [0x194]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/03/19	19:17:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/19	19:17:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/19	19:19:58.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	19:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2013/03/19	19:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	19:32:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	19:33:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/19	19:33:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	00 00 00 29 99	
2013/03/19	19:33:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/19	19:33:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/19	19:33:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	19:35:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	19:35:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	19:35:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2013/03/19	19:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/03/19	19:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	19:48:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	19:49:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/19	19:49:16.0	XRT_FLD_ENA_428_OG [0x1ac]	AOCU_NM	5	02-76	00 00 00 53 33	
2013/03/19	19:49:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/19	19:49:20.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/19	19:49:22.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	19:51:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	19:51:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	19:51:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2013/03/19	19:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/03/19	20:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	20:09:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	20:09:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	20:12:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/19	20:38:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/19	20:38:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	



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2013/03/19	20:38:30.0	AOCS_ORe-point_Start_7_OG [0x09d] AOCU_NM	5	02-76	00 d6 36 b7 8e
2013/03/19	20:38:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/19	20:38:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/19	20:38:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/19	20:38:52.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/19	20:41:24.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/19	20:41:26.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2013/03/19	20:41:28.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/19	20:41:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/19	20:48:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/19	20:48:26.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/19	20:48:30.0	AOCS_ORe-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 b4 b5 db 75
2013/03/19	20:48:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/19	20:48:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/19	20:48:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/19	20:48:52.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/19	20:51:24.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/19	20:51:26.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2013/03/19	20:51:28.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/19	20:51:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/19	21:04:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/19	21:04:26.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/19	21:04:30.0	AOCS_ORe-point_Start_9_OG [0x09f] AOCU_NM	5	02-76	00 ac 5b 00 00
2013/03/19	21:04:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/19	21:04:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/19	21:04:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/19	21:04:52.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/19	21:07:24.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/19	21:07:26.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2013/03/19	21:07:28.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/19	21:07:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/19	21:20:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/19	21:20:26.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/19	21:20:30.0	AOCS_ORe-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00 b4 b5 24 8b
2013/03/19	21:20:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/19	21:20:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/19	21:20:50.0	XRT_AEC_RESET_423_OG [0x1a7] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/19	21:20:52.0	XRT_ARS_DIS_437_OG [0x1b5] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/19	21:23:24.0	XRT_FLD_RESET_424_OG [0x1a8] MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/19	21:23:26.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2013/03/19	21:23:28.0	XRT_FL_PROG_SET_444_OG [0x1bc] MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2013/03/19	21:23:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/19	21:36:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/19	21:36:26.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/19	21:36:30.0	AOCS_ORe-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00 d6 36 48 72
2013/03/19	21:36:46.0	XRT_FLD_ENA_428_OG [0x1ac] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/19	21:36:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]			

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2013/03/19	21:36:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	21:36:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	21:39:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	21:39:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2013/03/19	21:39:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/19	21:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	21:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	21:46:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	21:46:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/19	21:49:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/19	22:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	22:14:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/03/19	22:14:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	29 ca b7 8e
2013/03/19	22:14:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/19	22:14:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/19	22:14:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	22:14:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	22:17:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	22:17:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2013/03/19	22:17:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/19	22:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	22:24:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	22:24:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/03/19	22:24:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	4b 4b db 75
2013/03/19	22:24:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/19	22:24:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/19	22:24:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	22:24:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	22:27:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	22:27:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2013/03/19	22:27:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/19	22:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	22:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	22:40:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/03/19	22:40:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	53 a5 00 00
2013/03/19	22:40:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/19	22:40:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/19	22:40:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/19	22:40:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/19	22:43:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/19	22:43:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2013/03/19	22:43:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2013/03/19	22:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/19	22:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/19	22:56:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/03/19	22:56:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	4b 4b 24 8b

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2013/03/19	22:56:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/19	22:56:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/19	22:56:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/19	22:56:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/19	22:59:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/19	22:59:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/03/19	22:59:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/19	22:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/19	23:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/19	23:12:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/03/19	23:12:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2013/03/19	23:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/19	23:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/19	23:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/19	23:12:52.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/19	23:15:24.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/19	23:15:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/03/19	23:15:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/19	23:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/19	23:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/19	23:24:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/19	23:24:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/19	23:27:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/20	00:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/20	00:19:56.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/03/20	00:20:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 e5 59 b1 cb				
2013/03/20	00:20:16.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/03/20	00:20:18.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/03/20	00:20:20.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/03/20	00:20:22.0	XRT_ARS_DIS_437_OG [0x1b5]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/03/20	00:22:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/20	00:22:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2013/03/20	00:22:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2013/03/20	00:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/20	00:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/20	00:52:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/20	00:52:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/20	00:55:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/20	01:07:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/20	01:08:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/20	02:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/20	02:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/20	02:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/20	02:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/20	02:43:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/20	02:44:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/03/20	03:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/20	03:51:02.0	XRT_FLD_RESET_424_OG [0x1a8]							

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2013/03/20	03:51:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/20	03:54:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/20	04:21:00.0	XRT_Custom_434_OG [0x1b2]							
2013/03/20	04:22:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/20	05:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	05:31:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/20	05:31:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/20	05:34:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/20	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	06:07:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/03/20	06:08:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/03/20	06:08:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/20	06:08:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/20	06:08:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/20	06:10:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2013/03/20	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/20	06:17:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	06:18:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 53 4b 01 58			
2013/03/20	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	17:56:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/03/20	17:56:30.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/03/20	17:56:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/20	17:56:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/20	17:56:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/20	17:59:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2013/03/20	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/20	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	18:06:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/03/20	18:06:30.0	AOCS_Orе-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 e5 59 b1 cb			
2013/03/20	18:06:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/03/20	18:06:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/03/20	18:06:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/03/20	18:06:52.0	XRT_ARS_DIS_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/20	18:09:24.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/20	18:09:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2013/03/20	18:09:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10			
2013/03/20	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/20	19:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	19:08:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/20	19:08:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/20	19:11:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/20	19:31:30.0	XRT_Custom_434_OG [0x1b2]							
2013/03/20	19:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/20	20:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/20	20:45:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/20	20:45:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/20	20:48:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2013/03/20	21:09:00.0	XRT_Custom_434_OG [0x1b2]				
2013/03/20	21:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/20	22:23:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/20	22:23:02.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/20	22:23:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/20	22:26:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/20	22:44:30.0	XRT_Custom_434_OG [0x1b2]				
2013/03/20	22:45:30.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/21	00:00:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/21	00:00:32.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/21	00:00:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/21	00:03:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/21	00:09:00.0	XRT_Custom_434_OG [0x1b2]				
2013/03/21	00:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/21	01:24:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/21	01:24:32.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/21	01:24:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/21	01:27:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/21	03:20:00.0	XRT_Custom_434_OG [0x1b2]				
2013/03/21	03:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/21	04:29:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/21	04:29:02.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/21	04:29:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/21	04:32:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/21	05:41:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/21	05:41:26.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/21	05:41:30.0	AOCS_ORe-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2013/03/21	05:41:46.0	XRT_FLD_DIS_404_OG [0x194]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/21	05:41:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/21	05:41:50.0	XRT_ARS_DIS_406_OG [0x196]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/21	05:44:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2013/03/21	05:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/21	05:51:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/21	05:51:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/03/21	05:51:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]				
		AOCU_NM	5	02-76	00 e5 59 b1 cb	
2013/03/21	05:51:46.0	XRT_FLD_ENA_428_OG [0x1ac]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/21	05:51:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/21	05:51:50.0	XRT_AEC_RESET_423_OG [0x1a7]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/21	05:51:52.0	XRT_ARS_DIS_437_OG [0x1b5]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/21	05:54:24.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/21	05:54:26.0	XRT_QT_PROG_SET_415_OG [0x19f]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2013/03/21	05:54:28.0	XRT_FL_PROG_SET_444_OG [0x1bc]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10	
2013/03/21	05:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/21	06:09:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/21	06:09:32.0	XRT_FLD_RESET_424_OG [0x1a8]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/21	06:09:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/21	06:12:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/21	06:35:00.0	XRT_Custom_434_OG [0x1b2]				
2013/03/21	06:36:00.0	XRT_CTRL_AUTO_413_OG [0x19d]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2013/03/21	07:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/03/21	07:50:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/03/21	07:50:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/03/21	07:53:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/03/21	09:31:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00 00				