

# XRT Timeline to be uploaded on 2013/03/28

Period: 2013/03/28 09:20:00 - 2013/04/02 11:03:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #1978: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2898) + Thin-Be(250)													
Term	Pointing (x, y)			Comment									
03/29 17:50:30 - 03/29 17:52:00	Fixed ( 0.0, 0.0)			synoptic, shifted manually.									
03/30 06:03:00 - 03/30 06:09:54	Fixed ( 0.0, 0.0)			synoptic									
<b>PROG= 04 1-time(s)</b>													
└─ Subr= 1 1-time(s) 14.0sec													
└─ Seqn= 16 1-time(s) 4.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Seqn= 8 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 14 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #197A: VLS Gband and Ti/Poly Test, G-band (33ms) ,Ti/Poly (1024ms)													
Term	Pointing (x, y)			Comment									
03/29 17:55:06 - 03/29 17:57:24	Fixed ( 0.0, 0.0)			synoptic, shifted manually.									
<b>PROG= 05 2-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 25 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al open Safe Norm 1.00s Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms)													
Term	Pointing (x, y)			Comment									
03/29 18:00:30 - 03/29 18:09:30	Track ( 31.0, 343.8) @ 03/29 17:57:30			# AR 11704									
03/29 19:18:00 - 03/30 05:59:54	Track ( 42.6, 343.7) @ 03/29 19:15:00			* AR 11704									
03/30 06:13:00 - 03/30 08:29:00	Track ( 140.0, 342.2) @ 03/30 06:10:00			# Cont.									
<b>PROG= 02 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 18 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec													
└─ Seqn= 13 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec													
└─ Seqn= 17 4-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 20 150-time(s) 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #197B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long-w leak image-33ms													
Term		Pointing (x, y)					Comment						
03/29 18:38:00 - 03/29 18:44:54		Fixed ( -528.4, -528.4)					* XRT post-bakeout four-quadrant sequence.						
<b>PROG= 06 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 1 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─┬─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec													
└─┬─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

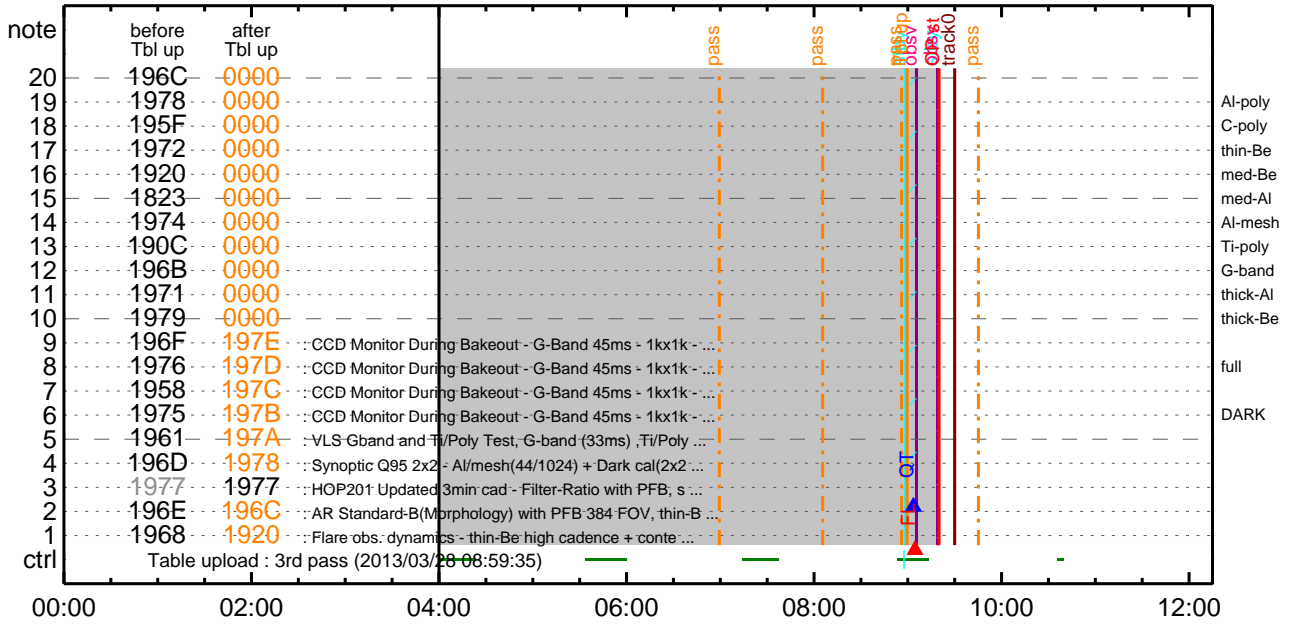
XOB #197C: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long-w leak image-33ms													
Term		Pointing (x, y)					Comment						
03/29 18:48:00 - 03/29 18:54:54		Fixed ( 528.4, -528.4)											
<b>PROG= 07 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 2 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─┬─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec													
└─┬─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #197D: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long-w leak image-33ms													
Term		Pointing (x, y)					Comment						
03/29 18:58:00 - 03/29 19:04:54		Fixed ( 528.4, 528.4)											
<b>PROG= 08 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 3 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─┬─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec													
└─┬─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

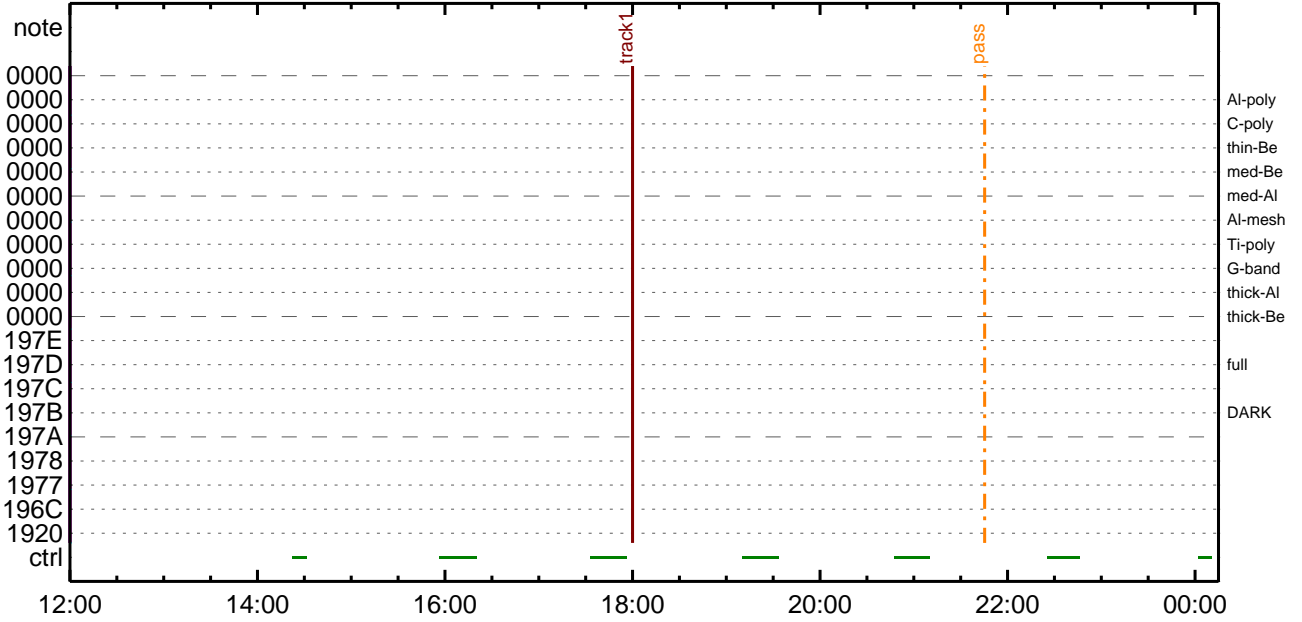
XOB #197E: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long-w leak image-33ms													
Term		Pointing (x, y)					Comment						
03/29 19:08:05 - 03/29 19:14:54		Fixed ( -528.4, 528.4)					* Post-bakeout four-quadrant, final pointing.						
<b>PROG= 09 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 4 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─┬─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec



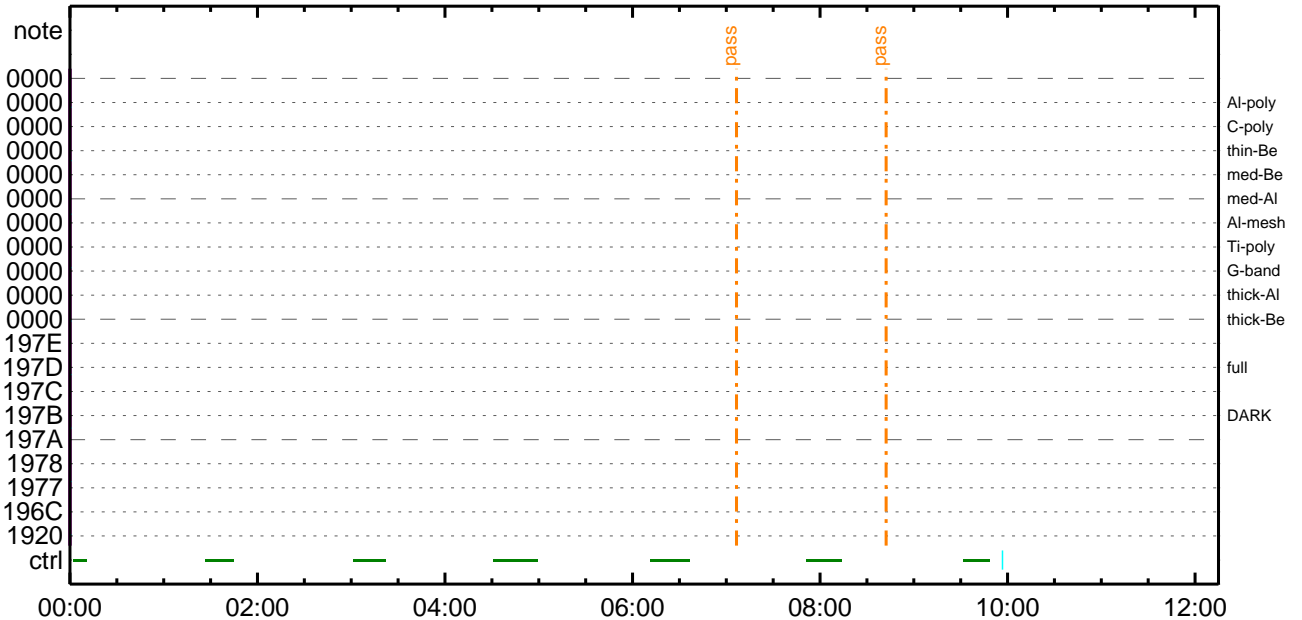
### CMDI #0362 2013/03/28



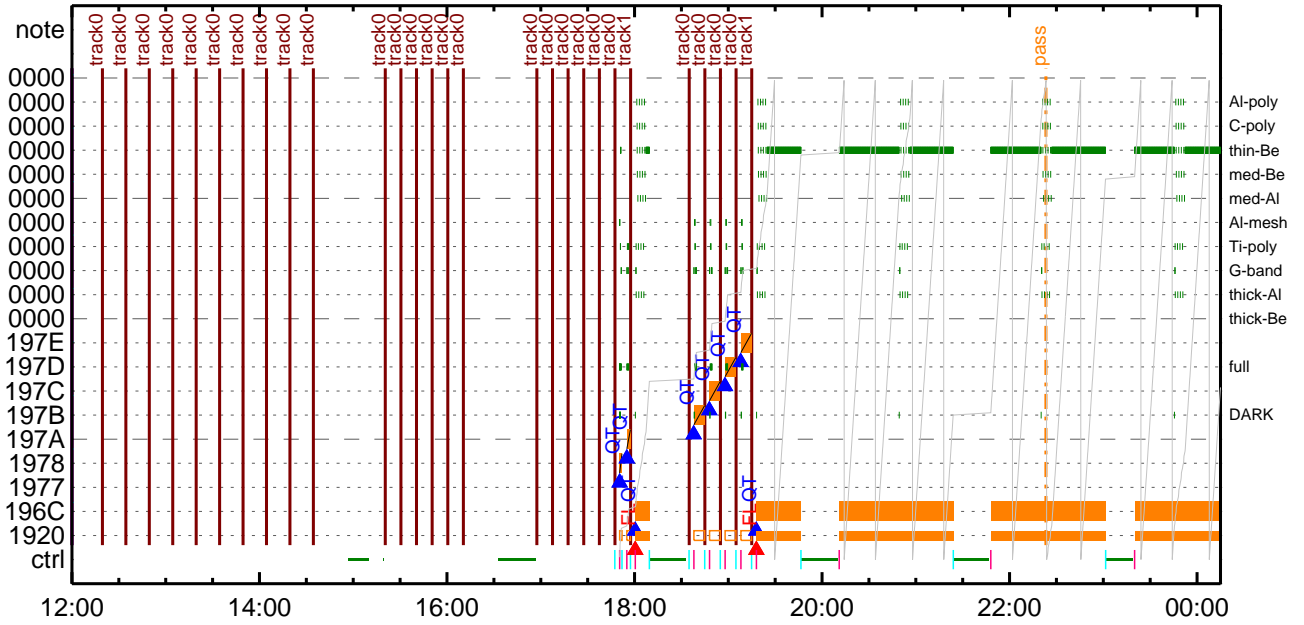
### CMDI #0362 2013/03/28



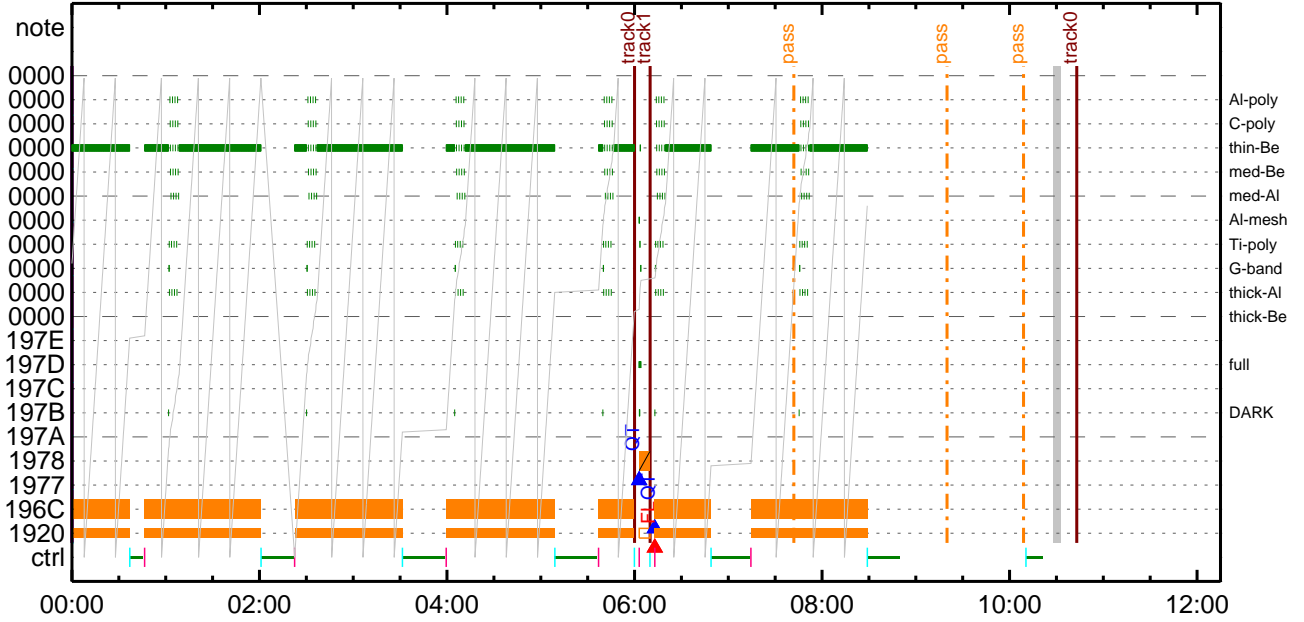
### CMDI #0362 2013/03/29



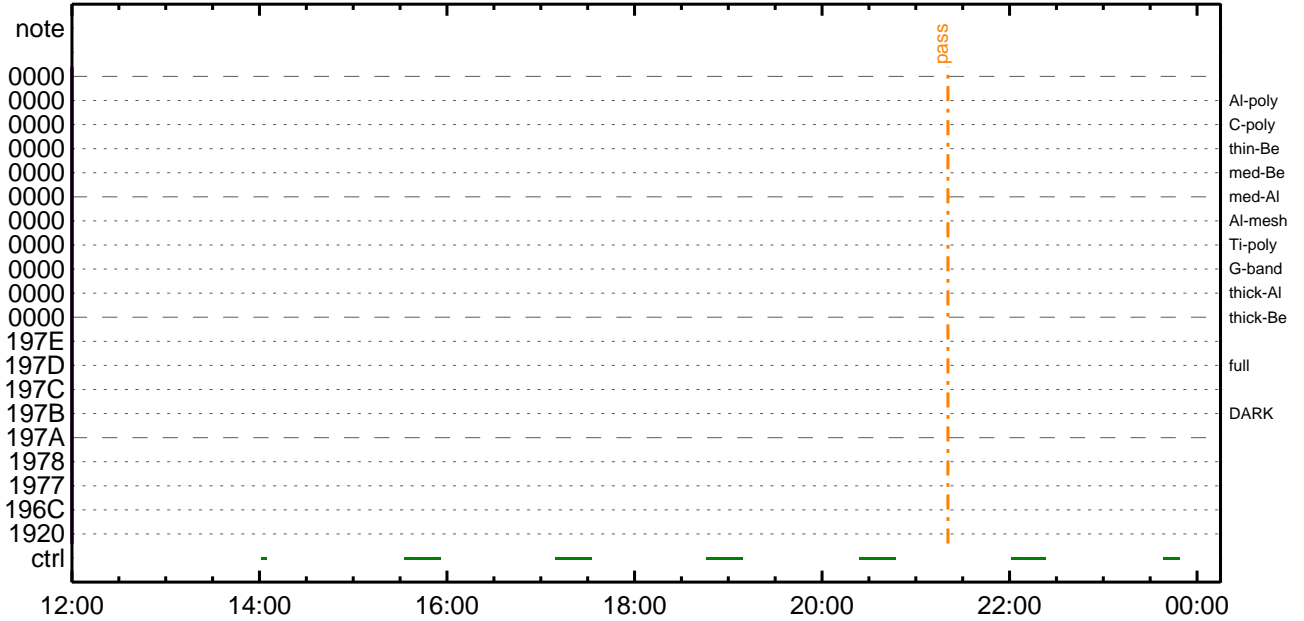
### CMDI #0362 2013/03/29



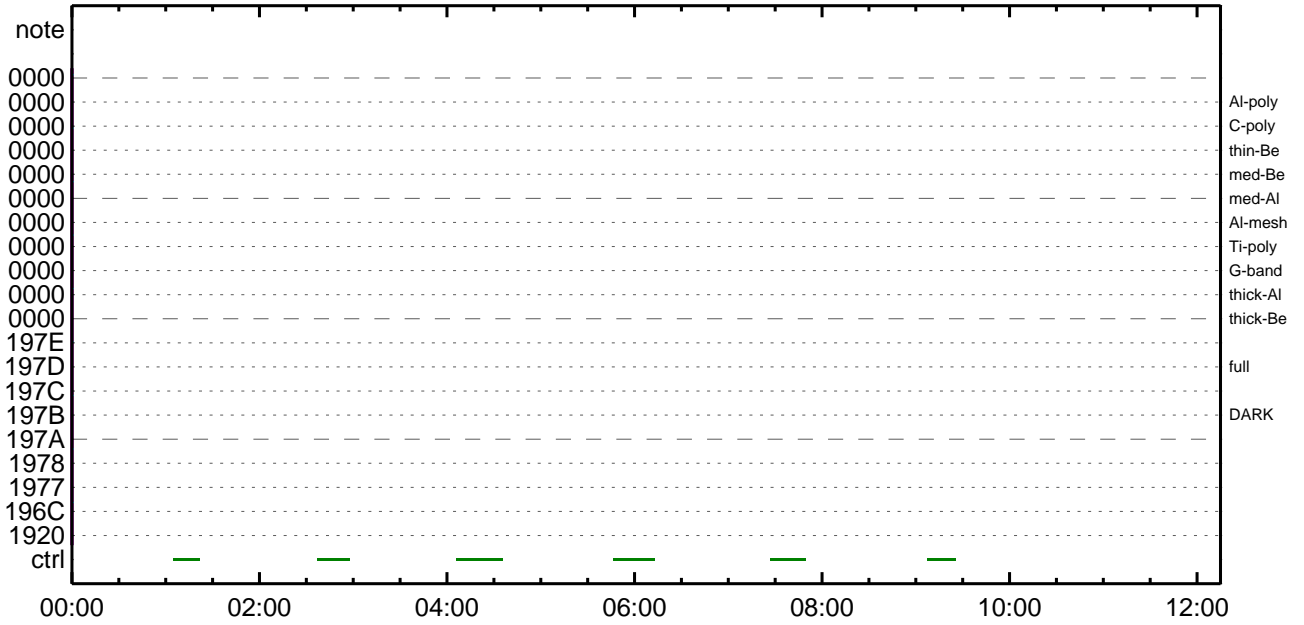
### CMDI #0362 2013/03/30



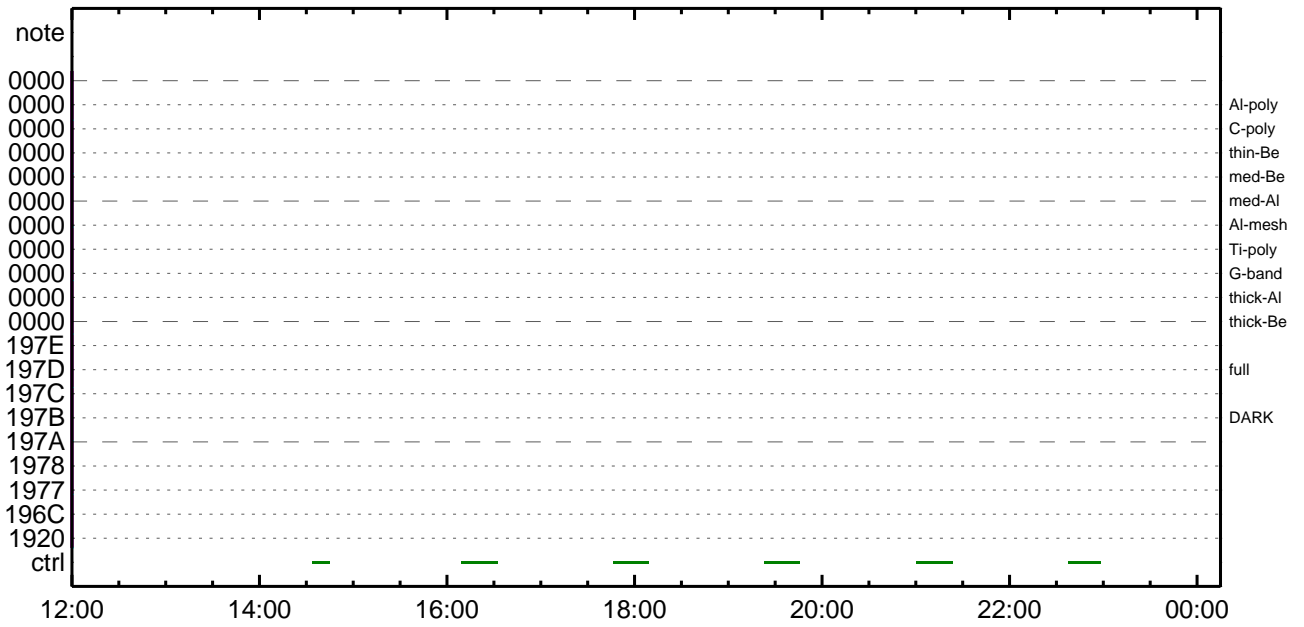
### CMDI #0362 2013/03/30



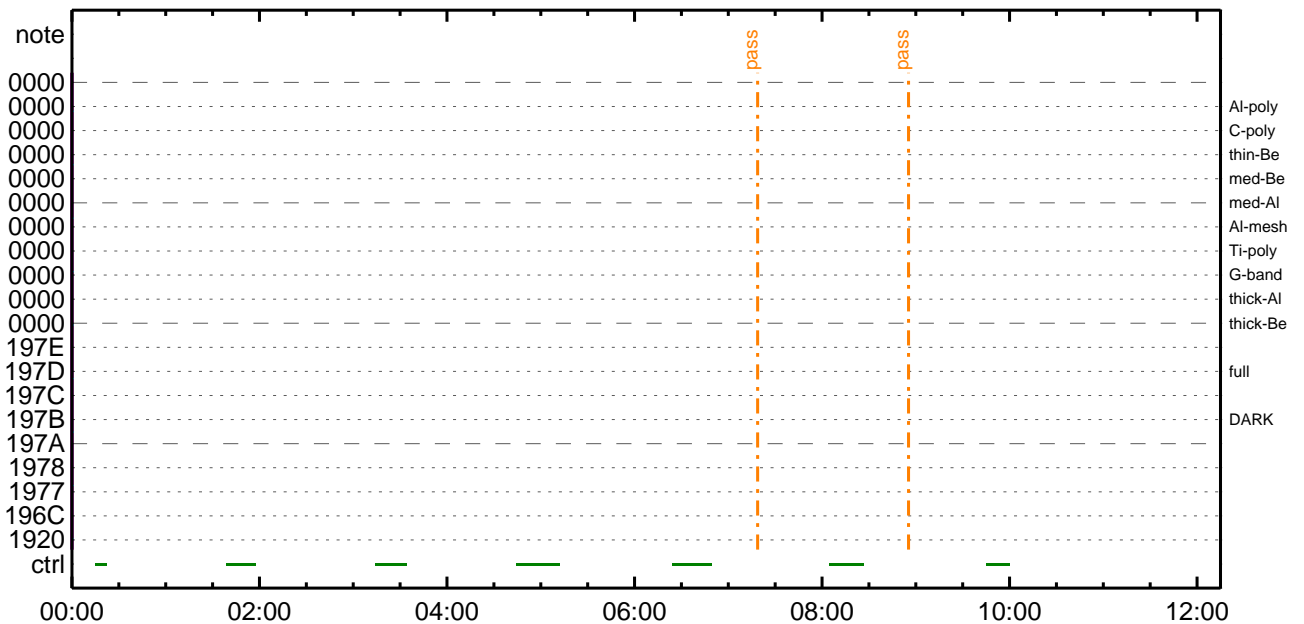
CMDI #0362 2013/03/31



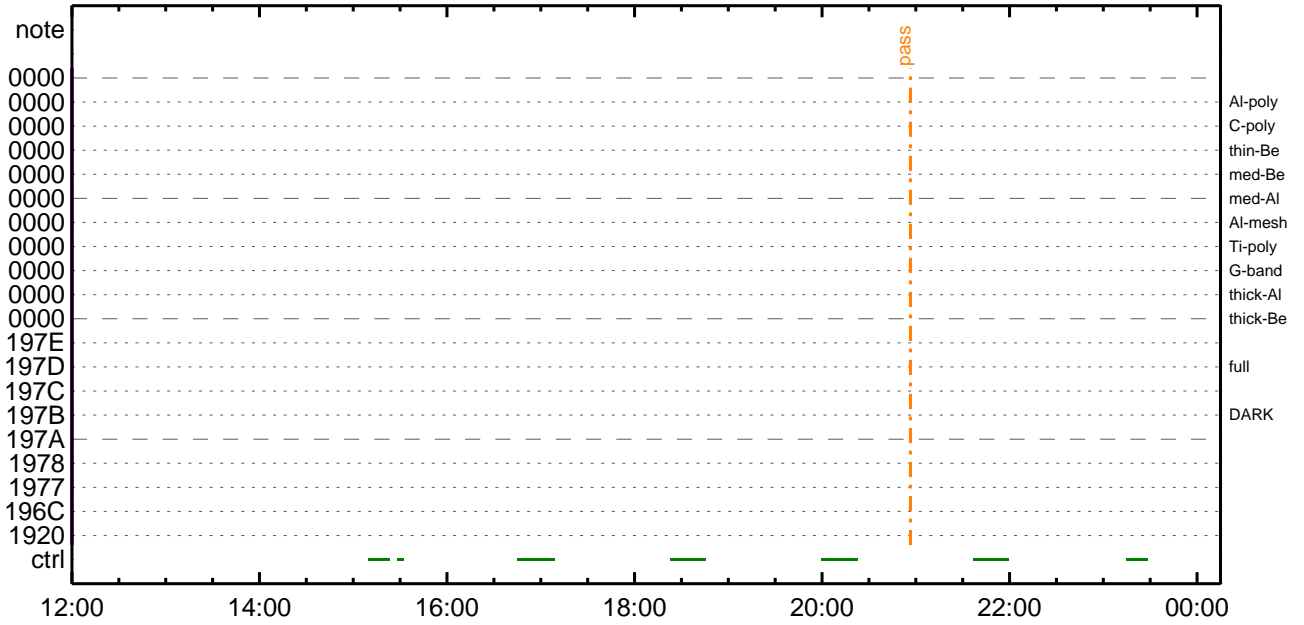
CMDI #0362 2013/03/31



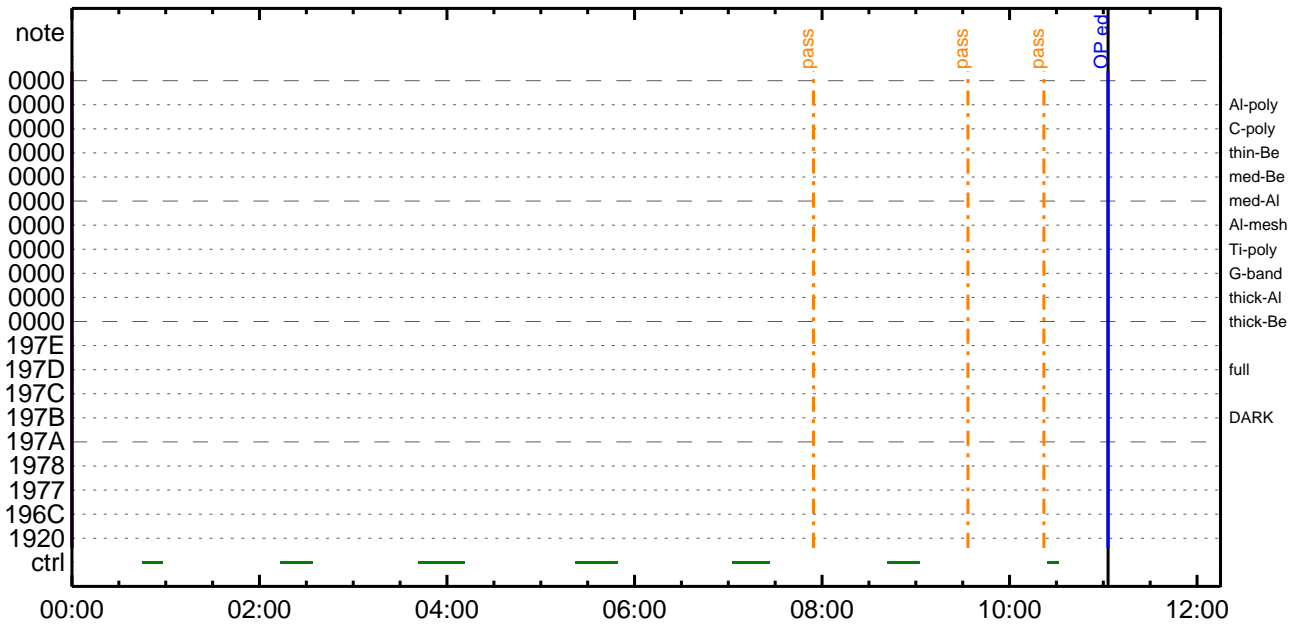
CMDI #0362 2013/04/01



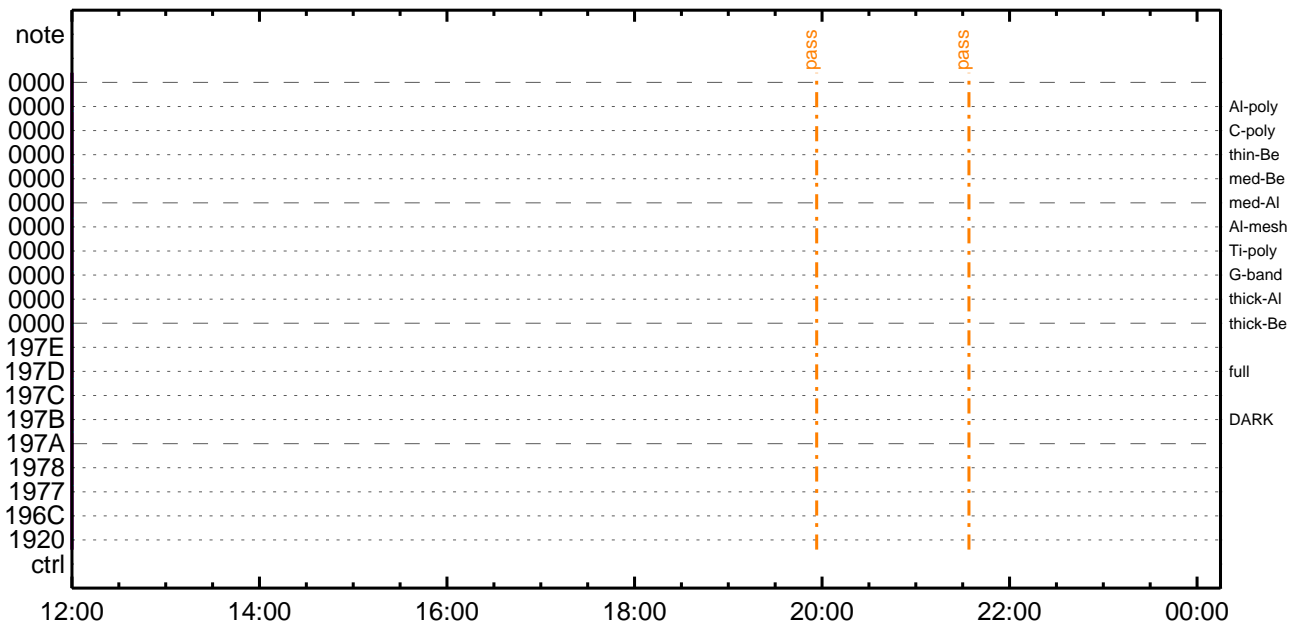
CMDI #0362 2013/04/01



CMDI #0362 2013/04/02



CMDI #0362 2013/04/02



(a) Spacecraft Operation Procedure (real-commands)

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main-549 2013-03-28 13:05:46 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ä
0005 C.
0006 C. YÀY$;¼Y³YF¥ÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ÒÀÒ•µ°Ë»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÒÈ¼°ÇÒÒ•Ò¿¼í¹ÇÒÍ;çÀ®, ùÒ¹ÒÈÒÒÒÇÁ+¿®Ò•ÒÈÒÒÒ³ÒÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-549:OP
0020 ()
0021 . S. OG og-549:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×¼ªªÍ»Ò¿³ÍÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGÒÍ¼È¹Ç•è²ÍOKÒ¿³ÍÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×¼ªªÍ»Ò¿³ÍÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGÒÍ¼È¹Ç•è²ÍOKÒ¿³ÍÇ$
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×¼ªªÍ»Ò¿³ÍÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPÒÍ¼È¹Ç•è²ÍOKÒ¿³ÍÇ$
0081 C.
0082 . C. ***** òÈ²¼ÒÍ¼Ä´¶¼ÒÈÈ-ÒÒÄ+¿® (¼áµ-YÁYÖY×¼ªªÇÒ¿ÒÄÖÁæÇªªÒ²ÒÈ¼í¹ÇÒÇÒâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼;Yí;¼YÉ;ÈÒÍáÒ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADÒ-Á+¿®NGÒÍ¼í¹Ç;ç°È²¼ÒÍTI-CMDÁ+¿®ÒÍ¼Á¹ÒÒÒÒÒ³ÒÈ;f

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0096 C.                0303; SET EDUMP I A ± 0 i Y N Y 1 a Ç 1 0 a | a 3 a E ; E
0097 C.
0098 . C. TI Y 3 Y P Y 6 Y E 0 d A D I j (UT)
0099 +. TI 2013-03-28 09:15:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2013-03-28 09:15:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2013-03-28 09:15:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2013-03-28 09:19:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °E 2 % a I A e % i I N a I Y A Y S Y A Y - 1 a I U
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]            EQ        ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]              EQ        4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]            EQ        DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]             EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TI I I ° e Y A Y 6 Y x
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]            EQ        07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]            EQ        2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]            EQ        3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]           EQ        0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]             EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]              EQ        7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]             EQ        0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]           EQ        32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]          EQ        4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]             EQ        EXEC
0140 C.
0141 . C. Y A Y 6 Y x % a I » a d 3 I Ç S
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]             EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL a I % E 1 Ç • e 2 I O K a d 3 I Ç S
0145 C.
0146 . C. DHU Y a ; % Y E ; E % Y % , Y i ; % Y E ; E a d I a a 1
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]              EQ        2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]             EQ        0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]           EQ        32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]          EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2013-03-28 09:19:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2013-03-28 09:19:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]            EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2013-03-28 09:19:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 . C.                [ ] [HK1_TI_CMD_NUM]            EQ        1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 . C. ***** MDP ' u A I a I » o % Y a E A D a 1 a e DCBC • x 2 e *****
0181 C. (% a ° i Y O Y A Y E Y P Y E Y A Y Ç Y e a E % a a % A » U a 1 a e )
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** Y D Y 1 • I Daily ± z I N a E ' 0 a 1 a e DCBC • x 2 e *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ; a L O S Y A Y S Y A Y - % A » U ; a
0192 C.
0193 . C. ***** LOS *****

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 32s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-03-28 09:19:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîï»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0153 C. (%ã°ï¥Ö¥Ä¥Ë¥Ï¥Ë¥ã¥ç¥èñ¾¼¼¼Ä»Û¹ñè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¥Ð¥¹•Ï Daily±çÏÑñË'Ø¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOS¥Á¥§¥Ä¥¹¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-551 2013-03-28 13:05:46 106 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°•È×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈ%µ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□í;çÁ®, ù□¹□è□□□çÁ+¿®□•□È□□□□è;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 08 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 85 83 08 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c c0 c0 10 10)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 40 c0 10 10)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0e 40 40 10 10)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 + DC 07-F0 MDP_XRT_FLD_ENA
0065 BC (d8)
0066 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067 BC (c8)
0068 + DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 + DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 + DC 07-F0 MDP_XRT_FLD_RESET
0073 BC (da)
0074 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0075 BC (c4 03)
0076 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0077 BC (c5 01)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C.
0081 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0082 C.
0083 +. DC 07-F0 MDP_XRT_MODE_OBSV
0084 BC (c2)
0085 +. TI 2013-03-28 09:19:02.0
0086 DC 07-F0 MDP_XRT_MODE_OBSV
0087 BC (c2)
0088 . C. ----- Success Verify ? OK / NG ____
0089 C.
0090 C. ***** XRT END *****
0091 C.
0092 . C. ***** MDP `úÁííí»ò¼Y□ÈÁ□□□èDCBC•×²è *****
0093 C. (¼°íYÖYÁYÈY¥YÉYÁYçYè□È¼□□¼Á»Û□□è)
0094 . S. DC-BC dcbc-402:DCBC
0095 (MDP_known_event)
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0096 C.  
0097 C.  
0098 . C. \*\*\*\*\* ¥Ð¥¹•ï Daily±¿ÍÑ¤È´Ø¤¹¤èDCBC•x²è \*\*\*\*\*  
0099 . S. DC-BC dcbc-153:DCBC  
0100 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0101 C.  
0102 C.  
0103 . C. ;ãLOS¥Á¥\$¥Ã¥-¼Á»Û;ã  
0104 C.  
0105 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0106 C.

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XRT\_OGLIST\_0362.chk

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\*\*\* OP Sequence for XRT \*\*\*

2013/03/28	09:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	53	4b	01	58
2013/03/28	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/03/29	09:56:45.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/29	09:56:47.0	XRT_TCIB_XRT_S_HTR_A_DIS_401_OG [0x191]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2013/03/29	12:19:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	01	81	50	e5
2013/03/29	12:34:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	01	81	3f	33
2013/03/29	12:49:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	01	81	2d	81
2013/03/29	13:04:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	01	81	1b	ce
2013/03/29	13:19:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	01	81	0a	0c
2013/03/29	13:34:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	01	81	f8	5b
2013/03/29	13:49:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	01	81	e6	a8
2013/03/29	14:04:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	01	81	d4	fe
2013/03/29	14:19:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	01	81	c3	4c
2013/03/29	14:34:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	01	81	b1	9a
2013/03/29	15:20:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	14	9b	51	3f
2013/03/29	15:30:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	11	b2	43	ce
2013/03/29	15:40:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	0f	be	34	00
2013/03/29	15:50:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	0e	97	23	8d
2013/03/29	16:00:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	0d	f3	12	c0
2013/03/29	16:10:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	0d	60	00	31
2013/03/29	16:57:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	0d	db	ef	e8
2013/03/29	17:07:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	0e	97	df	03
2013/03/29	17:17:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	0f	be	ce	a8
2013/03/29	17:27:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	11	b2	be	da
2013/03/29	17:37:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	16	24	b0	19
2013/03/29	17:47:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/29	17:47:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/03/29	17:47:30.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/03/29	17:47:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/03/29	17:47:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/03/29	17:47:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/29	17:50:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 04
2013/03/29	17:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/03/29	17:52:00.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/29	17:52:02.5	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/03/29	17:52:22.5	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/03/29	17:52:24.5	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/03/29	17:52:26.5	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/03/29	17:55:04.5	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 05
2013/03/29	17:55:06.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/03/29	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/03/29	17:57:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2013/03/29	17:57:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/03/29	17:57:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/03/29	17:57:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/03/29	17:57:50.0	XRT_AEC_RESET_413_OG [0x19d]							

2013/03/29	17:57:52.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/29	18:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/29	18:00:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2013/03/29	18:00:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01		
2013/03/29	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/29	18:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/29	18:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/29	18:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/29	18:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/29	18:34:54.0	XRT_CTRL_MANU_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/29	18:35:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00	2e f9 2e f9		
2013/03/29	18:37:32.0	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2013/03/29	18:37:52.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2013/03/29	18:37:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/29	18:37:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/29	18:37:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/29	18:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/29	18:44:54.0	XRT_CTRL_MANU_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/29	18:45:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00	2e f9 d1 07		
2013/03/29	18:47:32.0	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2013/03/29	18:47:52.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2013/03/29	18:47:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/29	18:47:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/29	18:47:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/29	18:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/29	18:54:54.0	XRT_CTRL_MANU_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/29	18:55:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00	d1 07 d1 07		
2013/03/29	18:57:32.0	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2013/03/29	18:57:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2013/03/29	18:57:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/29	18:57:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/29	18:57:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/29	18:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/29	19:04:54.0	XRT_CTRL_MANU_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/29	19:05:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]	AOCU_NM	5	02-76	00	d1 07 2e f9		
2013/03/29	19:07:32.0	XRT_ROI_A_427_OG [0x1ab]	MDP_XRT_ROI_SET	6	07-F0	cd	05 85 83 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd	06 85 83 08 06		
			MDP_XRT_ROI_SET	6	07-F0	cd	07 80 80 20 20		
			MDP_XRT_ROI_SET	6	07-F0	cd	08 80 80 20 08		
			MDP_XRT_ROI_SET	6	07-F0	cd	09 80 80 08 20		
			MDP_XRT_ROI_SET	6	07-F0	cd	0a 85 83 08 08		
			MDP_XRT_ROI_SET	6	07-F0	cd	0b 85 83 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd	0c c0 40 10 10		
2013/03/29	19:07:32.5	XRT_ROI_B_428_OG [0x1ac]	MDP_XRT_ROI_SET	6	07-F0	cd	0c c0 40 10 10		
			MDP_XRT_ROI_SET	6	07-F0	cd	0f 80 80 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd	10 80 80 08 08		
2013/03/29	19:07:37.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2013/03/29	19:07:57.5	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2013/03/29	19:07:59.5	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/29	19:08:01.5	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/29	19:08:03.5	XRT_ARS_DIS_423_OG [0x1a7]							



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2013/03/29	19:08:05.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/29	19:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/29	19:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/29	19:15:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2013/03/29	19:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/29	19:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/29	19:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/29	19:15:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/29	19:17:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/29	19:17:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/03/29	19:17:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2013/03/29	19:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/29	19:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/29	19:46:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/29	19:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/29	19:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/29	20:10:00.0	XRT_Custom_430_OG [0x1ae]				
2013/03/29	20:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/29	21:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/29	21:24:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/29	21:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/29	21:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/29	21:47:00.0	XRT_Custom_430_OG [0x1ae]				
2013/03/29	21:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/29	23:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/29	23:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/29	23:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/29	23:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/29	23:19:00.0	XRT_Custom_430_OG [0x1ae]				
2013/03/29	23:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	00:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	00:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	00:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	00:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	00:45:30.0	XRT_Custom_430_OG [0x1ae]				
2013/03/30	00:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	02:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	02:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	02:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	02:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	02:21:30.0	XRT_Custom_430_OG [0x1ae]				
2013/03/30	02:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	03:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	03:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	03:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	03:58:30.0	XRT_Custom_430_OG [0x1ae]				
2013/03/30	03:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	05:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2013/03/30	05:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/30	05:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/30	05:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/30	05:36:00.0	XRT_Custom_430_OG [0x1ae]					
2013/03/30	05:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/30	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/30	06:00:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/03/30	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/30	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/30	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/30	06:02:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2013/03/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/30	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/30	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/03/30	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/03/30	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/30	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/30	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/30	06:10:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/30	06:12:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/30	06:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2013/03/30	06:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2013/03/30	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/30	06:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/30	06:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/30	06:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/30	06:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/30	07:13:30.0	XRT_Custom_430_OG [0x1ae]					
2013/03/30	07:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/30	08:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/30	08:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/30	08:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/30	08:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/30	10:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/30	10:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/30	10:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/30	10:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/30	10:43:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00 00 00 00 00	