

XRT Timeline to be uploaded on 2013/03/30

Period: 2013/03/30 10:33:00 - 2013/04/04 10:53:00

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Normal mode

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XOB #197F: HOP226_AR obs with PFB 384 FOV, Al-poly+ multifilter (512FOV) context, 150s-cads, at 1064 1048, - shorter G-band (33ms) w/ G-Band VLS CI

Term	Pointing (x, y)	Comment
03/30 10:46:00 - 03/30 18:03:54	Track (200.6, 336.6) ^{03/30 10:43:00}	# OP start + 10min, track AR 11704, with HOP 226, non-Maxwellian distributions, over first p
PROG= 09 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 18 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 13 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 17 4-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 27 5-time(s) 150.0sec		
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 6.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250)

Term	Pointing (x, y)	Comment
03/30 18:07:00 - 03/30 18:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min
03/31 06:26:00 - 03/31 06:32:54	Fixed (0.0, 0.0)	synoptic, shifted 23.0 min
03/31 18:12:30 - 03/31 18:19:24	Fixed (0.0, 0.0)	synoptic, shifted 9.5 min
04/01 05:59:30 - 04/01 06:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min
04/01 18:02:30 - 04/01 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min
04/02 06:03:00 - 04/02 06:09:54	Fixed (0.0, 0.0)	synoptic
PROG= 08 1-time(s)		
Subr= 1 1-time(s) 14.0sec		
Seqn= 16 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 26 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 19 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 14 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #196D: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad, shorter G-band (32ms) w/ G-Band VLS CI

Term	Pointing (x, y)	Comment
03/30 18:17:00 - 03/31 06:22:54	Track (266.0, 334.4) ^{03/30 18:14:00}	# HOP180 NOAA11704
03/31 14:46:00 - 03/31 17:46:00	Track (-380.8, -260.9) ^{03/31 14:33:00}	* New AR located in S-E
03/31 18:22:30 - 04/01 05:56:24	Track (-351.6, -259.5) ^{03/31 18:19:30}	# Cont.
04/01 06:09:30 - 04/01 17:59:24	Track (552.8, 316.6) ^{04/01 06:06:30}	# HOP180 NOAA11704
04/01 18:12:30 - 04/02 05:59:54	Track (-158.4, -253.8) ^{04/01 18:09:30}	# New AR located in S-E
04/02 06:17:10 - 04/02 10:10:00	Track (-57.4, -252.8) ^{04/02 06:10:00}	# Cont.
PROG= 07 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 24 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 28 1-time(s) 2.0sec		

Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 31 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 29 30-time(s) 120.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms												
Term			Pointing (x, y)					Comment				
03/31 06:36:00 - 03/31 14:32:54			Track (-17.9, -937.0) @ 03/31 06:33:00					# S pointing for SOT.				
PROG= 06 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 13 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 30 1-time(s) 30.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec												
Seqn= 25 1-time(s) 60.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2												
Term			Pointing (x, y)					Comment				
03/30 10:46:00 - 03/30 18:03:54			Track (200.6, 336.6) @ 03/30 10:43:00					# OP start + 10min, track AR 11704, with HOP 226, non-Maxwellian distributions, over first p				
03/30 18:17:00 - 03/31 06:22:54			Track (266.0, 334.4) @ 03/30 18:14:00					# HOP180 NOAA11704				
03/31 06:36:00 - 03/31 14:32:54			Track (-17.9, -937.0) @ 03/31 06:33:00					# S pointing for SOT.				
03/31 14:46:00 - 03/31 17:46:00			Track (-380.8, -260.9) @ 03/31 14:33:00					* New AR located in S-E				
03/31 18:22:30 - 04/01 05:56:24			Track (-351.6, -259.5) @ 03/31 18:19:30					# Cont.				
04/01 06:09:30 - 04/01 17:59:24			Track (552.8, 316.6) @ 04/01 06:06:30					# HOP180 NOAA11704				
04/01 18:12:30 - 04/02 05:59:54			Track (-158.4, -253.8) @ 04/01 18:09:30					# New AR located in S-E				
04/02 06:17:10 - 04/02 10:10:00			Track (-57.4, -252.8) @ 04/02 06:10:00					# Cont.				
PROG= 05 15-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 9 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

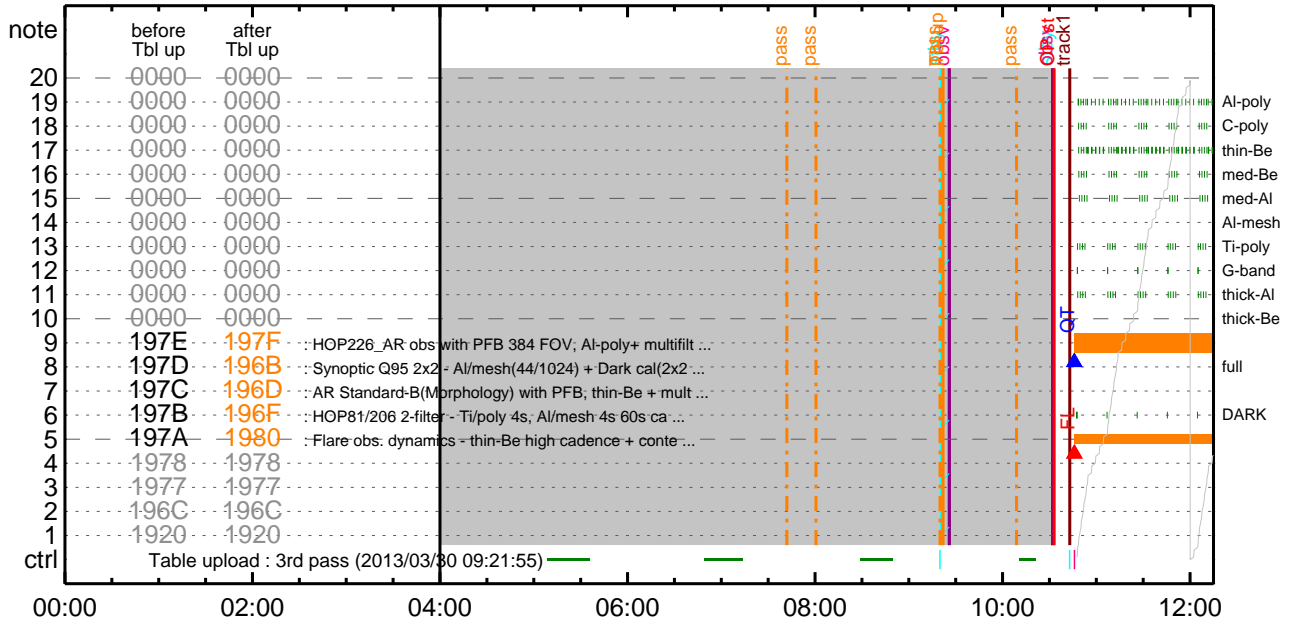
* * * * * **Active Region Search** * * * * *

NOT USED

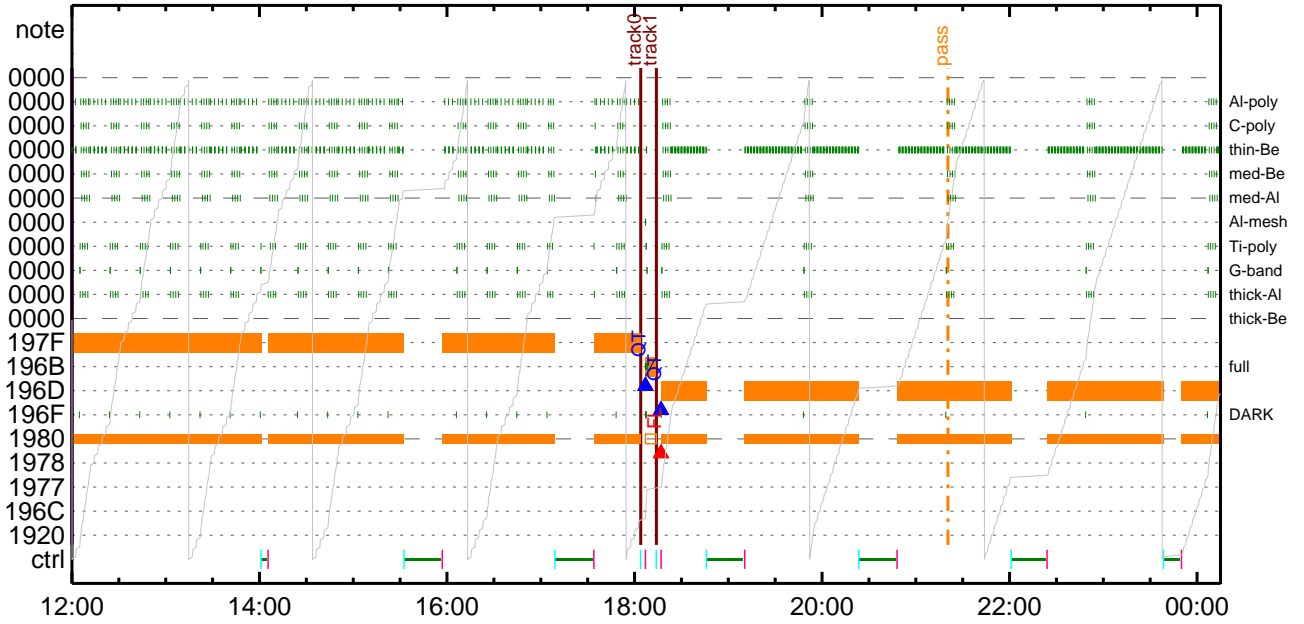
* * * * * **Flare Detection** * * * * *

FLD Patrol												
Term			Pointing (x, y)					Comment				
03/30 18:14:16 - 03/31 06:23:16			Track (266.0, 334.4) @ 03/30 18:14:00					# HOP180 NOAA11704				
03/31 06:33:16 - 03/31 18:09:46			Track (-17.9, -937.0) @ 03/31 06:33:00					# S pointing for SOT.				
03/31 18:19:46 - 04/01 05:56:46			Track (-351.6, -259.5) @ 03/31 18:19:30					# Cont.				
04/01 06:06:46 - 04/01 17:59:46			Track (552.8, 316.6) @ 04/01 06:06:30					# HOP180 NOAA11704				
04/01 18:09:46 - 04/02 06:00:16			Track (-158.4, -253.8) @ 04/01 18:09:30					# New AR located in S-E				
04/02 06:14:26 - 04/04 10:53:00			Track (-57.4, -252.8) @ 04/02 06:10:00					# Cont.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

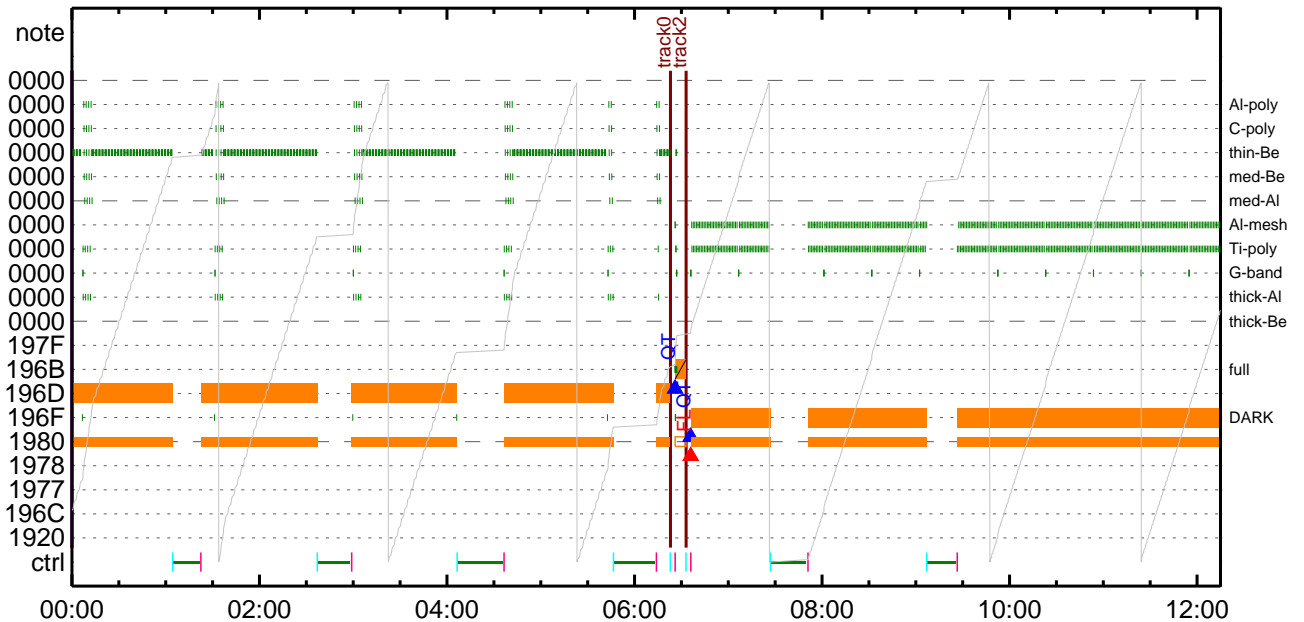
CMDI #0373 2013/03/30



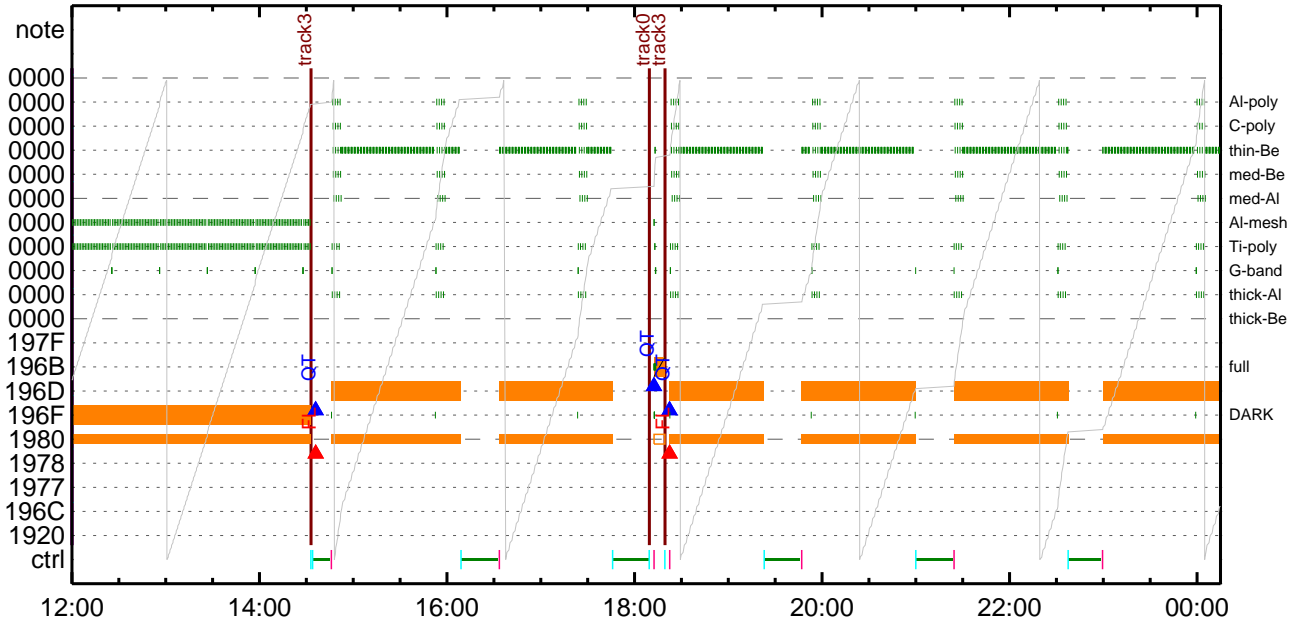
CMDI #0373 2013/03/30



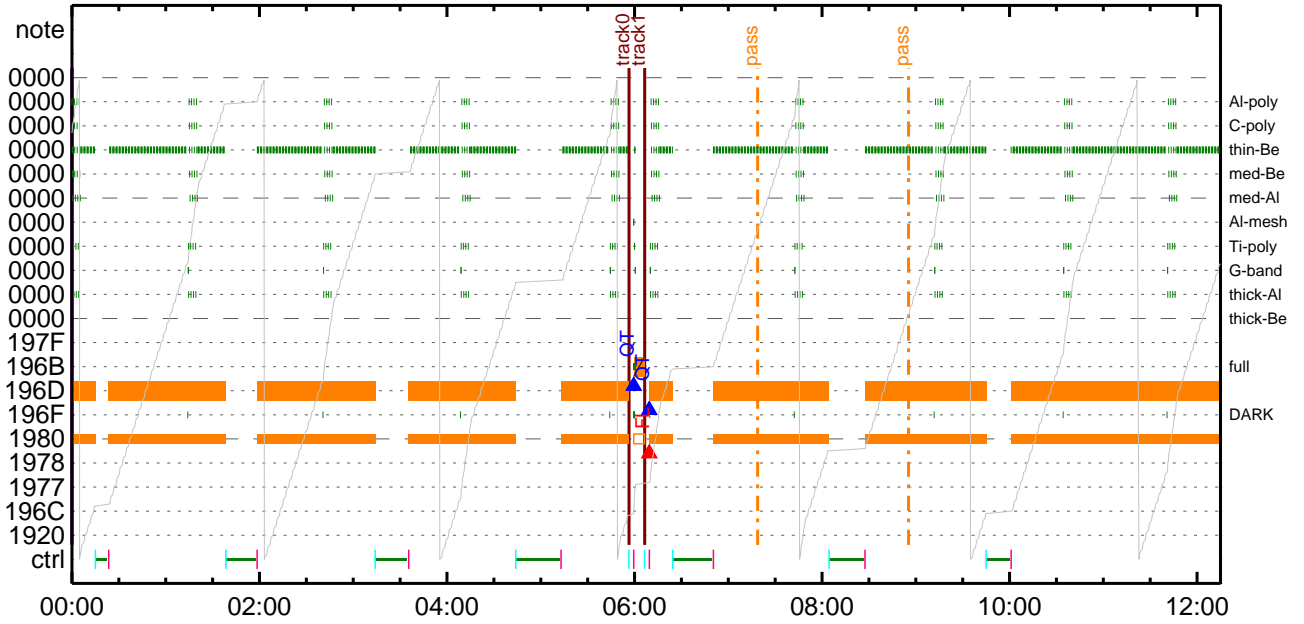
CMDI #0373 2013/03/31



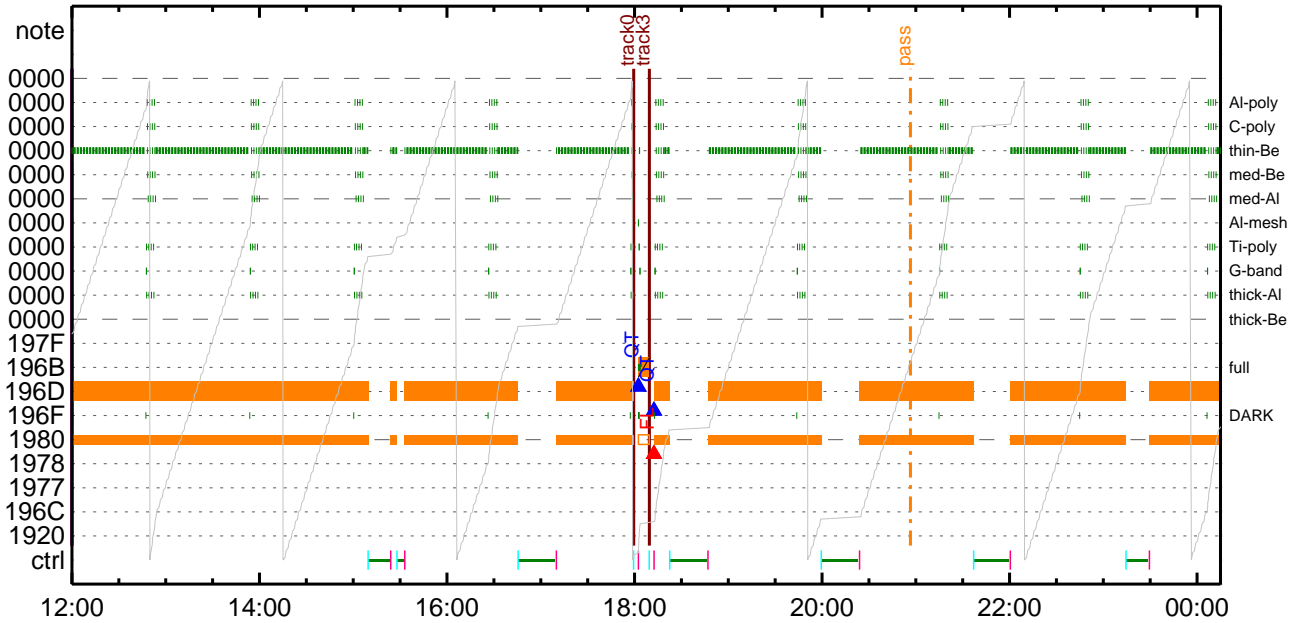
CMDI #0373 2013/03/31



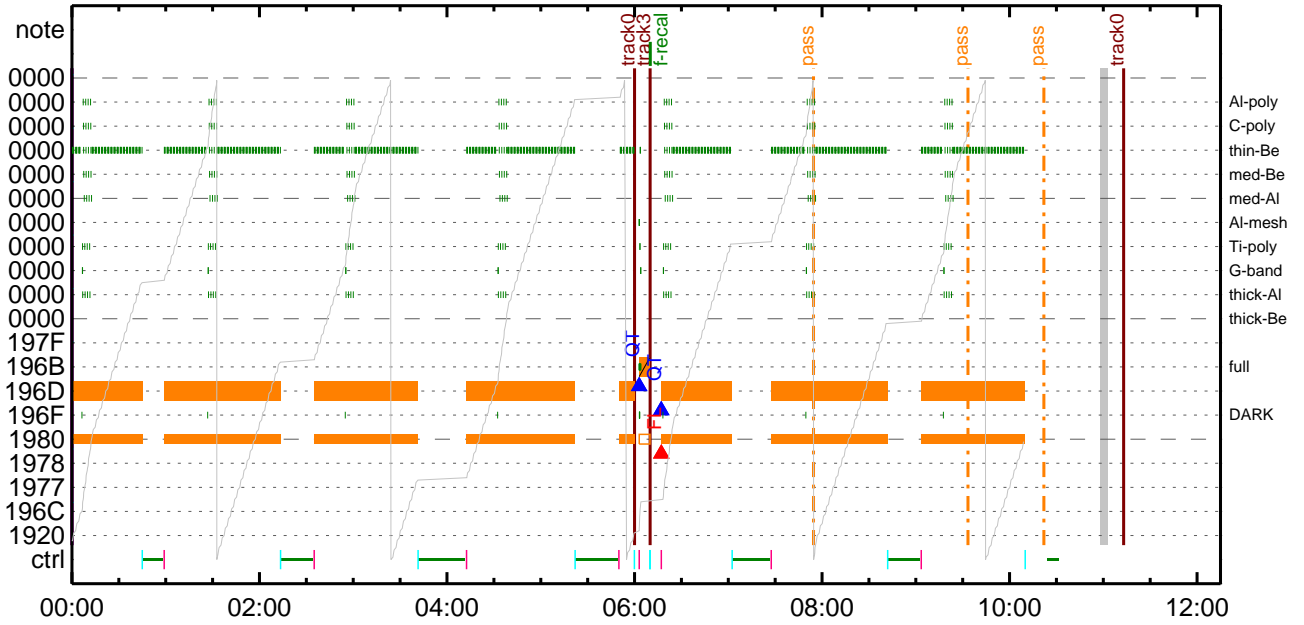
CMDI #0373 2013/04/01



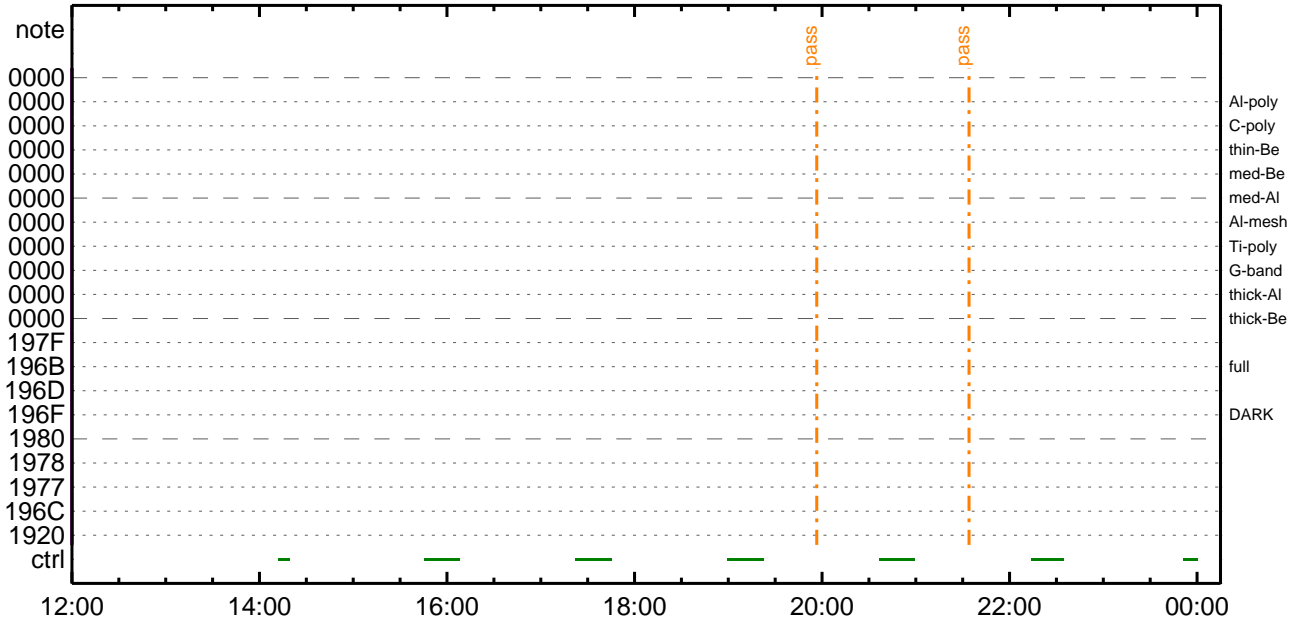
CMDI #0373 2013/04/01



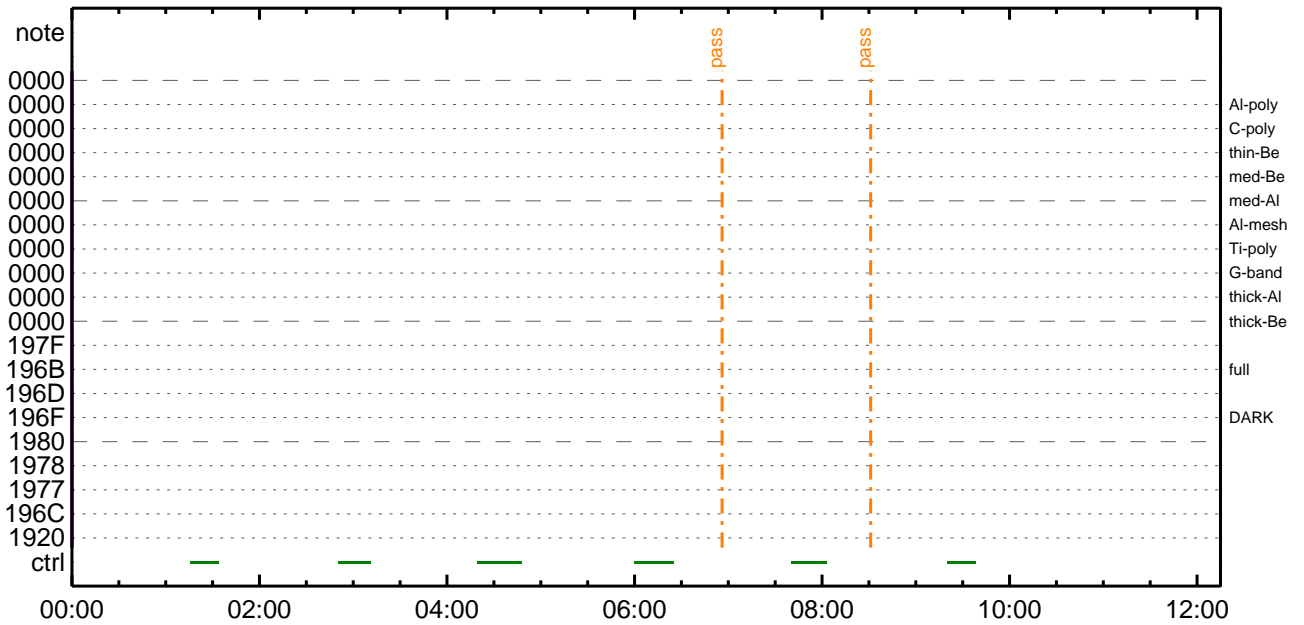
CMDI #0373 2013/04/02



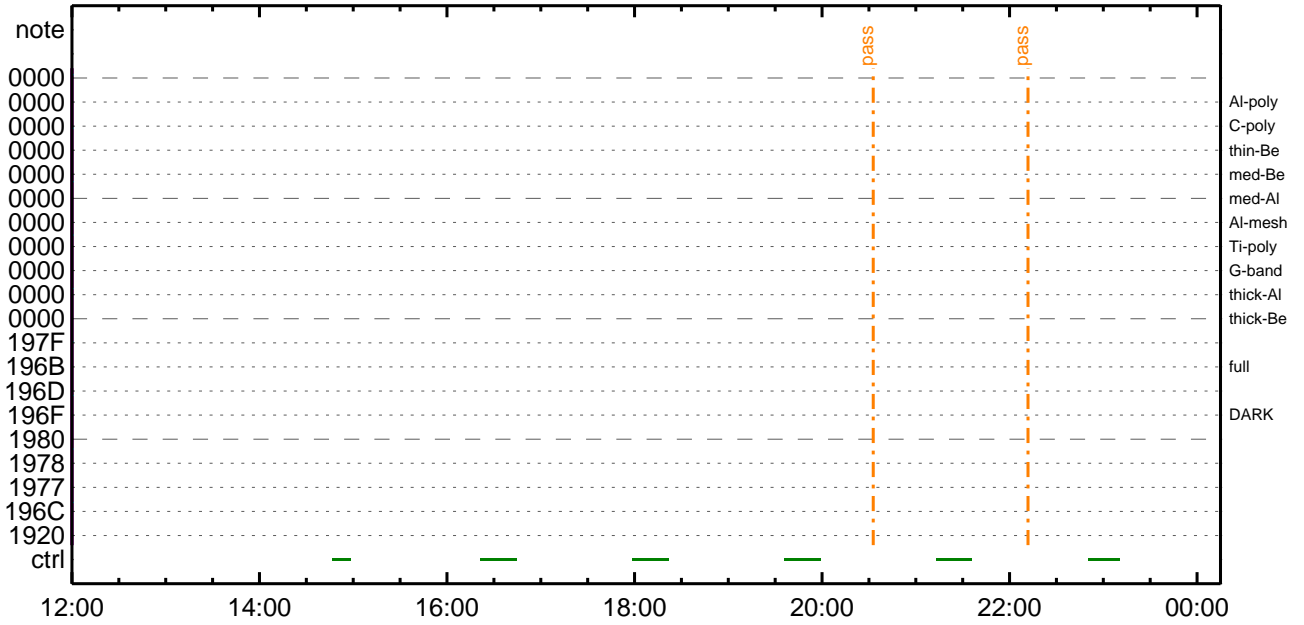
CMDI #0373 2013/04/02



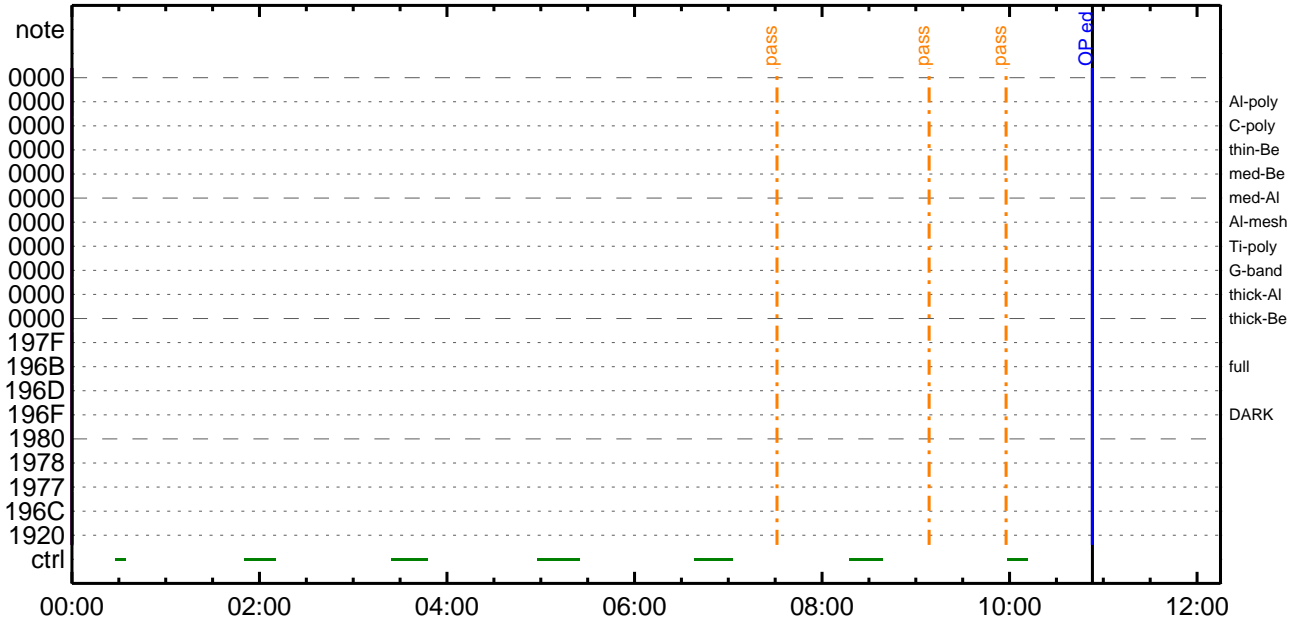
CMDI #0373 2013/04/03



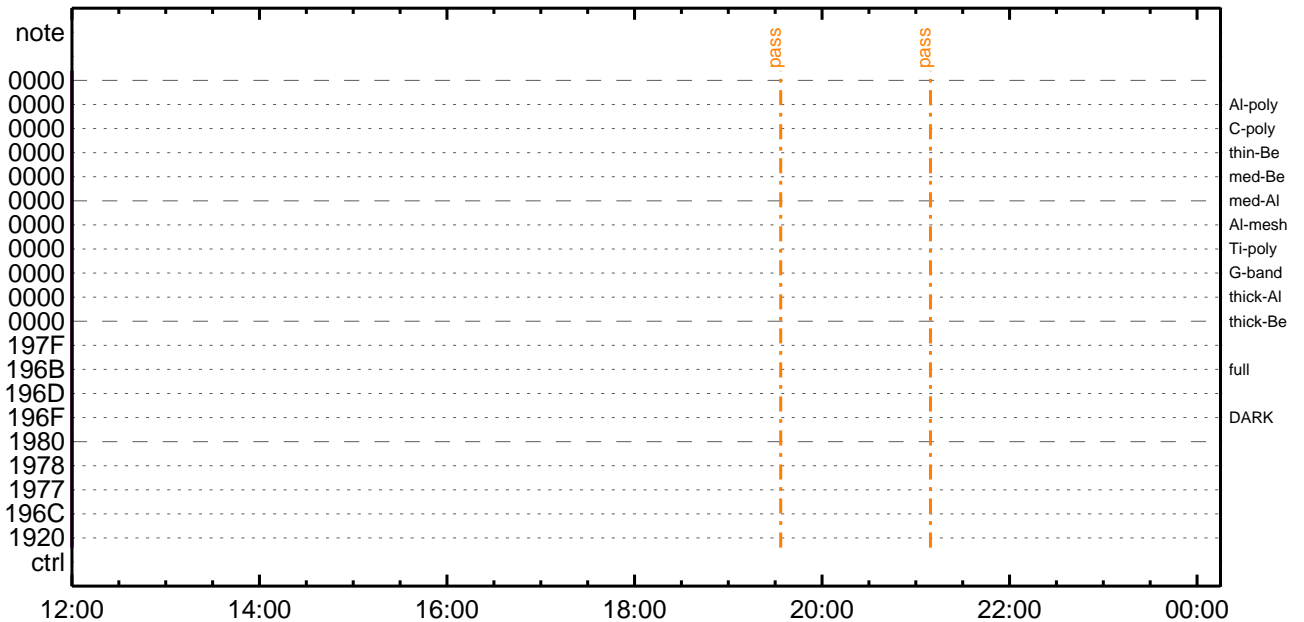
CMDI #0373 2013/04/03



CMDI #0373 2013/04/04



CMDI #0373 2013/04/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;1;YAY6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP      op-555:OP
0104 . ( )
0105 . S. OG      og-555:OG
0106 . ( )
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 C.           çç[HK1_DMP_TOP_ADRS_1]          EQ      40
0113 C.           çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0114 C.           çç[HK1_DMP_BLOCK_NUM]          EQ     127
0115 C.           çç[HK1_DMP_REPEAT_NUM]         EQ      0
0116 C.           çç[HK1_DMA_DMP_PIM]            EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 C.           çç[HK1_PKT_FORM_NO]             EQ      7
0120 C.           çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0121 C.           çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0122 C.           çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0123 C.           çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0124 . C.  YAY6Yx½ªî»ò³îç§
0125 C.           çç[HK1_DMP_CHK_FLG]           EQ     NON
0126 . C. RAM ID=NMOG²î¼ē¹ç•ē²îOK²³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 C.           çç[HK1_DMP_TOP_ADRS_1]          EQ     41
0132 C.           çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0133 C.           çç[HK1_DMP_BLOCK_NUM]          EQ     127
0134 C.           çç[HK1_DMP_REPEAT_NUM]         EQ      0
0135 C.           çç[HK1_DMA_DMP_PIM]            EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 C.           çç[HK1_PKT_FORM_NO]             EQ      7
0139 C.           çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0140 C.           çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0141 C.           çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0142 C.           çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0143 . C.  YAY6Yx½ªî»ò³îç§
0144 C.           çç[HK1_DMP_CHK_FLG]           EQ     NON
0145 . C. RAM ID=NMOG²î¼ē¹ç•ē²îOK²³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 C.           çç[HK1_DMP_TOP_ADRS_1]          EQ     42
0151 C.           çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0152 C.           çç[HK1_DMP_BLOCK_NUM]          EQ     65
0153 C.           çç[HK1_DMP_REPEAT_NUM]         EQ      0
0154 C.           çç[HK1_DMA_DMP_PIM]            EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 C.           çç[HK1_PKT_FORM_NO]             EQ      7
0158 C.           çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0159 C.           çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0160 C.           çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0161 C.           çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0162 . C.  YAY6Yx½ªî»ò³îç§
0163 C.           çç[HK1_DMP_CHK_FLG]           EQ     NON
0164 . C. RAM ID=NMOG,RAM ID=OP²î¼ē¹ç•ē²îOK²³îç§
0165 C.
0166 . C. ***** °ê²¼²î¼Ä´¶Á°èēē-²°Á÷¿® (¼âµ-YAY6Yx½ê½ç²òÄÖæ²ç¼ª²°²è¼î¹ç²ç²â) *****
0167 C. DHU²â;¼YE;î¼Y½;Y¹;¼YE;îòîâ²¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 C.           çç[HK1_PKT_FORM_NO]             EQ      2
0171 C.           çç[HK1_PKT_GEN_TIME]           EQ     0.5S
0172 C.           çç[HK1_S_TLM_BIT_RATE]         EQ     32K
0173 C.           çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |§ OPOG UPLOAD²-Á÷¿®NG²î¼î¹ç;ç°ê²¼²îTI-CMDÁ÷¿®²¼Ä¹Ö²°²è²²²³²è;f
0180 C. ²²²¿;çSET²èDUMP²îÆ²±²îY²¹²ç¹Ö²²²³²è;f
0181 C.
0182 . C. TI²³Y²P²Y6Y²E²ò²Äî¿¿(UT)
0183 +. TI 2013-03-30 10:28:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.           çç[HK1_TI_CMD_NUM]             EQ     1COUNTUP
0186 C.
0187 +. TI 2013-03-30 10:28:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.           çç[HK1_TI_CMD_NUM]             EQ     1COUNTUP
0190 C.
0191 +. TI 2013-03-30 10:28:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.           çç[HK1_TI_CMD_NUM]             EQ     1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-556 2013-03-30 13:14:38 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁã•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÔã•¿¿¼í¹ç¿Í;çÁ®, ù¿¹ãÈãBãÇÁ+¿®ã•¿Èããã³ãÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 . C. *****
0046 C. SOT table upload
0047 C. *****
0048 . C. < Stop FG table >
0049 +. DC 07-F0 MDP_FG_CTRL_MANU
0050 BC (51)
0051 . C. -----
0052 C. MDP_FG_CTRL_MODE = MANU [ ]
0053 C. -----
0054 C.
0055 . C. <Upload FG Observation Table>
0056 . S. RAM ram-263:MDP_OBS_F
0057 ( )
0058 C.
0059 . C. < Dump RAMID=MDP_OBS_F >
0060 +. DC 07-F0 MDP_DUMP_FGTBL
0061 BC (82 07 00 00 00 38 b8)
0062 C. -----
0063 C. MDP_OBS_F verify = OK/NG [ ]
0064 C. -----
0065 C.
0066 . C. < Stop SP table >
0067 +. DC 07-F0 MDP_SP_CTRL_MANU
0068 BC (61)
0069 C. -----
0070 C. MDP_SP_CTRL_MODE = MANU [ ]
0071 C. -----
0072 C.
0073 . C. <Upload SP Observation Table>
0074 . S. RAM ram-285:MDP_OBS_S
0075 ( )
0076 C.
0077 . C. < Dump RAMID=MDP_OBS_S >
0078 +. DC 07-F0 MDP_DUMP_SPTBL
0079 BC (83 07 00 00 00 38 b8)
0080 C. -----
0081 C. MDP_OBS_S verify = OK/NG [ ]
0082 C. -----
0083 C.
0084 C. *****
0085 C. SOT TI command set
0086 C. *****
0087 C. Execute, after the success of TBL upload.
0088 +. TI 2013-03-30 10:32:18.0
0089 DC 07-F0 MDP_SOT_MODE_OBSV
0090 BC (40)
0091 . C. -----
0092 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0093 C. -----
0094 C.
0095 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
```

```

0096 +. DC 07-FC EIS_MODE_MANU
0097 BC      (21 02)
0098 . C. Verify EIS in MANUAL mode
0099 . C. Estimated OBSTBL upload time is 26s
0100 C. *****
0101 C. EIS START OBSTBL LOAD
0102 C. *****
0103 . S. RAM ram-820:EIS_OBSTBL
0104 ( )
0105 +. DC 07-FC EIS_DUMP_OBSTBL
0106 BC      (07 07 07 00 00 70 00)
0107 C.
0108 C. Execute, after the success of OBSTBL upload.
0109 C. Set EIS TI-commands
0110 +. TI 2013-03-30 10:32:50.0
0111 DC 07-FC EIS_MODE_CHG_ENA
0112 BC      (20)
0113 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0114 C. *****
0115 C. EIS END OBSTBL LOAD
0116 C. *****
0117 C.
0118 . C. ***** MDP `úÃîñî»ö¼ÿñēĀðñ¹ñēDCBC•x²è *****
0119 C. (¼ā°îÿÓŸĀŸĒŸĤŸĒŸĀŸçŸēñ¼ñ¼Ā»Ûñ¹ñē)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ŸDŸ¹•İ Daily±;îññē´øñ¹ñēDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSŸĀŸSŸĀŸ´¼Ā»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

```


0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 (
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. TI 2013-03-30 10:32:02.0
0158 DC 07-F0 MDP_XRT_MODE_OBSV
0159 BC (c2)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C. ***** XRT END *****
0163 C.
0164 . C. ***** MDP `úÃîâî»ö¼ÝðĒÂð¹ñēDCBC•x²è *****
0165 C. (¼á°îÝÖYÁYĒYpYĒYáYçYēñE¼q¼Ā»Ūñ¹ñē)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** YĐY¹•Ī Daily±;ĪÑñĒ'Øñ¹ñēDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSŸÁY\$YĀY-¼Ā»Ū;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-558 2013-03-30 13:14:38 29 33 SOLAR-B MAIN //
0001   C.
0002   . C. ***** AOS *****
0003   C.
0004   . C. ;ãAOSYÁYŞYÁY^-¼Á»Û;ã
0005   C.
0006   C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008   C.
0009   . C. ***** AOCs : Reload orbital element (send every contact) *****
0010   C.   Áí;È¿µ°Æ»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔó•¿¼i¹ç¿Í;çÀ®, ùó¹óèóóçÁ+¿®ó•óÈóóó³óÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012   C.
0013   C.
0014   C.
0015   . C. ***** MDP `úÁÍóÍ»ò¼YóÈÁÐó¹óèDCBC•x²è *****
0016   C. (%á°íYÓYÁYÈYB¥ÈYáYçYèóÈ¼óó¼Á»Ûó¹óè)
0017   . S. DC-BC dcbc-402:DCBC
0018   (MDP_known_event)
0019   C.
0020   C.
0021   . C. ***** YD¥¹•İ Daily+¿ÍÑóÈ´Øó¹óèDCBC•x²è *****
0022   . S. DC-BC dcbc-153:DCBC
0023   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024   C.
0025   C.
0026   . C. ;ãLOSÁYŞYÁY^-¼Á»Û;ã
0027   C.
0028   . C. ***** LOS *****
0029   C.
```

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*** OP Sequence for XRT ***

2013/03/30	10:42:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	10:42:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/30	10:43:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/03/30	10:43:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/30	10:43:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/30	10:43:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/30	10:43:22.0	XRT_ARS_DIS_414_OG [0x19e]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/30	10:45:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	10:45:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/03/30	10:45:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/03/30	10:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	14:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	14:01:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	14:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	14:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	14:04:30.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	14:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	15:32:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	15:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	15:56:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	15:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	17:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	17:09:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	17:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	17:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	17:33:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	17:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	18:03:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/03/30	18:04:00.0	AOCS_Orе-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/03/30	18:04:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/03/30	18:04:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/03/30	18:04:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/30	18:06:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/03/30	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	18:13:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	18:13:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/03/30	18:14:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/03/30	18:14:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/03/30	18:14:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/03/30	18:14:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/03/30	18:14:22.0	XRT_ARS_DIS_414_OG [0x19e]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/03/30	18:16:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	18:16:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2013/03/30	18:16:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05

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2013/03/30	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	18:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	18:46:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	18:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	18:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	19:09:30.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	19:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	20:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	20:23:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	20:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	20:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	20:47:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	20:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	22:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	22:01:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	22:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	22:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	22:23:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	22:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/30	23:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/30	23:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/30	23:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/30	23:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/30	23:49:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/30	23:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/31	01:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/31	01:04:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/31	01:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/31	01:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/31	01:21:30.0	XRT_Custom_430_OG [0x1ae]			
2013/03/31	01:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/31	02:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/31	02:37:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/31	02:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/31	02:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/31	02:58:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/31	02:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/31	04:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/31	04:06:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/31	04:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/31	04:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/31	04:35:30.0	XRT_Custom_430_OG [0x1ae]			
2013/03/31	04:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/31	05:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/31	05:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/03/31	05:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/03/31	05:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/03/31	06:13:00.0	XRT_Custom_430_OG [0x1ae]			
2013/03/31	06:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/03/31	06:22:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/03/31	06:22:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			

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2013/03/31	06:23:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2013/03/31	06:23:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/03/31	06:23:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/03/31	06:23:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/31	06:25:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2013/03/31	06:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/31	06:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/31	06:32:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/03/31	06:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02	00	00	00
2013/03/31	06:33:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/03/31	06:33:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/03/31	06:33:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/03/31	06:33:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/31	06:35:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/31	06:35:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2013/03/31	06:35:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05		
2013/03/31	06:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/31	07:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/31	07:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/31	07:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/31	07:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/31	07:50:00.0	XRT_Custom_430_OG [0x1ae]							
2013/03/31	07:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/31	09:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/31	09:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/31	09:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/31	09:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/31	09:25:30.0	XRT_Custom_430_OG [0x1ae]							
2013/03/31	09:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/31	14:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/31	14:32:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/03/31	14:33:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00	00	00
2013/03/31	14:33:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/03/31	14:33:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/03/31	14:33:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/03/31	14:33:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/03/31	14:34:01.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/31	14:34:03.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/31	14:34:05.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/03/31	14:35:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/31	14:35:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2013/03/31	14:35:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05		
2013/03/31	14:37:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/03/31	14:45:00.0	XRT_Custom_430_OG [0x1ae]							
2013/03/31	14:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/03/31	16:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/03/31	16:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/03/31	16:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

2013/03/31	16:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/31	16:32:30.0	XRT_Custom_430_OG [0x1ae]					
2013/03/31	16:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/31	17:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/31	17:46:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/31	17:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/31	17:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/31	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/31	18:09:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/03/31	18:09:30.0	AOCS_Or-e-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2013/03/31	18:09:46.0	XRT_FLD_DIS_404_OG [0x194]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/03/31	18:09:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/03/31	18:09:50.0	XRT_ARS_DIS_406_OG [0x196]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/31	18:12:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2013/03/31	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/31	18:19:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/31	18:19:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/03/31	18:19:30.0	AOCS_Or-e-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2013/03/31	18:19:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/03/31	18:19:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/03/31	18:19:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/03/31	18:19:52.0	XRT_ARS_DIS_414_OG [0x19e]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/03/31	18:22:24.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/31	18:22:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2013/03/31	18:22:28.0	XRT_FL_PROG_SET_431_OG [0x1af]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 05	
2013/03/31	18:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/31	19:23:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/31	19:23:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/31	19:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/31	19:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/31	19:46:00.0	XRT_Custom_430_OG [0x1ae]					
2013/03/31	19:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/31	21:00:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/31	21:00:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/31	21:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/31	21:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/31	21:23:30.0	XRT_Custom_430_OG [0x1ae]					
2013/03/31	21:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/03/31	22:37:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/03/31	22:37:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/03/31	22:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/03/31	22:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/03/31	22:58:30.0	XRT_Custom_430_OG [0x1ae]					
2013/03/31	22:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	00:15:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	00:15:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	00:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/01	00:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2013/04/01	00:22:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/01	00:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	01:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	01:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/01	01:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/01	01:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/01	01:57:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/01	01:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	03:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	03:14:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/01	03:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/01	03:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/01	03:34:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/01	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	04:44:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	04:44:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/01	04:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/01	04:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/01	05:12:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/01	05:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	05:56:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/04/01	05:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/04/01	05:56:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/04/01	05:56:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/04/01	05:56:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/01	05:59:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2013/04/01	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	06:06:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/04/01	06:06:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/04/01	06:06:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/01	06:06:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/01	06:06:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/01	06:06:52.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/01	06:09:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/01	06:09:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2013/04/01	06:09:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05				
2013/04/01	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	06:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	06:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/01	06:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/01	06:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/01	06:49:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/01	06:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/01	08:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/01	08:04:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/01	08:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/01	08:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2013/04/01	08:26:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/01	08:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	09:45:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	09:45:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	09:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/01	09:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/04/01	10:00:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/01	10:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	15:09:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	15:09:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	15:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/01	15:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/04/01	15:23:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/01	15:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	15:28:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	15:28:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	15:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/01	15:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/04/01	15:32:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/01	15:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	16:45:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	16:45:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	16:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/01	16:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/04/01	17:09:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/01	17:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	17:59:26.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/04/01	17:59:30.0	AOCS_OrE-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2013/04/01	17:59:46.0	XRT_FLD_DIS_404_OG [0x194]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/04/01	17:59:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/04/01	17:59:50.0	XRT_ARS_DIS_406_OG [0x196]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/04/01	18:02:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2013/04/01	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	18:09:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/04/01	18:09:30.0	AOCS_OrE-point_Start_4_OG [0x09a]				
		AOCU_NM	5	02-76	03 00 00 00 00	
2013/04/01	18:09:46.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/04/01	18:09:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/04/01	18:09:50.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/04/01	18:09:52.0	XRT_ARS_DIS_414_OG [0x19e]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/04/01	18:12:24.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	18:12:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2013/04/01	18:12:28.0	XRT_FL_PROG_SET_431_OG [0x1af]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05	
2013/04/01	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/01	18:22:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/01	18:22:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/01	18:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/01	18:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2013/04/01	18:46:00.5	XRT_Custom_430_OG [0x1ae]			
2013/04/01	18:47:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/01	19:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/01	19:59:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/01	19:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/01	20:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/01	20:23:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/01	20:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/01	21:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/01	21:37:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/01	21:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/01	21:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/01	21:59:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/01	22:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/01	23:14:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/01	23:14:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/01	23:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/01	23:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/01	23:28:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/01	23:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/02	00:45:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/02	00:45:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/02	00:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/02	00:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/02	00:58:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/02	00:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/02	02:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/02	02:13:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/02	02:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/02	02:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/02	02:34:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/02	02:35:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/02	03:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/02	03:41:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/02	03:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/02	03:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/02	04:11:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/02	04:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/02	05:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/02	05:22:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/02	05:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/02	05:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/02	05:49:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/02	05:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/02	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/02	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/02	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/04/02	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/02	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/02	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/02	06:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			

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2013/04/02	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/02	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/02	06:09:56.0	XRT_CTRL_MANU_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/02	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00	
2013/04/02	06:10:06.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]	XRT_FOCUS_RECAL	2	07-F8	78	00
2013/04/02	06:14:06.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/04/02	06:14:26.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/04/02	06:14:28.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/04/02	06:14:30.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/04/02	06:14:32.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/04/02	06:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/02	06:17:06.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2013/04/02	06:17:08.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05
2013/04/02	06:17:10.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/02	07:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/02	07:02:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/02	07:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/02	07:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/04/02	07:26:30.0	XRT_Custom_430_OG [0x1ae]					
2013/04/02	07:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/02	08:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/02	08:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/04/02	08:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/04/02	08:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/04/02	09:02:30.0	XRT_Custom_430_OG [0x1ae]					
2013/04/02	09:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/04/02	10:10:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/04/02	11:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	