

XRT Timeline to be uploaded on 2013/04/16

Period: 2013/04/16 09:36:00 - 2013/04/20 10:25:00

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Normal mode

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XOB #190F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s, G-band - 384x384 45ms													
Term	Pointing (x, y)							Comment					
04/16 09:53:10 - 04/16 15:54:00	Fixed (-15.0, 880.0)							# OP start + 10min, HOP-81 at North Pole.					
PROG= 18 Inf.-time(s)													
├─ Subr= 1 1-time(s) 2.0sec													
│ └─ Seqn= 13 2-time(s) 2.0sec													
│ │ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec													
├─ Subr= 2 1-time(s) 2.0sec													
│ └─ Seqn= 30 1-time(s) 30.0sec													
│ │ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
├─ Subr= 3 30-time(s) 2.0sec													
│ └─ Seqn= 25 2-time(s) 30.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
│ │ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms)													
Term	Pointing (x, y)							Comment					
04/16 16:10:00 - 04/16 17:49:00	Track (833.8, 177.5) @ 04/16 16:00:00							# Observe AR 11719.					
04/16 18:25:30 - 04/17 05:59:24	Track (843.8, 175.6) @ 04/16 18:22:30							# AR 11719 cont.					
PROG= 02 Inf.-time(s)													
├─ Subr= 1 1-time(s) 2.0sec													
│ └─ Seqn= 18 1-time(s) 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec													
│ │ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec													
│ └─ Seqn= 13 1-time(s) 2.0sec													
│ │ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec													
│ └─ Seqn= 17 4-time(s) 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
├─ Subr= 2 1-time(s) 2.0sec													
│ └─ Seqn= 20 150-time(s) 2.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(25ms)													
Term	Pointing (x, y)							Comment					
04/16 18:15:30 - 04/16 18:22:24	Fixed (0.0, 0.0)							synoptic, shifted 12.5 min					
04/17 06:02:30 - 04/17 06:09:24	Fixed (0.0, 0.0)							synoptic, shifted -0.5 min					
PROG= 08 1-time(s)													
├─ Subr= 1 1-time(s) 14.0sec													
│ └─ Seqn= 16 1-time(s) 4.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ └─ Seqn= 5 1-time(s) 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec													
│ └─ Seqn= 26 1-time(s) 4.0sec													
│ │ Open/Ti-poly Open/Ti-poly close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ └─ Seqn= 19 1-time(s) 2.0sec													
│ │ thin-Be/Open thin-Be/Open close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ └─ Seqn= 14 1-time(s) 2.0sec													
│ │ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
├─ Subr= 2 1-time(s) 2.0sec													
│ └─ Seqn= 12 1-time(s) 2.0sec													
│ │ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
04/17 06:12:30 - 04/17 09:48:30	Track (886.9, 165.8) @ 04/17 06:09:30	# AR 11719.
PROG= 16 1-inf-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 24 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 28 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 31 4-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 29 70-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)	Comment
04/16 09:53:10 - 04/16 15:54:00	Fixed (-15.0, 880.0)	# OP start + 10min, HOP-81 at North Pole.
04/16 16:10:00 - 04/16 17:49:00	Track (833.8, 177.5) @ 04/16 16:00:00	# Observe AR 11719.
04/16 18:25:30 - 04/17 05:59:24	Track (843.8, 175.6) @ 04/16 18:22:30	# AR 11719 cont.
04/17 06:12:30 - 04/17 09:48:30	Track (886.9, 165.8) @ 04/17 06:09:30	# AR 11719.
PROG= 05 15-time(s)		
Subr= 1 45-time(s) 10.0sec		
Seqn= 9 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 15 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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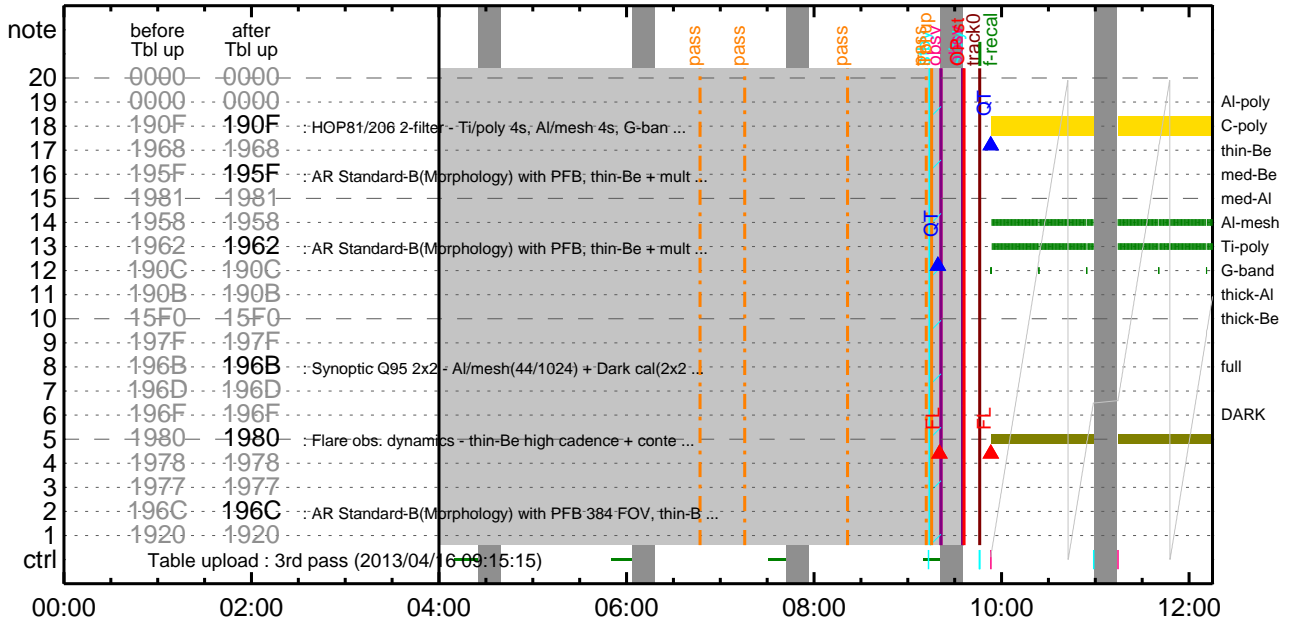
Flare Detection

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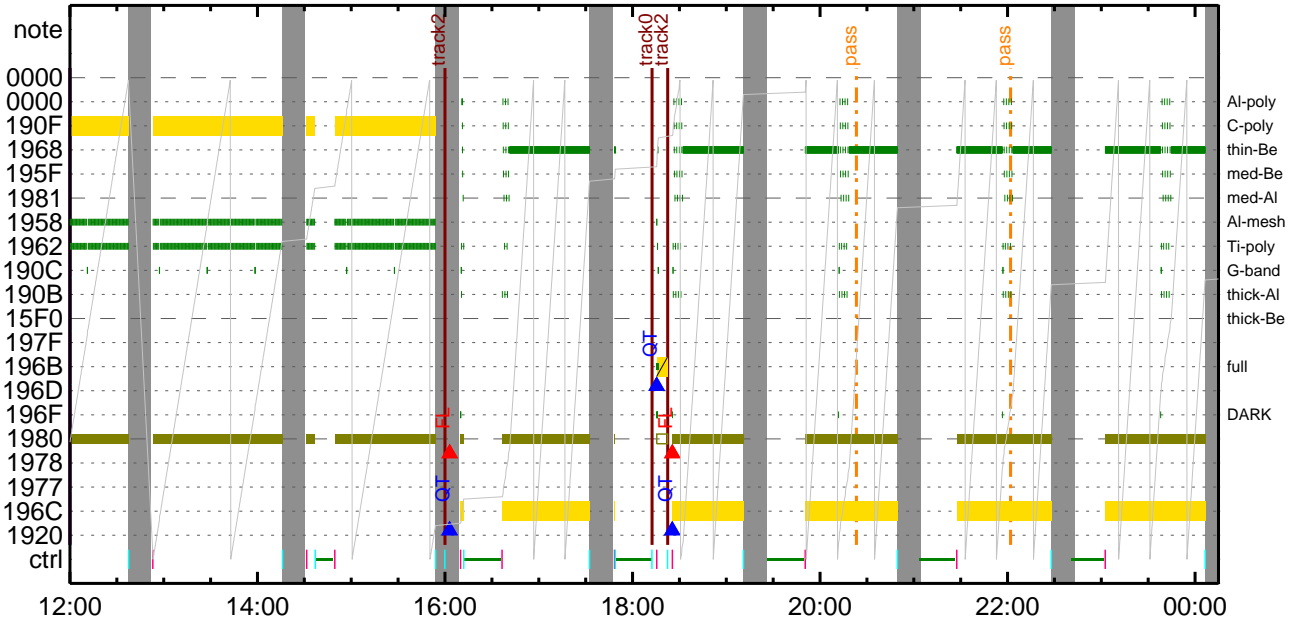
FLD Patrol

Term	Pointing (x, y)	Comment
04/16 18:22:46 - 04/17 05:59:46	Track (843.8, 175.6) @ 04/16 18:22:30	# AR 11719 cont.
04/17 06:09:46 - 04/20 10:25:00	Track (886.9, 165.8) @ 04/17 06:09:30	# AR 11719.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

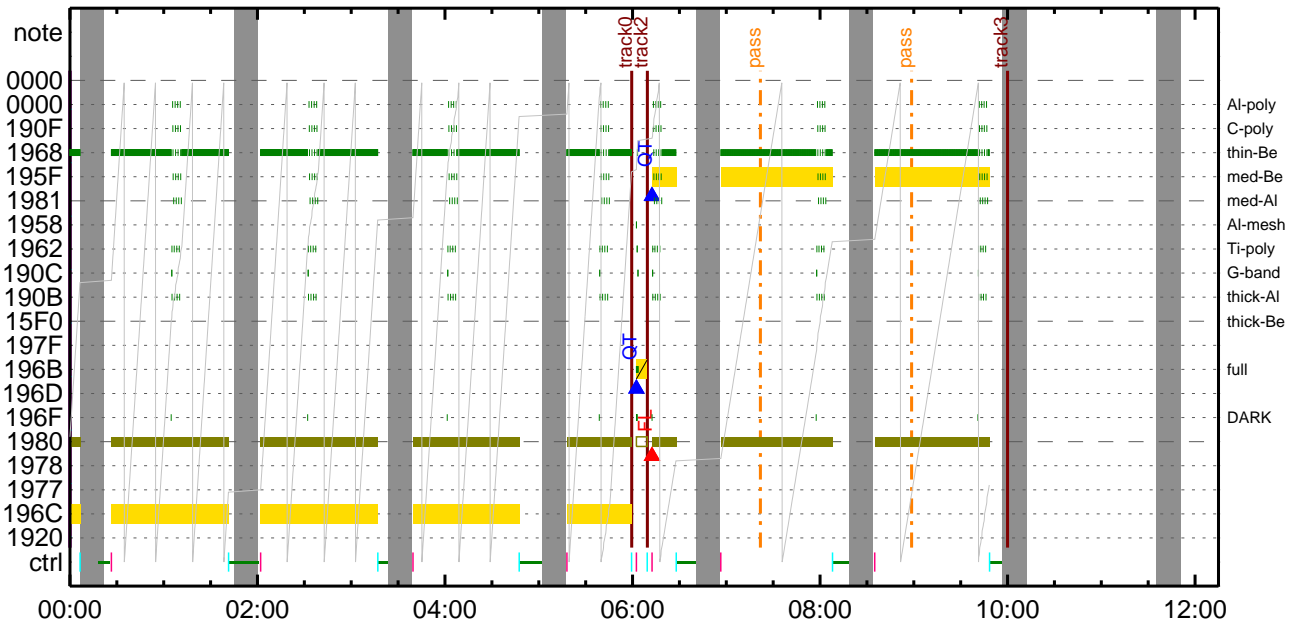
CMDI #0401 2013/04/16



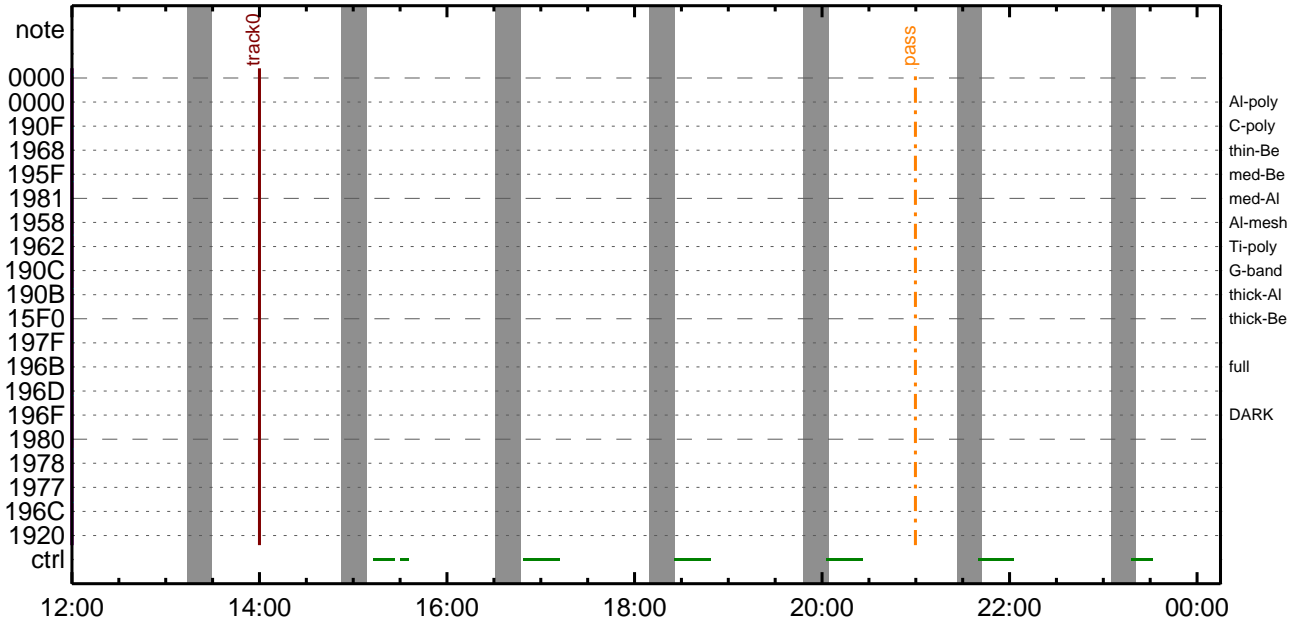
CMDI #0401 2013/04/16



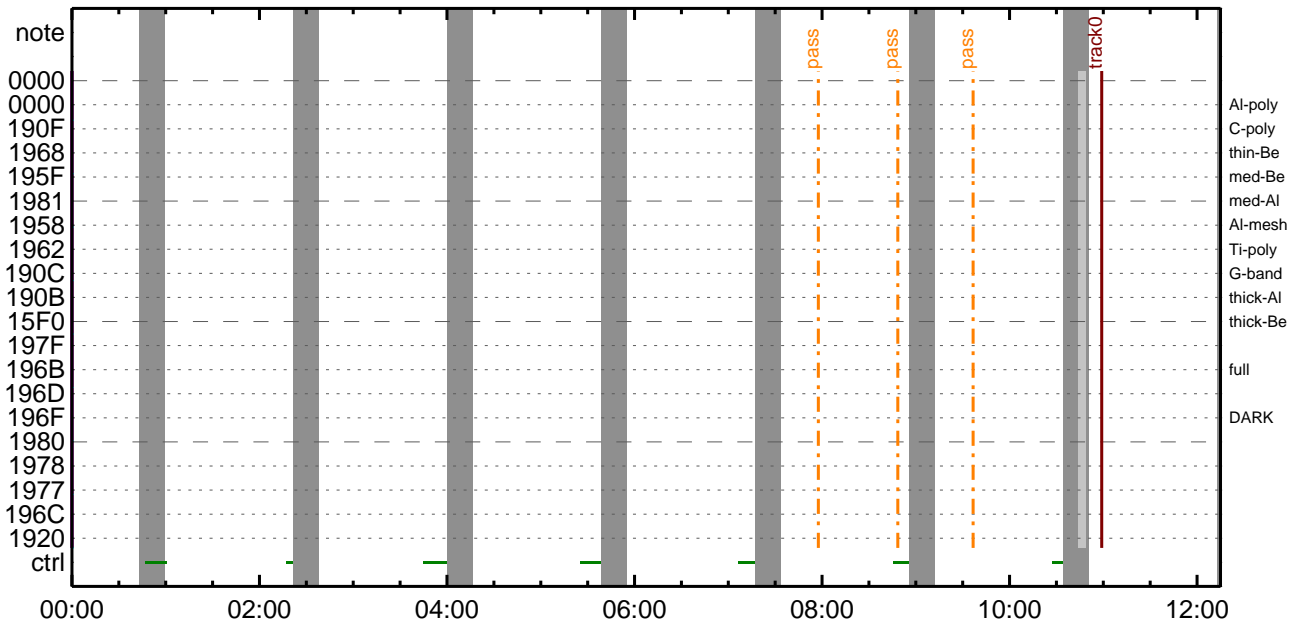
CMDI #0401 2013/04/17



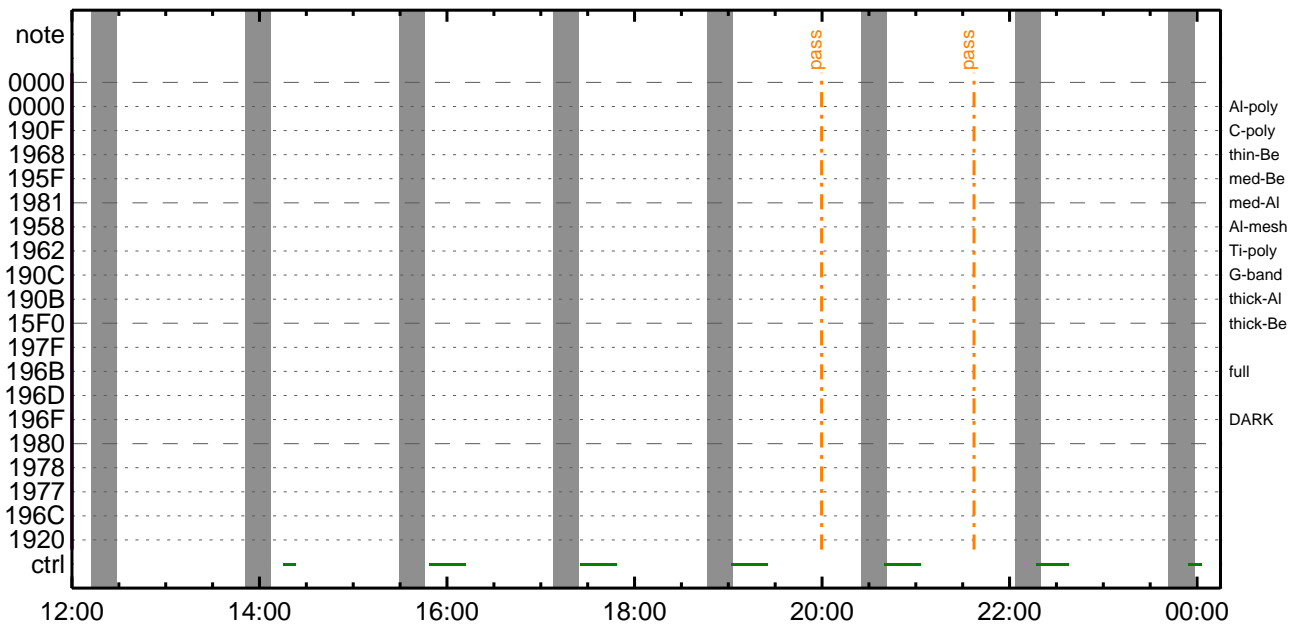
CMDI #0401 2013/04/17



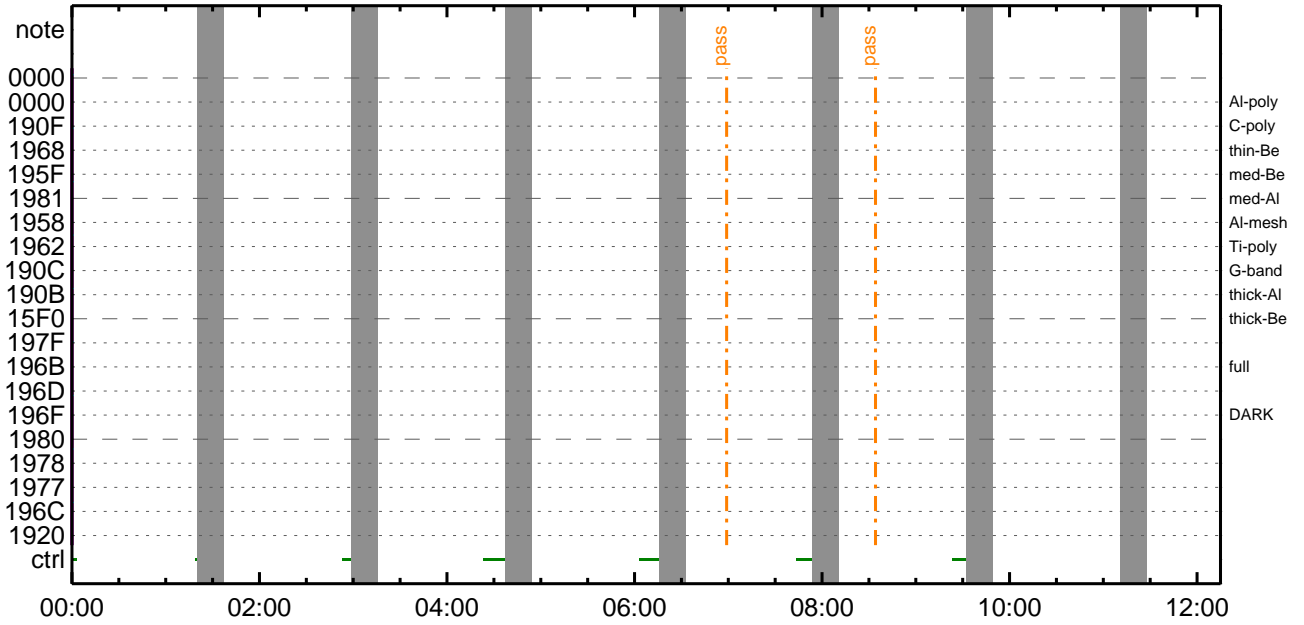
CMDI #0401 2013/04/18



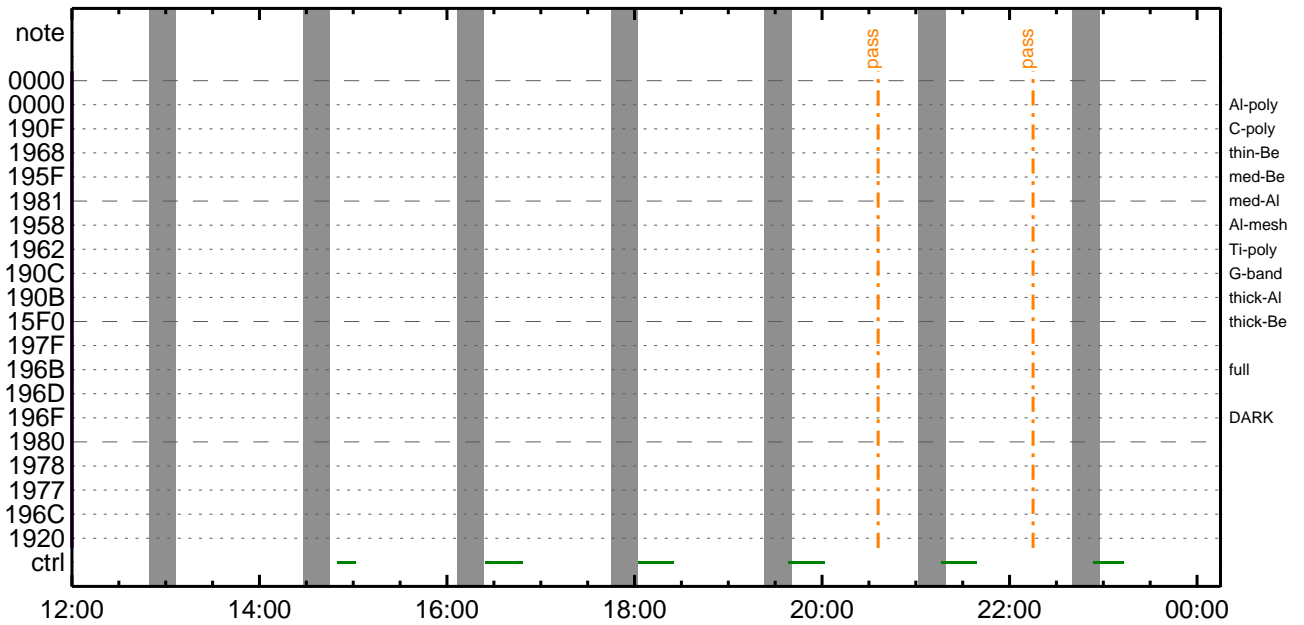
CMDI #0401 2013/04/18



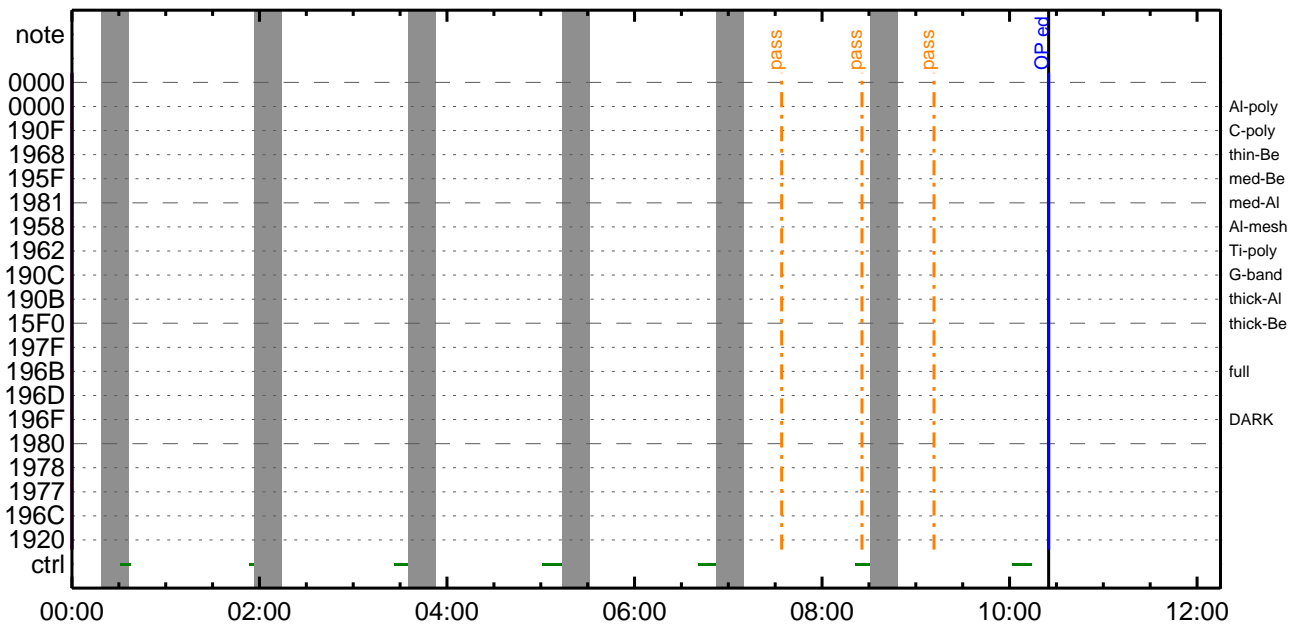
CMDI #0401 2013/04/19



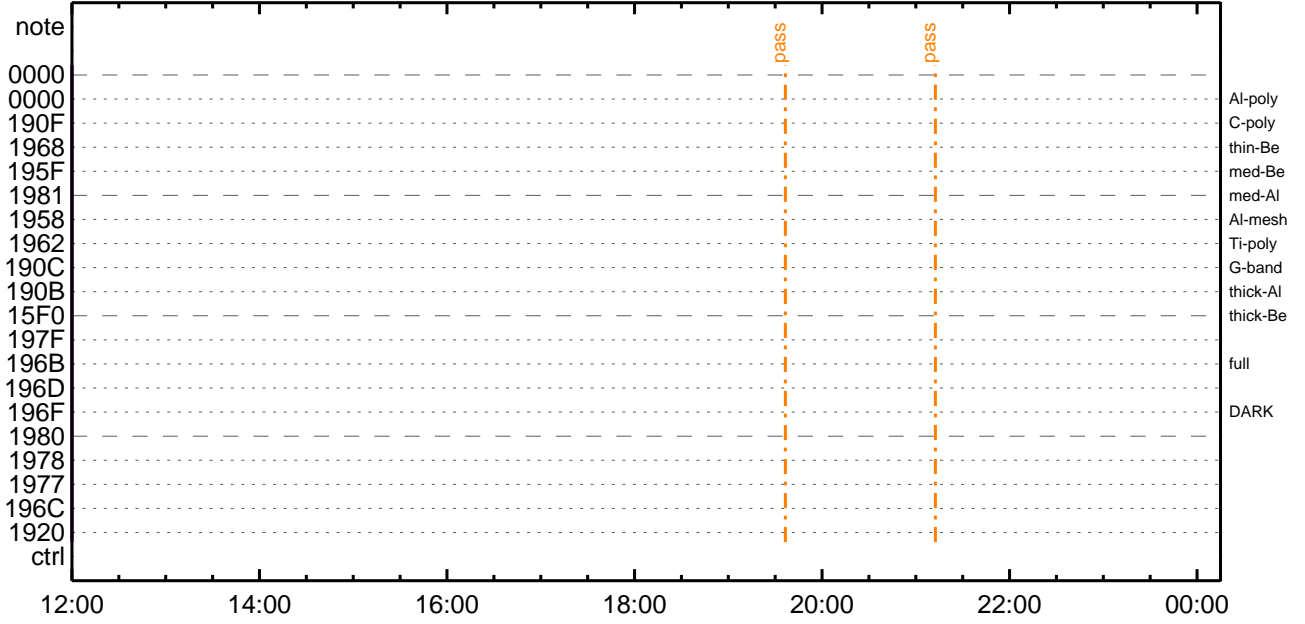
CMDI #0401 2013/04/19



CMDI #0401 2013/04/20



CMDI #0401 2013/04/20




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0096 C.                0x0000; SET 0x0000 DUMP 0x0000 0x0000 0x0000 0x0000 0x0000; E
0097 C.
0098 . C. TI 2013-04-16 09:31:00.0
0099 +. TI 2013-04-16 09:31:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2013-04-16 09:31:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2013-04-16 09:31:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2013-04-16 09:35:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.                0x0000 [HK1_TI_CMD_ENA/DIS] EQ ENA
0116 C.                0x0000 [HK1_TI_CMD_NUM] EQ 4
0117 C.                0x0000 [HK1_NEXT_EXEC_PIM] EQ DHU
0118 C.                0x0000 [HK1_NEXT_EXEC_DC] EQ 0xB3
0119 C.
0120 C.
0121 . C. *****
0122 C. TI 2013-04-16 09:35:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                0x0000 [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.                0x0000 [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.                0x0000 [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.                0x0000 [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.                0x0000 [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                0x0000 [HK1_PKT_FORM_NO] EQ 7
0136 C.                0x0000 [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.                0x0000 [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.                0x0000 [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.                0x0000 [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 . C. 0x0000 [HK1_DMP_CHK_FLG] EQ NON
0142 C.                0x0000 [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 . C. RAM ID=TI_TBL 0x03AB00-0x03AEFF
0145 C.
0146 . C. DHU 2013-04-16 09:35:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                0x0000 [HK1_PKT_FORM_NO] EQ 2
0150 C.                0x0000 [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.                0x0000 [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.                0x0000 [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2013-04-16 09:35:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2013-04-16 09:35:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2013-04-16 09:35:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2013-04-16 09:35:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 2013-04-16 09:35:00.0 *****
0192 C. (%A 0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOSŸÁŸSŸÃŸ-¼Â»Û;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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Apr 16, 13 11:58

XRT_OGLIST_0401.chk

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*** OP Sequence for XRT ***

2013/04/16	09:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	09:45:56.0	XRT_CTRL_MANU_432_OG [0x1b0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	09:46:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 b1 cb 01 58				
2013/04/16	09:46:06.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/04/16	09:50:06.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/04/16	09:50:26.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/16	09:50:28.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/16	09:50:30.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/16	09:50:32.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/16	09:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	09:53:06.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/04/16	09:53:08.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05				
2013/04/16	09:53:10.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	10:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	10:59:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	10:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	11:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	11:13:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/16	11:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	12:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	12:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	12:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	12:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	12:52:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/16	12:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	14:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	14:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	14:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	14:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	14:30:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/16	14:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	14:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	14:37:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	14:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	14:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	14:48:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/16	14:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	15:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	15:54:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	15:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	15:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	15:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/04/16	16:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/04/16	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/16	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/16	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/16	16:00:22.0	XRT_ARS_DIS_414_OG [0x19e]							

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2013/04/16	16:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	16:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/04/16	16:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05			
2013/04/16	16:09:00.0	XRT_Custom_430_OG [0x1ae]								
2013/04/16	16:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	16:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	16:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	16:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	16:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	16:35:30.0	XRT_Custom_430_OG [0x1ae]								
2013/04/16	16:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	17:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	17:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	17:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	17:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	17:47:30.0	XRT_Custom_430_OG [0x1ae]								
2013/04/16	17:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	17:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	17:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	17:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	17:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	18:12:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	18:12:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/04/16	18:12:30.5	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2013/04/16	18:12:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/04/16	18:12:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/04/16	18:12:50.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/16	18:15:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2013/04/16	18:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	18:22:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	18:22:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/04/16	18:22:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00	00
2013/04/16	18:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/16	18:22:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/16	18:22:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/16	18:22:52.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/16	18:25:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	18:25:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/04/16	18:25:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05			
2013/04/16	18:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	19:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	19:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	19:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	19:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	19:49:30.0	XRT_Custom_430_OG [0x1ae]								
2013/04/16	19:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	20:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	20:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				

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2013/04/16	20:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	20:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	21:26:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/16	21:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/16	22:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/16	22:28:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/16	22:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/16	22:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/16	23:01:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/16	23:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/17	00:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/17	00:06:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/17	00:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/17	00:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/17	00:25:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/17	00:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/17	01:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/17	01:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/17	01:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/17	01:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/17	02:01:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/17	02:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/17	03:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/17	03:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/17	03:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/17	03:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/17	03:38:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/17	03:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/17	04:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/17	04:47:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/17	04:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/17	04:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/17	05:17:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/17	05:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/17	05:59:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/17	05:59:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/04/17	05:59:30.5	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/04/17	05:59:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/04/17	05:59:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/04/17	05:59:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/17	06:02:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2013/04/17	06:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/17	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/17	06:09:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/04/17	06:09:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/04/17	06:09:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/17	06:09:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/17	06:09:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/17	06:09:52.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/17	06:12:24.0	XRT_FLD_RESET_415_OG [0x19f]							

2013/04/17	06:12:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/17	06:12:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2013/04/17	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05	
2013/04/17	06:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/17	06:28:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/17	06:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/17	06:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/17	06:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/17	06:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2013/04/17	08:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/04/17	08:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/17	08:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/17	08:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/17	08:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/17	08:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/17	09:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/04/17	09:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/17	09:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/17	09:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/17	10:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/17	10:00:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_448_OG [0x1c0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/17	14:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCS_OrE-point_Start_4_OG [0x09a]					
2013/04/18	10:59:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCS_OrE-point_Start_5_OG [0x09b]					
			AOCS_OrE-point_Start_3_OG [0x099]					
			AOCS_OrE-point_Start_3_OG [0x099]	5	02-76	03	00 00 00 00	
			AOCS_OrE-point_Start_3_OG [0x099]	0	04-BC			
			AOCS_OrE-point_Start_3_OG [0x099]	5	02-76	00	f2 36 af 8e	
			AOCS_OrE-point_Start_3_OG [0x099]	5	02-76	00	00 00 00 00	