

XRT Timeline to be uploaded on 2013/04/23

Period: 2013/04/23 10:38:00 - 2013/04/27 09:17:00

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Normal mode

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XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo

Term	Pointing (x, y)	Comment
04/23 10:51:00 - 04/23 16:44:54	Track (606.8, 239.2) @ 04/23 10:48:00	# OP start + 10min AR11726
04/23 21:03:00 - 04/24 06:27:24	Track (674.1, 233.3) @ 04/23 21:00:00	#AR11726
04/24 12:33:00 - 04/24 15:39:54	Track (763.6, 223.5) @ 04/24 12:30:00	AR11726
04/24 20:03:00 - 04/25 05:59:54	Track (780.6, 221.3) @ 04/24 15:50:00	# EUNIS (AR11726)
04/25 06:13:00 - 04/25 08:55:00	Fixed (896.0, 200.0)	AR11726 (HOP135)

PROG= 16 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Open/G-band	Open/G-band open	Safe Norm 32ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Seqn= 28	1-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm 63ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Seqn= 31	4-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
med-Be/Open	med-Be/Open close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
Subr= 2	1-time(s)	2.0sec										
Seqn= 29	70-time(s)	2.0sec										
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec				

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250)

Term	Pointing (x, y)	Comment
04/23 16:49:24 - 04/23 17:15:30	Fixed (0.0, 0.0)	synoptic, shifted -75 min
04/24 06:30:30 - 04/24 06:37:00	Fixed (0.0, 0.0)	synoptic, shifted 27.5 min Extended for test run for HOP217
04/24 15:43:00 - 04/24 15:49:54	Fixed (0.0, 0.0)	synoptic, shifted -140.0 min
04/25 06:03:00 - 04/25 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 08 1-time(s)

Subr= 1	1-time(s)	14.0sec										
Seqn= 16	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh close	Safe Norm 44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec				
Seqn= 26	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Seqn= 19	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open close	Safe Norm 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Seqn= 14	1-time(s)	2.0sec										
Open/G-band	Open/G-band open	Safe Norm 8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Subr= 2	1-time(s)	2.0sec										
Seqn= 12	1-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm 32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec				

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1987: High cadence (thin-Be only) 384x384 AEC3 at 1064 1048-HOP214

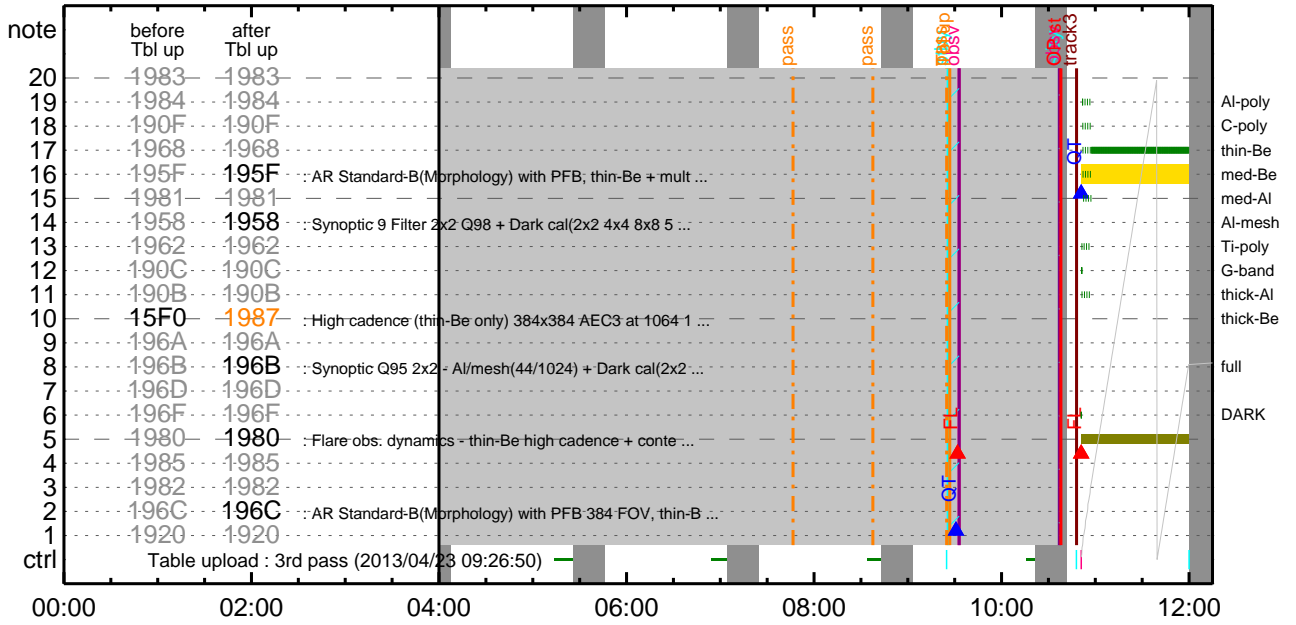
Term	Pointing (x, y)	Comment
04/23 17:16:06 - 04/23 20:49:56	Track (647.9, 235.8) @ 04/23 16:55:00	# EUNIS (AR11726)
04/24 15:53:00 - 04/24 19:59:54	Track (780.6, 221.3) @ 04/24 15:50:00	# EUNIS (AR11726)

PROG= 10 Inf.-time(s)

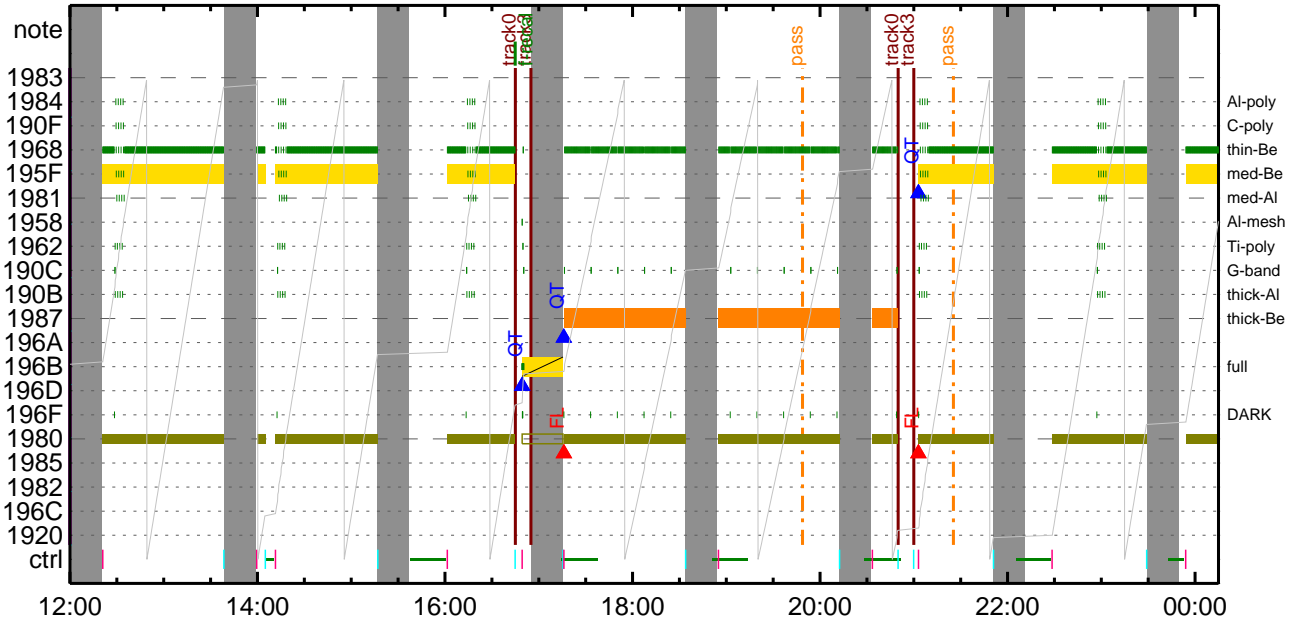
Subr= 1	1-time(s)	2.0sec										
Seqn= 51	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Open/G-band	Open/G-band open	Safe Norm 44ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
Seqn= 28	1-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm 63ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Subr= 2	1-time(s)	2.0sec										

Term	Pointing (x, y)		Comment
04/23 17:15:52 - 04/24 06:27:46	Track (647.9, 235.8)	@ 04/23 16:55:00	# EUNIS (AR11726)
04/24 12:30:16 - 04/24 15:40:16	Track (763.6, 223.5)	@ 04/24 12:30:00	AR11726
04/24 15:50:16 - 04/25 06:00:16	Track (780.6, 221.3)	@ 04/24 15:50:00	# EUNIS (AR11726)
04/25 06:10:16 - 04/27 09:17:00	Fixed (896.0, 200.0)		AR11726 (HOP135)
Open/Ti-poly	Open/thick-Al	close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

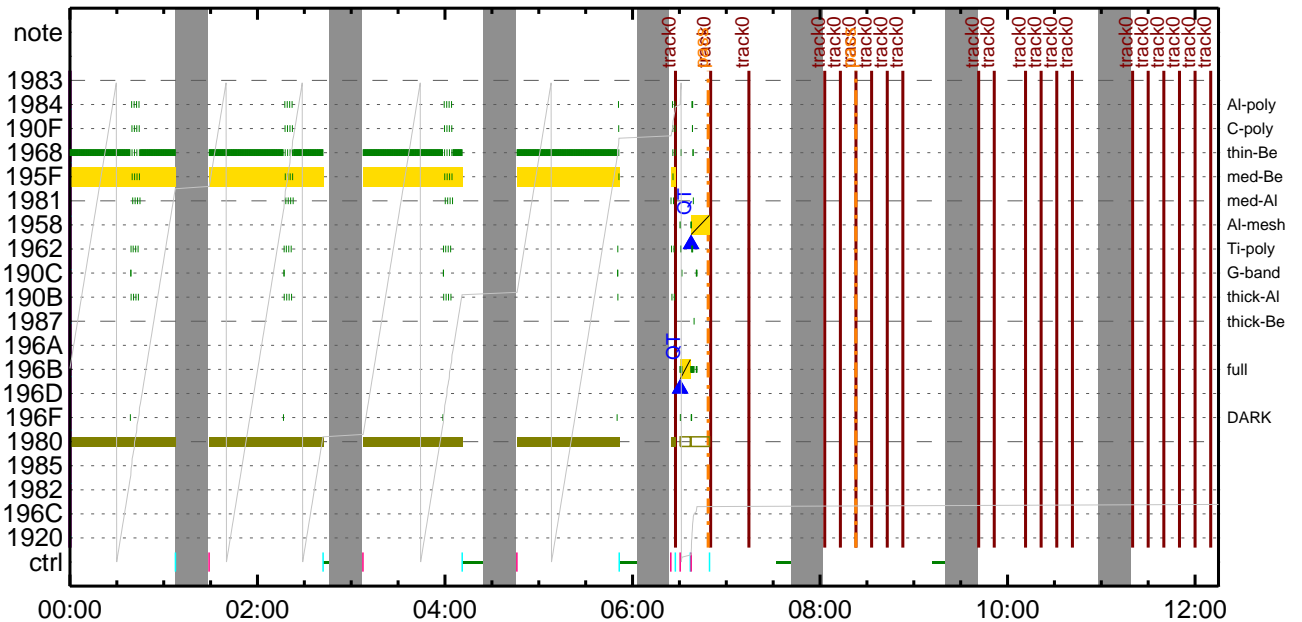
CMDI #0415 2013/04/23



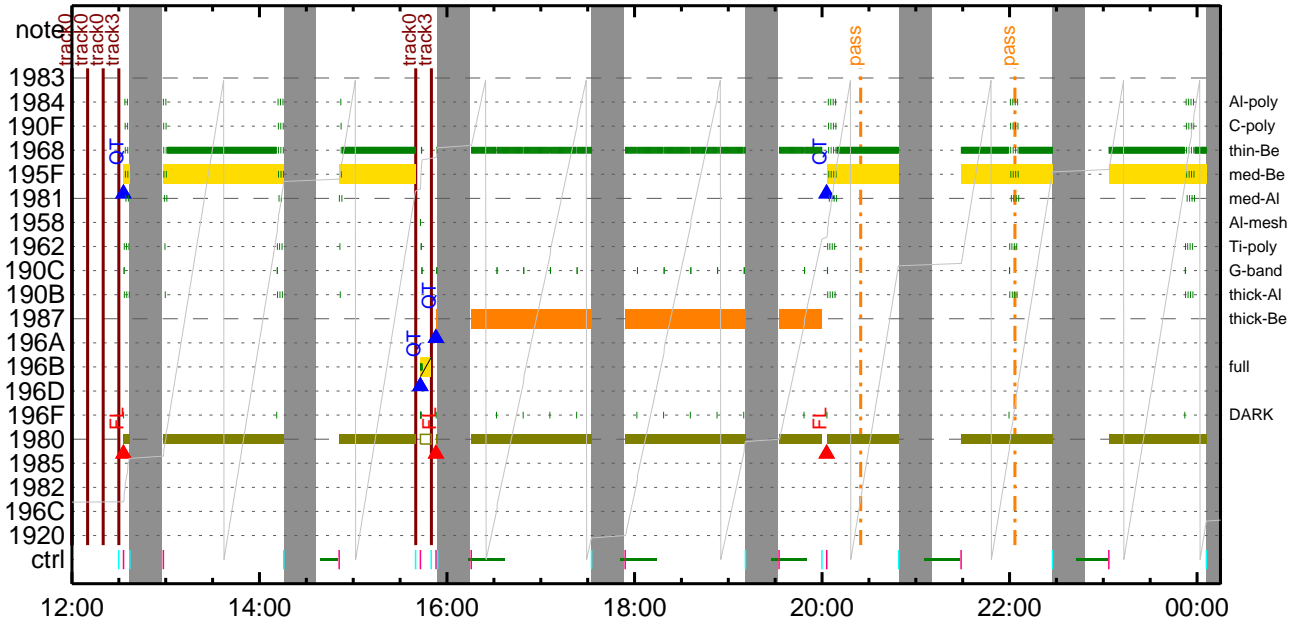
CMDI #0415 2013/04/23



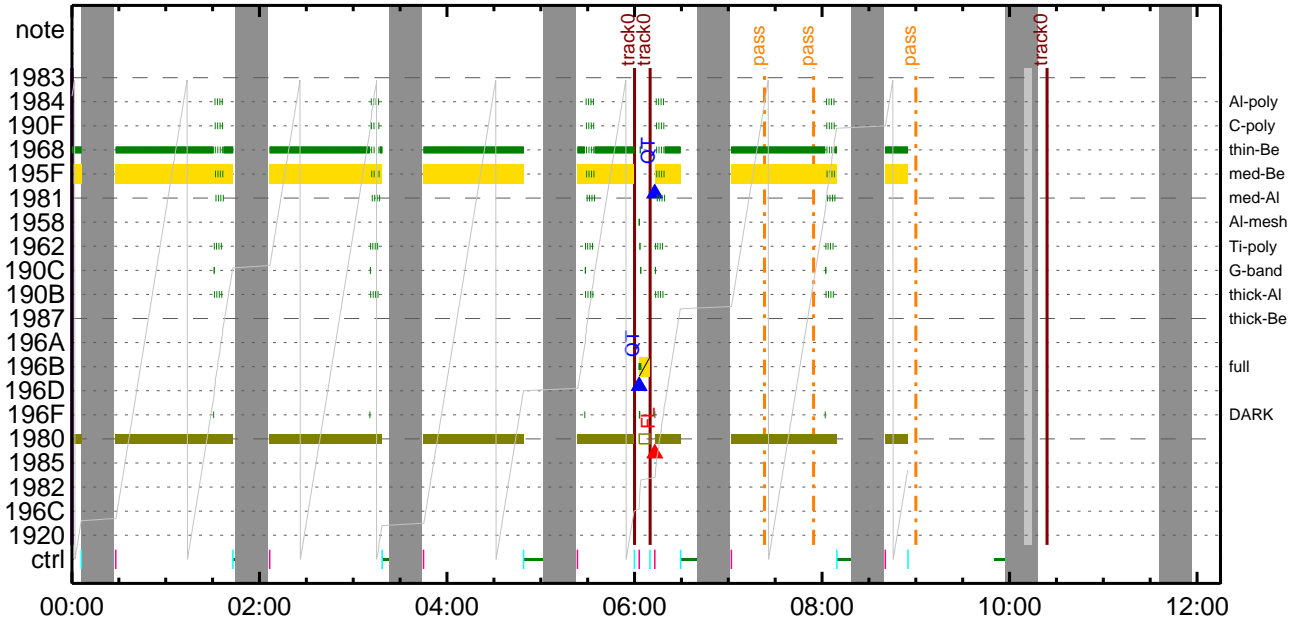
CMDI #0415 2013/04/24



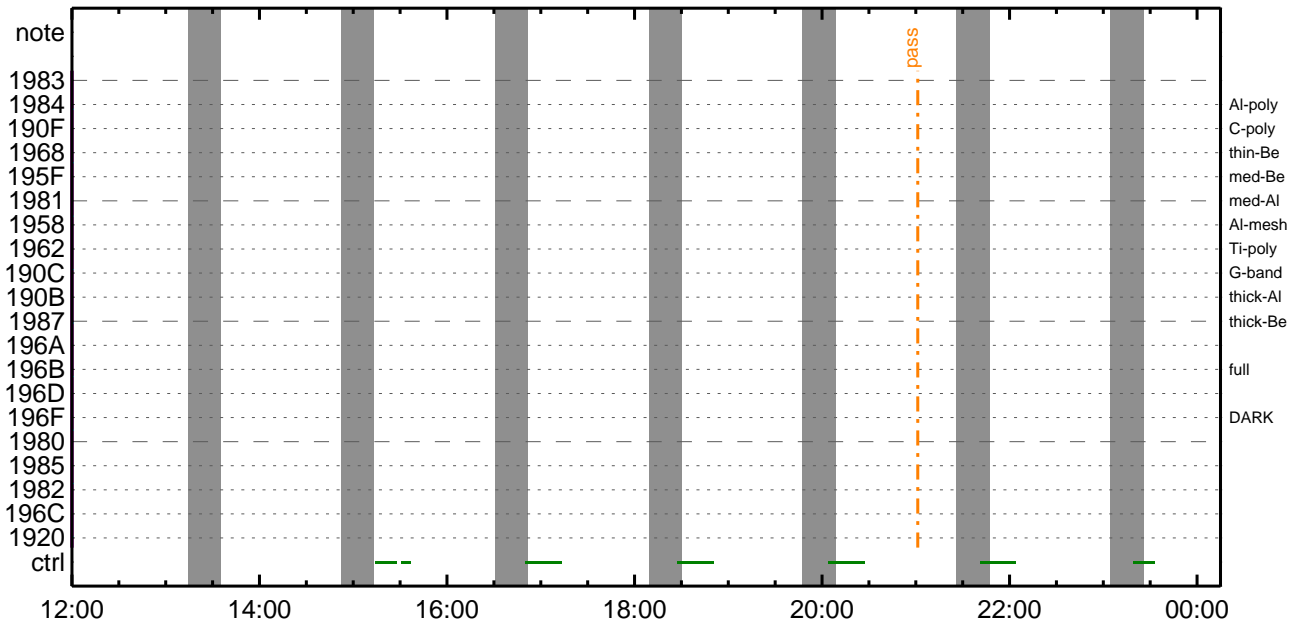
CMDI #0415 2013/04/24



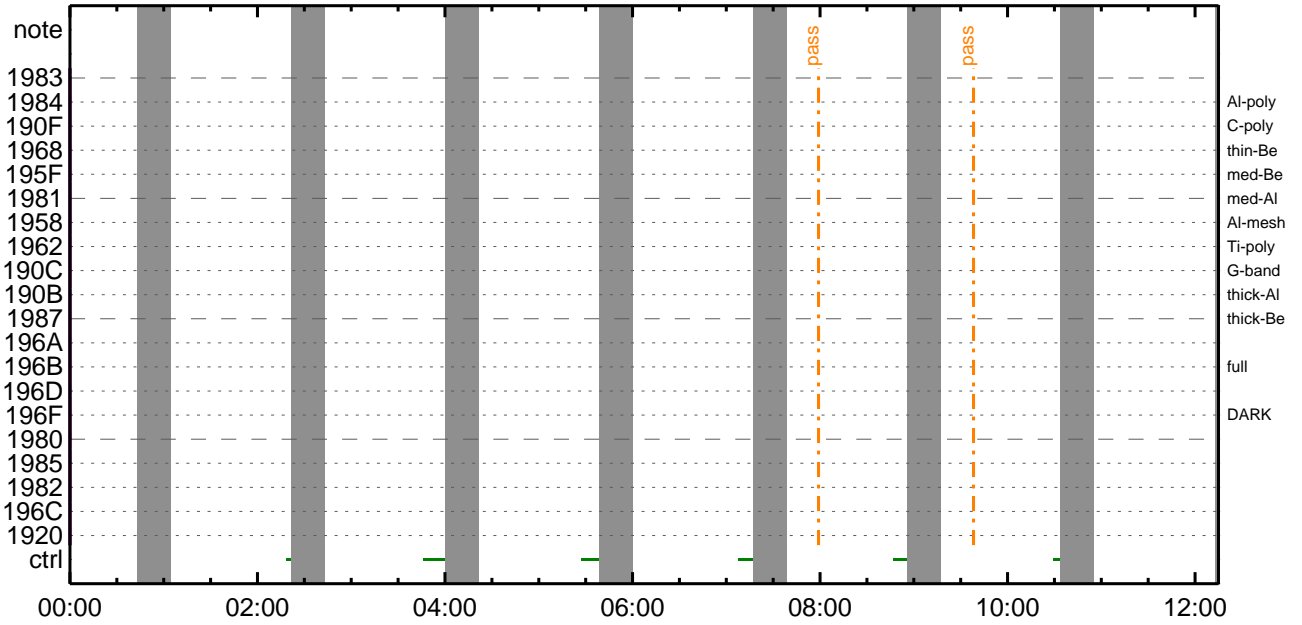
CMDI #0415 2013/04/25



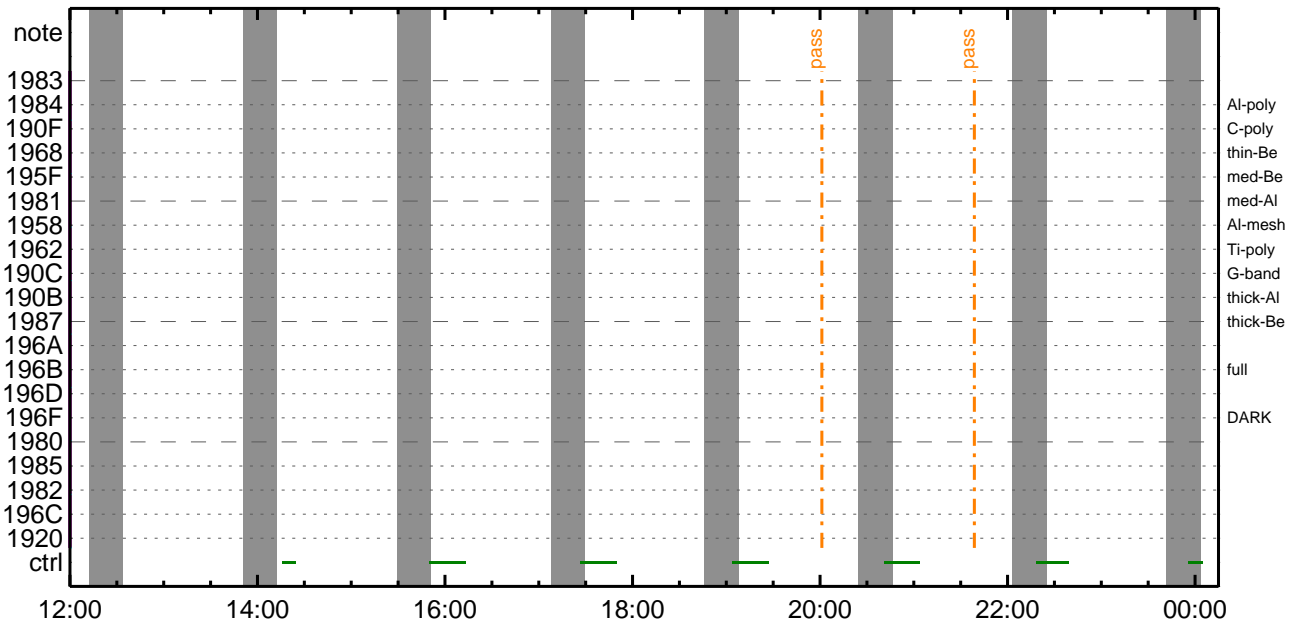
CMDI #0415 2013/04/25



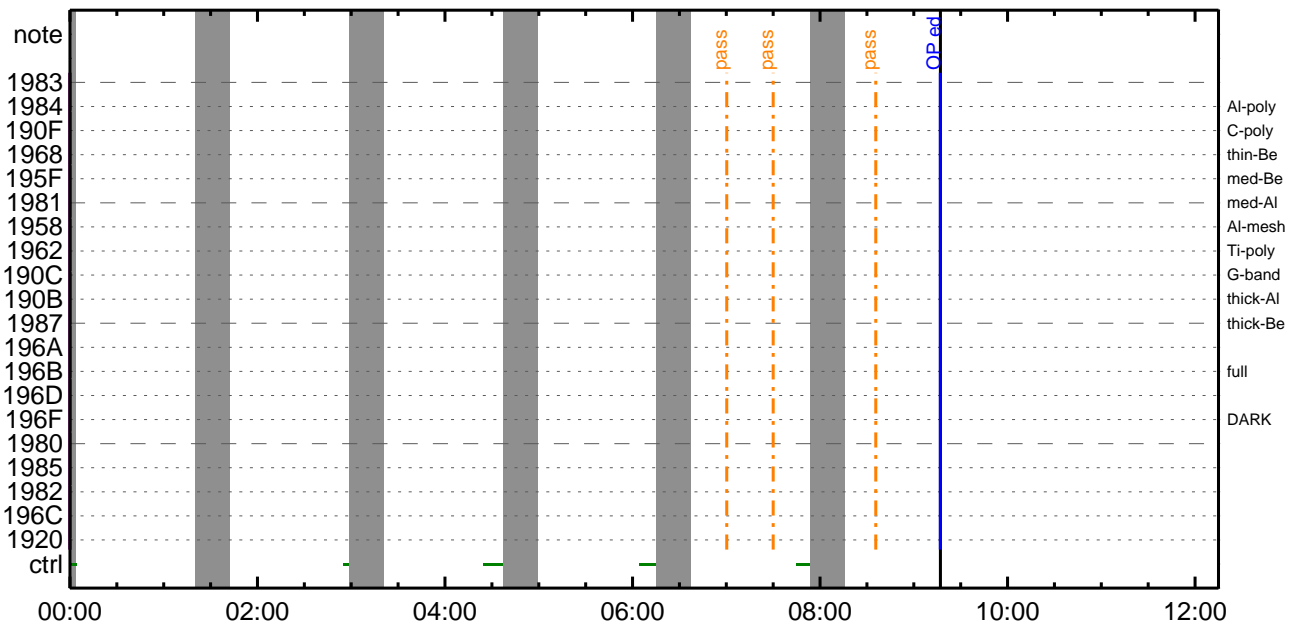
CMDI #0415 2013/04/26



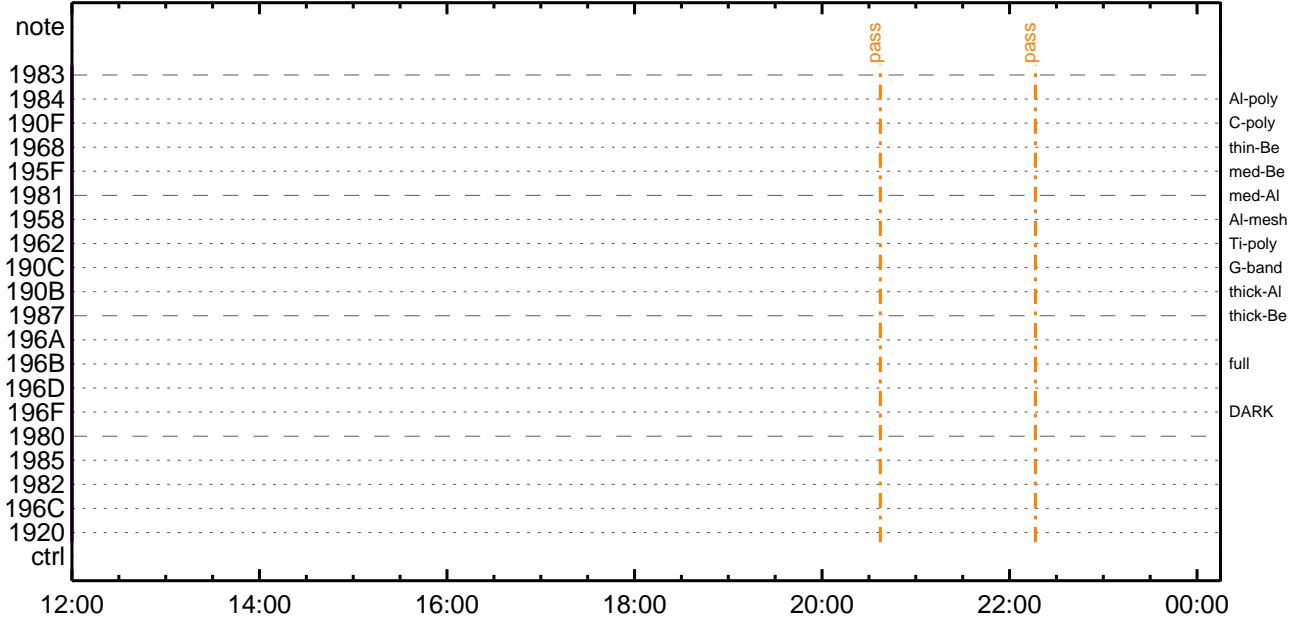
CMDI #0415 2013/04/26



CMDI #0415 2013/04/27



CMDI #0415 2013/04/27




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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 C.
0121 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°ªÈSTS_CHKªðOFFªÈª¹ªë
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 . C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKªðONªÈª¹ªë
0134 C.
0135 . C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 . C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2013-04-23 10:37:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 . C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 . C. ----- Success Verify ? OK / NG_____
0162 C.
0163 C. XRT Obs. Table Upload
0164 . S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 . C. ----- Comparison Check ? OK / ERR _____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 08 08)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 85 83 06 06)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 20)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 20 08)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 80 80 08 20)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0f 80 80 06 06)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 10 80 80 08 08)
0196 + DC 07-F0 MDP_XRT_FLD_ENA
0197 BC (d8)
0198 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0199 BC (c8)
0200 + DC 07-F0 MDP_XRT_AEC_RESET
0201 BC (d0)
0202 + DC 07-F0 MDP_XRT_ARS_DIS
0203 BC (d5)
0204 + DC 07-F0 MDP_XRT_FLD_RESET
0205 BC (da)
0206 + DC 07-F0 MDP_XRT_QT_PROG_SET
0207 BC (c4 02)
0208 + DC 07-F0 MDP_XRT_FL_PROG_SET
0209 BC (c5 05)
0210 . C. ----- Success Verify ? OK / NG ____
0211 C.
0212 C.
0213 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0214 C.
0215 + DC 07-F0 MDP_XRT_MODE_OBSV
0216 BC (c2)
0217 + TI 2013-04-23 10:37:02.0
0218 DC 07-F0 MDP_XRT_MODE_OBSV
0219 BC (c2)
0220 . C. ----- Success Verify ? OK / NG ____
0221 C.
0222 C. ***** XRT END *****
0223 C.
0224 . C. ***** MDP `úÃîñî»ö%ÝñÊÃðñ¹ñèDCBC•x²è *****
0225 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝÁÝÇÝèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** ¥ÐÝ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Û;ã
0236 C.
0237 . C. ***** LOS *****
0238 C.
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Apr 23, 13 13:39

XRT_OGLIST_0415.chk

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*** OP Sequence for XRT ***

2013/04/23	10:47:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	10:47:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/23	10:48:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/04/23	10:48:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/23	10:48:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/23	10:48:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/23	10:48:22.0	XRT_ARS_DIS_414_OG [0x19e]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/23	10:50:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/23	10:50:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/04/23	10:50:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/23	10:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/23	12:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	12:00:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/23	12:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/23	12:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/23	12:20:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/23	12:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/23	13:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	13:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/23	13:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/23	13:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/23	13:58:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/23	13:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/23	14:05:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	14:05:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/23	14:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/23	14:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/23	14:10:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/23	14:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/23	15:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	15:17:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/23	15:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/23	15:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/23	16:00:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/23	16:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/23	16:44:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	16:44:56.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2013/04/23	16:45:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/04/23	16:48:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/23	16:49:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/23	16:49:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/23	16:49:20.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/23	16:49:22.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/04/23	16:49:24.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/23	16:55:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/04/23	17:15:30.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/23	17:15:32.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/23	17:15:52.0	XRT_FLD_ENA_411_OG [0x19b]			

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2013/04/23	17:15:54.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/04/23	17:15:56.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/04/23	17:15:58.0	XRT_ARS_DIS_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/04/23	17:16:00.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/23	17:16:02.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2013/04/23	17:16:04.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05		
2013/04/23	17:16:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/23	18:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/23	18:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/23	18:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/23	18:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/23	18:54:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/23	18:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/23	20:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/23	20:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/23	20:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/23	20:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/23	20:32:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/23	20:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/23	20:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/23	20:50:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	e9	cb	b1
2013/04/23	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/23	20:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/04/23	21:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00	00	00
2013/04/23	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/04/23	21:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/04/23	21:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/04/23	21:00:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/04/23	21:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/23	21:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2013/04/23	21:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05		
2013/04/23	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/23	21:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/23	21:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/23	21:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/23	21:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/23	22:27:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/23	22:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/23	23:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/23	23:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/23	23:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/23	23:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/23	23:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/23	23:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/24	01:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/24	01:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/24	01:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/24	01:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

2013/04/24	01:28:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/24	01:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/24	02:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/24	02:42:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/24	02:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/24	02:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/24	03:06:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/24	03:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/24	04:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/24	04:11:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/24	04:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/24	04:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/24	04:45:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/24	04:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/24	05:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/24	05:51:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/24	05:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/24	05:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/24	06:23:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/24	06:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/24	06:27:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/24	06:27:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/24	06:27:30.0	AOCs_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/04/24	06:27:46.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/24	06:27:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/24	06:27:50.0	XRT_ARS_DIS_406_OG [0x196]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/24	06:30:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/04/24	06:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/24	06:37:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/24	06:37:02.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/24	06:37:22.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/24	06:37:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/24	06:37:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/24	06:37:28.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2013/04/24	06:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/24	06:49:15.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/24	06:50:00.0	AOCs_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 01 ca 50 31
2013/04/24	07:14:30.0	AOCs_OrE-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	00 01 ca 3e 7e
2013/04/24	08:03:00.0	AOCs_OrE-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	00 01 ca 2c cc
2013/04/24	08:13:00.0	AOCs_OrE-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76	00 01 ca 1b 1a
2013/04/24	08:23:00.0	AOCs_OrE-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	00 01 ca 09 68
2013/04/24	08:33:00.0	AOCs_OrE-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 01 ca f7 cf
2013/04/24	08:43:00.0	AOCs_OrE-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 01 ca e6 25
2013/04/24	08:53:00.0	AOCs_OrE-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 01 ca d4 5b
2013/04/24	09:41:30.0	AOCs_OrE-point_Start_12_OG [0x0a2]			
		AOCU_NM	5	02-76	00 01 ca c2 a8
2013/04/24	09:51:30.0	AOCs_OrE-point_Start_13_OG [0x0a3]			
		AOCU_NM	5	02-76	00 01 ca b0 fe
2013/04/24	10:11:30.0	AOCs_OrE-point_Start_14_OG [0x0a4]			
		AOCU_NM	5	02-76	00 eb 1b 50 fd
2013/04/24	10:21:30.0	AOCs_OrE-point_Start_15_OG [0x0a5]			
		AOCU_NM	5	02-76	00 e7 75 41 81
2013/04/24	10:31:30.0	AOCs_OrE-point_Start_16_OG [0x0a6]			

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2013/04/24	10:41:30.0	AOCs_OrE-point_Start_17_OG [0x0a7]	AOCu_NM	5	02-76	00	e6	25	31	26
			AOCu_NM	5	02-76	00	e5	59	20	72
2013/04/24	11:20:00.0	AOCs_OrE-point_Start_18_OG [0x0a8]	AOCu_NM	5	02-76	00	e4	e6	0f	74
			AOCu_NM	5	02-76	00	e4	cd	fe	36
2013/04/24	11:30:00.0	AOCs_OrE-point_Start_19_OG [0x0a9]	AOCu_NM	5	02-76	00	e4	e6	f3	34
			AOCu_NM	5	02-76	00	e5	59	e2	36
2013/04/24	11:40:00.0	AOCs_OrE-point_Start_20_OG [0x0aa]	AOCu_NM	5	02-76	00	e6	25	d1	82
			AOCu_NM	5	02-76	00	e7	75	c1	27
2013/04/24	11:50:00.0	AOCs_OrE-point_Start_21_OG [0x0ab]	AOCu_NM	5	02-76	00	ec	e6	b0	5b
			AOCu_NM	5	02-76	00	ec	e6	b0	5b
2013/04/24	12:00:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]	AOCu_NM	5	02-76	00	ec	e6	b0	5b
			AOCu_NM	5	02-76	00	ec	e6	b0	5b
2013/04/24	12:10:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]	AOCu_NM	5	02-76	00	ec	e6	b0	5b
			AOCu_NM	5	02-76	00	ec	e6	b0	5b
2013/04/24	12:20:00.0	AOCs_OrE-point_Start_24_OG [0x0ae]	AOCu_NM	5	02-76	00	ec	e6	b0	5b
			AOCu_NM	5	02-76	00	ec	e6	b0	5b
2013/04/24	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	12:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/04/24	12:30:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCu_NM	5	02-76	03	00	00	00	00
			AOCu_NM	5	02-76	03	00	00	00	00
2013/04/24	12:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/24	12:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/24	12:30:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/24	12:30:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/24	12:32:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	12:32:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2013/04/24	12:32:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	05			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	05			
2013/04/24	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	12:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	12:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	12:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	12:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	12:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CUSTOM_430	1	07-F0	c0				
			MDP_XRT_CUSTOM_430	1	07-F0	c0				
2013/04/24	12:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	14:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	14:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	14:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	14:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	14:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CUSTOM_430	1	07-F0	c0				
			MDP_XRT_CUSTOM_430	1	07-F0	c0				
2013/04/24	14:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	15:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	15:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/04/24	15:40:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCu_NM	5	02-76	00	00	00	00	00
			AOCu_NM	5	02-76	00	00	00	00	00
2013/04/24	15:40:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/04/24	15:40:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/04/24	15:40:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/24	15:42:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2013/04/24	15:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	15:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	15:49:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/04/24	15:50:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCu_NM	5	02-76	03	00	00	00	00
			AOCu_NM	5	02-76	03	00	00	00	00
2013/04/24	15:50:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/24	15:50:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/24	15:50:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/24	15:50:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/24	15:52:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				

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2013/04/24	15:52:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2013/04/24	15:52:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	05			
2013/04/24	15:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	15:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	15:54:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	15:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	15:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	16:14:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/24	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	17:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	17:32:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	17:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	17:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	17:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/24	17:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	19:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	19:11:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	19:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	19:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	19:31:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/24	19:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	19:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2013/04/24	20:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/24	20:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/24	20:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/24	20:00:22.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/24	20:02:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	20:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2013/04/24	20:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	05			
2013/04/24	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	20:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	20:49:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	20:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	20:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	21:28:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/24	21:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/24	22:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/24	22:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/24	22:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/24	22:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/24	23:02:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/24	23:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/25	00:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/25	00:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/25	00:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/25	00:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/25	00:27:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/25	00:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/25	01:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2013/04/25	01:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	01:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/25	01:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/25	02:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/25	02:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	03:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	03:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/25	03:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/25	03:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/25	03:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	03:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	04:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	04:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/25	04:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/25	04:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/25	05:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	05:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/04/25	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/04/25	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/04/25	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/04/25	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/04/25	06:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2013/04/25	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/04/25	06:10:00.0	AOCS_OrE-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00 ee 36 b0 5b			
2013/04/25	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/04/25	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/04/25	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/04/25	06:10:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/04/25	06:12:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/25	06:12:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2013/04/25	06:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05			
2013/04/25	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	06:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	06:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/25	06:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/25	06:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/25	07:01:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	07:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	08:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	08:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/25	08:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/25	08:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/25	08:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/25	08:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/25	08:55:00.0	XRT_CTRL_MANU_402_OG [0x192]							

2013/04/25	10:24:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00