

XRT Timeline to be uploaded on 2013/04/25

Period: 2013/04/25 10:14:00 - 2013/04/30 10:03:00

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Normal mode

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XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms)												
Term	Pointing (x, y)						Comment					
04/25 10:27:00 - 04/25 17:59:24	Track (811.0, 234.9) ^{@ 04/25 10:24:00}						# OP start + 10min: AR11726					
PROG= 02 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 18 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 13 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 17 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 20 150-time(s) 2.0sec												
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(250)												
Term	Pointing (x, y)						Comment					
04/25 18:02:30 - 04/25 18:09:30	Fixed (0.0, 0.0)						synoptic, shifted -0.5 min					
04/26 06:06:30 - 04/26 06:13:24	Fixed (0.0, 0.0)						synoptic, shifted 3.5 min					
04/26 18:09:00 - 04/26 18:15:54	Fixed (0.0, 0.0)						synoptic, shifted 6.0 min					
04/27 05:40:00 - 04/27 05:46:54	Fixed (0.0, 0.0)						synoptic, shifted -23.0 min					
PROG= 08 1-time(s)												
└─ Subr= 1 1-time(s) 14.0sec												
└─ Seqn= 16 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 26 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 19 1-time(s) 2.0sec												
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 14 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 12 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #195F: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad, shorter G-band (32ms) w/ G-Band VLS Clo												
Term	Pointing (x, y)						Comment					
04/25 18:51:06 - 04/25 21:26:00	Track (843.1, 229.6) ^{@ 04/25 18:09:30}						AR 11726					
04/25 22:07:06 - 04/26 06:03:24	Fixed (894.0, 210.0)						AR 11726					
04/26 06:16:30 - 04/26 18:05:54	Fixed (894.0, 210.0)						AR11726					
04/26 18:19:00 - 04/27 05:36:54	Fixed (894.0, 210.0)						AR11726					
04/27 05:50:00 - 04/27 08:20:30	Fixed (894.0, 210.0)						AR11726					
PROG= 16 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 24 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 28 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 31 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
	Seqn= 29 70-time(s) 2.0sec												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)		Comment										
04/25 10:27:00 - 04/25 17:59:24	Track (811.0, 234.9)		# OP start + 10min: AR11726										
04/25 18:51:06 - 04/25 21:26:00	Track (843.1, 229.6)		AR 11726										
04/25 22:07:06 - 04/26 06:03:24	Fixed (894.0, 210.0)		AR 11726										
04/26 06:16:30 - 04/26 18:05:54	Fixed (894.0, 210.0)		AR11726										
04/26 18:19:00 - 04/27 05:36:54	Fixed (894.0, 210.0)		AR11726										
04/27 05:50:00 - 04/27 08:20:30	Fixed (894.0, 210.0)		AR11726										
PROG= 05 15-time(s)													
Subr= 1 45-time(s) 10.0sec													
Seqn= 9 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

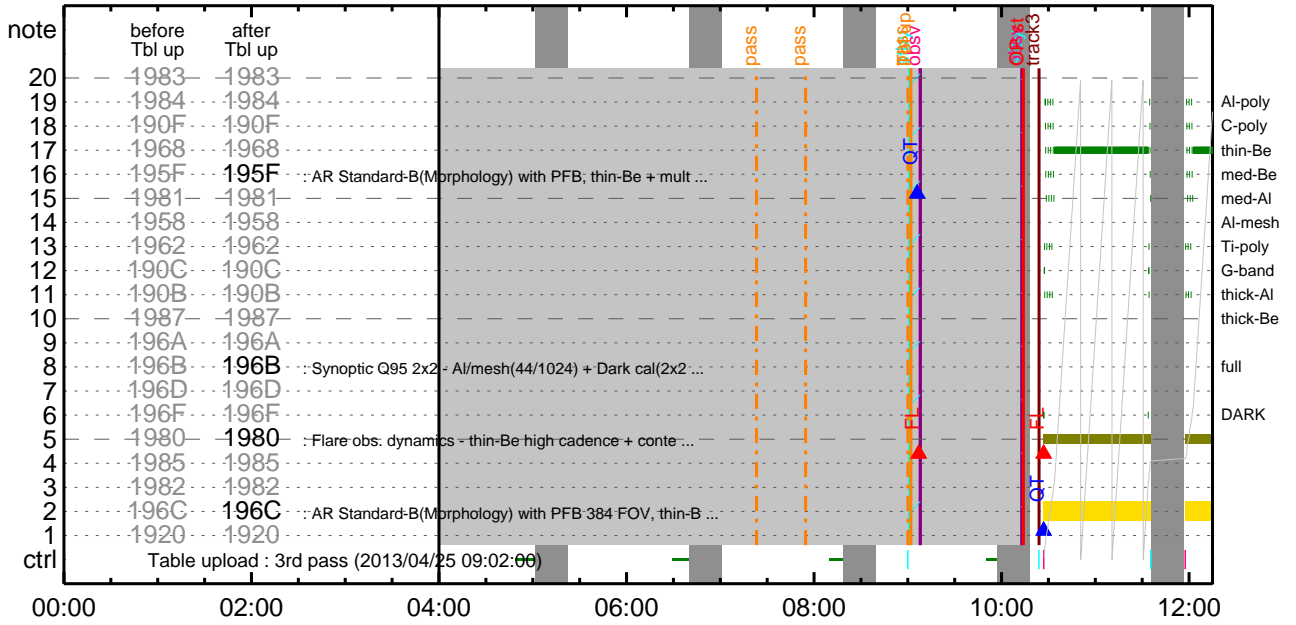
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Flare Detection

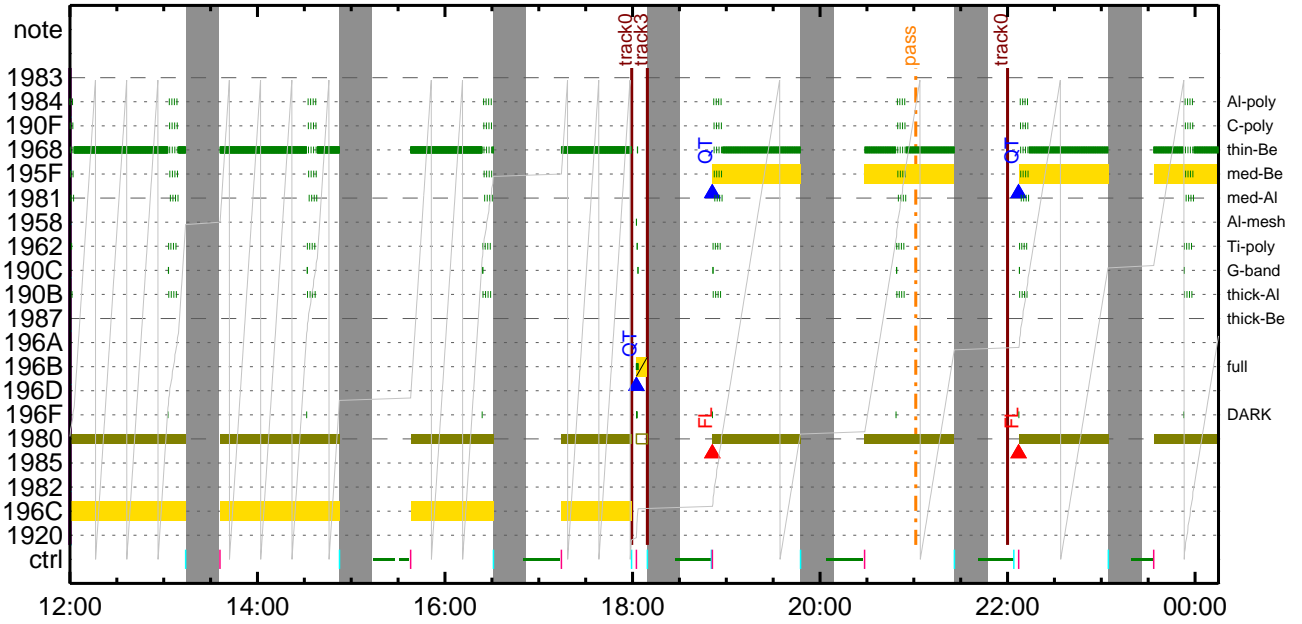
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FLD Patrol													
Term	Pointing (x, y)		Comment										
04/25 18:50:52 - 04/26 06:03:46	Track (843.1, 229.6)		AR 11726										
04/26 06:13:46 - 04/26 18:06:16	Fixed (894.0, 210.0)		AR11726										
04/26 18:16:16 - 04/27 05:37:16	Fixed (894.0, 210.0)		AR11726										
04/27 05:47:16 - 04/30 10:03:00	Fixed (894.0, 210.0)		AR11726										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

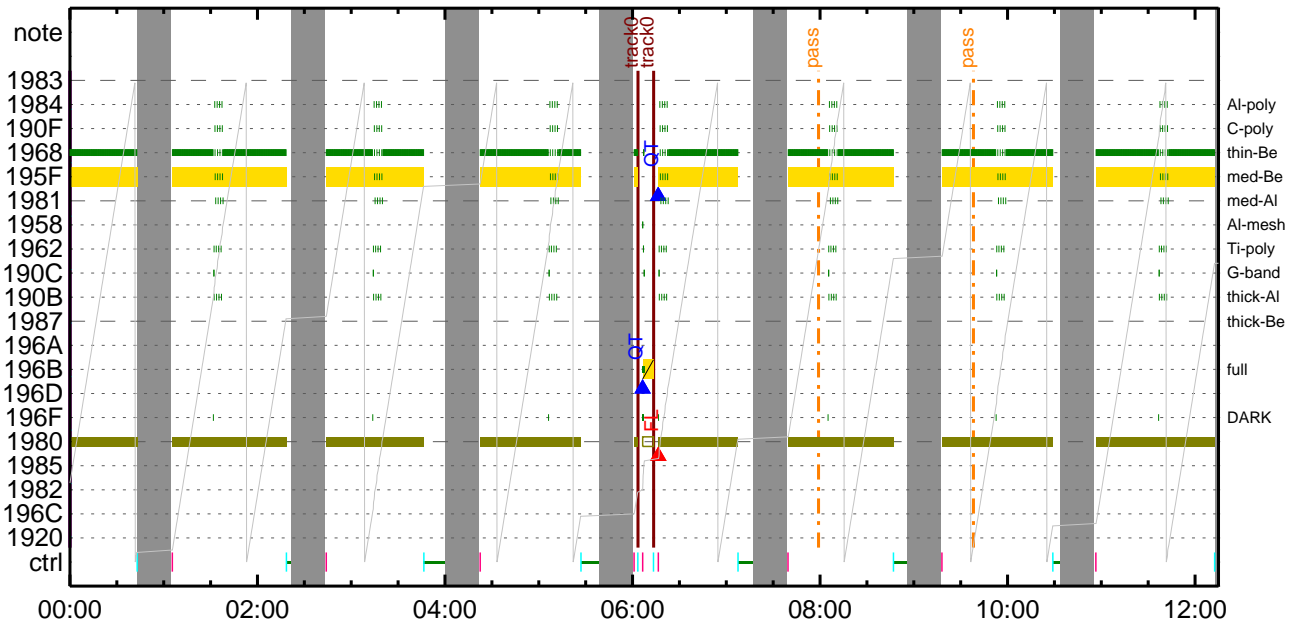
CMDI #0419 2013/04/25



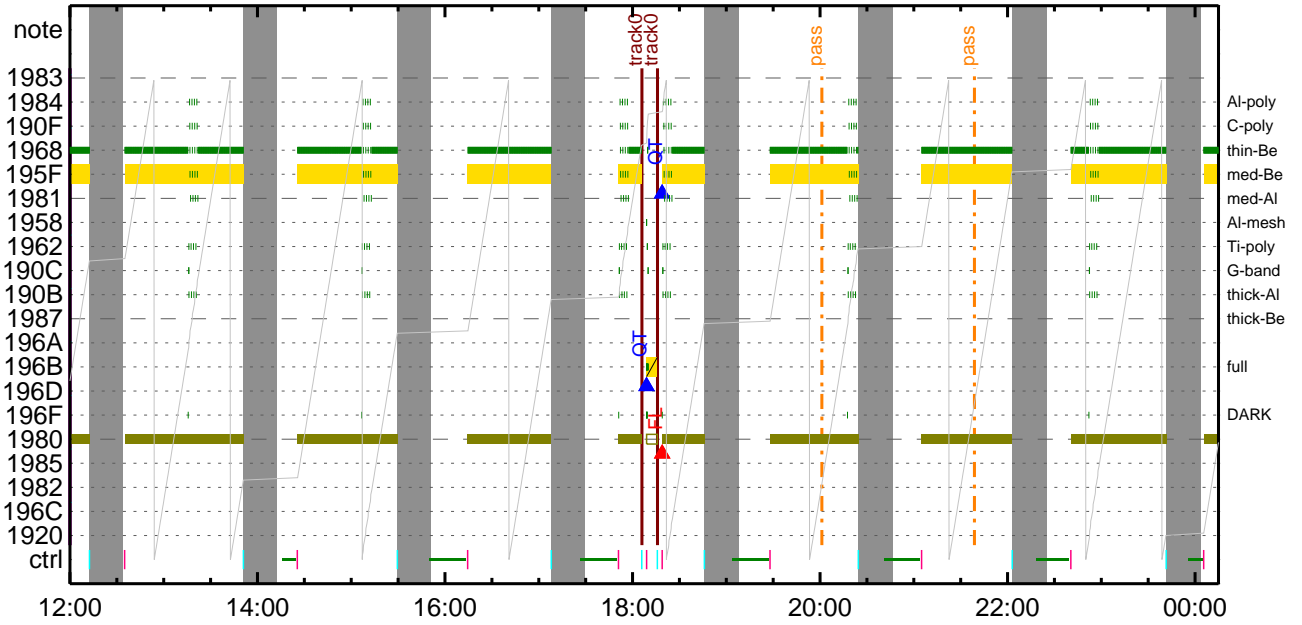
CMDI #0419 2013/04/25



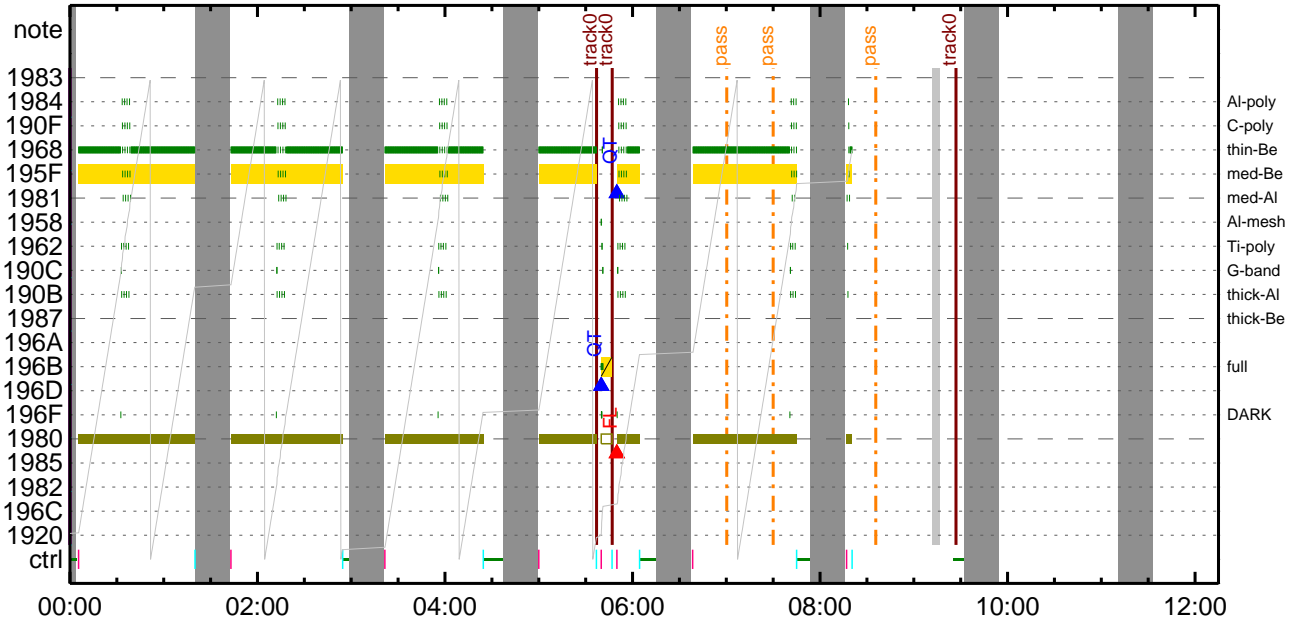
CMDI #0419 2013/04/26



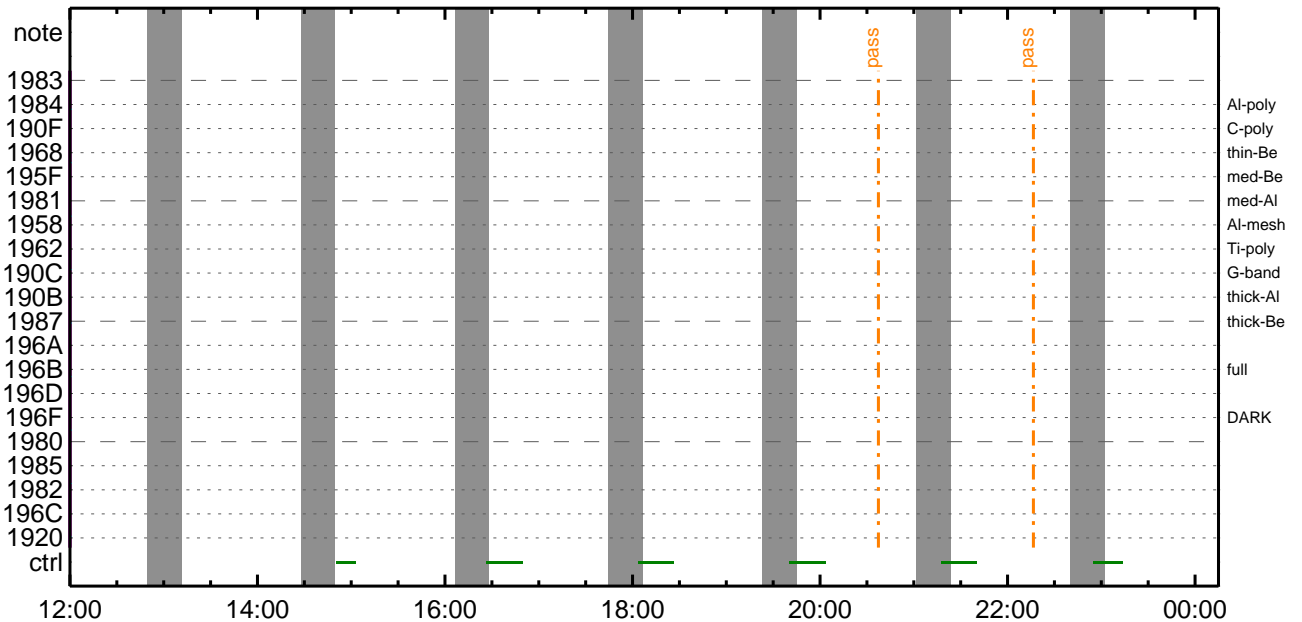
CMDI #0419 2013/04/26



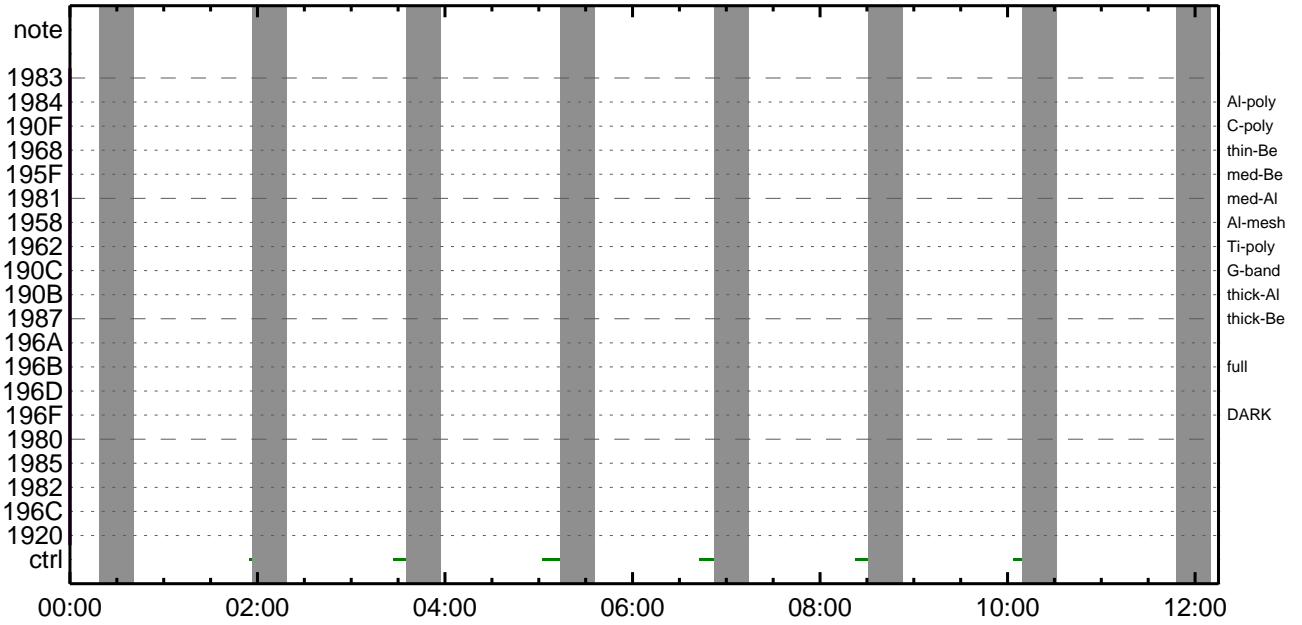
CMDI #0419 2013/04/27



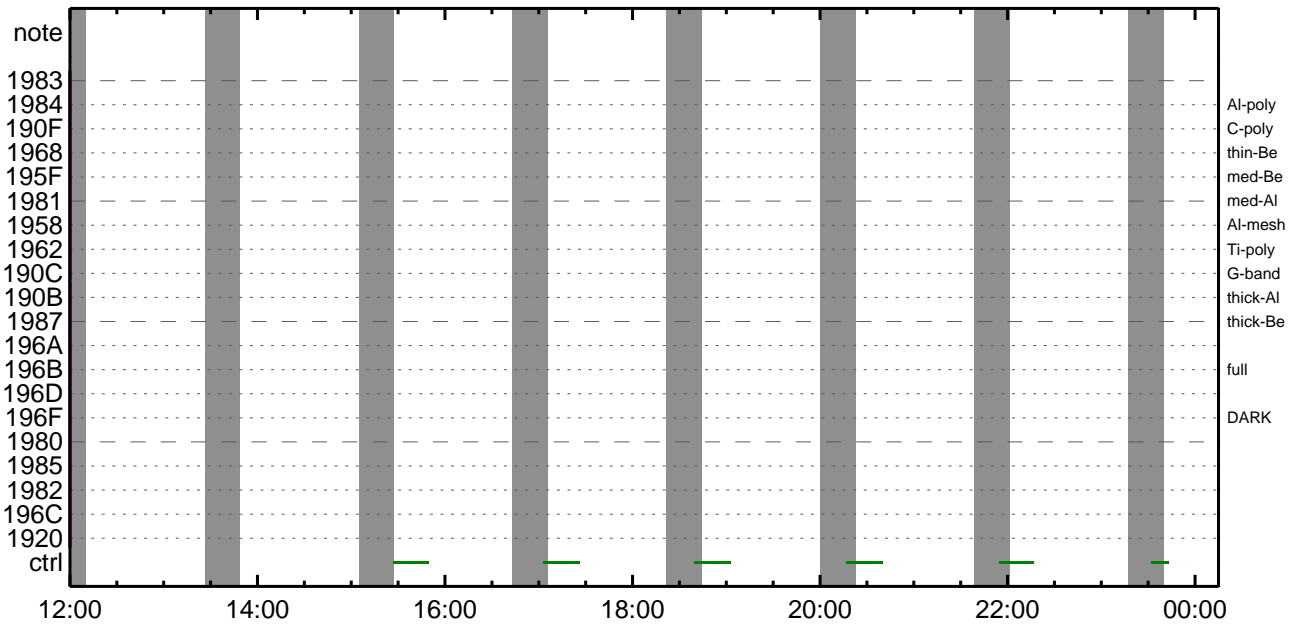
CMDI #0419 2013/04/27



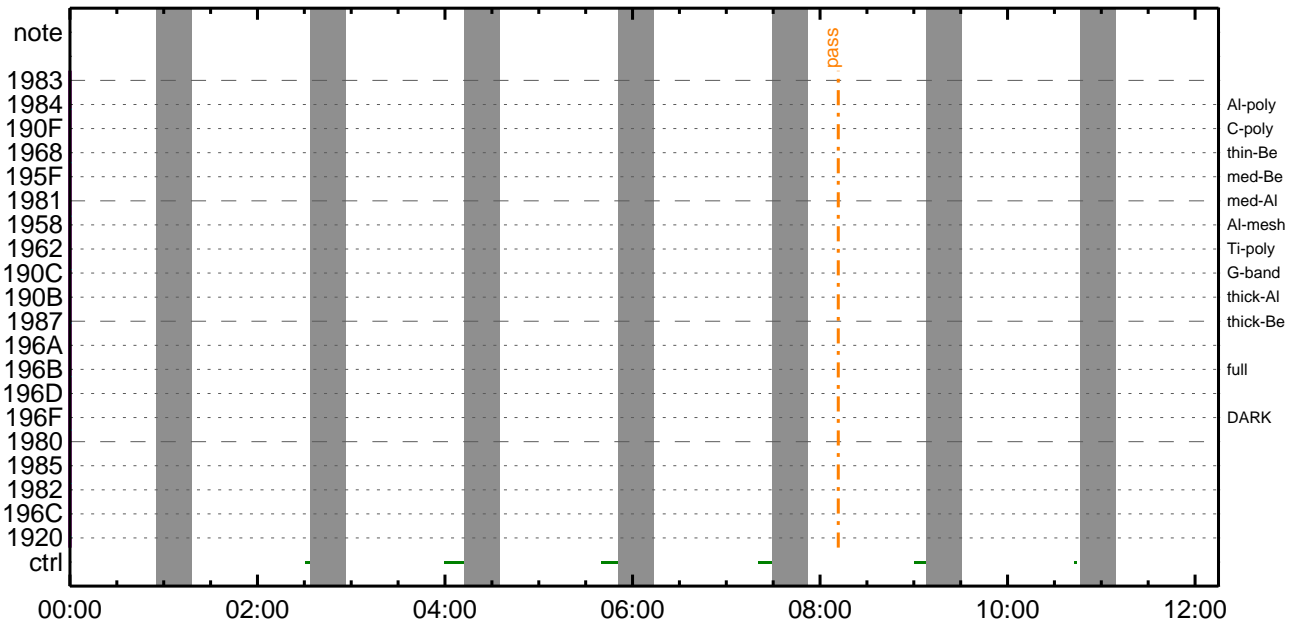
CMDI #0419 2013/04/28



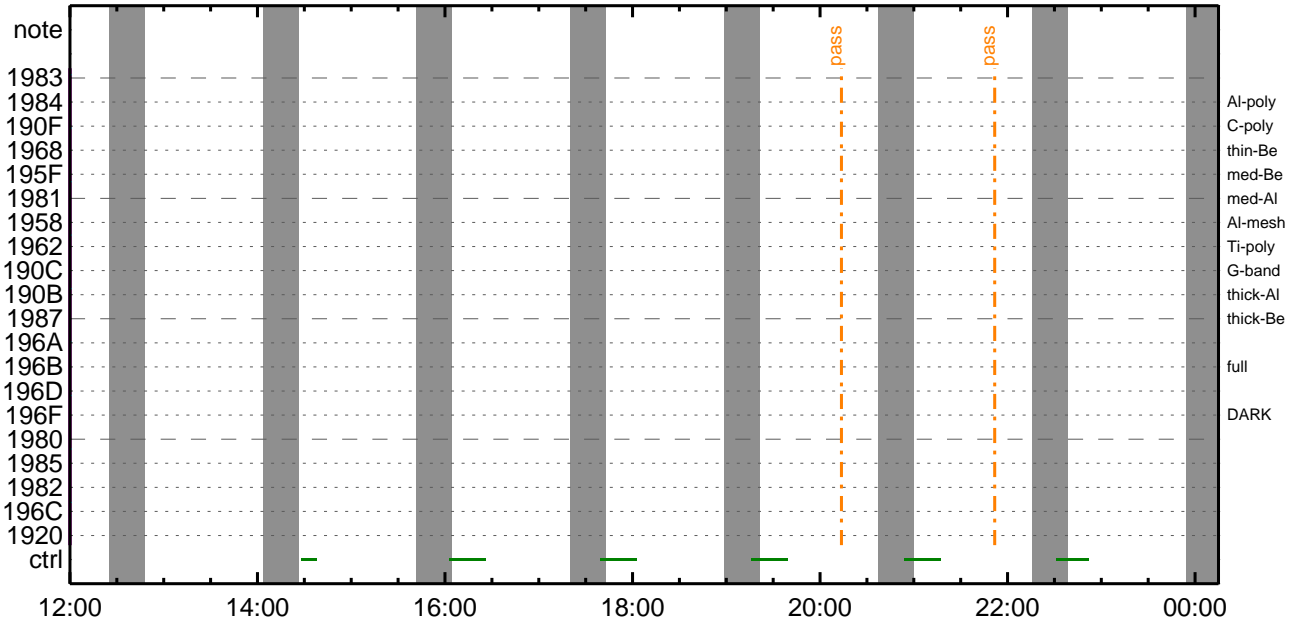
CMDI #0419 2013/04/28



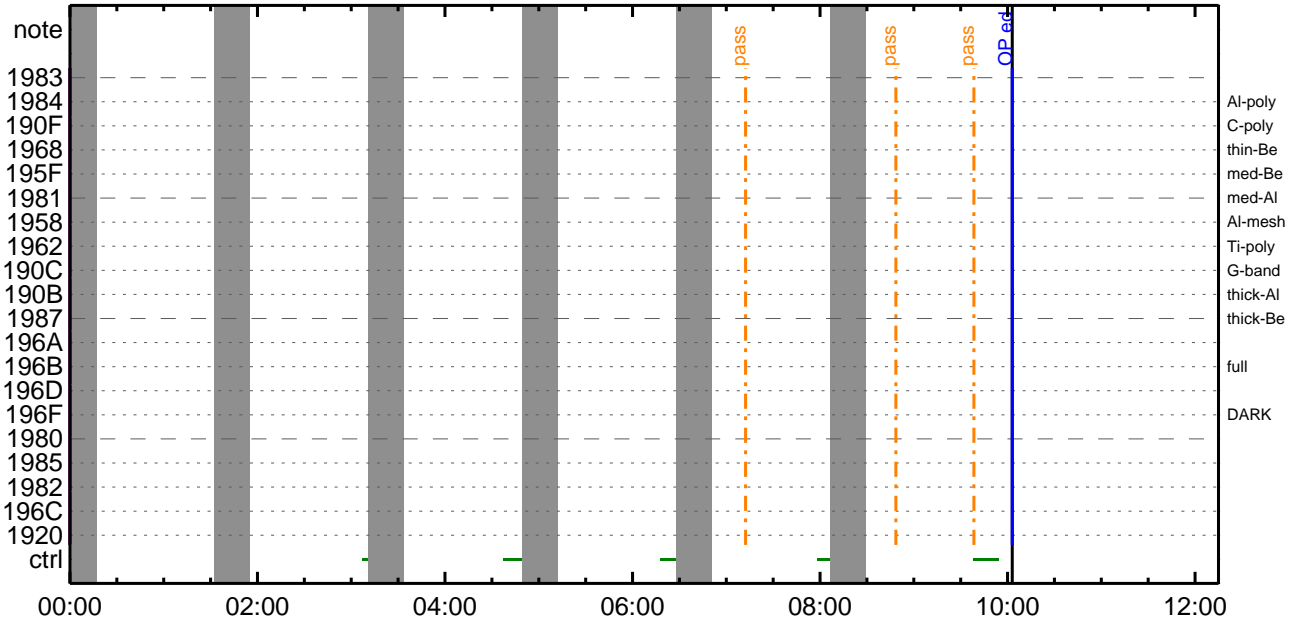
CMDI #0419 2013/04/29



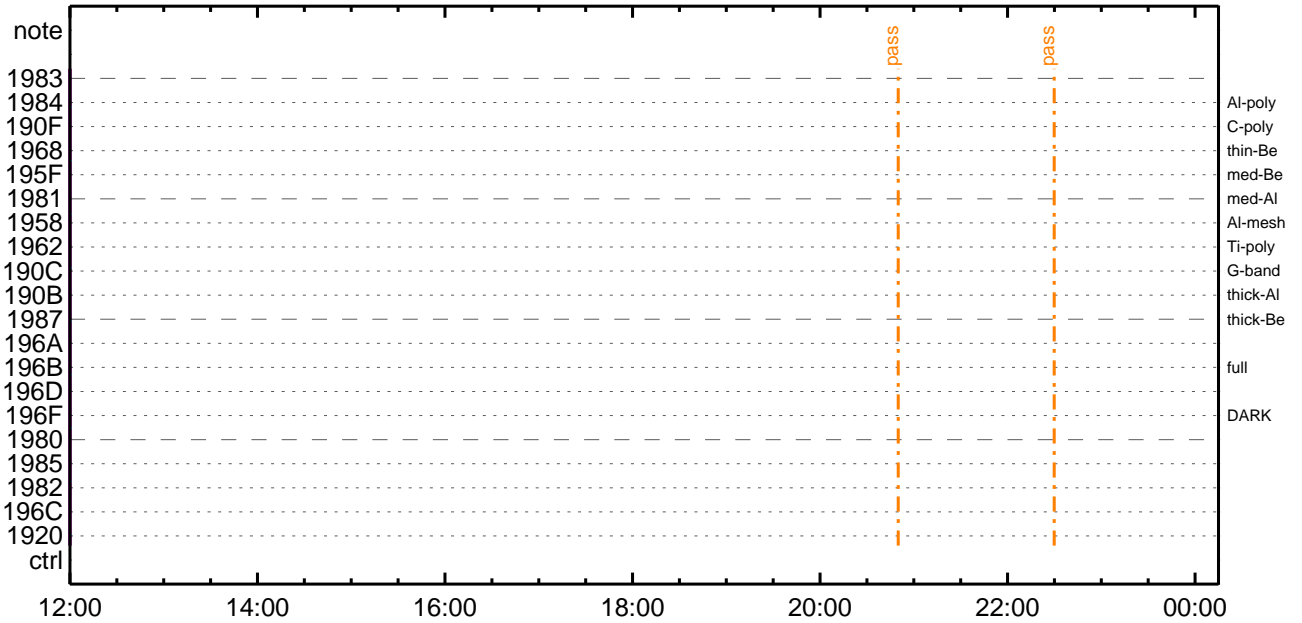
CMDI #0419 2013/04/29



CMDI #0419 2013/04/30



CMDI #0419 2013/04/30



(a) Spacecraft Operation Procedure (real-commands)

```
main-610 2013-04-25 12:47:57 214 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY§YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÁÍ;Éò¿òÀò•µ°È»Í×ÁÇòÍYÇYÁY×YÍ;¼YÉ;ÉÈÈ½µ•íÉ;ÉòÈ½°Çòò•ò¿¼í¹ÇòÍ;çÀ®, ùò¹òÈòÈòÇÁ+¿®ò•òÉòòò³òÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@µ;ON
0016 C. *****
0017 C. ç" °ÈÀ, Í×ÉYòÀÌÒòÈòÇòÍ»p´Òòò¹íí, ò•; çÉÔÍ×òÈXÁÒòòò¹¹ÒòÈòÍòÈòòò³òÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÓÁÒòò-òÁÁÈò•ò¿òÉ; ç°È²¼òÍ°ÈÀ, ¼È½Çòò½Á¹Òò¹òÈ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÈÀ,
0033 C. *****
0034 C. ç" RESTART;ÉPT1;Éò•ò¿òò½¼í¹ÇòÍ; ç°È²¼òÍ¼Á¹Òòòò°; çDCBC-150òò¿¿Èòòà;f
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÁÙÁÏ;ÉÁ•Á²óÈò;É, àòÍ°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ò-¼«È°Áá»ßò•ò¿, á; ç°È²¼òò½Á¹Òò¹òÈ;f
0055 C. YçYÓYÉYÉÁÙÁÏòòÁÁ•Á²óÈòò-¼áòò½¼í¹ÇòÍ´°Í»ò¹òÈòÈòÇÁÒòòÁ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÈÀ,
0059 C. *****
0060 C. ç" RESTART;ÉPT2;Éò•ò¿òò½¼í¹ÇòÍ; ç°È²¼òÍ¼Á¹Òòòò°; çDCBC-151òò¿¿Èòòà;f
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÁÙÁÏ;ÉÁ•Á²óÈò;É, àòÍ°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß;çXÁ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2013-04-25 10:13:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 85 83 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 20)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 20 08)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 20)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 + DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 10)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 05)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2013-04-25 10:13:02.0

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```
0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP `úÃîñî»ò¼ŷñÊÃĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŲŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOSŷÁŷŲŷÃŷ¬¼Ã»Û¿
0212 C.
0213 . C. ***** LOS *****
0214 C.
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*** OP Sequence for XRT ***

2013/04/25	10:23:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	10:23:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/25	10:24:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/04/25	10:24:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/25	10:24:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/25	10:24:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/25	10:24:22.0	XRT_ARS_DIS_414_OG [0x19e]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/25	10:26:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	10:26:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/04/25	10:26:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/25	10:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	11:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	11:35:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	11:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	11:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	11:56:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/25	11:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	13:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	13:14:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	13:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	13:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	13:35:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/25	13:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	14:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	14:52:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	14:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	14:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	15:37:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/25	15:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	16:31:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	16:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	16:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	17:13:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/25	17:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	17:59:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/25	17:59:30.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/04/25	17:59:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/25	18:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/25	18:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/25	18:02:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/04/25	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	18:09:30.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/04/25	18:09:30.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	18:09:32.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	18:09:34.5	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	18:12:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]			

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2013/04/25	18:50:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	18:50:32.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/25	18:50:52.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/25	18:50:54.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/25	18:50:56.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/25	18:50:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/25	18:51:00.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	18:51:02.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/04/25	18:51:04.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/25	18:51:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	19:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	19:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	19:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	19:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	20:27:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/25	20:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	21:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	21:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	21:26:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	21:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	22:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 ed 59 b0 8c
2013/04/25	22:04:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	22:04:02.5	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/25	22:04:22.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/25	22:04:24.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/25	22:04:26.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/25	22:04:28.5	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/25	22:07:00.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	22:07:02.5	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/04/25	22:07:04.5	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/25	22:07:06.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/25	23:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/25	23:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/25	23:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/25	23:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/25	23:32:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/25	23:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/26	00:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/26	00:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/26	00:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/26	00:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/26	01:04:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/26	01:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/26	02:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/26	02:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/26	02:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/26	02:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/26	02:43:00.0	XRT_Custom_430_OG [0x1ae]				

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2013/04/26	02:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	03:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	03:46:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/26	03:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/26	03:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/26	04:21:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/26	04:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	05:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	05:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/26	05:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/26	05:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/26	06:00:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/26	06:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	06:03:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/04/26	06:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/04/26	06:03:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/04/26	06:03:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/04/26	06:03:50.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/26	06:06:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2013/04/26	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	06:13:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/04/26	06:13:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 ed 59 b0 8c				
2013/04/26	06:13:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/26	06:13:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/26	06:13:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/26	06:13:52.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/26	06:16:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/26	06:16:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/04/26	06:16:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05				
2013/04/26	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	07:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	07:07:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/26	07:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/26	07:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/26	07:38:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/26	07:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	08:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	08:47:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/26	08:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/26	08:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/26	09:17:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/26	09:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/26	10:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/26	10:29:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/26	10:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/26	10:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/26	10:55:30.0	XRT_Custom_430_OG [0x1ae]							

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2013/04/26	10:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	12:12:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	12:12:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/04/26	12:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/04/26	12:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/04/26	12:34:00.0	XRT_Custom_430_OG [0x1ae]								
2013/04/26	12:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	13:51:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	13:51:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/04/26	13:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/04/26	13:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/04/26	14:24:30.0	XRT_Custom_430_OG [0x1ae]								
2013/04/26	14:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	15:29:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	15:29:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/04/26	15:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/04/26	15:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/04/26	16:13:30.0	XRT_Custom_430_OG [0x1ae]								
2013/04/26	16:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	17:08:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	17:08:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/04/26	17:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/04/26	17:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/04/26	17:50:00.0	XRT_Custom_430_OG [0x1ae]								
2013/04/26	17:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2013/04/26	18:06:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2013/04/26	18:06:16.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2013/04/26	18:06:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2013/04/26	18:06:20.0	XRT_ARS_DIS_406_OG [0x196]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/04/26	18:08:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08					
2013/04/26	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	18:15:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2013/04/26	18:16:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00 ed 59 b0 8c					
2013/04/26	18:16:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/04/26	18:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/04/26	18:16:20.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/04/26	18:16:22.0	XRT_ARS_DIS_414_OG [0x19e]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/04/26	18:18:54.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/04/26	18:18:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10					
2013/04/26	18:18:58.0	XRT_FL_PROG_SET_431_OG [0x1af]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05					
2013/04/26	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/04/26	18:46:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/04/26	18:46:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/04/26	18:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/04/26	18:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/04/26	19:27:00.0	XRT_Custom_430_OG [0x1ae]								

2013/04/26	19:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/26	20:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/26	20:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1 07-F0 da
2013/04/26	20:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1 07-F0 e8
2013/04/26	20:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1 07-F0 e9
2013/04/26	21:04:00.0	XRT_Custom_430_OG [0x1ae]		
2013/04/26	21:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/26	22:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/26	22:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1 07-F0 da
2013/04/26	22:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1 07-F0 e8
2013/04/26	22:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1 07-F0 e9
2013/04/26	22:39:30.0	XRT_Custom_430_OG [0x1ae]		
2013/04/26	22:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/26	23:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/26	23:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1 07-F0 da
2013/04/26	23:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1 07-F0 e8
2013/04/26	23:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1 07-F0 e9
2013/04/27	00:04:30.0	XRT_Custom_430_OG [0x1ae]		
2013/04/27	00:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/27	01:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/27	01:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1 07-F0 da
2013/04/27	01:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1 07-F0 e8
2013/04/27	01:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1 07-F0 e9
2013/04/27	01:42:00.0	XRT_Custom_430_OG [0x1ae]		
2013/04/27	01:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/27	02:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/27	02:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1 07-F0 da
2013/04/27	02:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1 07-F0 e8
2013/04/27	02:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1 07-F0 e9
2013/04/27	03:20:30.0	XRT_Custom_430_OG [0x1ae]		
2013/04/27	03:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/27	04:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/27	04:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1 07-F0 da
2013/04/27	04:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1 07-F0 e8
2013/04/27	04:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1 07-F0 e9
2013/04/27	04:59:00.0	XRT_Custom_430_OG [0x1ae]		
2013/04/27	05:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/27	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/27	05:36:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2013/04/27	05:37:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5 02-76 00 00 00 00
2013/04/27	05:37:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1 07-F0 d9
2013/04/27	05:37:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2013/04/27	05:37:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1 07-F0 d5
2013/04/27	05:39:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2 07-F0 c4 08
2013/04/27	05:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2013/04/27	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2013/04/27	05:46:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2013/04/27	05:47:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5 02-76 00 ed 59 b0 8c
2013/04/27	05:47:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1 07-F0 d8
2013/04/27	05:47:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		

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2013/04/27	05:47:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/27	05:47:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/27	05:49:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	05:49:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/04/27	05:49:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/27	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/27	06:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/27	06:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	06:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/27	06:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/27	06:37:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/27	06:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/27	07:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/27	07:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	07:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/27	07:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/27	08:16:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/27	08:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/27	08:20:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/27	09:27:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00