

# XRT Timeline to be uploaded on 2013/04/27

Period: 2013/04/27 09:17:00 - 2013/05/02 10:10:00

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## Normal mode

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### XOB #196C: AR Standard-B(Morphology) with PFB 384 FOV, thin-Be (384FOV) + multifilter (512FOV) context, at 1064 1048, 24s-cad - shorter G-band (33ms)

Term	Pointing (x, y)	Comment
04/27 09:55:30 - 04/27 17:34:24	Fixed ( 894.0, 210.0)	# OP start + 10min, AR 11726, beyond the W limb.
04/27 18:27:30 - 04/28 05:58:24	Fixed ( 894.0, 210.0)	# Cont.
04/30 07:05:00 - 04/30 09:30:00	Track ( -147.9, 199.9) <sup>04/30 07:02:00</sup>	* AR 11731.

**PROG= 02 Inf-time(s)**

Subr= 1	1-time(s)	2.0sec										
<b>Seqn= 18</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec				
Open/G-band	Open/G-band open	Safe Norm 32ms	Obs 1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec				
<b>Seqn= 13</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band close	Safe Norm 63ms	Obs 1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec				
<b>Seqn= 17</b>	<b>4-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
Al-poly/Open	Al-poly/thick-Be close	Safe Norm 250ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
C-poly/Open	C-poly/thick-Al close	Safe Norm 250ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
med-Be/Open	med-Be/Open close	Safe Norm 16.0s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
med-Al/Open	med-Al/thick-Al close	Safe Norm 16.0s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 20</b>	<b>150-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec				

Default Filter    Thicker Filter    VLS    mode image    Exp.    CCD Bin    ROI: size (center)    Comp.    AEC Buffer    Interval

### XOB #196B: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2048) + Thin-Be(25)

Term	Pointing (x, y)	Comment
04/27 17:37:30 - 04/27 17:44:24	Fixed ( 0.0, 0.0)	synoptic, shifted -25.5 min
04/28 06:01:30 - 04/28 06:08:24	Fixed ( 0.0, 0.0)	synoptic, shifted -1.5 min
04/28 18:03:00 - 04/28 18:09:54	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/29 06:17:00 - 04/29 06:23:54	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/29 18:09:30 - 04/29 18:17:24	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w

**PROG= 08 1-time(s)**

Subr= 1	1-time(s)	14.0sec										
<b>Seqn= 16</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Al-mesh	Open/Al-mesh close	Safe Norm 44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec				
<b>Seqn= 26</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Ti-poly	Open/Ti-poly close	Safe Norm 86ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 19</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	thin-Be/Open close	Safe Norm 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 14</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band open	Safe Norm 8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 12</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band close	Safe Norm 32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec				

Default Filter    Thicker Filter    VLS    mode image    Exp.    CCD Bin    ROI: size (center)    Comp.    AEC Buffer    Interval

### XOB #195B: AR Band during disk center pointing HOP217, 1024x1024 at 1024 1024, 5m-cad Al-poly w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
04/28 06:11:30 - 04/28 17:59:54	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/28 18:13:00 - 04/29 05:40:00	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/29 06:27:00 - 04/29 17:20:00	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/29 18:20:30 - 04/30 06:18:00	Track ( -220.6, 99.7) <sup>04/28 06:08:30</sup>	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w

**PROG= 11 Inf-time(s)**

Subr= 1	1-time(s)	2.0sec										
<b>Seqn= 53</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band close	Safe Norm 32ms	Obs 1x1	1024x1024 (1024, 1024)	DPCM	0	0	2.0sec				
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 54</b>	<b>12-time(s)</b>	<b>300.0sec</b>										
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.00s	Obs 2x2	1024x1024 (1024, 1024)	Q=95	3	0	2.0sec				

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	1024x1024 (1024, 1024)	Q=95	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1958: Synoptic 9 Filter 2x2 Q98 + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + G-Band VLS Closed Test (33) - 1 loop**

Term	Pointing (x, y)	Comment
04/30 06:55:00 - 04/30 07:01:54	Fixed ( 0.0, 0.0)	# Synoptic, shifted manually.
<b>PROG= 14 1-time(s)</b>		
<b>Subr= 1 1-time(s) 85.0sec</b>		
<b>Seqn= 41 1-time(s) 7.0sec</b>		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 5 1-time(s) 22.0sec</b>		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 40 1-time(s) 8.0sec</b>		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 37 1-time(s) 12.0sec</b>		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 45 1-time(s) 10.0sec</b>		
C-poly/Open	C-poly/Open close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
C-poly/Open	C-poly/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 43 1-time(s) 10.0sec</b>		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 36 1-time(s) 15.0sec</b>		
thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 2 1-time(s) 150.0sec</b>		
<b>Seqn= 38 1-time(s) 29.0sec</b>		
med-Al/Open	med-Al/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 46 1-time(s) 84.0sec</b>		
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 14 2-time(s) 13.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 12 1-time(s) 10.0sec</b>		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2**

Term	Pointing (x, y)	Comment
04/27 09:55:30 - 04/27 17:34:24	Fixed ( 894.0, 210.0)	# OP start + 10min, AR 11726, beyond the W limb.
04/27 18:27:30 - 04/28 05:58:24	Fixed ( 894.0, 210.0)	# Cont.
04/28 06:11:30 - 04/28 17:59:54	Track ( -220.6, 99.7) @ 04/28 06:08:30	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/28 18:13:00 - 04/29 05:40:00	Track ( -220.6, 99.7) @ 04/28 06:08:30	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/29 06:27:00 - 04/29 17:20:00	Track ( -220.6, 99.7) @ 04/28 06:08:30	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/29 18:20:30 - 04/30 06:18:00	Track ( -220.6, 99.7) @ 04/28 06:08:30	# HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w
04/30 07:05:00 - 04/30 09:30:00	Track ( -147.9, 199.9) @ 04/30 07:02:00	* AR 11731.
<b>PROG= 05 15-time(s)</b>		
<b>Subr= 1 45-time(s) 10.0sec</b>		
<b>Seqn= 9 1-time(s) 2.0sec</b>		
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>		
<b>Seqn= 10 1-time(s) 2.0sec</b>		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

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**Flare Detection**

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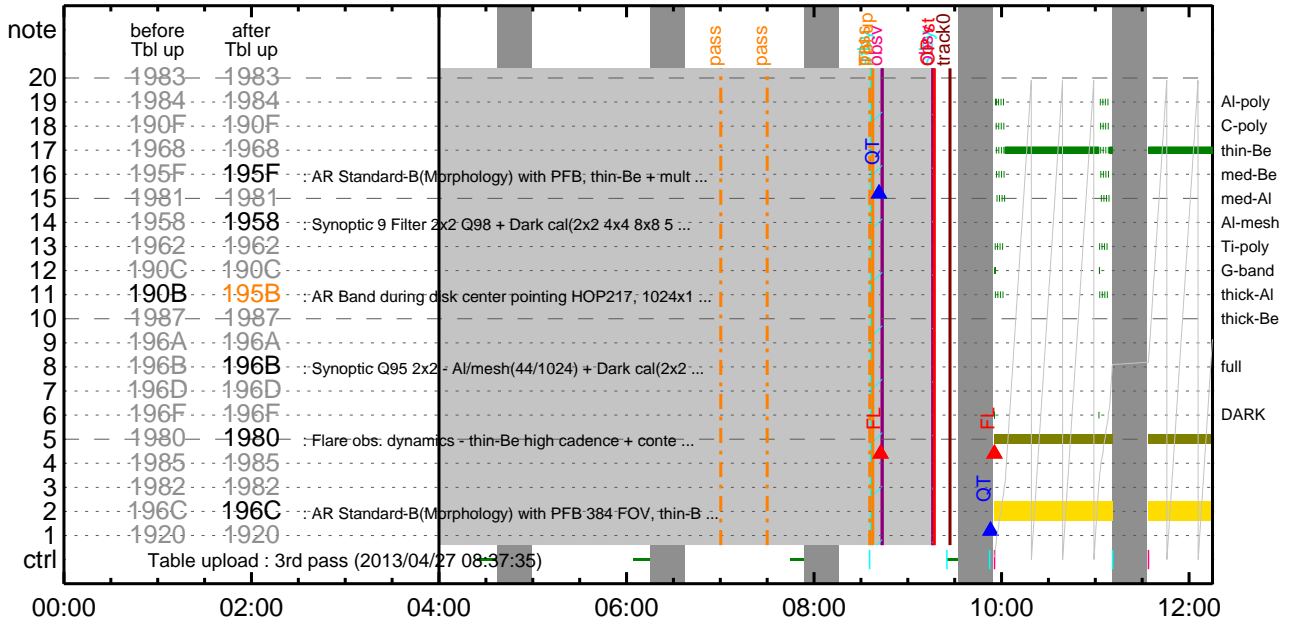
**FLD Patrol**

Term	Pointing (x, y)	Comment
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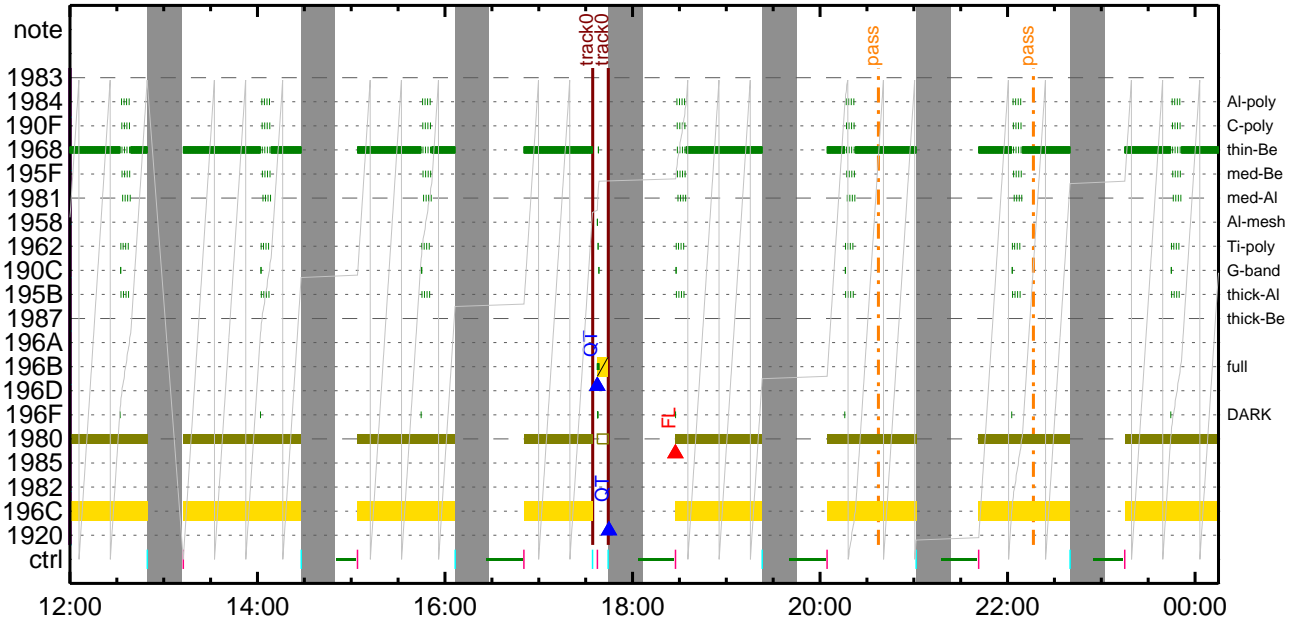
04/27 17:44:46 - 04/28 05:58:46 Fixed ( 894.0, 210.0) # Cont.  
 04/28 06:11:16 - 04/28 18:00:16 Track ( -220.6, 99.7) @ 04/28 06:08:30 # HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w  
 04/28 18:12:46 - 04/29 06:14:16 Track ( -220.6, 99.7) @ 04/28 06:08:30 # HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w  
 04/29 06:26:46 - 04/29 18:06:46 Track ( -220.6, 99.7) @ 04/28 06:08:30 # HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w  
 04/29 18:20:16 - 04/30 06:52:16 Track ( -220.6, 99.7) @ 04/28 06:08:30 # HOP 217, looking at granules and network formation with SOT, and at jets in on-disc CH w  
 04/30 07:02:16 - 05/02 10:10:00 Track ( -147.9, 199.9) @ 04/30 07:02:00 \* AR 11731.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

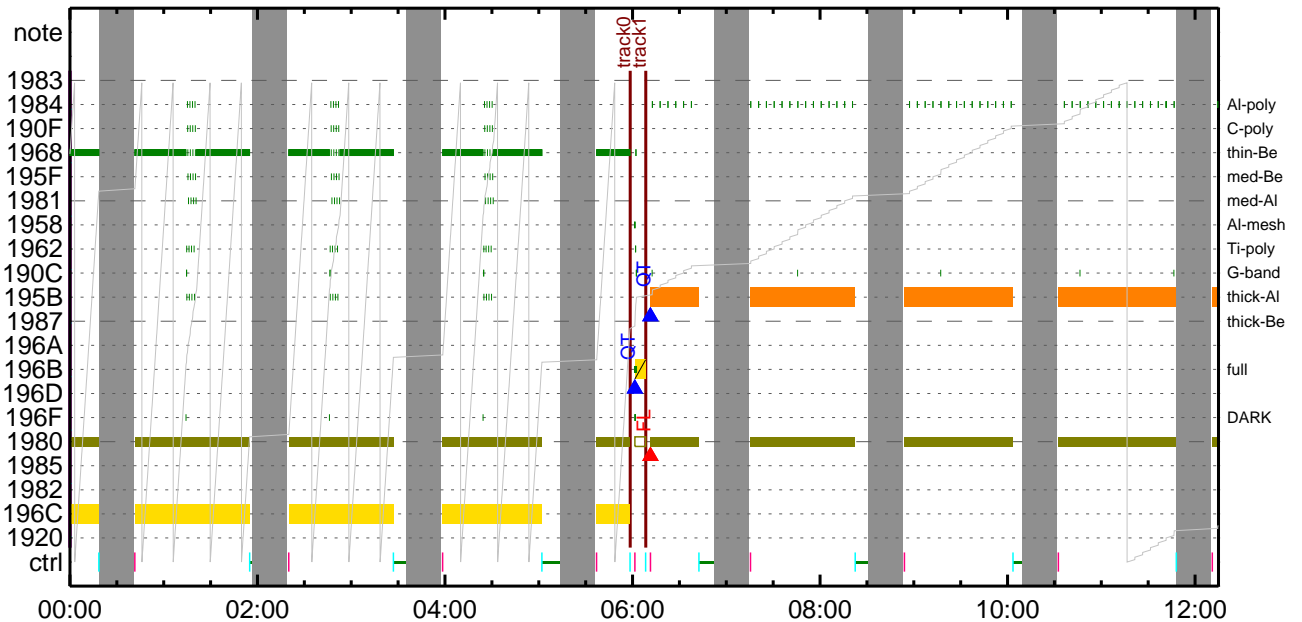
### CMDI #0424 2013/04/27



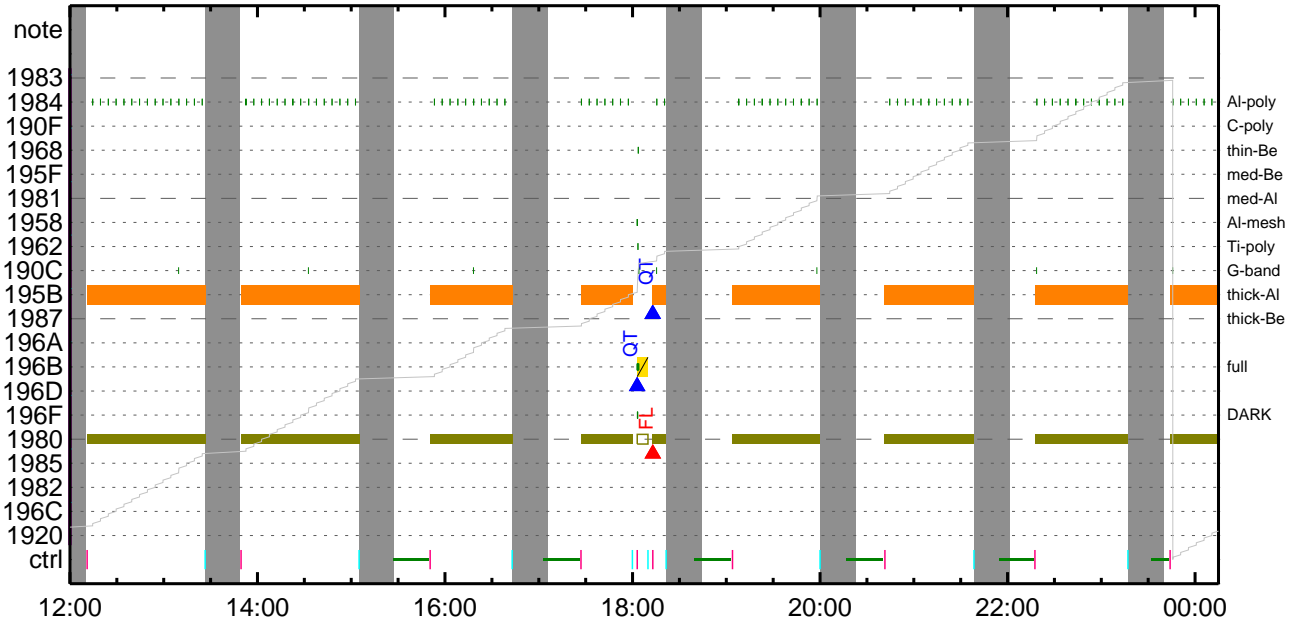
### CMDI #0424 2013/04/27



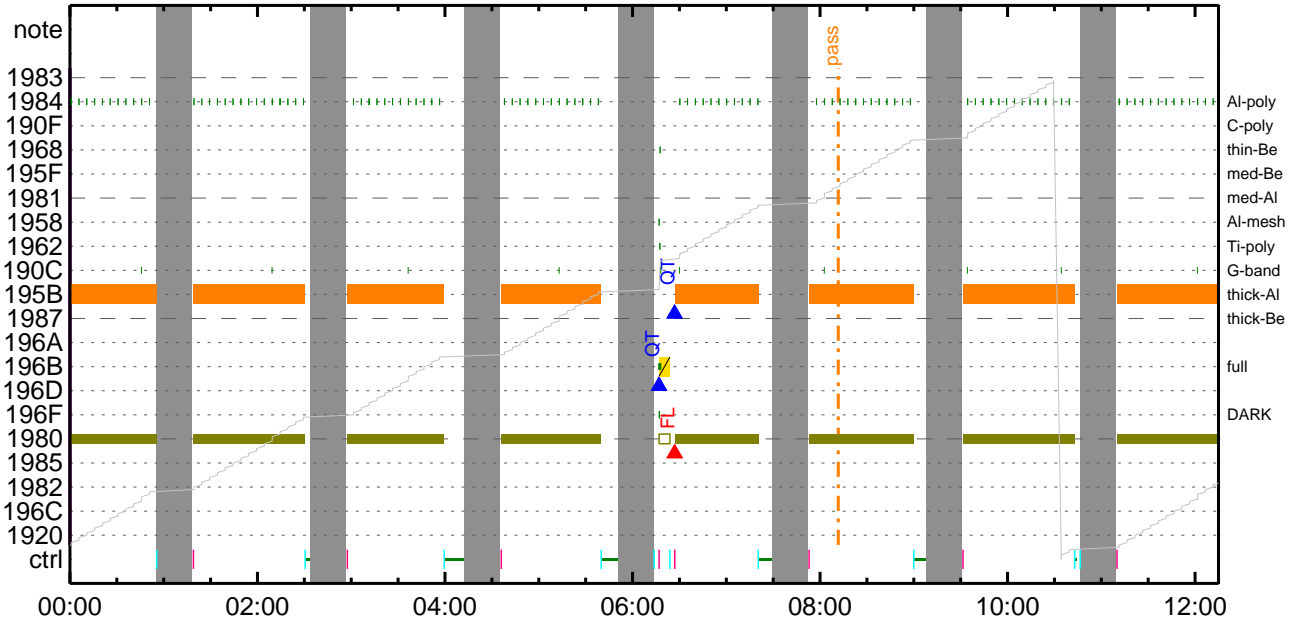
### CMDI #0424 2013/04/28



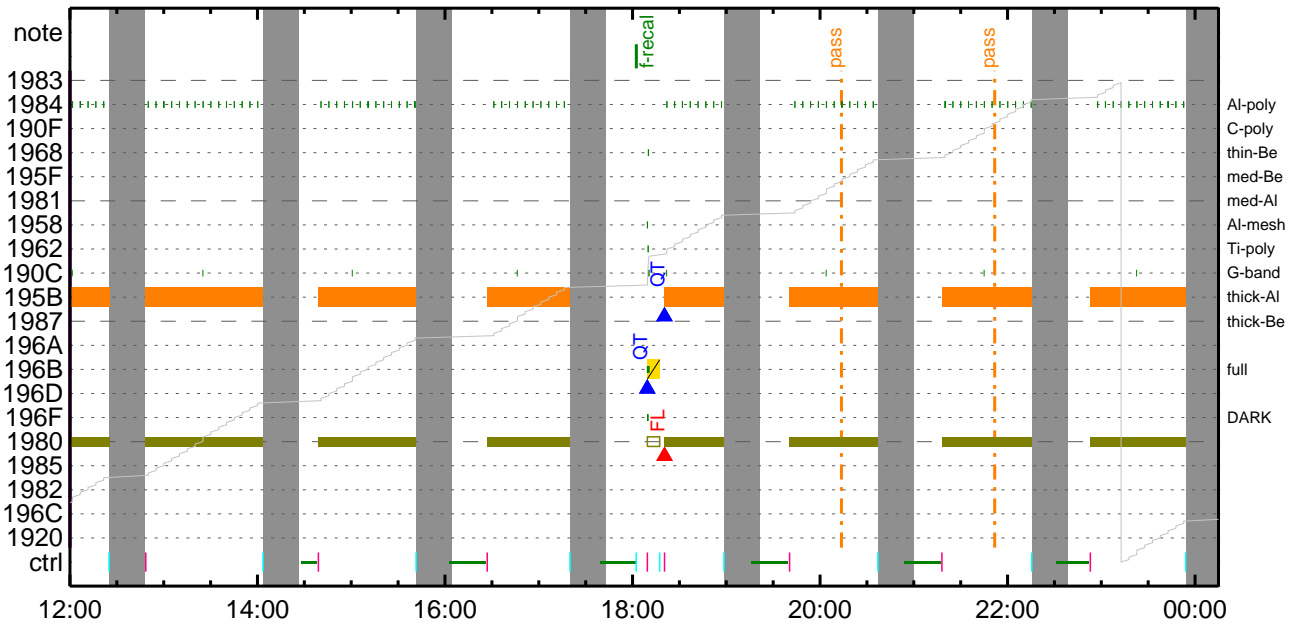
CMDI #0424 2013/04/28



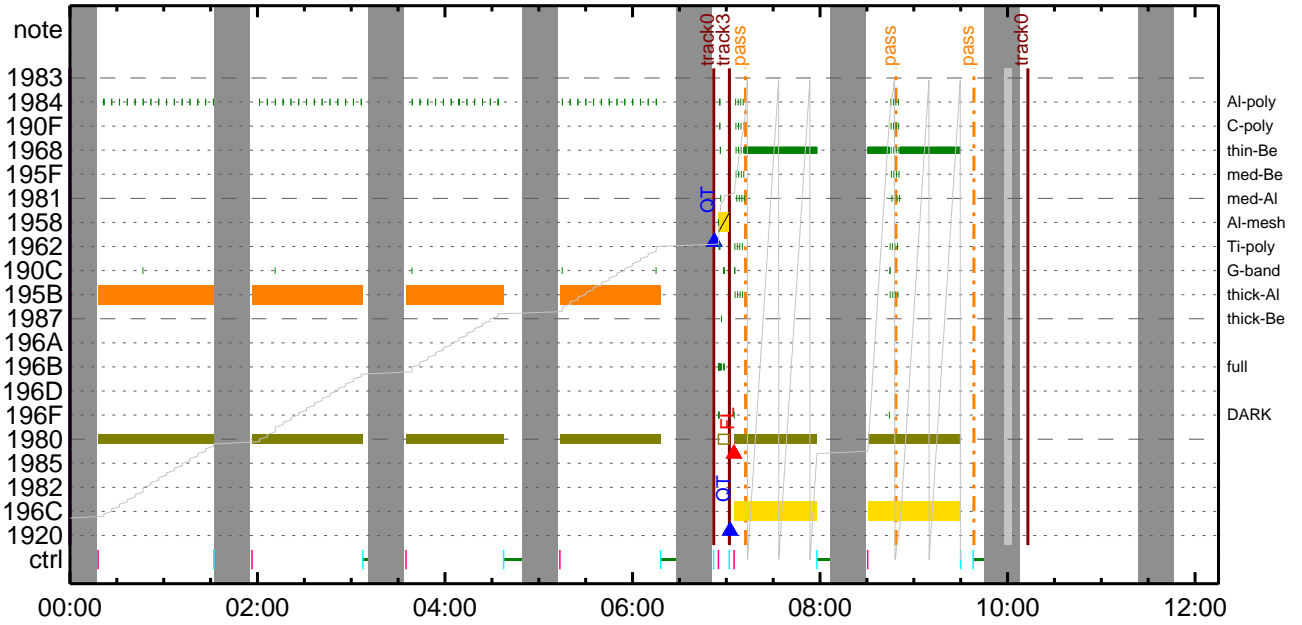
CMDI #0424 2013/04/29



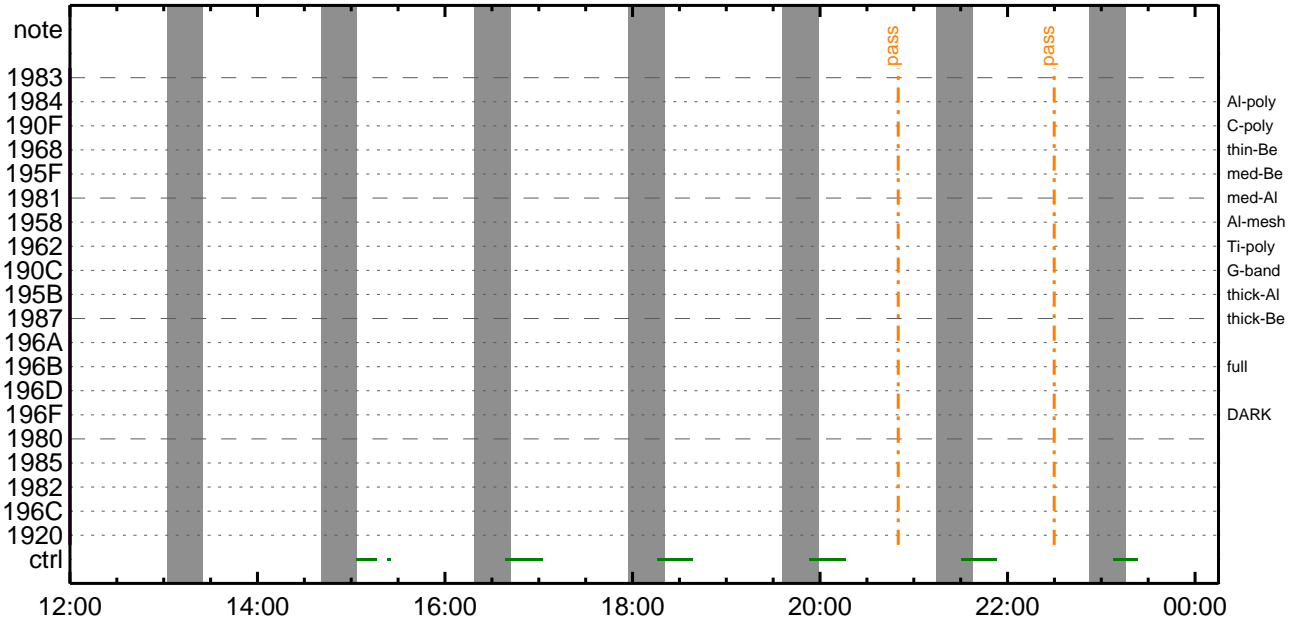
CMDI #0424 2013/04/29



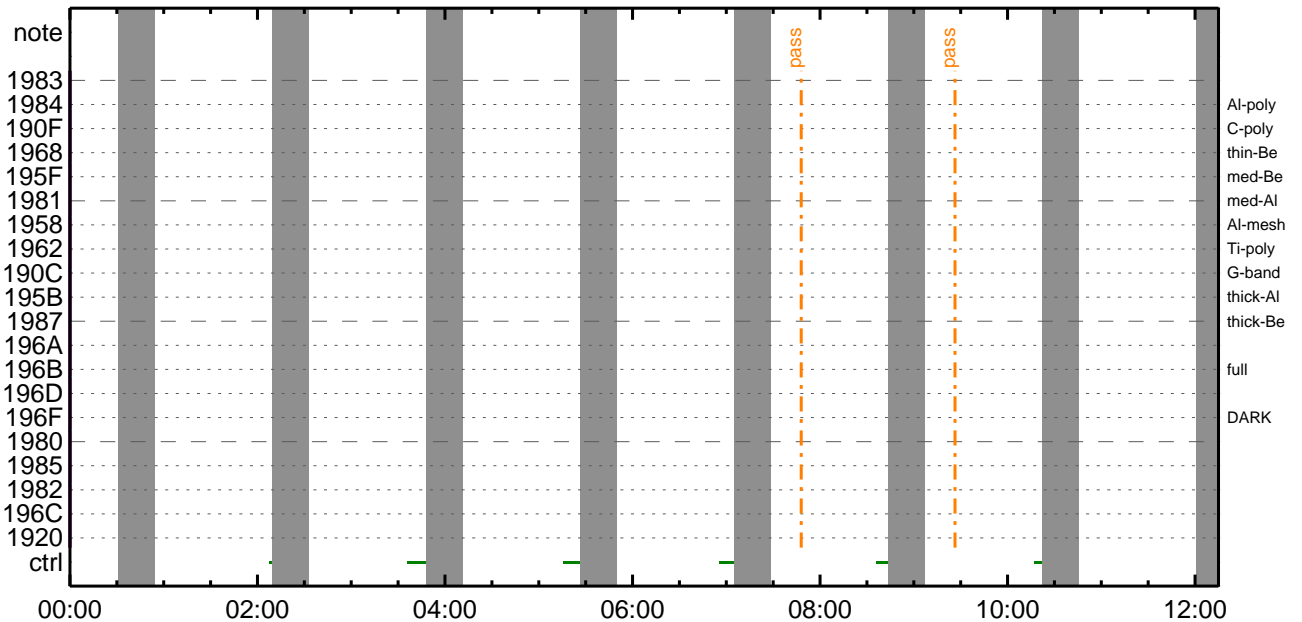
CMDI #0424 2013/04/30



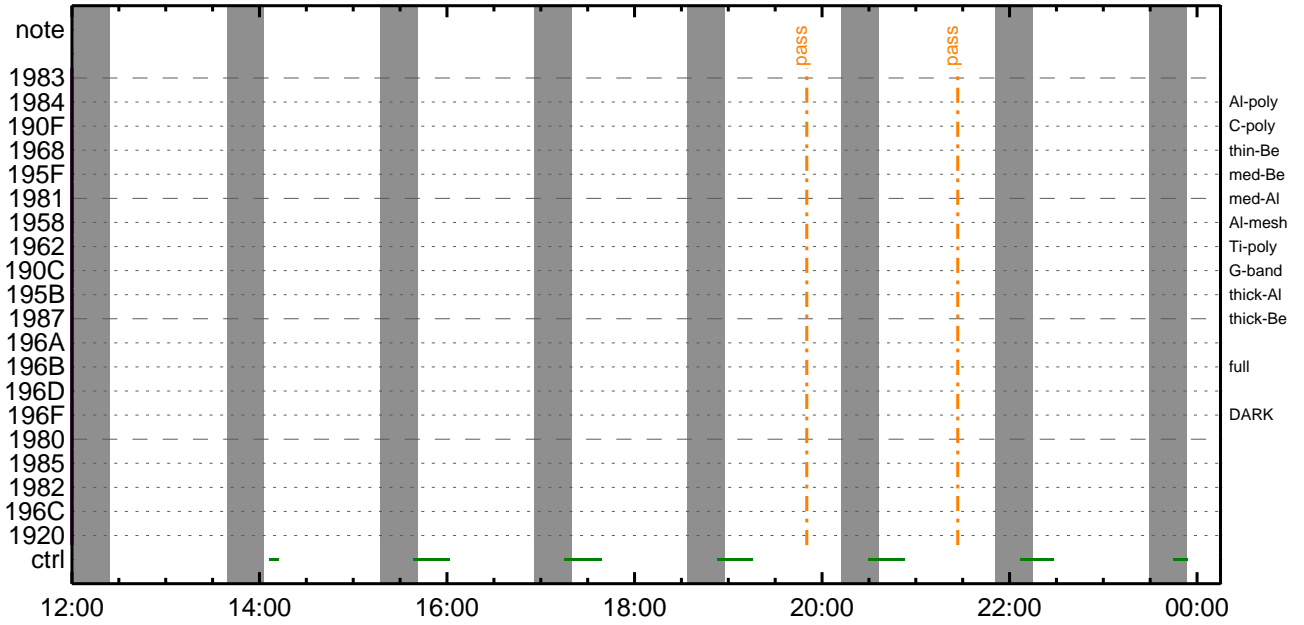
CMDI #0424 2013/04/30



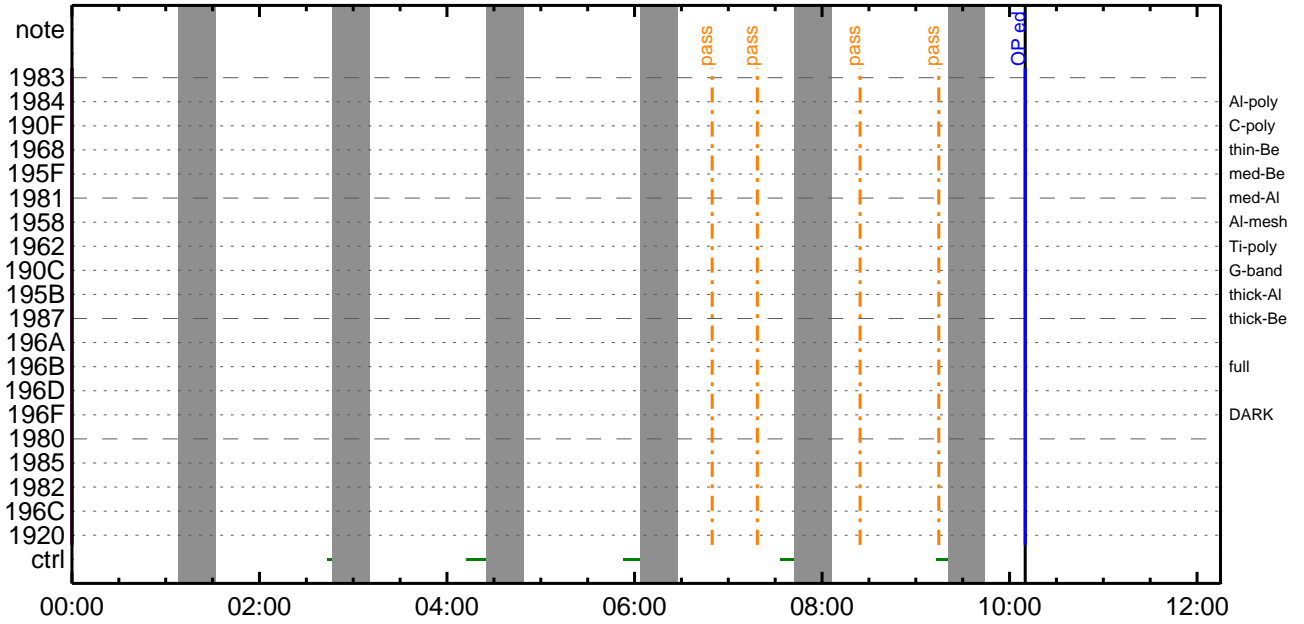
CMDI #0424 2013/05/01



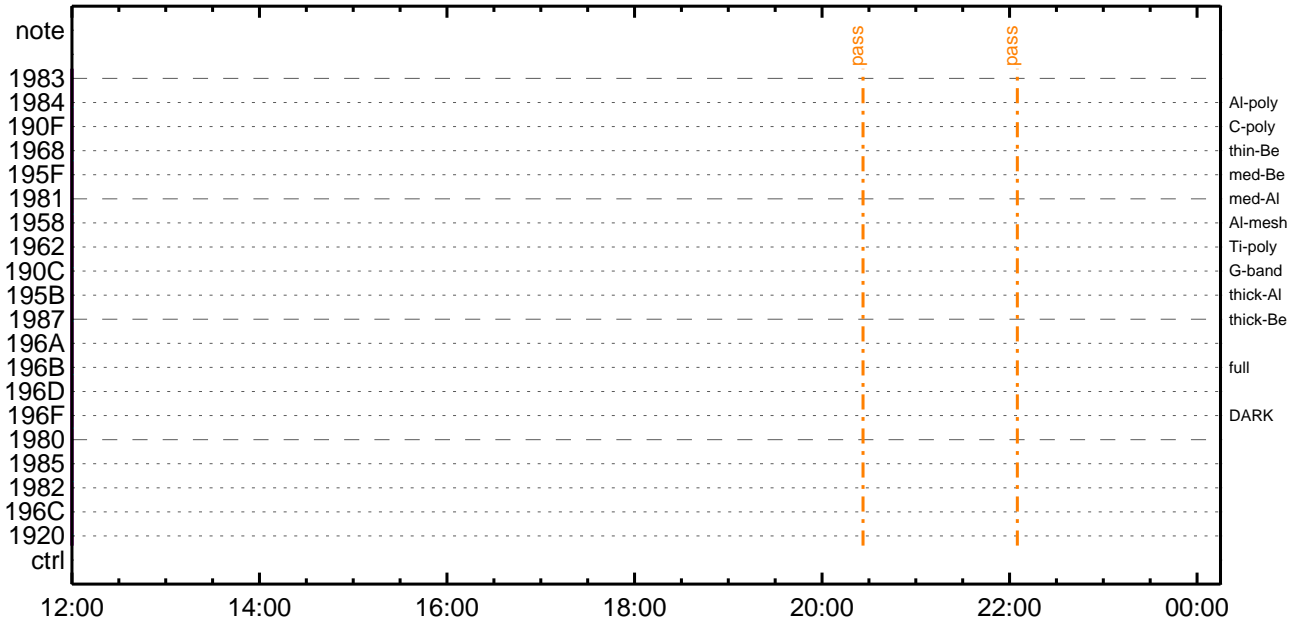
### CMDI #0424 2013/05/01



### CMDI #0424 2013/05/02



### CMDI #0424 2013/05/02







0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;4YE;ä  
0100 C. \*\*\*\*\*  
0101 C.  
0102 C. ;äOP/OGY1;4YE;ä  
0103 S. OP op-613:OP  
0104 (  
0105 S. OG og-613:OG  
0106 (  
0107 C.  
0108 C. ;äNMOG&OPfî°ëYÄYóYx;ä  
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 C. YÄYóYx½ªî»ò³îÇ§  
0125 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 C. RAM ID=NMOGñî½Ë¹ç•ë²îOKòð³îÇ§  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 C. YÄYóYx½ªî»ò³îÇ§  
0144 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 C. RAM ID=NMOGñî½Ë¹ç•ë²îOKòð³îÇ§  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 C. YÄYóYx½ªî»ò³îÇ§  
0163 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 C. RAM ID=NMOG, RAM ID=OPñî½Ë¹ç•ë²îOKòð³îÇ§  
0165 C.  
0166 C. \*\*\*\*\* òË²¼òî½Ä¹¶ÁºòËË-òºÄ÷¿® (¼âµ-YÄYóYx½ê½çòðÄÖÄæç¼ªªºðè¼i¹çòçòâ) \*\*\*\*\*  
0167 C. DHUÿâ;½YE;Ë½Y½;Yi;½YE;Ëòðîäº¹  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 C. NOTICE ;§ OPOG UPLOADº-Á÷¿@NGñî½î¹ç;çºË²¼òî½TI-CMDÁ÷¿@ñî½Ä¹Òº.ºËººººººººº;f  
0180 C. òþò¿;çSETºEDUMPAîÆ±ºiYÑY¹ºç¹Òº|º³ºË;f  
0181 C.  
0182 C. TIY³YþYóYËºòðÄî¿¿(UT)  
0183 +. TI 2013-04-27 09:12:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2013-04-27 09:12:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2013-04-27 09:12:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

```

0194 C.
0195 +. TI 2013-04-27 09:16:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄë%îîñîîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                   EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                       EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                    EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                     EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                   EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                   EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                    EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                   EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                      EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                      EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                      EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî¼È¹ç•è²îOKò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èòðîäò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                      EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-04-27 09:16:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-04-27 09:16:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-04-27 09:16:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-04-27 09:16:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄîî»ö¼ŷòÈÄð¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼ò¼Ä»Ûò¹è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñòÈ´Øò¹èDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-614 2013-04-27 12:22:46 82 33 SOLAR-B MAIN //  
0001 . C.  
0002 . C. ***** AOS *****  
0003 . C.  
0004 . C. ;ãAOSYÁY$YÁY~¼Ä»Û;ã  
0005 . C.  
0006 . C. YÀYB;¼Y³YFÏóYÉÁ+¿©  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 . C.  
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010 . C. Áí;Èá¿áÁá•µ°È»Í×ÁÇáÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈáÈ¼°ÇÖá•á¿¼í¹çáÍ;çÁ®, ùá¹áèáÈáÇÁ+¿©á•áÈááá³áÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 . C.  
0013 . C.  
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****  
0015 . C. Upload the Orbit Element and the Target Attitude  
0016 . C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 . C.  
0019 . C.  
0020 . C.  
0021 . C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 . BC (07 00 00 00 18 00)  
0024 . C.  
0025 . C. <A_ST$1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 . C.  
0027 . C.  
0028 . C. Change the TLMFormatNo for the AOCS Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 . BC (04 0b f8)  
0031 . C.  
0032 . C. Wait for AOCSDUMP to end  
0033 . C.  
0034 . C. Check the dump memory  
0035 . C.  
0036 . C. Result = OK [ ]  
0037 . C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 . BC (02 0a f8)  
0040 . C.  
0041 . C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 . C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0046 +. DC 07-FC EIS_MODE_MANU  
0047 . BC (21 02)  
0048 . C. Verify EIS in MANUAL mode  
0049 . C. Estimated OBSTBL upload time is 59s  
0050 . C. *****  
0051 . C. EIS START OBSTBL LOAD  
0052 . C. *****  
0053 . S. RAM ram-820:EIS_OBSTBL  
0054 . C.  
0055 +. DC 07-FC EIS_DUMP_OBSTBL  
0056 . BC (07 07 07 00 00 70 00)  
0057 . C.  
0058 . C. Execute, after the success of OBSTBL upload.  
0059 . C. Set EIS TI-commands  
0060 +. TI 2013-04-27 09:16:50.0  
0061 . DC 07-FC EIS_MODE_CHG_ENA  
0062 . BC (20)  
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0064 . C. *****  
0065 . C. EIS END OBSTBL LOAD  
0066 . C. *****  
0067 . C.  
0068 . C. ***** MDP `úÁÍáÍ»ó¼YáÈÁá¹áèDCBC•x²è *****  
0069 . C. (¼á°ÍYÓYÁYÉYB³YÉYáYçYÈáÈ¼áÈ¼¼Ä»Ûá¹áè)  
0070 . S. DC-BC dcbc-402:DCBC  
0071 . (MDP_known_event)  
0072 . C.  
0073 . C.  
0074 . C. ***** YBÿ!·Í Daily±;ÍÑáÈ´Øá¹áèDCBC•x²è *****  
0075 . S. DC-BC dcbc-153:DCBC  
0076 . (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0077 . C.  
0078 . C.  
0079 . C. ;ãLOSÁY$YÁY~¼Ä»Û;ã  
0080 . C.  
0081 . C. ***** LOS *****  
0082 . C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-615 2013-04-27 12:22:46 234 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Ä»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëø¿ðÀð•µ°È»Í×ÁÇøÍYÇYÁY×YÍ;¼YÉ;ËÈÈ¼µ•íÉ;ËøÈ¼°ÇÖð•ø¿¼í¹ÇøÍ; ÇÀ®, ùø¹øðøðÇÁ+¿®ð•øÈøøøøøÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. Ç" °ÅÀ, Í×ÈYðÀLOSøðÇøÍ»p´Öøð¹íÍ, ø, ; ÇÉÖÍ×øÈXÁÖONøí¹ÖøÈø¹øÈøøøøøÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYóYÉYíYÁY-¾ÖÄÖø-øÁÄêø•ø¿øé; Ç°È²¼øÍ°ÅÀ, ¼ê¼Çøð¼Å¹Öø¹øé; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÅÀ,
0033 C. *****
0034 C. Ç" RESTART; ÈPT1; Èø•ø¿øø¼í¹ÇøÍ; Ç°È²¼øÍ°Å¹Öø»ø°; ÇDCBC-150øø¿Èøà; f
0035 C.
0036 . C. ;ãPT1°ÅÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Å¹Ö, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Å¹Ö, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Å¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYóYÉYÉÄÜÄØ; ÈÁ•Á°²óÈø; È, áøÍ°ÅÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Å¹Ö, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Å¹Ö, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Å¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÅÀ, ø-¼«È°Áá»ßø•ø¿, á; Ç°È²¼øð¼Å¹Öø¹øé; f
0055 C. YÇYóYÉYÉÄÜÄØøðÁ•Á°²óÈøø-ø¼áøø¼í¹ÇøÍ´øÍ»ø¹øðøðøÇÄÖøÄ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÅÀ,
0059 C. *****
0060 C. Ç" RESTART; ÈPT2; Èø•ø¿øø¼í¹ÇøÍ; Ç°È²¼øÍ°Å¹Öø»ø°; ÇDCBC-151øø¿Èøà; f
0061 C.
0062 . C. ;ãPT2°ÅÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Å¹Ö, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Å¹Ö, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Å¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYóYÉYÉÄÜÄØ; ÈÁ•Á°²óÈø; È, áøÍ°ÅÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Å¹Ö, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Å¹Ö, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Å¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÅÀ, Áá»ß; ÇXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÅÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-282:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2013-04-27 09:16:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 C. ***** XRT START *****
0150 C.
0151 +. DC 07-F0 MDP_XRT_CTRL_MANU
0152 BC (c1)
0153 + DC 07-F0 MDP_XRT_MODE_STBY
0154 BC (c3)
0155 . C. ----- Success Verify ? OK / NG_____
0156 C.
0157 C. XRT Obs. Table Upload
0158 . S. RAM ram-291:MDP_OBS_X
0159 ( )
0160 C.
0161 +. DC 07-F0 MDP_DUMP_XRTTBL
0162 BC (84 07 00 00 00 3a d4)
0163 . C. ----- Comparison Check ? OK / ERR _____
0164 C.
0165 C.
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 01 b1 b1 04 04)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 02 b1 b1 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 03 b1 b1 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 04 b1 b1 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 05 85 83 06 06)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 06 85 83 06 06)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 07 85 83 08 08)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 08 80 80 20 20)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 09 80 80 20 08)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 0a 80 80 08 20)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 0b 80 80 10 10)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 0f 80 80 06 06)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 10 80 80 08 08)
0192 + DC 07-F0 MDP_XRT_FLD_ENA
0193 BC (d8)

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0194 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0195 BC (c8)
0196 + DC 07-F0 MDP_XRT_AEC_RESET
0197 BC (d0)
0198 + DC 07-F0 MDP_XRT_ARS_DIS
0199 BC (d5)
0200 + DC 07-F0 MDP_XRT_FLD_RESET
0201 BC (da)
0202 + DC 07-F0 MDP_XRT_QT_PROG_SET
0203 BC (c4 10)
0204 + DC 07-F0 MDP_XRT_FL_PROG_SET
0205 BC (c5 05)
0206 . C. ----- Success Verify ? OK / NG ____
0207 C.
0208 C.
0209 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0210 C.
0211 + DC 07-F0 MDP_XRT_MODE_OBSV
0212 BC (c2)
0213 + TI 2013-04-27 09:16:02.0
0214 DC 07-F0 MDP_XRT_MODE_OBSV
0215 BC (c2)
0216 . C. ----- Success Verify ? OK / NG ____
0217 C.
0218 C. ***** XRT END *****
0219 C.
0220 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0221 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÁÿçÿèñ¼ññ¼Ã»Ûñ¹ñè)
0222 . S. DC-BC dcbc-402:DCBC
0223 (MDP_known_event)
0224 C.
0225 C.
0226 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎñÈ´Øñ¹ñèDCBC•x²è *****
0227 . S. DC-BC dcbc-153:DCBC
0228 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0229 C.
0230 C.
0231 . C. ;ãLOSÿÃÿ§ÿÄÿÿ-¼Ã»Û;ã
0232 C.
0233 . C. ***** LOS *****
0234 C.
```

Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 1/8

\*\*\* OP Sequence for XRT \*\*\*

2013/04/27	09:25:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	09:25:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/27	09:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/27	09:27:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 ed 59 b0 8c		
2013/04/27	09:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/27	09:52:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	09:52:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/04/27	09:52:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/04/27	09:52:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/04/27	09:52:50.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/04/27	09:52:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/04/27	09:52:54.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/27	09:52:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02		
2013/04/27	09:55:28.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05		
2013/04/27	09:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/27	11:11:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	11:11:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/27	11:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/27	11:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/27	11:33:00.0	XRT_Custom_430_OG [0x1ae]					
2013/04/27	11:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/27	12:49:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	12:49:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/27	12:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/27	12:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/27	13:11:30.0	XRT_Custom_430_OG [0x1ae]					
2013/04/27	13:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/27	14:28:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	14:28:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/27	14:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/27	14:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/27	15:03:00.0	XRT_Custom_430_OG [0x1ae]					
2013/04/27	15:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/27	16:06:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	16:06:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/04/27	16:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/04/27	16:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/04/27	16:49:30.0	XRT_Custom_430_OG [0x1ae]					
2013/04/27	16:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/27	17:34:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/04/27	17:34:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/04/27	17:34:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/04/27	17:34:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/04/27	17:37:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/04/27	17:37:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/04/27	17:37:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2013/04/27	17:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/04/27	17:44:24.0	XRT_CTRL_MANU_402_OG [0x192]					

Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 2/8

2013/04/27	17:44:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2013/04/27	17:44:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 ed 59 b0 8c
2013/04/27	17:44:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/27	17:44:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/27	17:44:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/27	17:44:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/27	17:44:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	17:44:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/04/27	18:27:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/27	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/27	19:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/27	19:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	19:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/27	19:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/27	20:03:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/27	20:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/27	21:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/27	21:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	21:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/27	21:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/27	21:40:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/27	21:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/27	22:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/27	22:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/27	22:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/27	22:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/27	23:14:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/27	23:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/28	00:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/28	00:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/28	00:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/28	00:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/28	00:40:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/28	00:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/28	01:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/28	01:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/28	01:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/28	01:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/28	02:19:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/28	02:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/28	03:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/28	03:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/28	03:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/28	03:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/28	03:57:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/28	03:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/28	05:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/28	05:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/28	05:02:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8



Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 3/8

2013/04/28	05:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/28	05:36:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/28	05:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	05:58:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/04/28	05:58:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/04/28	05:58:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/04/28	06:01:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/04/28	06:01:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/28	06:01:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2013/04/28	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	06:08:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	06:08:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/04/28	06:10:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/04/28	06:11:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/04/28	06:11:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/04/28	06:11:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/04/28	06:11:22.0	XRT_ARS_DIS_425_OG [0x1a9]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/04/28	06:11:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/28	06:11:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2013/04/28	06:11:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05				
2013/04/28	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	06:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	06:42:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/28	06:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/28	06:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/28	07:14:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/28	07:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	08:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	08:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/28	08:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/28	08:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/28	08:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/28	08:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	10:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	10:03:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/28	10:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/28	10:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/28	10:31:30.0	XRT_Custom_430_OG [0x1ae]							
2013/04/28	10:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	11:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	11:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/28	11:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/04/28	11:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/04/28	12:10:00.0	XRT_Custom_430_OG [0x1ae]							
2013/04/28	12:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/04/28	13:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/04/28	13:26:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/04/28	13:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 4/8

2013/04/28	13:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/28	13:48:30.0	XRT_Custom_430_OG [0x1ae]						
2013/04/28	13:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	15:05:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	15:05:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/28	15:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/28	15:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/28	15:49:30.0	XRT_Custom_430_OG [0x1ae]						
2013/04/28	15:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	16:43:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	16:43:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/28	16:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/28	16:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/28	17:26:00.0	XRT_Custom_430_OG [0x1ae]						
2013/04/28	17:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/04/28	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/04/28	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/04/28	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/04/28	18:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2013/04/28	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	18:12:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/04/28	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/04/28	18:12:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/04/28	18:12:50.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/04/28	18:12:52.0	XRT_ARS_DIS_425_OG [0x1a9]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/04/28	18:12:54.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/28	18:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2013/04/28	18:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05			
2013/04/28	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	18:21:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	18:21:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/28	18:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/28	18:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/28	19:03:00.0	XRT_Custom_430_OG [0x1ae]						
2013/04/28	19:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	20:00:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	20:00:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/28	20:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/28	20:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/28	20:40:30.0	XRT_Custom_430_OG [0x1ae]						
2013/04/28	20:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/04/28	21:38:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/04/28	21:38:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/04/28	21:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/04/28	21:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/04/28	22:16:30.5	XRT_Custom_430_OG [0x1ae]						
2013/04/28	22:17:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]						

Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 5/8

2013/04/28	23:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/28	23:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/28	23:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/28	23:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/28	23:43:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/28	23:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	00:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	00:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	00:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	00:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	01:18:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/29	01:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	02:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	02:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	02:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	02:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	02:56:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/29	02:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	03:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	03:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	03:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	04:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	04:35:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/29	04:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	05:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	05:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	05:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	05:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	06:13:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	06:13:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/29	06:14:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/29	06:16:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/29	06:16:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/29	06:16:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/04/29	06:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	06:23:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	06:26:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/29	06:26:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/29	06:26:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/29	06:26:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/29	06:26:52.0	XRT_ARS_DIS_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/29	06:26:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	06:26:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/04/29	06:26:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/29	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	07:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	07:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	07:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

2013/04/29	07:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	07:52:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/29	07:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	09:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	09:00:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	09:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	09:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	09:30:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/29	09:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	10:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	10:43:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	10:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	10:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	10:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	10:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	10:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	10:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	11:09:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/29	11:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	12:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	12:25:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	12:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	12:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	12:47:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/29	12:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	14:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	14:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	14:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	14:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	14:38:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/29	14:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	15:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	15:41:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	15:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	15:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	16:26:00.0	XRT_Custom_430_OG [0x1ae]			
2013/04/29	16:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	17:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	17:20:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	17:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	17:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	18:02:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	18:02:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2013/04/29	18:06:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/29	18:06:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/29	18:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/29	18:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/29	18:09:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/04/29	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	18:17:24.0	XRT_CTRL_MANU_428_OG [0x1ac]			

Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 7/8

2013/04/29	18:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2013/04/29	18:20:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/29	18:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/29	18:20:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/29	18:20:22.0	XRT_ARS_DIS_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/29	18:20:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	18:20:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/04/29	18:20:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/29	18:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	18:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	18:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	18:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	19:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	19:39:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/29	19:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	20:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	20:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	20:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	20:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	21:17:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/29	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	22:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	22:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	22:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	22:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/29	22:52:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/29	22:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/29	23:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/29	23:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/29	23:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/29	23:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/30	00:17:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/30	00:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	01:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	01:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	01:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/30	01:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/30	01:55:30.0	XRT_Custom_430_OG [0x1ae]				
2013/04/30	01:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	03:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	03:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	03:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/30	03:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/30	03:34:00.0	XRT_Custom_430_OG [0x1ae]				
2013/04/30	03:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	04:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	04:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	04:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/30	04:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

Apr 27, 13 12:23

## XRT\_OGLIST\_0424.chk

Page 8/8

2013/04/30	05:12:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/30	05:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	06:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	06:18:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	06:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/30	06:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/30	06:51:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	06:51:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/04/30	06:52:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/04/30	06:52:16.0	XRT_FLD_DIS_404_OG [0x194]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/04/30	06:52:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/04/30	06:52:20.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/30	06:52:22.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2013/04/30	06:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	07:01:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	07:01:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/04/30	07:02:00.0	AOCS_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/04/30	07:02:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/04/30	07:02:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/04/30	07:02:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/04/30	07:02:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/04/30	07:02:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	07:02:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/04/30	07:04:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 05
2013/04/30	07:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	07:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	07:58:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	07:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/30	08:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/30	08:29:30.0	XRT_Custom_430_OG [0x1ae]			
2013/04/30	08:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/04/30	09:30:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	09:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/04/30	09:38:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/04/30	09:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/04/30	09:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/04/30	10:13:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00