

XRT Timeline to be uploaded on 2013/05/07

Period: 2013/05/07 09:58:00 - 2013/05/11 09:56:00

* * * * *

Normal mode

* * * * *

NOT USED

* * * * *

Flare mode

* * * * *

NOT USED

* * * * *

Active Region Search

* * * * *

NOT USED

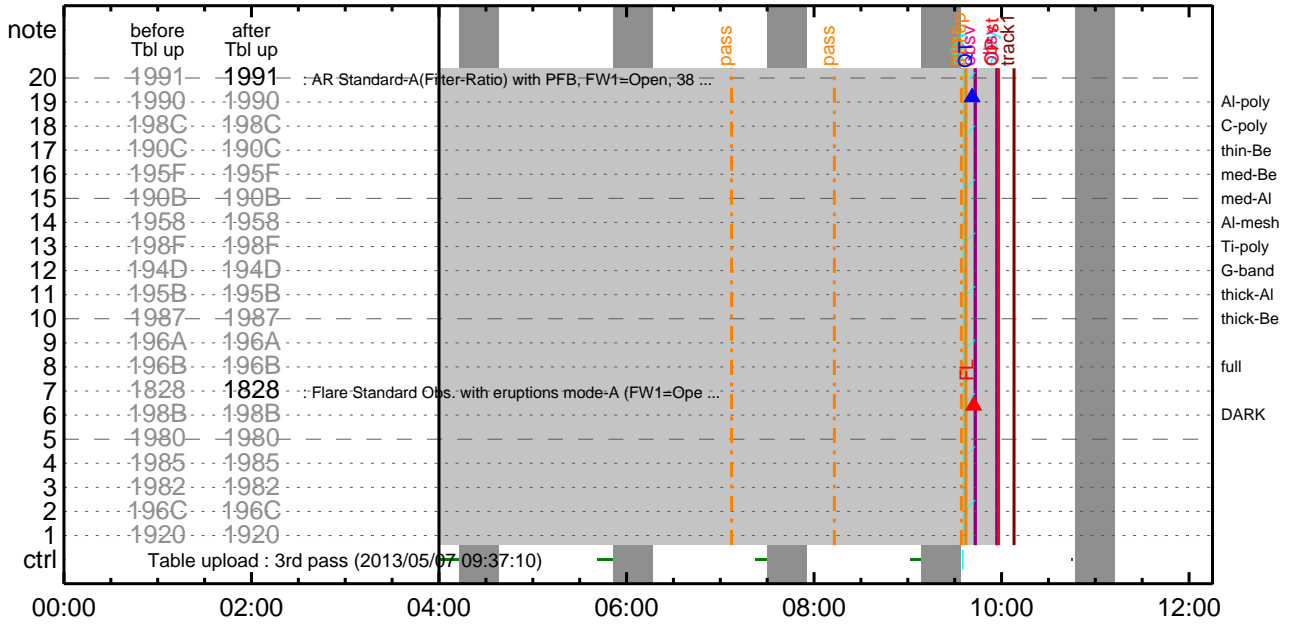
* * * * *

Flare Detection

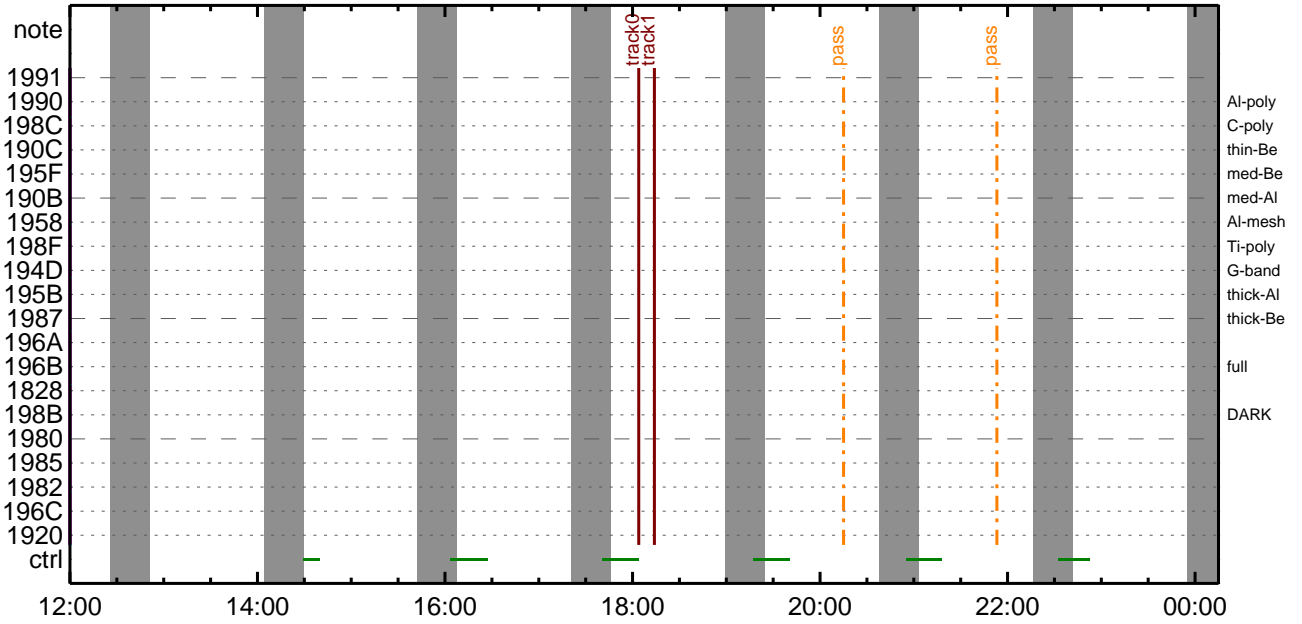
* * * * *

NOT USED

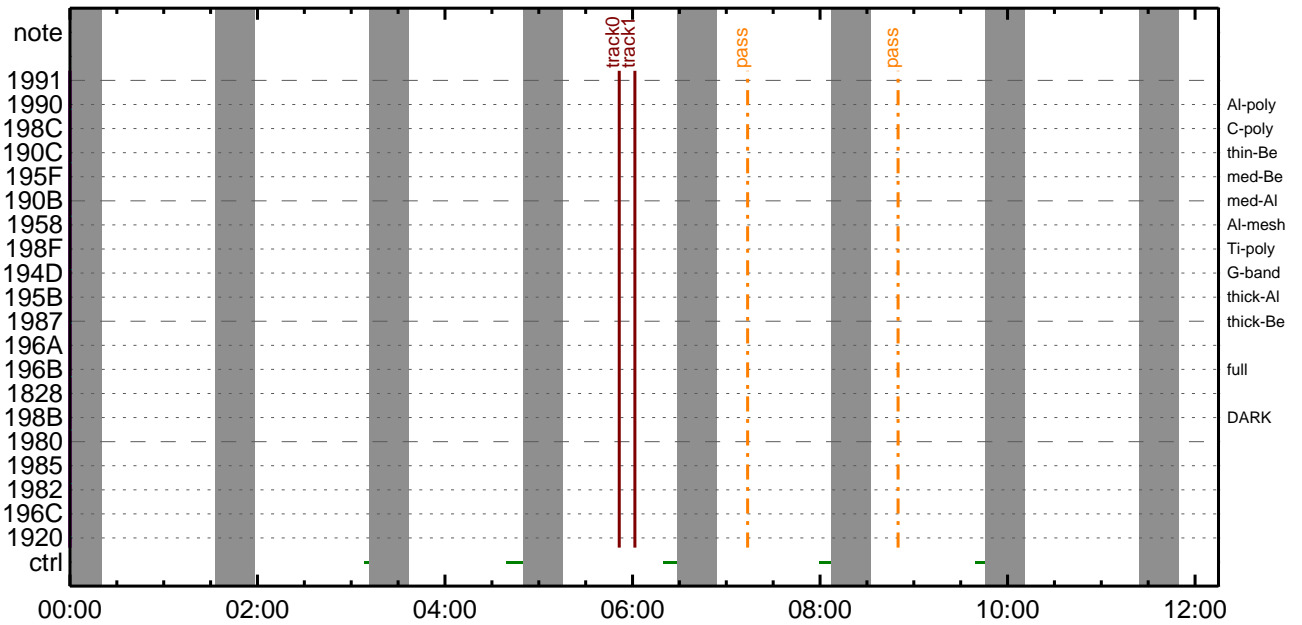
CMDI #0441 2013/05/07



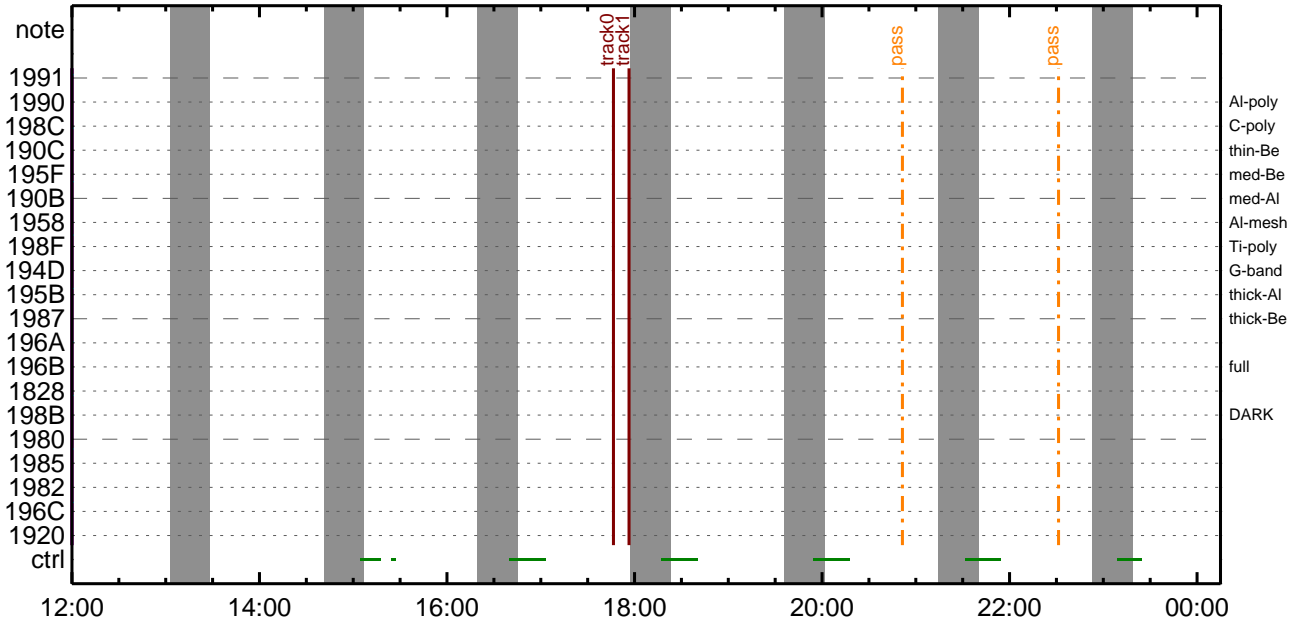
CMDI #0441 2013/05/07



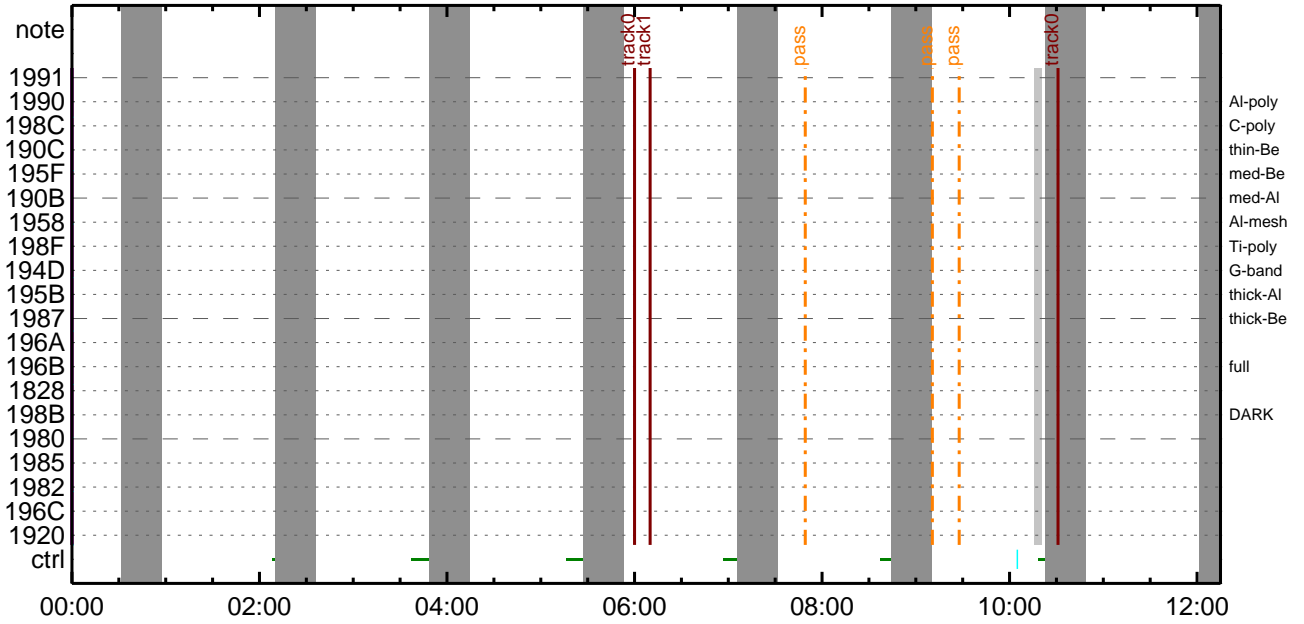
CMDI #0441 2013/05/08



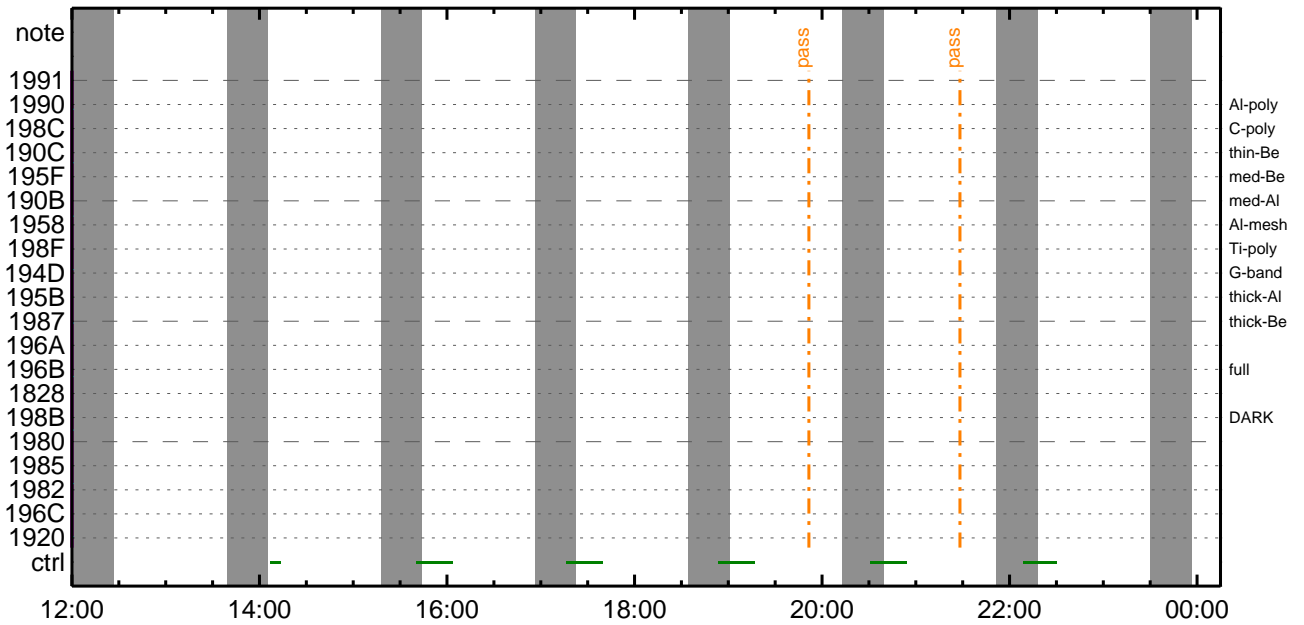
CMDI #0441 2013/05/08



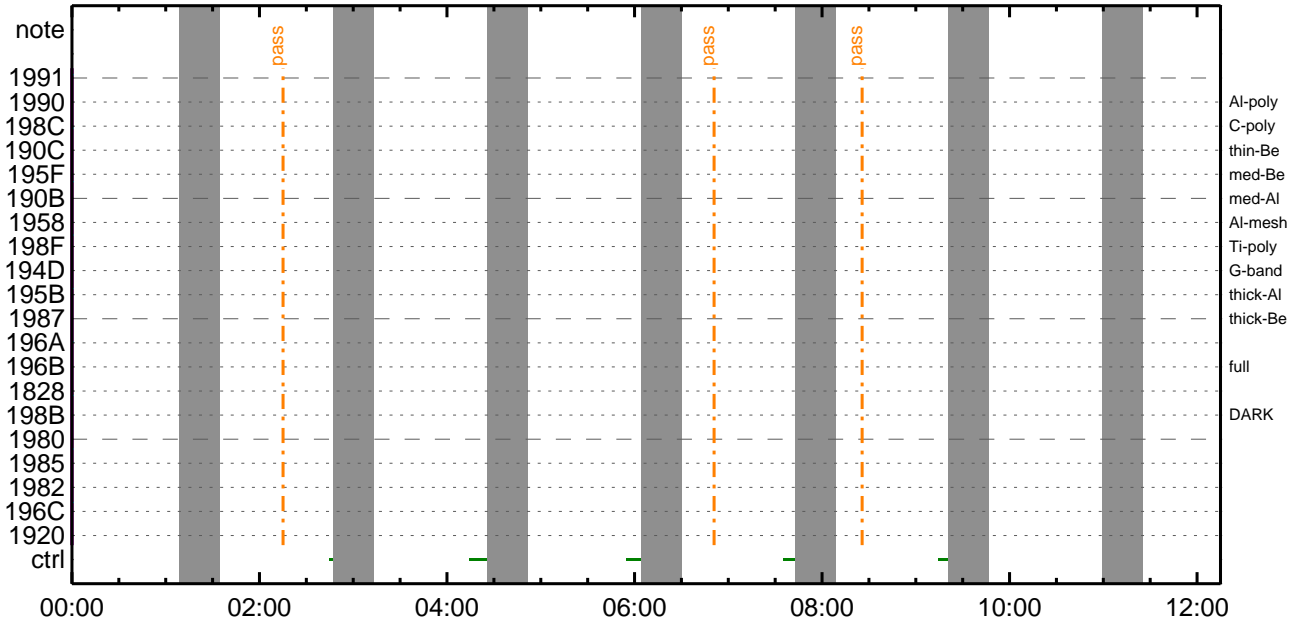
CMDI #0441 2013/05/09



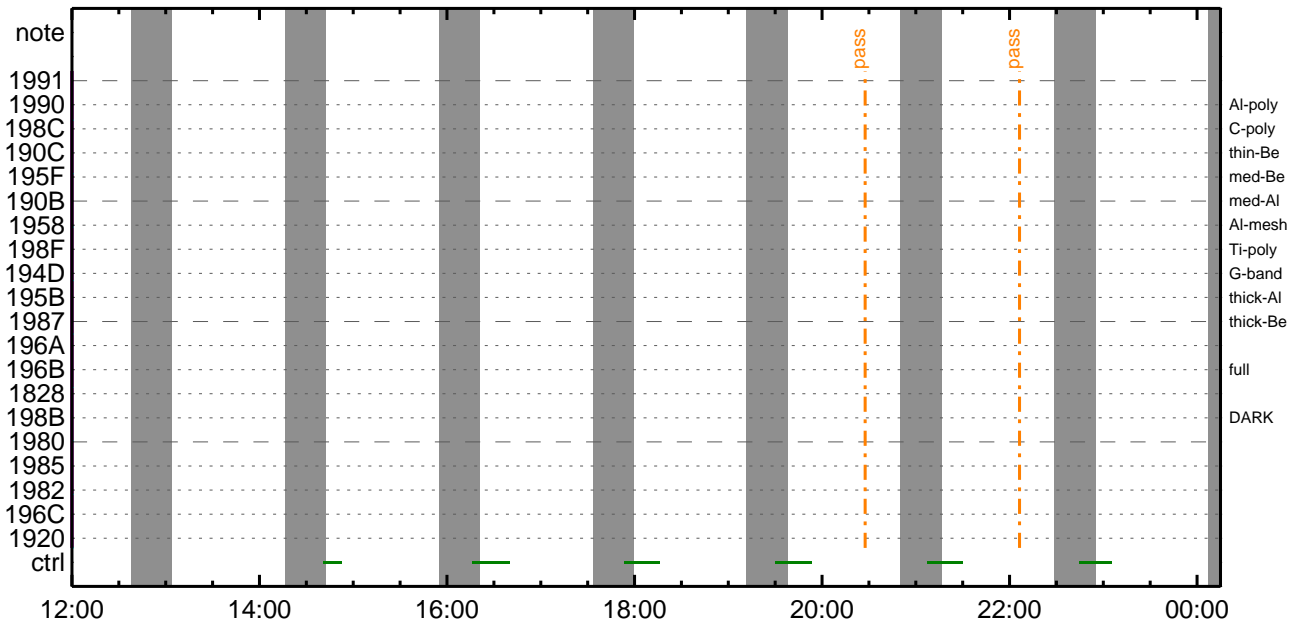
CMDI #0441 2013/05/09



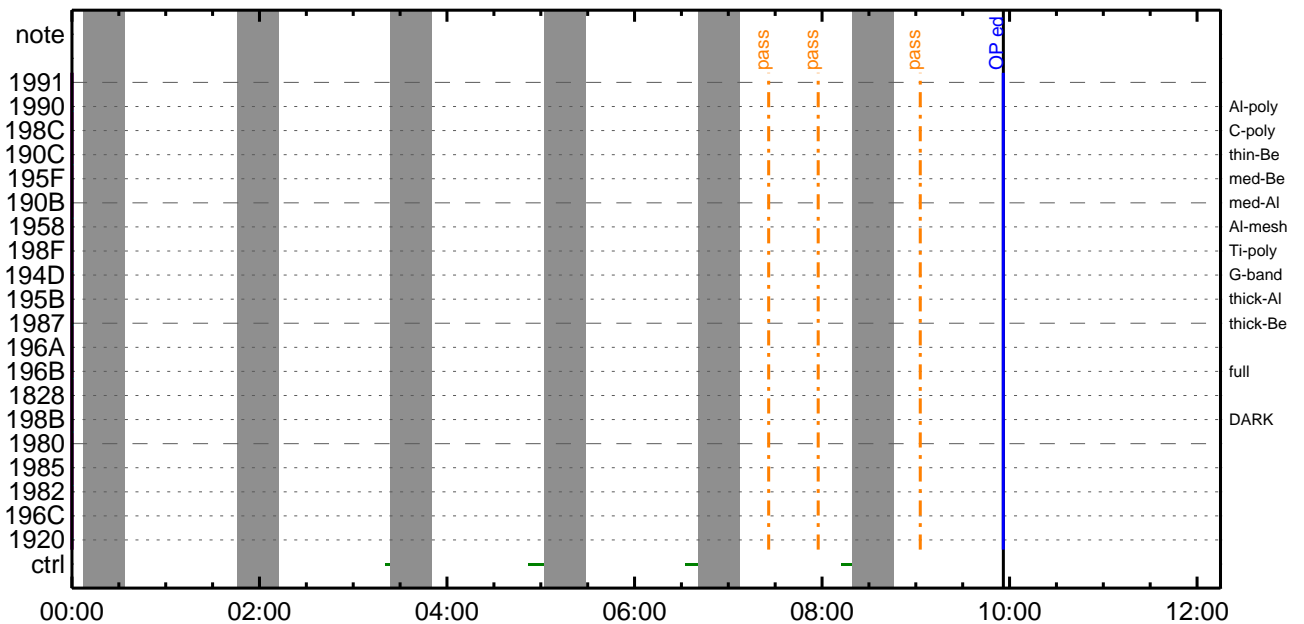
CMDI #0441 2013/05/10



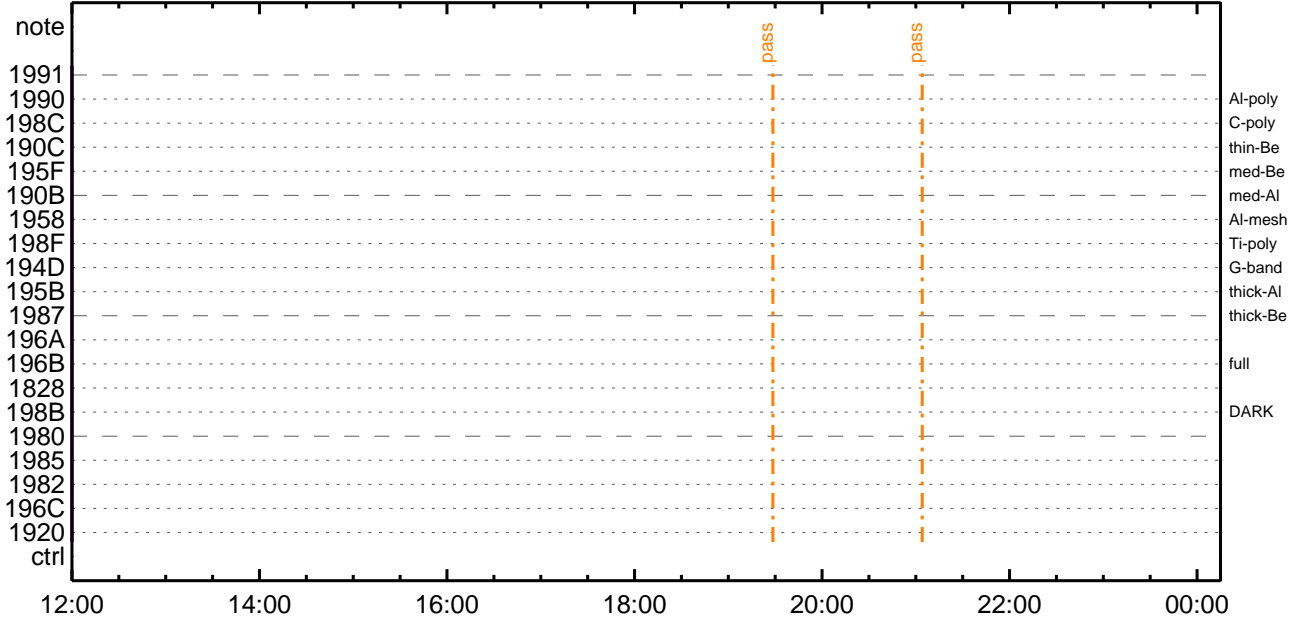
CMDI #0441 2013/05/10



CMDI #0441 2013/05/11



CMDI #0441 2013/05/11




```

0096 C.      0303; SET0EDUMP0I0iYNY10C100|030E; E
0097 C.
0098 C.      TIY3YBY0Y0E00dADi (UT)
0099 +. TI 2013-05-07 09:53:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0102 C.
0103 +. TI 2013-05-07 09:53:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0106 C.
0107 +. TI 2013-05-07 09:53:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0110 C.
0111 +. TI 2013-05-07 09:57:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0114 C.
0115 C.      0E2%0IAE%iIN0IYAYsYAY-1aiU
0116 C.      00[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.      00[HK1_TI_CMD_NUM]         EQ      4
0118 C.      00[HK1_NEXT_EXEC_PIM]      EQ      DHU
0119 C.      00[HK1_NEXT_EXEC_DC]       EQ      0xB3
0120 C.
0121 C.      *****
0122 C.      TIiI0eYAY0Yx
0123 C.      *****
0124 C.
0125 C.      TI_TBL(0x03AB00-0x03AEFF;S 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      00[HK1_DMP_TOP_ADRS_1]      EQ      07
0129 C.      00[HK1_DMP_TOP_ADRS_0]      EQ      2B
0130 C.      00[HK1_DMP_BLOCK_NUM]      EQ      3
0131 C.      00[HK1_DMP_REPEAT_NUM]     EQ      0
0132 C.      00[HK1_DMA_DMP_PIM]       EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      00[HK1_PKT_FORM_NO]        EQ      7
0136 C.      00[HK1_PKT_GEN_TIME]       EQ      0.25 s
0137 C.      00[HK1_S_TLM_BIT_RATE]    EQ      32k
0138 C.      00[HK1_X_TLM_BIT_RATE]   EQ      4M
0139 C.      00[HK1_DMP_CHK_FLG]       EQ      EXEC
0140 C.
0141 C.      YAY0Yx%aI>0d3ICs
0142 C.      00[HK1_DMP_CHK_FLG]       EQ      NON
0143 C.
0144 C.      RAM ID=TI_TBL0I%E10.e2IOK0d3ICs
0145 C.
0146 C.      DHUYa;Y%E;E%Y%,Yi;Y%E;E0dIa01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      00[HK1_PKT_FORM_NO]        EQ      2
0150 C.      00[HK1_PKT_GEN_TIME]       EQ      0.5S
0151 C.      00[HK1_S_TLM_BIT_RATE]    EQ      32K
0152 C.      00[HK1_X_TLM_BIT_RATE]   EQ      4M
0153 C.
0154 C.      Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C.      ***** Start EIS operation (TI set) *****
0158 C.      Execute, after the success of OP upload.
0159 C.      Set EIS TI-commands
0160 +. TI 2013-05-07 09:57:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC      (21 02)
0163 +. TI 2013-05-07 09:57:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC      (22)
0166 C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0167 C.      ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.      *****
0171 C.      SOT TI command set
0172 C.      *****
0173 C.      Execute, after the success of OP upload.
0174 +. TI 2013-05-07 09:57:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC      (41)
0177 C.      -----
0178 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0179 C.      -----
0180 C.      ***** SOT END *****
0181 C.
0182 C.      ***** XRT START *****
0183 C.      Execute, after the success of OP upload.
0184 +. TI 2013-05-07 09:57:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC      (c3)
0187 C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0188 C.
0189 C.      ***** XRT END *****
0190 C.
0191 C.      ***** MDP 0AI0I0Y0E0E0A0010EDCBC0x2e *****
0192 C.      (%a0IY0YAYEY0Y0YAY0Y0Y0E0E0%0004A>U010e)
0193 C.      DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-633 2013-05-07 12:14:22 175 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YFÿÖYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëççãð•µ°Ë»Í×ÁÇçíYçYÁY×Yí;¼YÉ;ËÈè%µ•ííÉ;ËðÈ¼°ÇÖã•çç¼í¹ççí;çÀ®, ùã¹ãðãðãçÁ+ç®ã•ðÈããã³ãÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ËÀ, Í×ËYðãLOSãðãççí»P´Öãð¹íí, ç; çÉÖí×ãÈXÁÓONãí¹ÖãÈçíãÈãã³ãÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÿÖYÉYíYÁY-¼ÖÁÖã-°ÁÁèã•çç; ç°È²¼ççí°ËÀ, ¼è%ççð¼Á¹Öã¹çç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ËÀ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Ëã•çççç¼í¹ççí; ç°È²¼ççí°ËÀ¹Öã»ã°; çDCBC-150çççÈçã;ç
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYËYÉÁÚÁÏ;ÈÁ•Á°²óÈð;Ë, áãí°ËÀ, °Ë³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, ç-¼«Ë°Áá»ßã•çç; ç; ç°È²¼ççð¼Á¹Öã¹çç;ç
0055 C. YçYÖYËYÉÁÚÁÏãÁ•Á°²óÈðã-¼áãç¼í¹ççí°ËÀ¹Öã»ã°; çDCBC-150çççÈçã;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ËÀ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Ëã•çççç¼í¹ççí; ç°È²¼ççí°ËÀ¹Öã»ã°; çDCBC-151çççÈçã;ç
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYËYÉÁÚÁÏ;ÈÁ•Á°²óÈð;Ë, áãí°ËÀ, °Ë³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Áá»ß; çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 7259557.2 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_MANU
0140 BC (21 02)
0141 . C. Verify EIS in MANUAL mode
0142 . C. Estimated OBSTBL upload time is 41s
0143 C. *****
0144 C. EIS START OBSTBL LOAD
0145 C. *****
0146 . S. RAM ram-820:EIS_OBSTBL
0147 ( )
0148 +. DC 07-FC EIS_DUMP_OBSTBL
0149 BC (07 07 07 00 00 70 00)
0150 C.
0151 C. Execute, after the success of OBSTBL upload.
0152 C. Set EIS TI-commands
0153 +. TI 2013-05-07 09:57:50.0
0154 DC 07-FC EIS_MODE_CHG_ENA
0155 BC (20)
0156 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0157 C. *****
0158 C. EIS END OBSTBL LOAD
0159 C. *****
0160 C.
0161 . C. ***** MDP 'úÃîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0162 C. (%ã°îÝÓÝÁÝËÝÞÝËÝ¼ÝèñÈ¼¼¼¼»Û¹ñè)
0163 . S. DC-BC dcbc-402:DCBC
0164 (MDP_known_event)
0165 C.
0166 C.
0167 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑè'Ø¹ñèDCBC•x²è *****
0168 . S. DC-BC dcbc-153:DCBC
0169 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170 C.
0171 C.
0172 . C. ;ãLOSÝÁÝ$ÝÄÝ¹¼Ä»Û;ã
0173 C.
0174 . C. ***** LOS *****
0175 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-634 2013-05-07 12:14:22 135 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÀø•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÒø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 0f 80 80 06 06)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 10 80 80 08 08)
0050 + DC 07-F0 MDP_XRT_FLD_ENA
0051 BC (d8)
0052 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0053 BC (c8)
0054 + DC 07-F0 MDP_XRT_AEC_RESET
0055 BC (d0)
0056 + DC 07-F0 MDP_XRT_ARS_DIS
0057 BC (d5)
0058 + DC 07-F0 MDP_XRT_FLD_RESET
0059 BC (da)
0060 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0061 BC (c4 14)
0062 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0063 BC (c5 07)
0064 . C. ----- Success Verify ? OK / NG ____
0065 C.
0066 C.
0067 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0068 C.
0069 +. DC 07-F0 MDP_XRT_MODE_OBSV
0070 BC (c2)
0071 +. TI 2013-05-07 09:57:02.0
0072 DC 07-F0 MDP_XRT_MODE_OBSV
0073 BC (c2)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C. ***** XRT END *****
0077 . C. *****
0078 C. SOT table upload
0079 C. *****
0080 . C. < Upload DPL table >
0081 +. DC 07-F0 MDP_FG_CTRL_MANU
0082 BC (51)
0083 . C. -----
0084 C. MDP_FG_CTRL_MODE = MANU [ ]
0085 C. -----
0086 C.
0087 C. YçYÁY×Yí;¼YÉøÍÁ°øÈÈSTS_CHKøøOFFøÈø¹øÈ
0088 C.
0089 . S. RAM ram-271:MDP_DPL
0090 ( )
0091 C.
0092 . C. < Dump RAMID=MDP_DPL >
0093 +. DC 07-F0 MDP_DUMP_FGTBL
0094 BC (82 07 00 38 b8 00 40)
0095 C. -----
```

```
0096 C. MDP_DPL verify = OK [ ]
0097 C. -----
0098 C.
0099 C. STS_CHKαδONαÈα¹αè
0100 C.
0101 . C. < Update MDP DSC PAR1 >
0102 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0103 BC (4c)
0104 C. MDP_CMD_CODE = F04C0700[ ]
0105 C. MDP_CMD_CNT (count-up 1) [ ]
0106 C. -----
0107 C.
0108 . C.
0109 C. *****
0110 C. SOT TI command set
0111 C. *****
0112 C. Execute, after the success of TBL upload.
0113 +. TI 2013-05-07 09:57:18.0
0114 DC 07-F0 MDP_SOT_MODE_OBSV
0115 BC (40)
0116 C. -----
0117 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0118 C. -----
0119 C.
0120 C.
0121 . C. ***** MDP `ûÃîαî»ò¼ÝαÈÃα¹αèDCBC•x²è *****
0122 C. (¼ã°îÝÓ¥Ã¥È¥Þ¥È¥ã¥ç¥èαÈ¼α¼Ã»Ûα¹αè)
0123 . S. DC-BC dcbc-402:DCBC
0124 (MDP_known_event)
0125 C.
0126 C.
0127 . C. ***** ¥D¥¹•î Daily±;îÑαÈ´Øα¹αèDCBC•x²è *****
0128 . S. DC-BC dcbc-153:DCBC
0129 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0130 C.
0131 C.
0132 . C. ;ãLOS¥Á¥S¥Ã¥-¼Ã»Û;ã
0133 C.
0134 . C. ***** LOS *****
0135 C.
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May 07, 13 12:14

XRT_OGLIST_0441.chk

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*** OP Sequence for XRT ***

2013/05/07	10:05:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_448_OG [0x1c0]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2013/05/07	10:08:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/05/07	18:04:00.5	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/05/07	18:14:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/05/08	05:51:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/05/08	06:01:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/05/08	17:46:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/05/08	17:56:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/05/09	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/05/09	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/05/09	10:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	10:05:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_401_OG [0x191]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2013/05/09	10:31:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00