XRT Timeline to be uploaded on 2013/05/28

Period: 2013/05/28 08:46:00 - 2013/06/01 10:07:00

Normal mode

XOB #198F: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad With G-band and VLS closed

Term: 05/28 09:28:00 - 05/28 17:10:00 | Pointing (x, y): Fixed (-827.0, 420.0) | Comment: # OP start + 10min HOP201 N-E limb

PROG= 13 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 67 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 62 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 63 52-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

XOB #1996: Synoptic Q95 2x2 - Al/mesh(12/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(64/1024) + G-band(8) G

Term: 05/28 18:00:00 - 05/28 18:08:10 | Pointing (x, y): Fixed (0.0, 0.0) | Comment: synoptic, shifted -3.0 min

PROG= 12 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 72 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 73 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 66 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Flare mode

XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term: 05/28 09:28:00 - 05/28 17:10:00 | Pointing (x, y): Fixed (-827.0, 420.0) | Comment: # OP start + 10min HOP201 N-E limb

PROG= 07 1-time(s)

Subr= 1 30-time(s) 20.0sec

Seqn= 58 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 64 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 59 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3 30-time(s) 60.0sec

Seqn= 58 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 60 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 59 1-time(s) 2.0sec

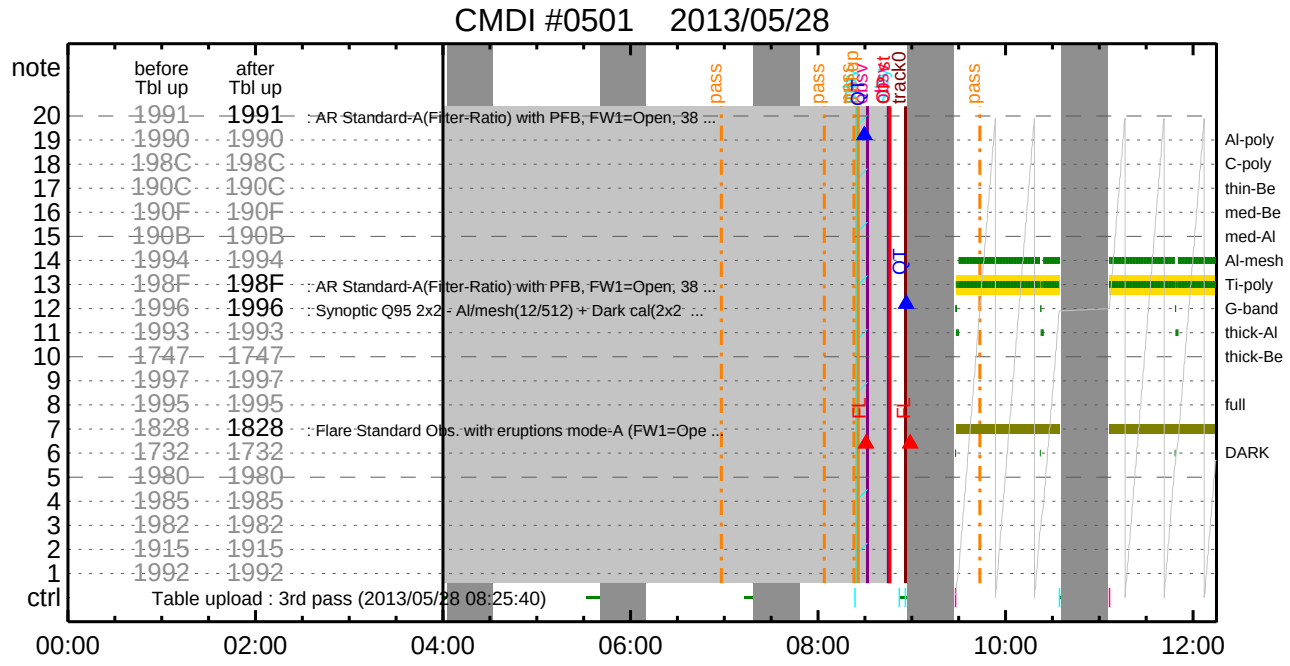
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	**30-time(s)**	**60.0sec**											
Seqn= 58	**1-time(s)**	**2.0sec**											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60	**1-time(s)**	**2.0sec**											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	**1-time(s)**	**2.0sec**											
Seqn= 59	**1-time(s)**	**2.0sec**											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	**30-time(s)**	**60.0sec**											
Seqn= 58	**1-time(s)**	**2.0sec**											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60	**1-time(s)**	**2.0sec**											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	**24-time(s)**	**600.0sec**											
Seqn= 61	**1-time(s)**	**2.0sec**											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

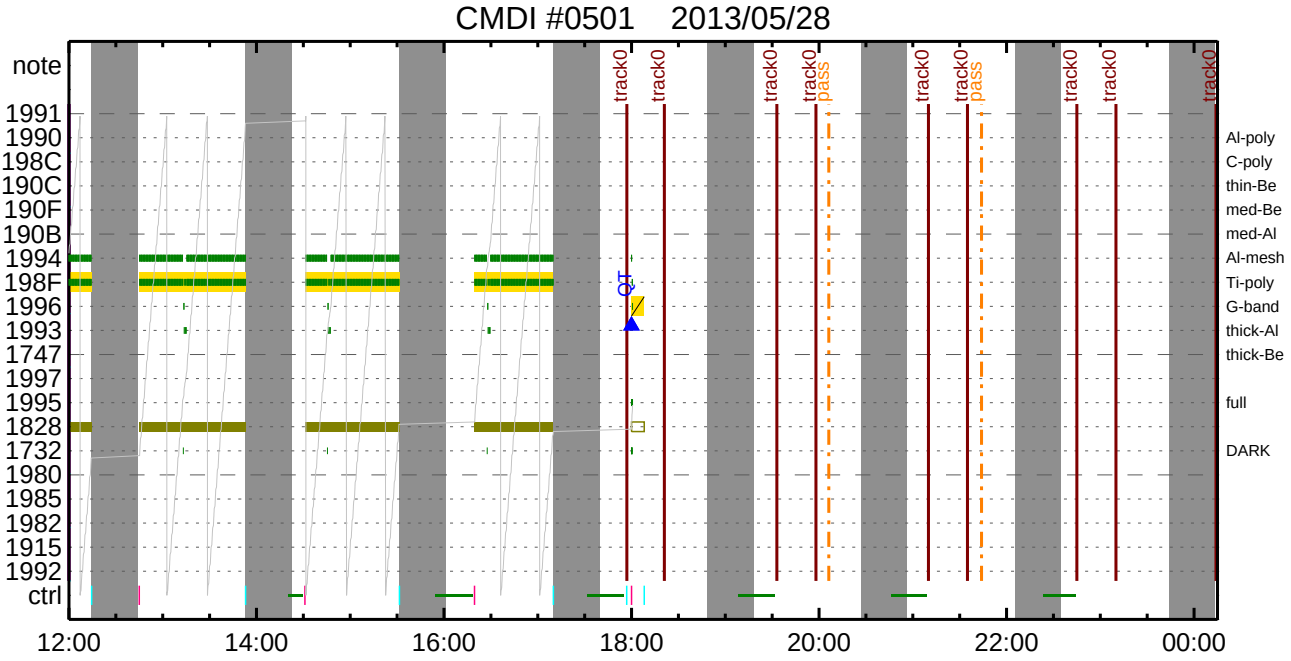
* * * * * **Active Region Search** * * * * *

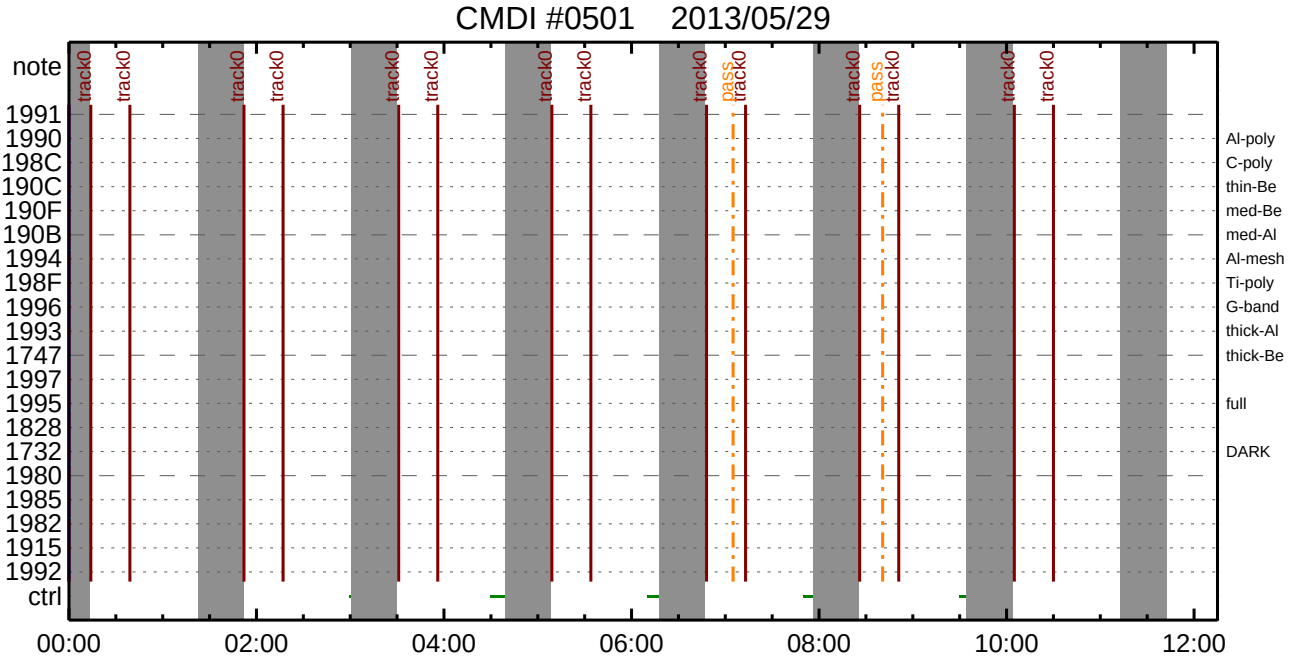
NOT USED

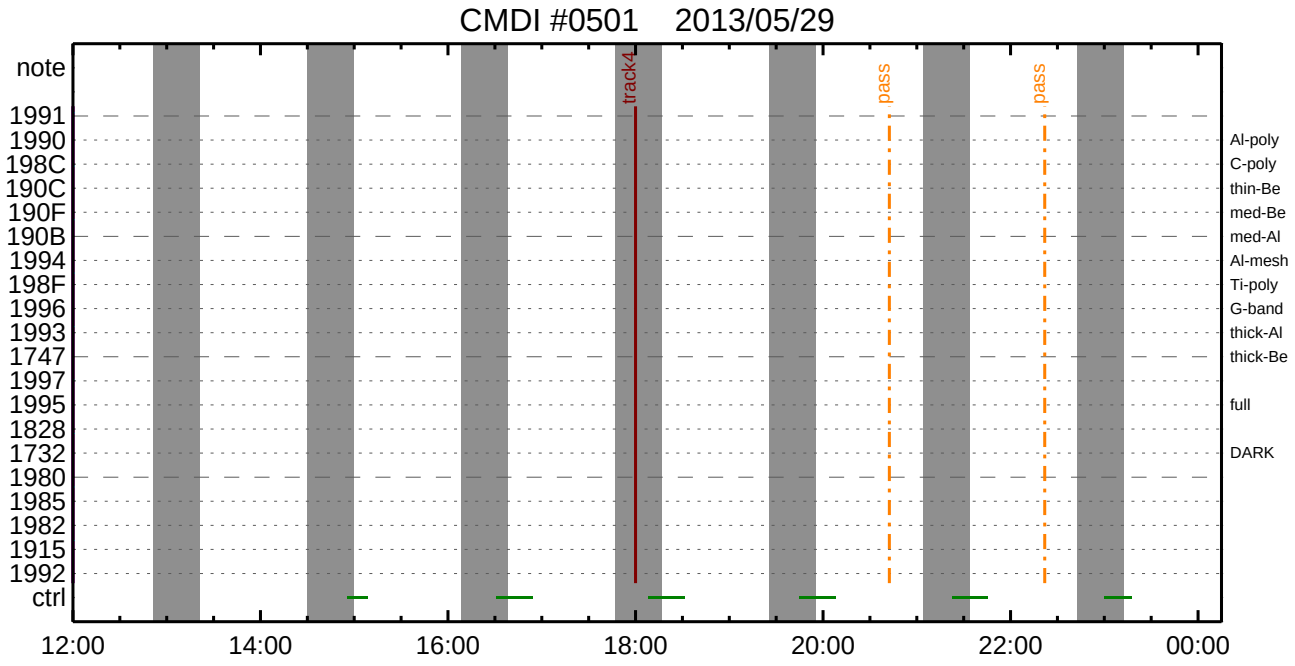
* * * * * **Flare Detection** * * * * *

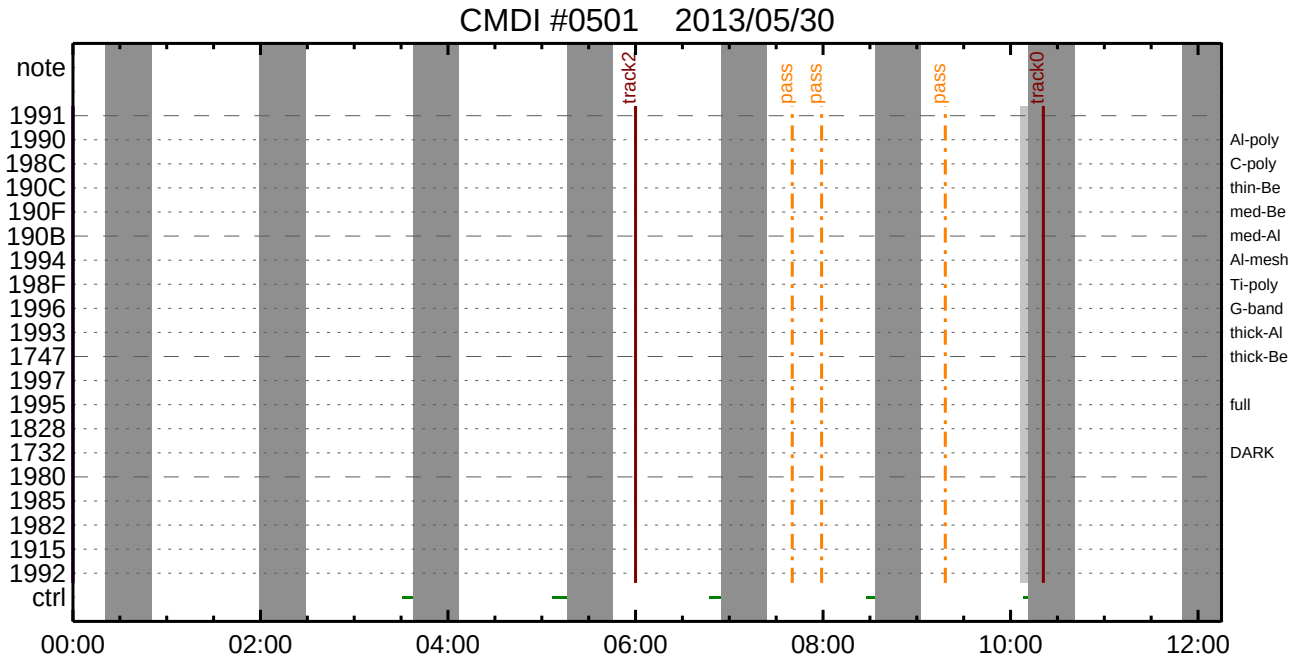
NOT USED

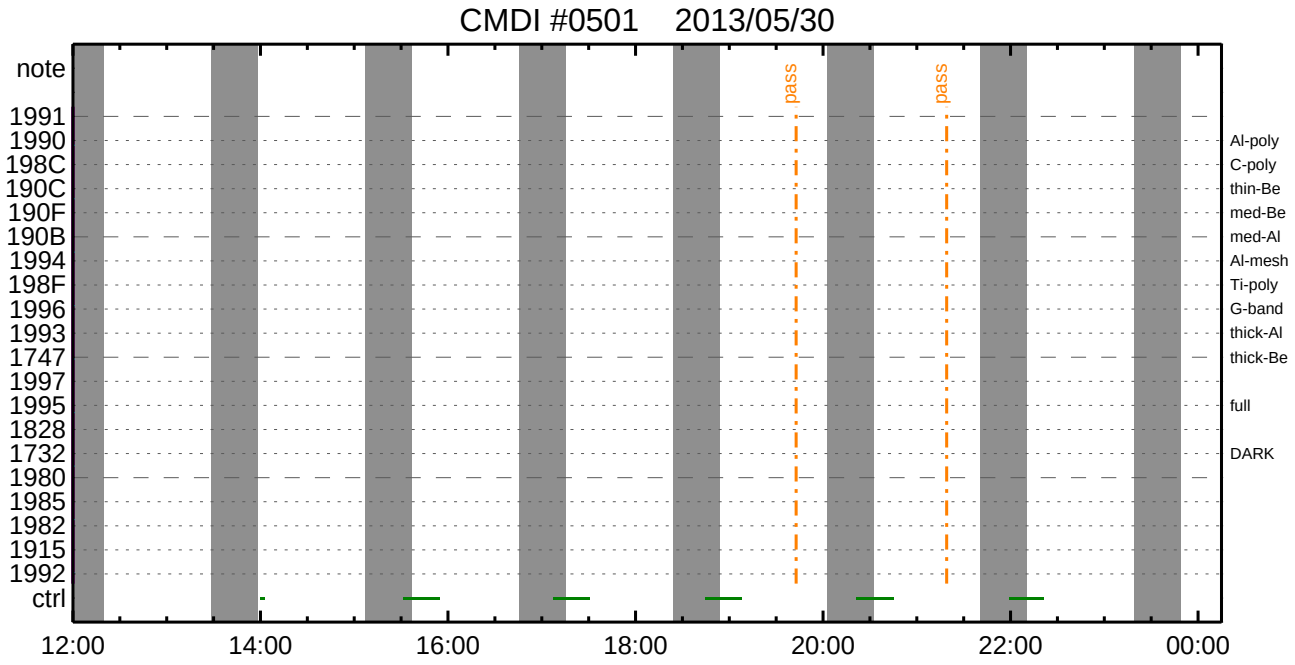

CMDI #0501 2013/05/28
note
before Tbl up
after Tbl up
20 1991 1991
19 1990 1990
18 198C 198C
17 190C 190C
16 190F 190F
15 190B 190B
14 1994 1994
13 198F 198F
12 1996 1996
11 1993 1993
10 1747 1747
9 1997 1997
8 1995 1995
7 1828 1828
6 1732 1732
5 1980 1980
4 1985 1985
3 1982 1982
2 1915 1915
1 1992 1992
ctrl
: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 38 ...
: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 38 ...
: Synoptic Q95 2x2 - Al/mesh(12/512) + Dark cal(2x2 ...
: Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
Table upload : 3rd pass (2013/05/28 08:25:40)
pass
QT
FL
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0501 2013/05/28
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
track0
pass
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0501 2013/05/29
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
track0
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0501 2013/05/29
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
track4
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0501 2013/05/30
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
track2
pass
pass
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0501 2013/05/30
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
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20:00
22:00
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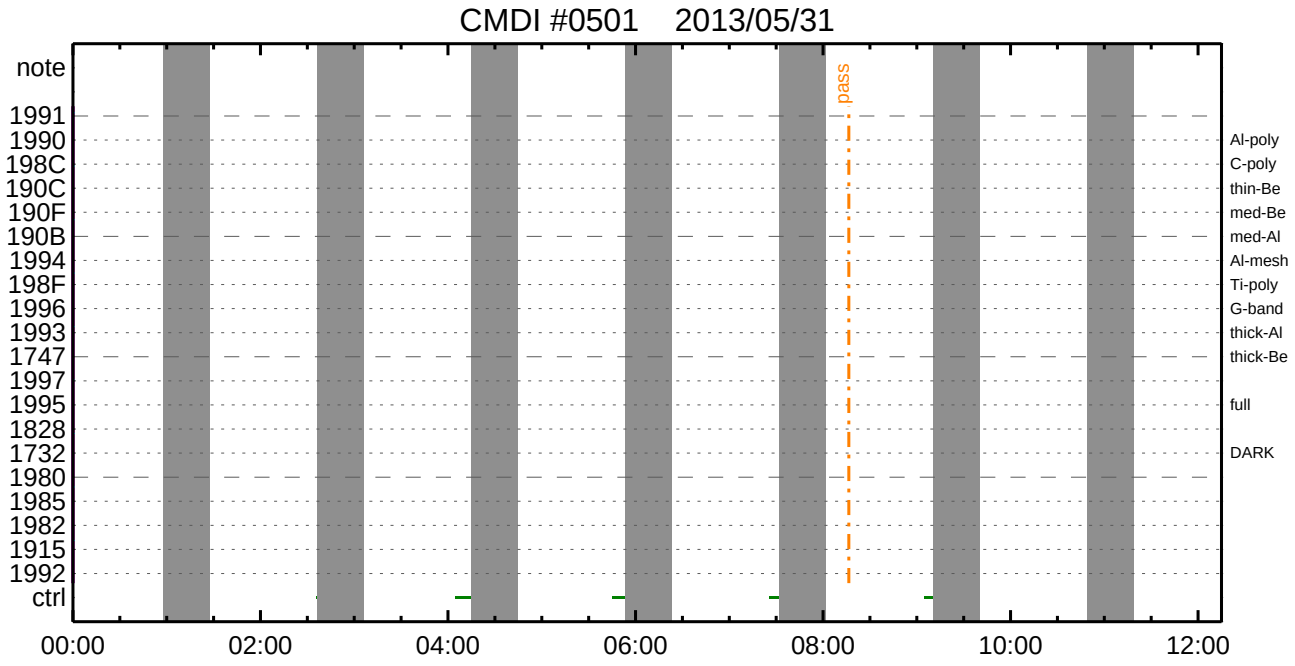
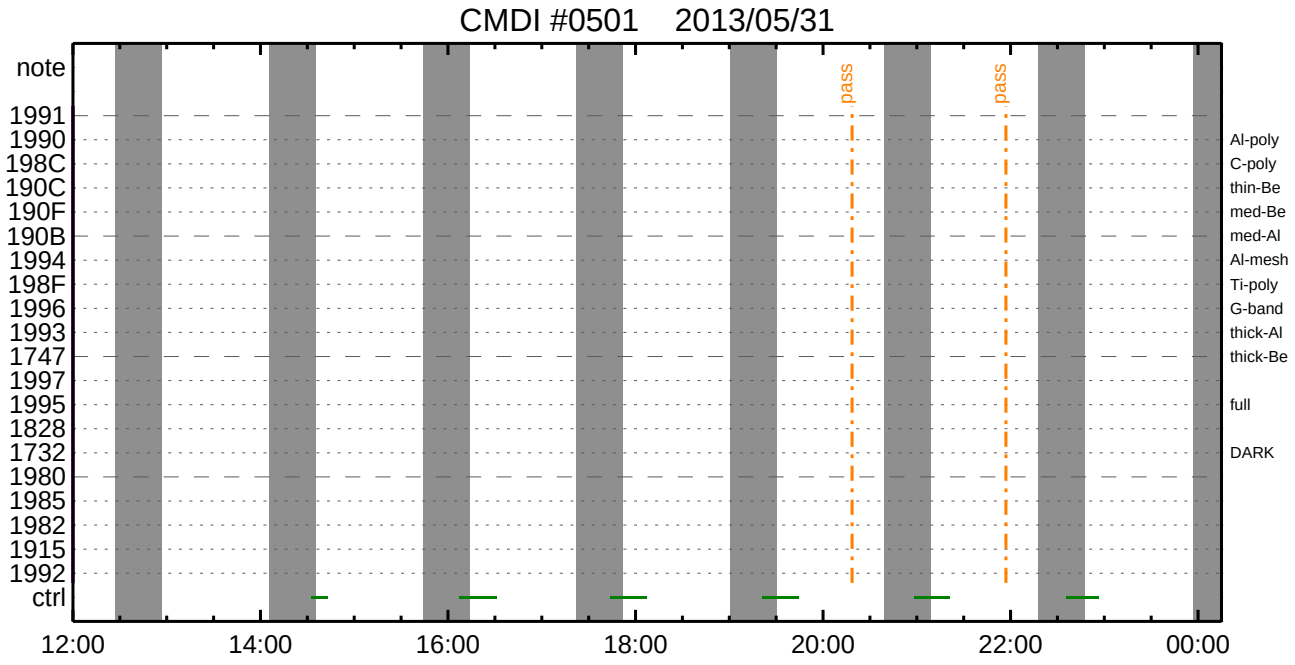
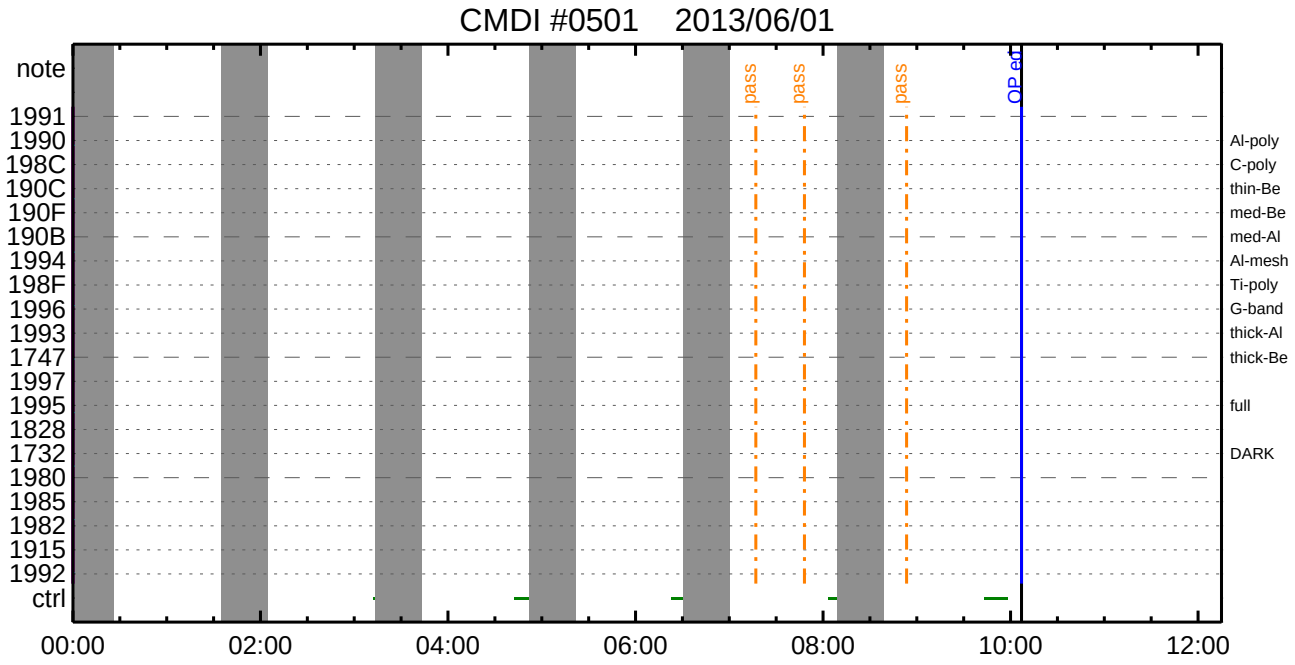

CMDI #0501 2013/05/31
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0501 2013/05/31
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0501 2013/06/01
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
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1997
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1828
1732
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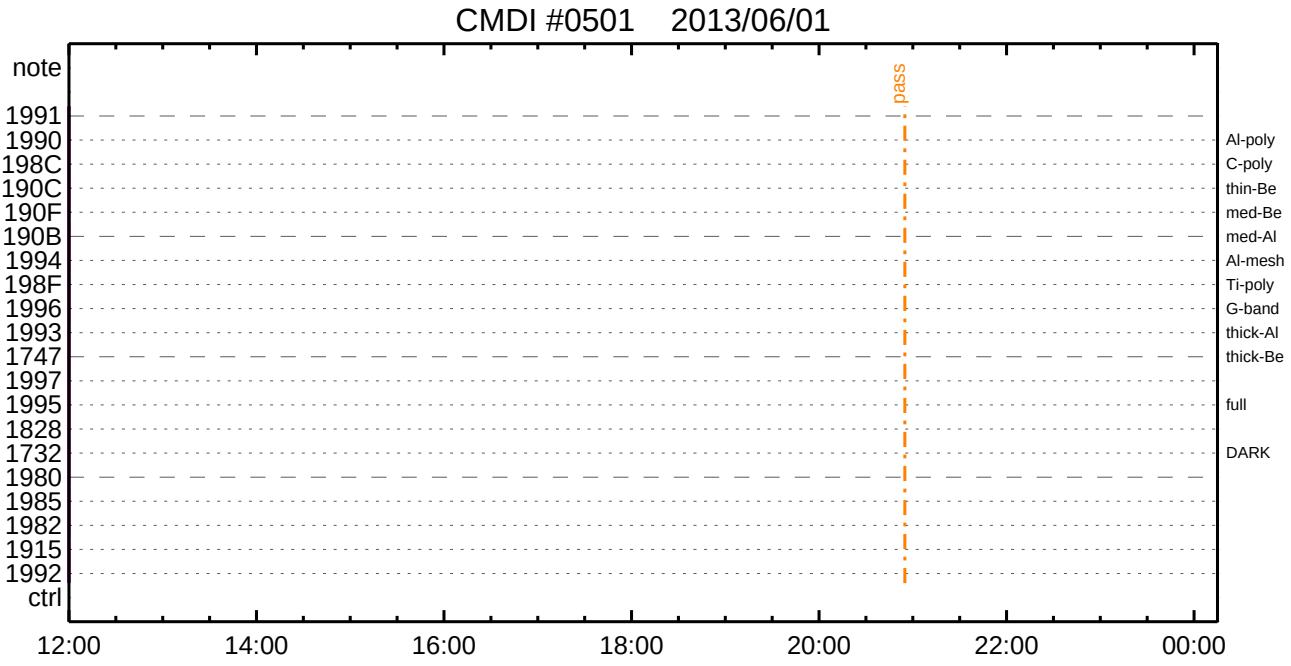
1985
1982
1915
1992
ctrl
pass
pass
pass
OP en
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0501 2013/06/01
note
1991
1990
198C
190C
190F
190B
1994
198F
1996
1993
1747
1997
1995
1828
1732
1980
1985
1982
1915
1992
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-678 2013-05-28 12:05:22 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-678:OP
0020     ()
0021   . S. OG    og-678:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
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```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2013-05-28 08:41:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2013-05-28 08:41:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2013-05-28 08:41:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2013-05-28 08:45:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ***************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ***************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. *******************************
0155     C.  SOT TI command set
0156     C. *******************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2013-05-28 08:45:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ***********
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2013-05-28 08:45:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2013-05-28 08:45:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ***********
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ***********
0183     C. Execute, after the success of OP upload.
0184   +. TI 2013-05-28 08:45:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ***********
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
```

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0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-679 2013-05-28 12:05:22 175 33 SOLAR-B MAIN //
0001     C.
0002   . C. ********** AOS **********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC =  685361.8 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_MANU
0140     BC       (21 02)
0141   . C. Verify EIS in MANUAL mode
0142   . C. Estimated OBSTBL upload time is 1m8s
0143     C. ********************************
0144     C. EIS START OBSTBL LOAD
0145     C. ********************************
0146   . S. RAM   ram-820:EIS_OBSTBL
0147     ()
0148   +. DC 07-FC EIS_DUMP_OBSTBL
0149     BC       (07 07 07 00 00 70 00)
0150     C.
0151     C. Execute, after the success of OBSTBL upload.
0152     C. Set EIS TI-commands
0153   +. TI 2013-05-28 08:45:50.0
0154     DC 07-FC EIS_MODE_CHG_ENA
0155     BC       (20)
0156   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0157     C. ********************************
0158     C. EIS END OBSTBL LOAD
0159     C. ********************************
0160     C.
0161   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0162     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0163   . S. DC-BC dcbc-402:DCBC
0164     (MDP_known_event)
0165     C.
0166     C.
0167   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0168   . S. DC-BC dcbc-153:DCBC
0169     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170     C.
0171     C.
0172   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0173     C.
0174   . C. ********** LOS **********
0175     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-680 2013-05-28 12:05:22 154 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 05 85 83 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 06 85 83 06 06)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 07 80 80 06 06)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 08 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 09 80 80 20 08)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0a 80 80 08 20)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0f 80 80 06 06)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 10 80 80 08 08)
0056   +  DC 07-F0 MDP_XRT_FLD_ENA
0057     BC       (d8)
0058   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059     BC       (c8)
0060   +  DC 07-F0 MDP_XRT_AEC_RESET
0061     BC       (d0)
0062   +  DC 07-F0 MDP_XRT_ARS_DIS
0063     BC       (d5)
0064   +  DC 07-F0 MDP_XRT_FLD_RESET
0065     BC       (da)
0066   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067     BC       (c4 14)
0068   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069     BC       (c5 07)
0070   . C. ---------- Success Verify ?            OK / NG ____
0071     C.
0072     C.
0073   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0074     C.
0075   +. DC 07-F0 MDP_XRT_MODE_OBSV
0076     BC       (c2)
0077   +. TI 2013-05-28 08:45:02.0
0078     DC 07-F0 MDP_XRT_MODE_OBSV
0079     BC       (c2)
0080   . C. ---------- Success Verify ?            OK / NG ____
0081     C.
0082     C. ************ XRT END ***********
0083   . C. ******************************
0084     C.  SOT table upload
0085     C. ******************************
0086   . C. < Stop FG table >
0087   +. DC 07-F0 MDP_FG_CTRL_MANU
0088     BC       (51)
0089   . C. ------------------------------------
0090     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0091     C. ------------------------------------
0092     C.
0093   . C. <Upload FG Observation Table>
0094   . S. RAM   ram-264:MDP_OBS_F
0095     ()
```

```
0096     C.
0097   . C. < Dump RAMID=MDP_OBS_F >
0098  +. DC 07-F0 MDP_DUMP_FGTBL
0099     BC       (82 07 00 00 00 38 b8)
0100     C. ------------------------------------
0101     C.  MDP_OBS_F verify = OK/NG        [ ]
0102     C. ------------------------------------
0103     C.
0104   . C. < Upload DPL table >
0105     C.
0106     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0107     C.
0108   . S. RAM   ram-271:MDP_DPL
0109     ()
0110     C.
0111   . C. < Dump RAMID=MDP_DPL >
0112  +. DC 07-F0 MDP_DUMP_FGTBL
0113     BC       (82 07 00 38 b8 00 40)
0114     C. ------------------------------------
0115     C.  MDP_DPL verify          = OK    [ ]
0116     C. ------------------------------------
0117     C.
0118     C. STS_CHK¤òON¤Ë¤¹¤ë
0119     C.
0120   . C. < Update MDP DSC PAR1 >
0121  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0122     BC       (4c)
0123     C.  MDP_CMD_CODE          = F04C0700[ ]
0124     C.  MDP_CMD_CNT        (count-up 1) [ ]
0125     C. ------------------------------------
0126     C.
0127   . C.
0128     C. ********************************
0129     C.  SOT TI command set
0130     C. ********************************
0131     C. Execute, after the success of TBL upload.
0132  +. TI 2013-05-28 08:45:18.0
0133     DC 07-F0 MDP_SOT_MODE_OBSV
0134     BC       (40)
0135   . C. ------------------------------------
0136     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0137     C. ------------------------------------
0138     C.
0139     C.
0140   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0141     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0142   . S. DC-BC dcbc-402:DCBC
0143     (MDP_known_event)
0144     C.
0145     C.
0146   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0147   . S. DC-BC dcbc-153:DCBC
0148     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149     C.
0150     C.
0151   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0152     C.
0153   . C. ********** LOS **********
0154     C.
```

```
*** OP Sequence for XRT ***

2013/05/28  08:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  08:52:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  08:52:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/05/28  08:55:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/05/28  08:55:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  08:55:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/05/28  08:56:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 da a8 49 81
2013/05/28  08:56:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2013/05/28  08:56:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/05/28  08:56:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2013/05/28  08:56:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/05/28  08:56:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  08:56:26.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2013/05/28  08:58:58.0  XRT_FL_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2013/05/28  09:27:00.0  XRT_Custom_430_OG [0x1ae]
2013/05/28  09:28:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/05/28  10:34:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  10:34:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  10:34:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/05/28  10:37:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/05/28  11:05:30.0  XRT_Custom_430_OG [0x1ae]
2013/05/28  11:06:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/05/28  12:14:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  12:14:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  12:14:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/05/28  12:17:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/05/28  12:44:00.0  XRT_Custom_430_OG [0x1ae]
2013/05/28  12:45:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/05/28  13:53:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  13:53:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  13:53:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/05/28  13:56:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/05/28  14:30:00.0  XRT_Custom_430_OG [0x1ae]
2013/05/28  14:31:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/05/28  15:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  15:31:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  15:31:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/05/28  15:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/05/28  16:18:30.0  XRT_Custom_430_OG [0x1ae]
2013/05/28  16:19:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/05/28  17:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  17:10:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2013/05/28  17:10:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/05/28  17:13:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/05/28  17:56:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  17:56:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/05/28  17:57:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2013/05/28  17:57:16.0  XRT_FLD_DIS_434_OG [0x1b2]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
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2013/05/28  17:59:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/05/28  17:59:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2013/05/28  17:59:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2013/05/28  18:00:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/05/28  18:08:10.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/05/28  18:10:00.0  XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]
                                TCIB_XRT_S_HTR_A_ENA 0 04-BC
2013/05/28  18:21:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 54 00 01 0e
2013/05/28  19:33:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 4d ca 01 0e
2013/05/28  19:58:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 44 e5 01 0e
2013/05/28  21:10:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 3c 00 01 0e
2013/05/28  21:35:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 33 1a 01 0e
2013/05/28  22:45:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 2a 35 01 0e
2013/05/28  23:10:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 21 58 01 0e
2013/05/29  00:14:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 18 72 01 0e
2013/05/29  00:39:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 0f 8d 01 0e
2013/05/29  01:52:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 06 a7 01 0e
2013/05/29  02:17:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 fe a8 01 0e
2013/05/29  03:31:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 f5 cb 01 0e
2013/05/29  03:56:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 ec e6 01 0e
2013/05/29  05:09:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 e4 01 01 0e
2013/05/29  05:34:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 db 1b 01 0e
2013/05/29  06:48:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 d2 36 01 0e
2013/05/29  07:13:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 c9 59 01 0e
2013/05/29  08:26:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 c0 73 01 0e
2013/05/29  08:51:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 00 b7 8e 01 0e
2013/05/29  10:05:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 00 ae a8 01 0e
2013/05/29  10:30:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 da a8 49 81
2013/05/29  18:00:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 04 00 00 00 00
2013/05/30  06:00:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                AOCU_NM              5 02-76 02 00 00 00 00
2013/05/30  10:21:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
```