

XRT Timeline to be uploaded on 2013/05/30

Period: 2013/05/30 10:11:00 - 2013/06/04 10:20:00

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Normal mode

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XOB #1982: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms													
Term	Pointing (x, y)							Comment					
05/31 00:03:00 - 05/31 00:09:54	Fixed (-528.4, -528.4)							# XRT post bake-out quadrant pointings 1/4.					
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 1 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1983: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms													
Term	Pointing (x, y)							Comment					
05/31 00:13:00 - 05/31 00:19:54	Fixed (528.4, -528.4)							# 2/4					
PROG= 18 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 2 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1984: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms													
Term	Pointing (x, y)							Comment					
05/31 00:23:00 - 05/31 00:29:54	Fixed (528.4, 528.4)							# 3/4					
PROG= 01 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 3 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1985: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms													
Term	Pointing (x, y)							Comment					
05/31 00:33:00 - 05/31 00:39:54	Fixed (-528.4, 528.4)							# 4/4					
PROG= 04 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 4 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
Subr= 2	1-time(s)	2.0sec												
	Seqn= 6	2-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 3	1-time(s)	2.0sec												
	Seqn= 12	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	Seqn= 7	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2														
Term		Pointing (x, y)						Comment						
05/31 00:55:00 - 05/31 02:39:54		Fixed (0.0, 945.0)						# Coalignment at North Pole						
PROG= 15	1-time(s)													
	Subr= 1	1-time(s)	360.0sec											
		Seqn= 32	24-time(s)	300.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2														
Term		Pointing (x, y)						Comment						
05/31 02:55:00 - 05/31 04:44:54		Fixed (-945.0, 0.0)						# Coalignment at East Limb						
PROG= 17	1-time(s)													
	Subr= 1	1-time(s)	360.0sec											
		Seqn= 33	15-time(s)	480.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1996: Synoptic Q95 2x2 - Al/mesh(12/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(64/1024) + G-band(8) G														
Term		Pointing (x, y)						Comment						
05/31 04:48:05 - 05/31 04:54:54		Fixed (0.0, 0.0)						synoptic, shifted a few hours						
05/31 18:10:30 - 05/31 18:17:24		Fixed (0.0, 0.0)						synoptic, shifted 7.5 min						
06/01 05:58:00 - 06/01 06:04:54		Fixed (0.0, 0.0)						synoptic, shifted -5.0 min						
PROG= 12	1-time(s)													
	Subr= 1	1-time(s)	12.0sec											
		Seqn= 72	1-time(s)	4.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		Seqn= 5	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	1024x1024 (512, 1536)	DPCM	0	0	2.0sec	
		Seqn= 73	1-time(s)	4.0sec										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		Seqn= 66	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1999: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band and VLS closed														
Term		Pointing (x, y)						Comment						
05/31 04:58:00 - 05/31 17:22:30		Track (852.7, -336.5) @ 05/31 04:55:00						# HOP 201 on AR 11756						
05/31 18:20:30 - 05/31 23:56:30		Fixed (875.0, -338.0)						# AR 11756 cont.						
06/01 00:27:00 - 06/01 05:54:54		Track (877.6, 160.7) @ 06/01 00:00:00						# HOP 201 on AR 11755.						
06/01 06:08:00 - 06/01 08:03:00		Track (894.7, 159.9) @ 06/01 06:05:00						# AR 11755 cont.						
PROG= 11	Inf.-time(s)													
	Subr= 2	1-time(s)	2.0sec											
		Seqn= 78	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
		Seqn= 62	4-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
		Seqn= 63	45-time(s)	80.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	

Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)												
Term	Pointing (x, y)		Comment									
05/31 04:58:00 - 05/31 17:22:30	Track (852.7, -336.5) ^{05/31 04:55:00}		# HOP 201 on AR 11756									
05/31 18:20:30 - 05/31 23:56:30	Fixed (875.0, -338.0)		# AR 11756 cont.									
06/01 00:27:00 - 06/01 05:54:54	Track (877.6, 160.7) ^{06/01 00:00:00}		# HOP 201 on AR 11755.									
06/01 06:08:00 - 06/01 08:03:00	Track (894.7, 159.9) ^{06/01 06:05:00}		# AR 11755 cont.									
PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 61 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

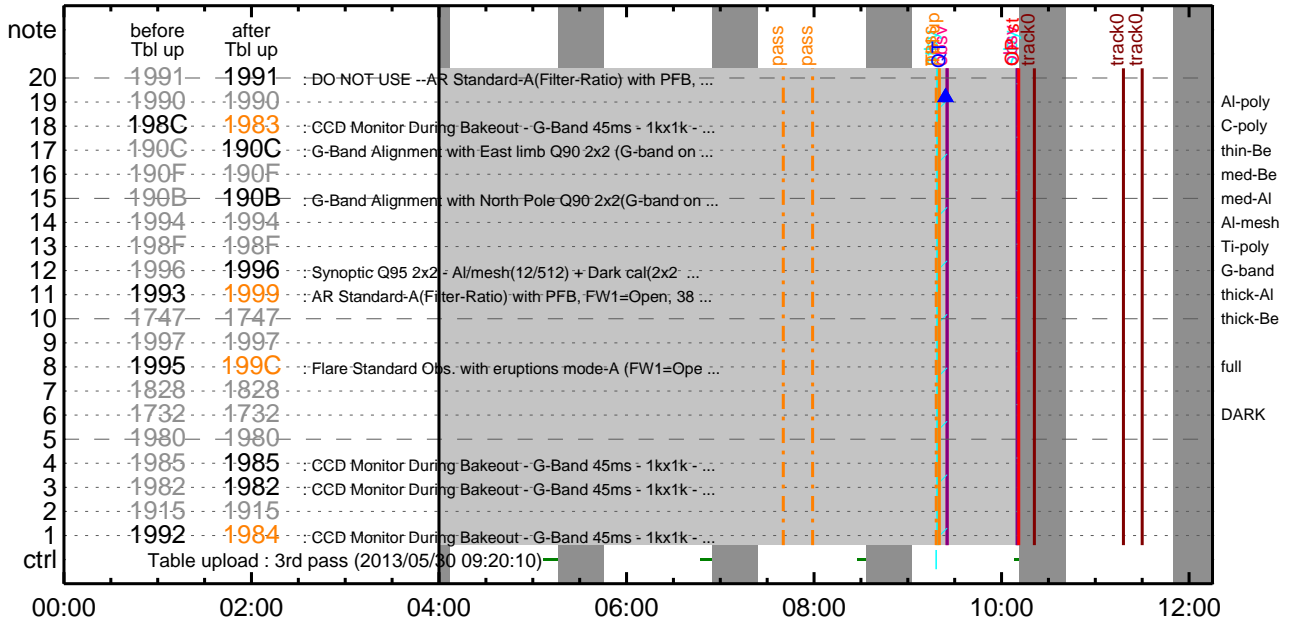
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FLD Patrol												
Term	Pointing (x, y)		Comment									
05/31 04:55:16 - 05/31 18:07:46	Track (852.7, -336.5) ^{05/31 04:55:00}		# HOP 201 on AR 11756									
05/31 18:17:46 - 06/01 05:55:16	Fixed (875.0, -338.0)		# AR 11756 cont.									

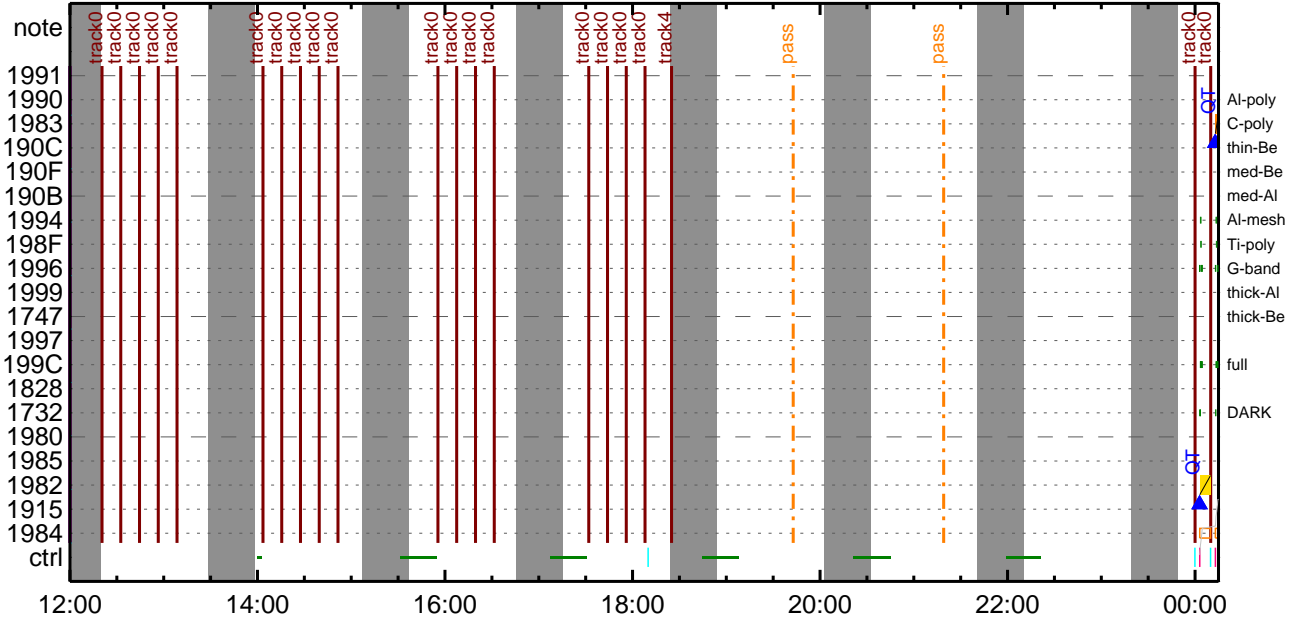
06/01 06:05:16 - 06/04 10:20:00 Track (894.7, 159.9) @ 06/01 06:05:00 # AR 11755 cont.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

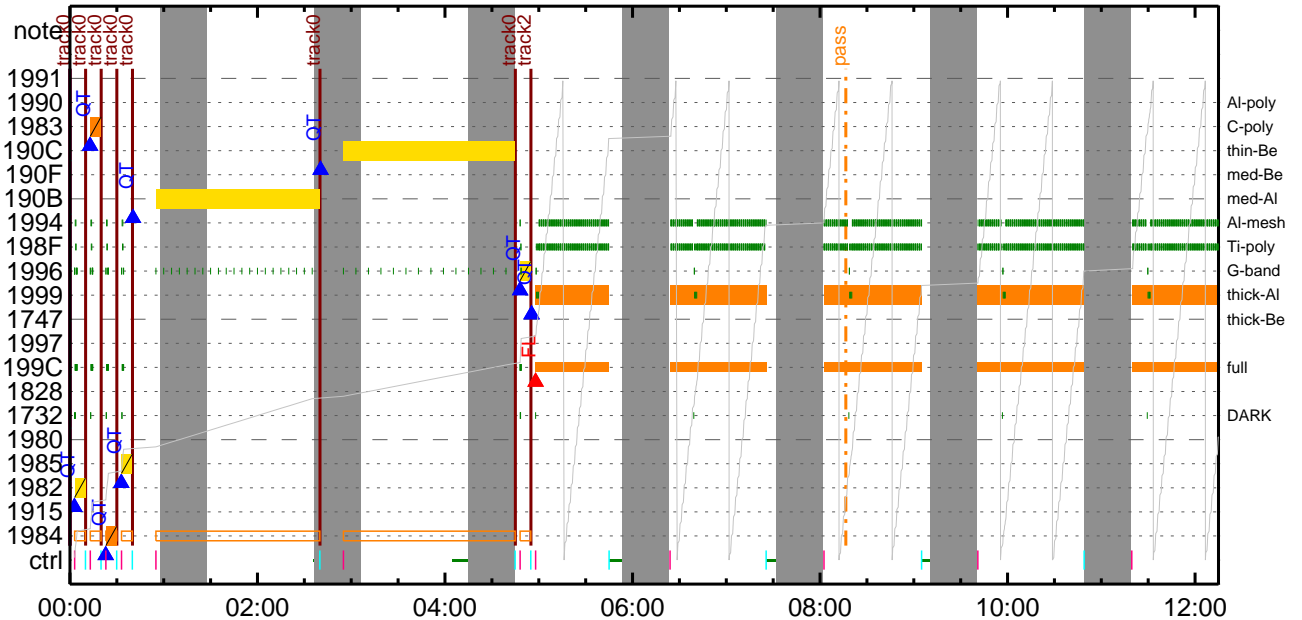
CMDI #0509 2013/05/30



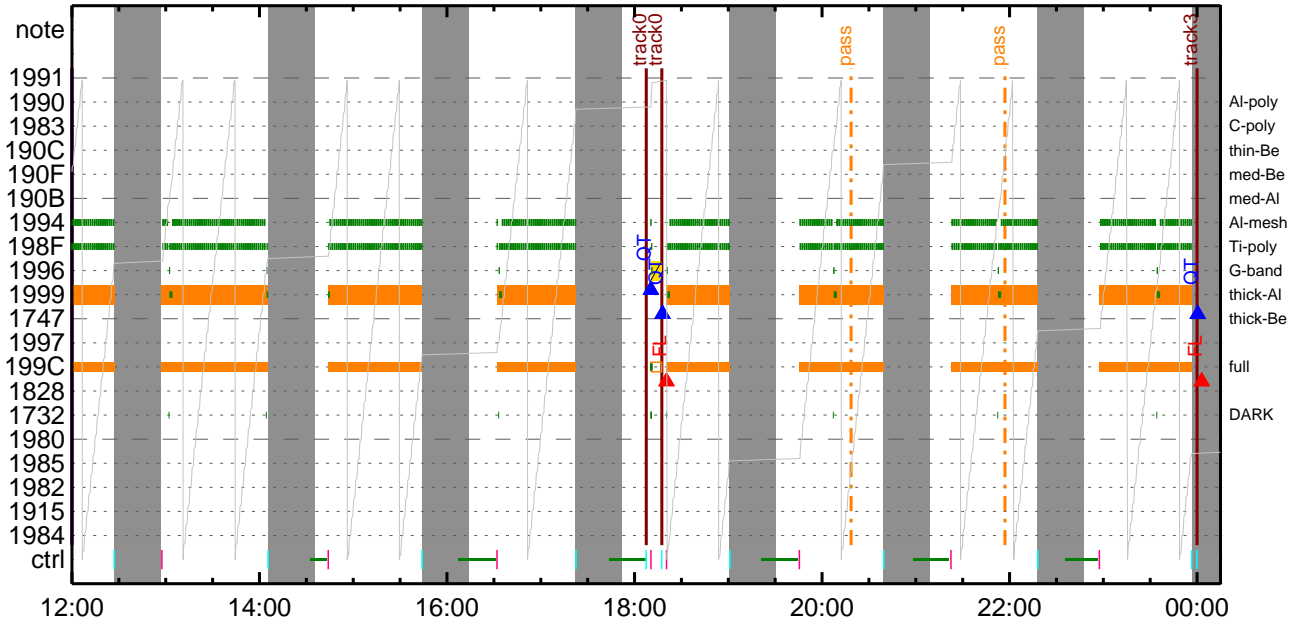
CMDI #0509 2013/05/30



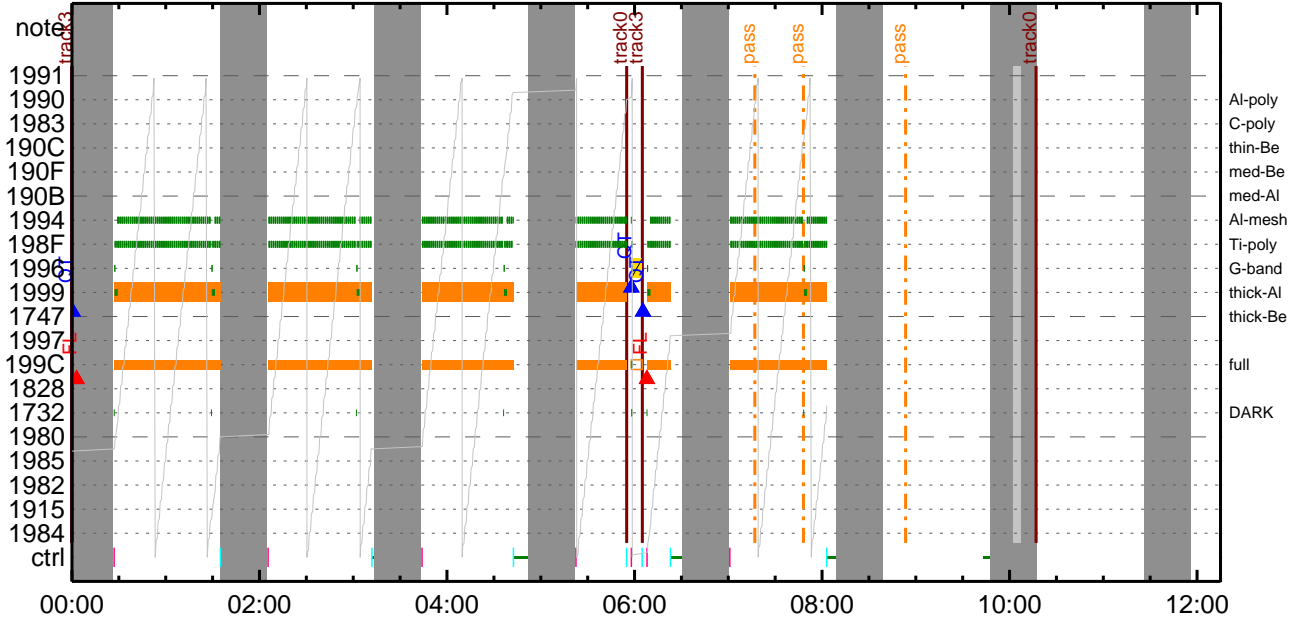
CMDI #0509 2013/05/31



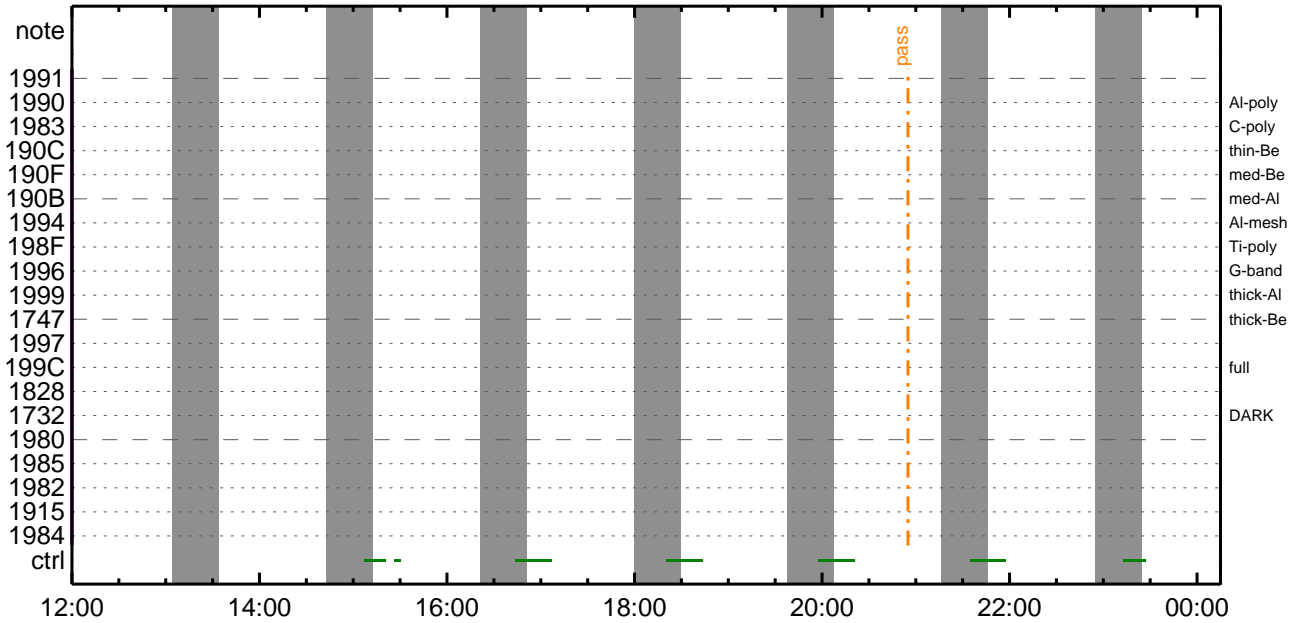
CMDI #0509 2013/05/31



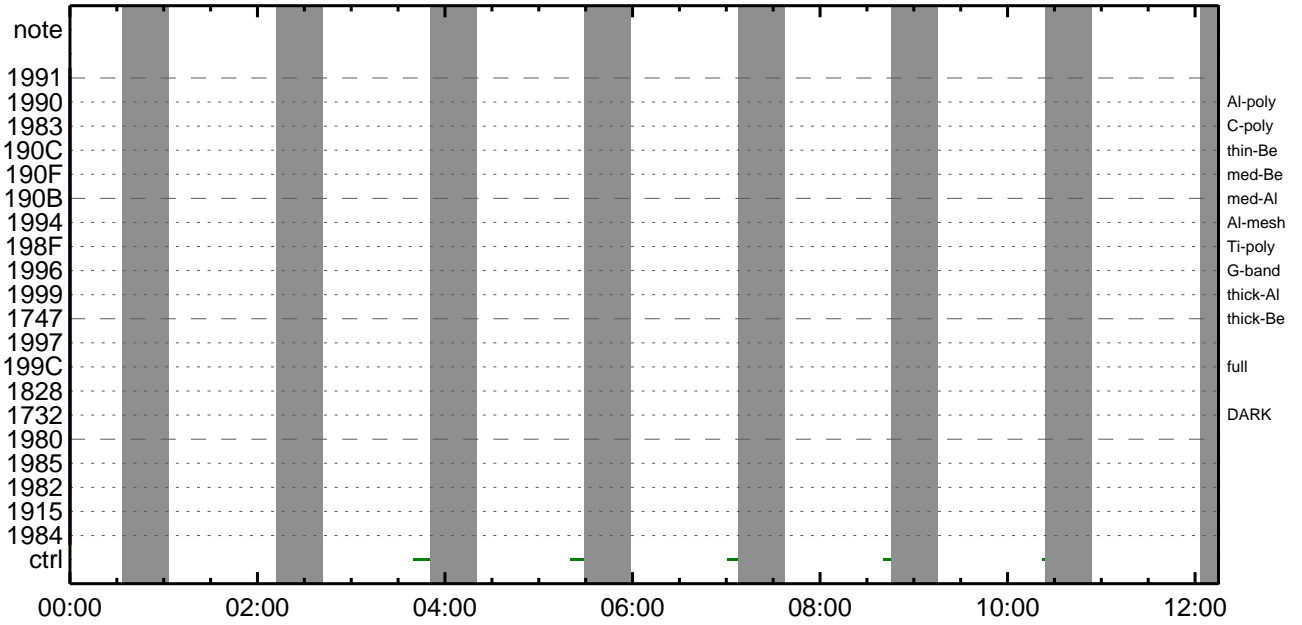
CMDI #0509 2013/06/01



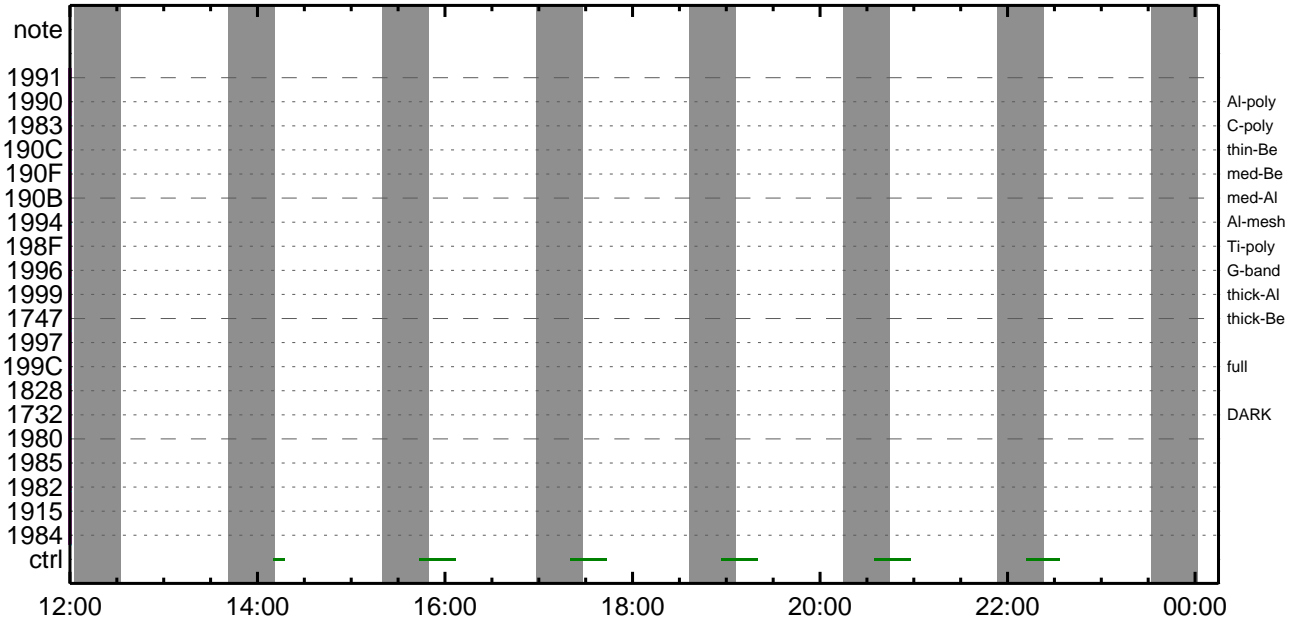
CMDI #0509 2013/06/01



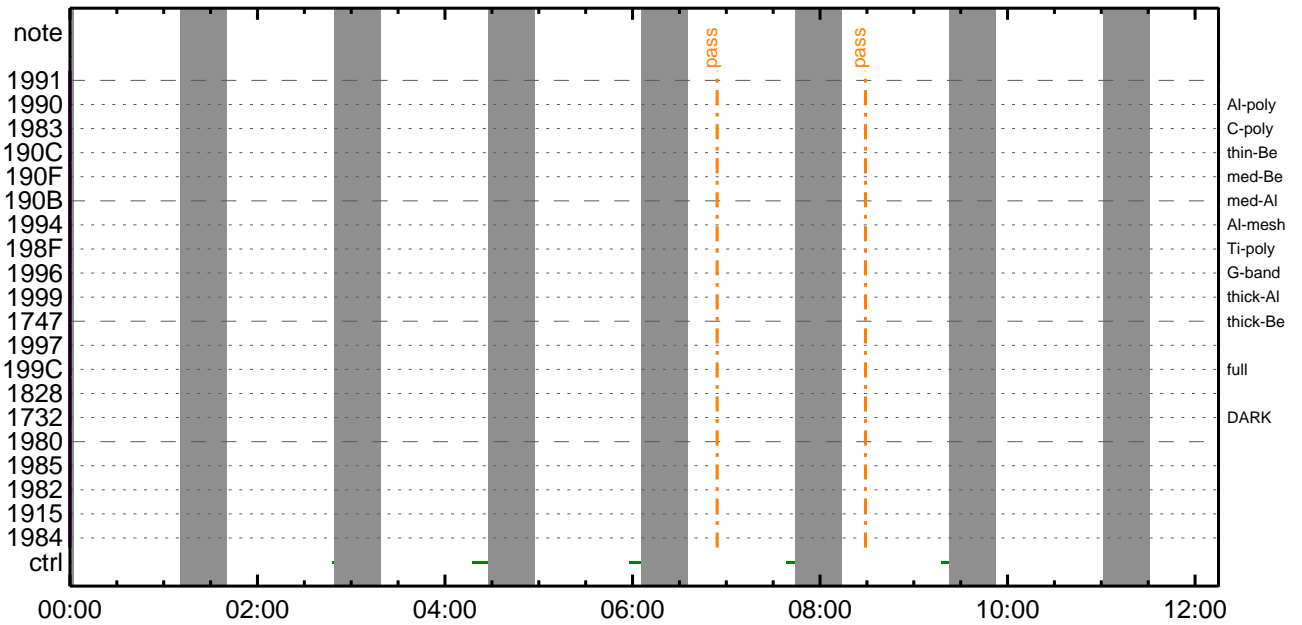
CMDI #0509 2013/06/02



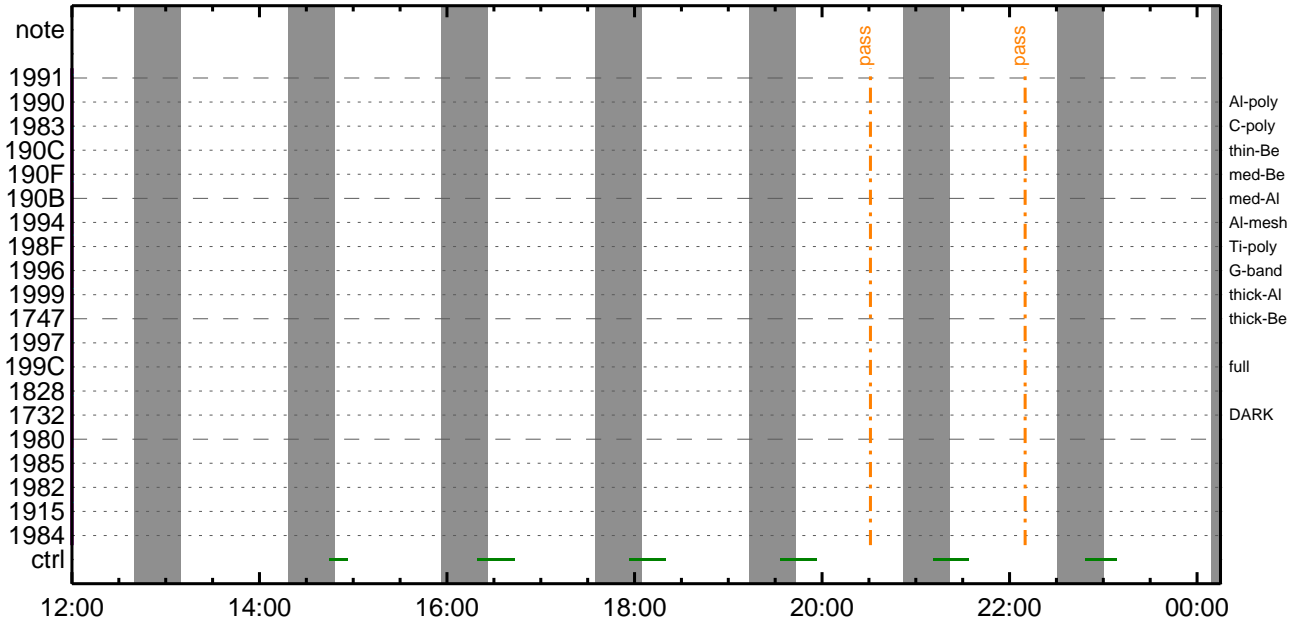
CMDI #0509 2013/06/02



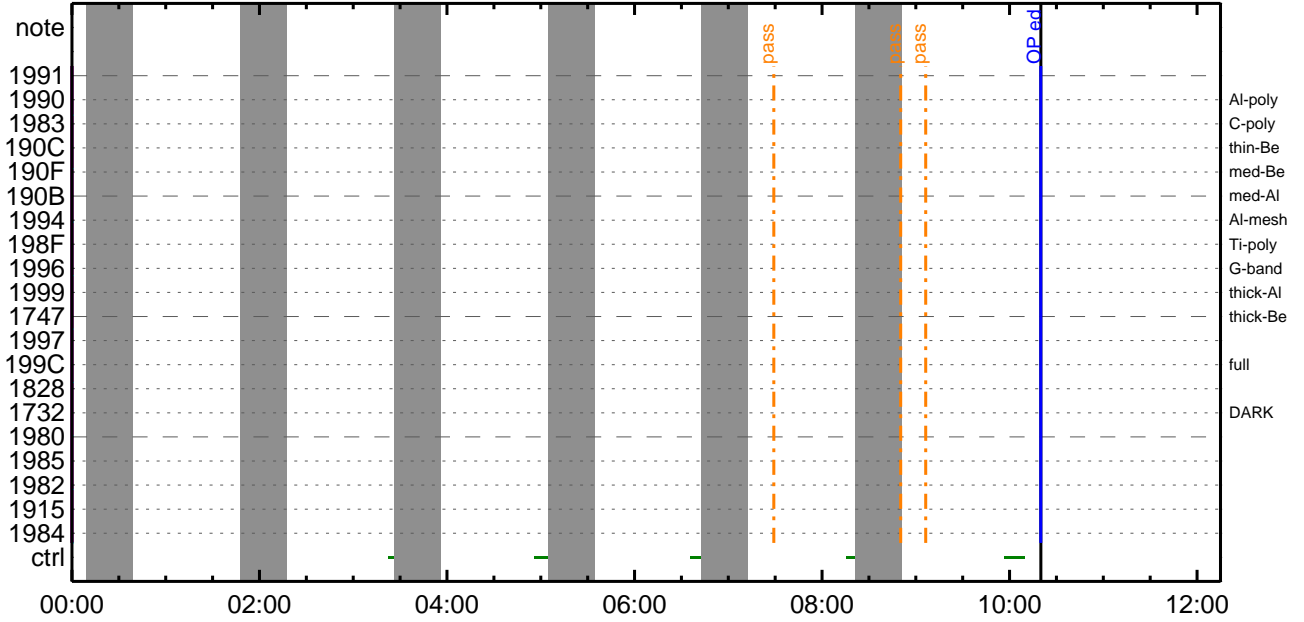
CMDI #0509 2013/06/03



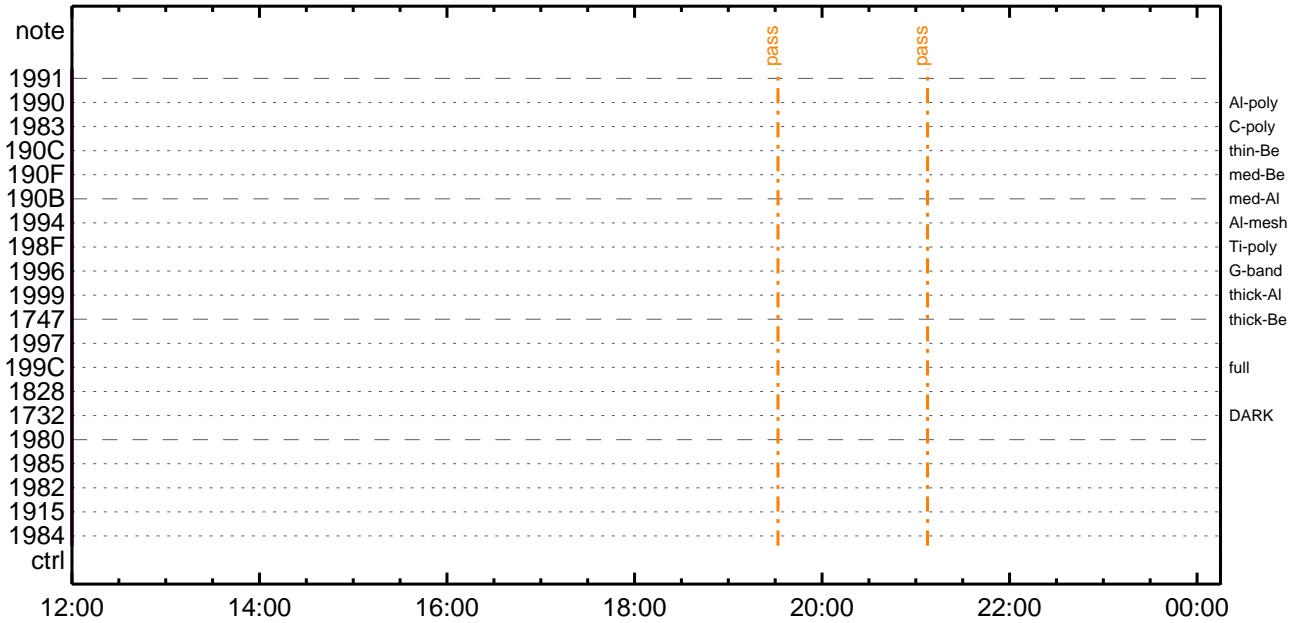
CMDI #0509 2013/06/03



CMDI #0509 2013/06/04



CMDI #0509 2013/06/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAY6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-684:OP
0104 ( )
0105 . S. OG og-684:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAY6Yx½ª î»òð³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½E¹ç.ë² îOK²ð³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAY6Yx½ª î»òð³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½E¹ç.ë² îOK²ð³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAY6Yx½ª î»òð³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG,RAM ID=OP²î½E¹ç.ë² îOK²ð³ îÇ§
0165 C.
0166 . C. ***** òE²¼ò î½Ã´ ¶À°òEÈ-ò°Á÷¿@ (¼âµ-YAY6Yx½ª ê½çòðÁÔÃæç¼ª°² òE¼¹ çòçðâ) *****
0167 C. DHUÿâ;½YE;Ê½Y½.¥i;½YE;Ëòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |§ OPOG UPLOAD²-Á÷¿@NG²î½E¹ç;ç°E²¼ò îTI-CMDÁ÷¿@²î½E¹Ô².²E²²²³²E;f
0180 C. òE²¼;çSET²EDUMPA²E²±²îY¹²ç¹Ô²|²³²E;f
0181 C.
0182 . C. TIY³Y½Y6YÈ²òðÁî¿ (UT)
0183 +. TI 2013-05-30 10:06:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-05-30 10:06:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-05-30 10:06:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 c0 c0 10 10)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 40 c0 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 40 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c c0 40 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 60 20 18)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e a0 80 18 20)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_DIS
0149 BC (d9)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0151 BC (c9)
0152 + DC 07-F0 MDP_XRT_AEC_RESET
0153 BC (d0)
0154 + DC 07-F0 MDP_XRT_ARS_DIS
0155 BC (d5)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 14)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C.
0163 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0164 C.
0165 +. DC 07-F0 MDP_XRT_MODE_OBSV
0166 BC (c2)
0167 +. TI 2013-05-30 10:10:02.0
0168 DC 07-F0 MDP_XRT_MODE_OBSV
0169 BC (c2)
0170 . C. ----- Success Verify ? OK / NG ____
0171 C.
0172 C. ***** XRT END *****
0173 . C. *****
0174 C. SOT table upload
0175 C. *****
0176 . C. < Stop FG table >
0177 +. DC 07-F0 MDP_FG_CTRL_MANU
0178 BC (51)
0179 . C. -----
0180 C. MDP_FG_CTRL_MODE = MANU [ ]
0181 C. -----
0182 C.
0183 . C. <Upload FG Observation Table>
0184 . S. RAM ram-265:MDP_OBS_F
0185 ( )
0186 C.
0187 . C. < Dump RAMID=MDP_OBS_F >
0188 +. DC 07-F0 MDP_DUMP_FGTBL
0189 BC (82 07 00 00 00 38 b8)
0190 C. -----
0191 C. MDP_OBS_F verify = OK/NG [ ]
0192 C. -----
0193 C.

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```
0194 C. *****
0195 C. SOT TI command set
0196 C. *****
0197 C. Execute, after the success of TBL upload.
0198 +. TI 2013-05-30 10:10:18.0
0199 DC 07-F0 MDP_SOT_MODE_OBSV
0200 BC (40)
0201 . C. -----
0202 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0203 C. -----
0204 C.
0205 C.
0206 . C. ***** MDP 'úÃîñî»ò¼ŷñÉÊĐñ¹ñèDCBC•x²è *****
0207 C. (%á°îŷÓŷÃŷÈŷŷÈŷáŷçŷèñ¼ññ¼Á»Ûñ¹ñè)
0208 . S. DC-BC dcbc-402:DCBC
0209 (MDP_known_event)
0210 C.
0211 C.
0212 . C. ***** ŷĐŷ¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0213 . S. DC-BC dcbc-153:DCBC
0214 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0215 C.
0216 C.
0217 . C. ;ãLOSŷÁŷŶŷËŷ-¼Á»Ûñ¹ñè
0218 C.
0219 . C. ***** LOS *****
0220 C.
```

*** OP Sequence for XRT ***

2013/05/30	10:21:00.0	AOCS_ORe-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	00	03	be	4e	f1	
2013/05/30	11:18:00.0	AOCS_ORe-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00	03	be	3d	3f	
2013/05/30	11:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00	03	be	2b	8d	
2013/05/30	12:20:30.0	AOCS_ORe-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	00	03	be	19	db	
2013/05/30	12:32:30.0	AOCS_ORe-point_Start_5_OG [0x09b]								
		AOCU_NM	5	02-76	00	03	be	08	31	
2013/05/30	12:44:30.0	AOCS_ORe-point_Start_6_OG [0x09c]								
		AOCU_NM	5	02-76	00	03	be	f6	7f	
2013/05/30	12:56:30.0	AOCS_ORe-point_Start_7_OG [0x09d]								
		AOCU_NM	5	02-76	00	03	be	e4	cd	
2013/05/30	13:08:30.0	AOCS_ORe-point_Start_8_OG [0x09e]								
		AOCU_NM	5	02-76	00	03	be	d3	1b	
2013/05/30	14:03:30.0	AOCS_ORe-point_Start_9_OG [0x09f]								
		AOCU_NM	5	02-76	00	03	be	c1	59	
2013/05/30	14:15:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]								
		AOCU_NM	5	02-76	00	03	be	af	a6	
2013/05/30	14:27:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]								
		AOCU_NM	5	02-76	00	19	0e	51	81	
2013/05/30	14:39:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]								
		AOCU_NM	5	02-76	00	18	9b	41	db	
2013/05/30	14:51:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]								
		AOCU_NM	5	02-76	00	18	41	31	db	
2013/05/30	15:55:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]								
		AOCU_NM	5	02-76	00	18	18	21	58	
2013/05/30	16:07:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]								
		AOCU_NM	5	02-76	00	17	ff	10	72	
2013/05/30	16:19:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]								
		AOCU_NM	5	02-76	00	17	ff	ff	1b	
2013/05/30	16:31:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]								
		AOCU_NM	5	02-76	00	17	ff	f0	fe	
2013/05/30	17:32:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]								
		AOCU_NM	5	02-76	00	18	18	e0	19	
2013/05/30	17:44:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]								
		AOCU_NM	5	02-76	00	18	41	cf	8e	
2013/05/30	17:56:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]								
		AOCU_NM	5	02-76	00	18	9b	bf	8e	
2013/05/30	18:08:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]								
		AOCU_NM	5	02-76	00	19	0e	af	e8	
2013/05/30	18:10:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0						c1
2013/05/30	18:10:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]								
		TCIB_XRT_S_HTR_A_DIS	0	04-C0						
2013/05/30	18:25:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]								
		AOCU_NM	5	02-76	04	00	00	00	00	00
2013/05/30	23:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0						c1
2013/05/31	00:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]								
		AOCU_NM	5	02-76	00	2e	f9	2e	f9	
2013/05/31	00:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa			00
2013/05/31	00:02:52.0	XRT_QT_PROG_SET_449_OG [0x1c1]								
		MDP_XRT_QT_PROG_SET	2	07-F0						c4 03
2013/05/31	00:02:54.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS	1	07-F0						d9
2013/05/31	00:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0						c9
2013/05/31	00:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0						d5
2013/05/31	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0						c0
2013/05/31	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0						c1
2013/05/31	00:10:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]								
		AOCU_NM	5	02-76	00	2e	f9	d1	07	
2013/05/31	00:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa			00
2013/05/31	00:12:52.0	XRT_QT_PROG_SET_447_OG [0x1bf]								
		MDP_XRT_QT_PROG_SET	2	07-F0						c4 12
2013/05/31	00:12:54.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS	1	07-F0						d9
2013/05/31	00:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0						c9
2013/05/31	00:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0						d5
2013/05/31	00:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0						c0
2013/05/31	00:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0						c1
2013/05/31	00:20:00.0	AOCS_ORe-point_Start_25_OG [0x0af]								
		AOCU_NM	5	02-76	00	d1	07	d1	07	
2013/05/31	00:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa			00
2013/05/31	00:22:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]								
		MDP_XRT_QT_PROG_SET	2	07-F0						c4 01
2013/05/31	00:22:54.0	XRT_FLD_DIS_404_OG [0x194]								
		MDP_XRT_FLD_DIS	1	07-F0						d9
2013/05/31	00:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]								

2013/05/31	00:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/05/31	00:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/05/31	00:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/05/31	00:30:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			AOCU_NM	5	02-76	00 d1 07 2e f9			
2013/05/31	00:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/05/31	00:32:52.0	XRT_QT_PROG_SET_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/05/31	00:32:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2013/05/31	00:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/05/31	00:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/05/31	00:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/05/31	00:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/05/31	00:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/05/31	00:40:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			AOCU_NM	5	02-76	00 ac 00 00 00			
2013/05/31	00:40:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/05/31	00:40:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/05/31	00:40:20.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/05/31	00:40:22.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/05/31	00:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2013/05/31	02:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/05/31	02:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/05/31	02:40:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			AOCU_NM	5	02-76	00 00 00 54 00			
2013/05/31	02:40:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/05/31	02:40:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/05/31	02:40:20.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/05/31	02:40:22.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/05/31	02:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2013/05/31	04:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/05/31	04:44:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/05/31	04:45:00.0	AOCS_ORe-point_Start_29_OG [0x0b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
			AOCU_NM	5	02-76	00 00 00 00 00			
2013/05/31	04:45:16.0	XRT_ROI_A_443_OG [0x1bb]	MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 20 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0a 80 80 08 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08			
2013/05/31	04:45:21.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06			
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/05/31	04:47:59.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/05/31	04:48:01.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/05/31	04:48:03.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/05/31	04:48:05.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2013/05/31	04:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/05/31	04:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/05/31	04:55:00.0	AOCS_ORe-point_Start_30_OG [0x0b4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			AOCU_NM	5	02-76	02 00 00 00 00			
2013/05/31	04:55:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/05/31	04:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/05/31	04:55:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/05/31	04:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/05/31	04:55:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2013/05/31	04:55:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/05/31	04:57:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/05/31	04:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	05:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	05:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	05:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	05:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	06:23:00.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	06:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	07:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	07:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	07:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	07:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	08:01:30.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	08:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	09:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	09:05:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	09:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	09:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	09:40:00.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	09:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	10:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	10:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	10:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	10:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	11:18:30.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	11:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	12:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	12:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	12:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	12:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	12:56:30.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	12:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	14:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	14:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	14:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	14:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	14:43:00.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	14:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	15:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	15:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	15:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	15:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	16:31:00.0	XRT_Custom_430_OG [0x1ae]				
2013/05/31	16:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	17:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	17:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	17:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	17:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]				

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2013/05/31	18:07:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	18:07:30.5	AOCS_OrE-point_Start_29_OG [0x0b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/05/31	18:07:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00
2013/05/31	18:10:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/05/31	18:10:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/05/31	18:10:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/05/31	18:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/05/31	18:17:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	18:17:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	18:17:30.0	AOCS_OrE-point_Start_31_OG [0x0b5]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/05/31	18:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 1e 0c b2 36
2013/05/31	18:17:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/05/31	18:17:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/05/31	18:17:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/05/31	18:17:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/05/31	18:17:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	18:20:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/05/31	18:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/05/31	19:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	19:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	19:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	19:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	19:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	19:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2013/05/31	20:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2013/05/31	20:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	20:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	20:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_FLD_RESET_415_OG [0x19f]			
2013/05/31	21:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	21:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	22:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	22:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2013/05/31	22:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	22:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	22:56:30.0	XRT_Custom_430_OG [0x1ae]	XRT_FLD_RESET_415_OG [0x19f]			
2013/05/31	22:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/31	23:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/31	23:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/31	23:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2013/05/31	23:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/31	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/31	23:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/06/01	00:00:00.0	AOCS_OrE-point_Start_32_OG [0x0b6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/06/01	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2013/06/01	00:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/06/01	00:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/06/01	00:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/06/01	00:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5

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2013/06/01	00:00:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	00:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2013/06/01	00:26:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/06/01	00:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	01:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	01:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	01:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/06/01	01:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/06/01	02:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	02:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	03:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	03:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/06/01	03:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/06/01	03:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	03:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	03:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	04:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/06/01	04:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/06/01	04:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	04:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	05:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	05:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/06/01	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/06/01	05:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	05:55:00.0	AOCS_OrE-point_Start_29_OG [0x0b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/06/01	05:55:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00 00 00 00
2013/06/01	05:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/06/01	05:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/06/01	05:57:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/06/01	05:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2013/06/01	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	06:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	06:05:00.0	AOCS_OrE-point_Start_32_OG [0x0b6]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/06/01	06:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00 00 00 00
2013/06/01	06:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/06/01	06:05:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/06/01	06:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/06/01	06:05:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/06/01	06:05:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	06:07:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2013/06/01	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/06/01	06:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	06:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	06:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/06/01	06:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/06/01	07:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/06/01	07:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/06/01	08:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/06/01	08:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2013/06/01	08:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/06/01	08:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/06/01	10:17:00.0	AOCS_ORe-point_Start_29_OG [0x0b3]	AOCU_NM	5	02-76	00 00 00 00 00